

# XRT Timeline to be uploaded on 2014/11/25

Period: 2014/11/25 09:25:00 - 2014/11/29 10:24:00

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Normal mode

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## XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
11/26 12:18:00 - 11/26 12:24:54	Fixed ( -528.4, -528.4)	# XRT post bakeout quadra OBS 1/4
<b>PROG= 20 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 88 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 12 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 14 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

## XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
11/26 12:28:00 - 11/26 12:34:54	Fixed ( 528.4, -528.4)	2/4
<b>PROG= 15 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 31 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 12 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 14 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

## XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
11/26 12:38:00 - 11/26 12:44:54	Fixed ( 528.4, 528.4)	3/4
<b>PROG= 11 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 81 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 12 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 14 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

## XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
11/26 12:48:00 - 11/26 12:54:54	Fixed ( -528.4, 528.4)	4/4
<b>PROG= 10 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 28 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec

	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	<b>Seqn= 23</b>	<b>2-time(s)</b>	<b>2.0sec</b>										
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 3</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
	<b>Seqn= 12</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	<b>Seqn= 14</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi</b>													
	Term	Pointing (x, y)	Comment										
	11/26 12:58:00 - 11/26 13:04:54	Fixed ( 0.0, 0.0)	synoptic, shifted manually										
	11/27 03:53:00 - 11/27 03:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted manually										

<b>PROG= 03</b>	<b>1-time(s)</b>												
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>12.0sec</b>											
	<b>Seqn= 33</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
	<b>Seqn= 40</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 77</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 6</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #19E2: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m</b>													
	Term	Pointing (x, y)	Comment										
	11/26 13:08:00 - 11/26 23:38:30	Track ( 46.9, -258.1) @ 11/26 13:05:00	# Track AR12216										
	11/27 01:53:00 - 11/27 03:49:54	Track ( 162.2, -256.7) @ 11/27 01:50:00	# Track AR12216										
	11/27 08:03:00 - 11/27 09:06:30	Track ( 217.2, -255.9) @ 11/27 08:00:00	# Track AR12216										

<b>PROG= 18</b>	<b>Inf.-time(s)</b>												
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	<b>Seqn= 8</b>	<b>2-time(s)</b>	<b>2.0sec</b>										
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
	<b>Seqn= 24</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	<b>Seqn= 42</b>	<b>4-time(s)</b>	<b>2.0sec</b>										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	<b>Seqn= 62</b>	<b>20-time(s)</b>	<b>90.0sec</b>										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #19EF: AR Standard-B(Morphology), Be/thin 24s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33m</b>													
	Term	Pointing (x, y)	Comment										
	11/26 23:53:00 - 11/27 01:49:54	Track ( 137.3, -276.2) @ 11/26 23:50:00	# HOP275 at AR12216										

<b>PROG= 06</b>	<b>Inf.-time(s)</b>												
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	<b>Seqn= 8</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	<b>Seqn= 24</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2 2-time(s) 2.0sec</b>												
<b>Seqn= 16 2-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 52 100-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2</b>												
Term		Pointing (x, y)				Comment						
11/27 04:15:05 - 11/27 05:59:54		Fixed ( 0.0, 945.0)				# Co-alignment at N-pole						
<b>PROG= 13 1-time(s)</b>												
<b>Subr= 1 1-time(s) 360.0sec</b>												
<b>Seqn= 76 24-time(s) 300.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec - 8 min cadence-wNGT-2</b>												
Term		Pointing (x, y)				Comment						
11/27 06:15:00 - 11/27 07:59:54		Fixed ( -945.0, 0.0)				# Co-alignment at E-limb						
<b>PROG= 09 1-time(s)</b>												
<b>Subr= 1 1-time(s) 360.0sec</b>												
<b>Seqn= 60 15-time(s) 480.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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<b>XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512</b>												
Term		Pointing (x, y)				Comment						
11/26 13:08:00 - 11/26 23:38:30		Track ( 46.9, -258.1) @ 11/26 13:05:00				# Track AR12216						
11/26 23:53:00 - 11/27 01:49:54		Track ( 137.3, -276.2) @ 11/26 23:50:00				# HOP275 at AR12216						
11/27 01:53:00 - 11/27 03:49:54		Track ( 162.2, -256.7) @ 11/27 01:50:00				# Track AR12216						
11/27 08:03:00 - 11/27 09:06:30		Track ( 217.2, -255.9) @ 11/27 08:00:00				# Track AR12216						
<b>PROG= 19 30-time(s)</b>												
<b>Subr= 1 20-time(s) 2.0sec</b>												
<b>Seqn= 11 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn=100 1-time(s) 10.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 10 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 15 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

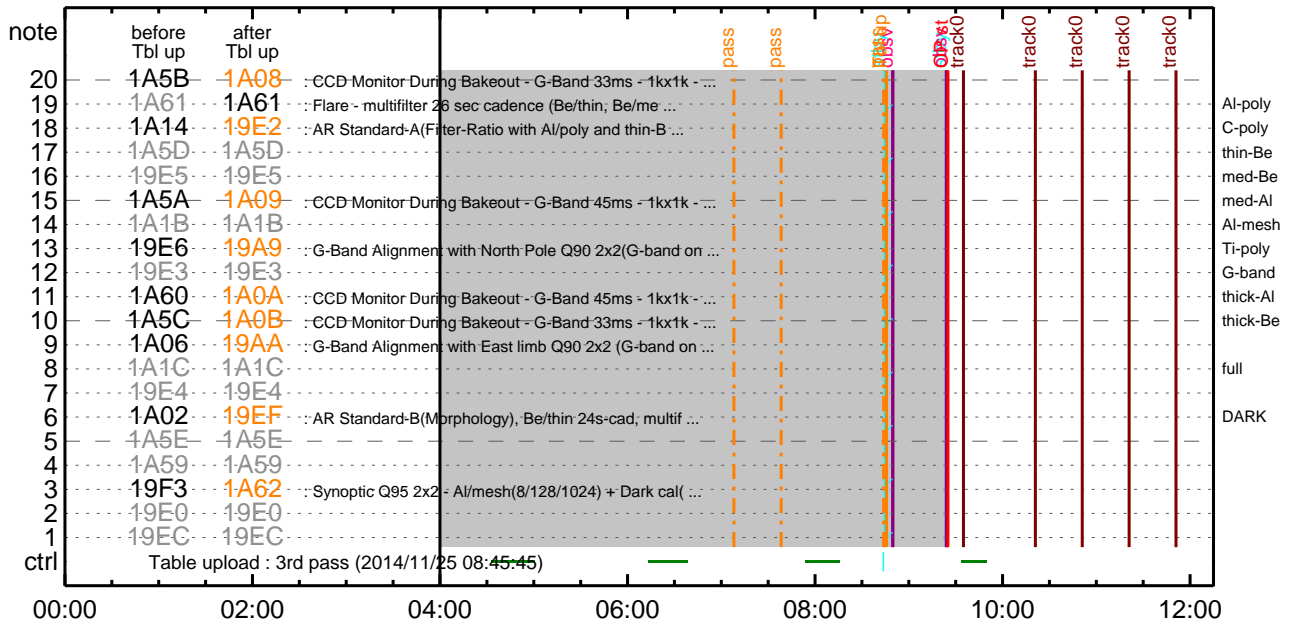
\* \* \* \* \*

**Flare Detection**

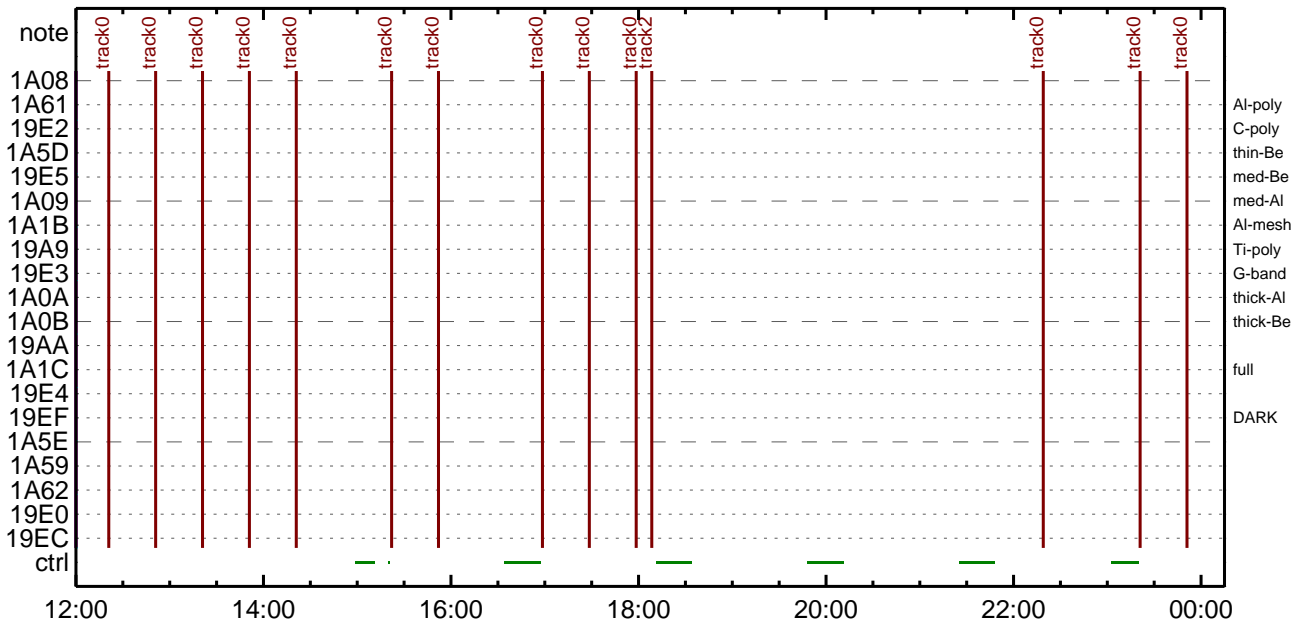
\* \* \* \* \*

<b>FLD Patrol</b>												
Term		Pointing (x, y)				Comment						
11/26 13:05:18 - 11/27 03:50:18		Track ( 46.9, -258.1) @ 11/26 13:05:00				# Track AR12216						
11/27 08:00:18 - 11/29 10:24:00		Track ( 217.2, -255.9) @ 11/27 08:00:00				# Track AR12216						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

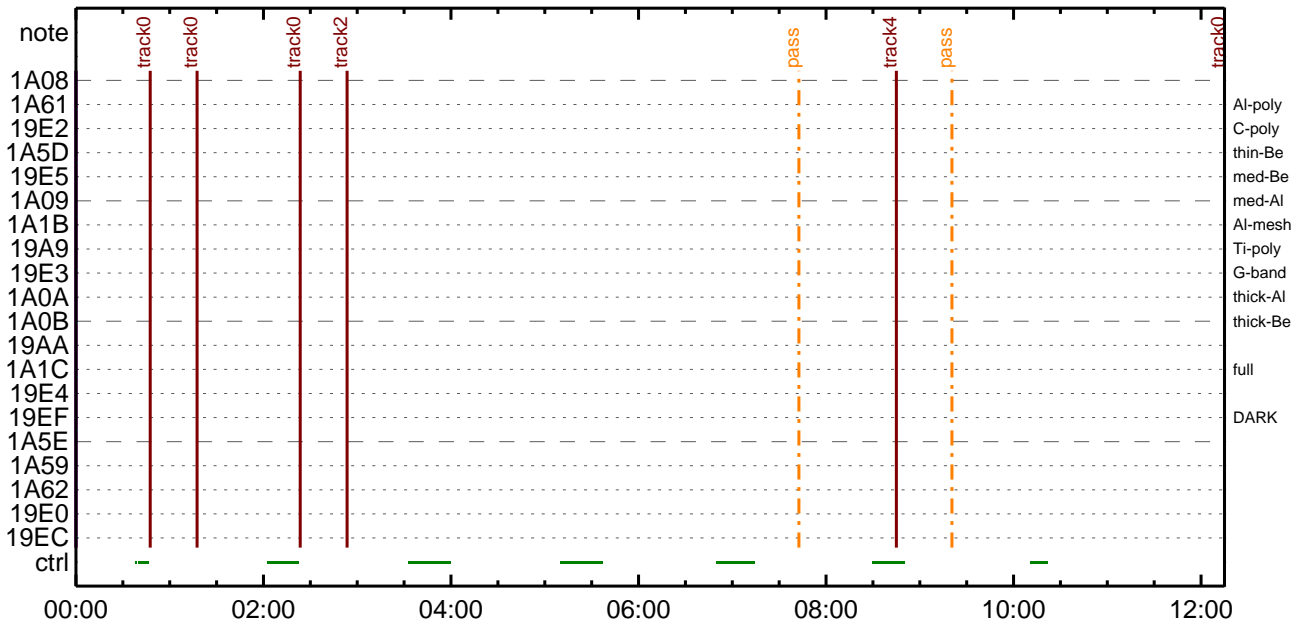
### CMDI #0748 2014/11/25



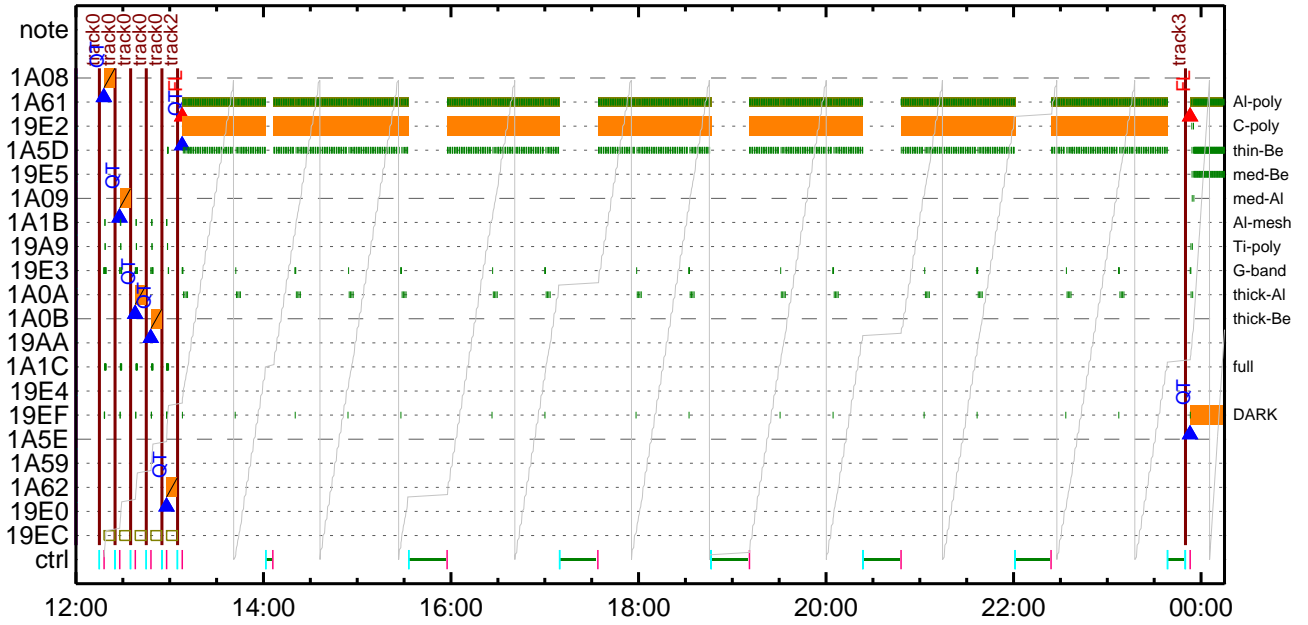
### CMDI #0748 2014/11/25



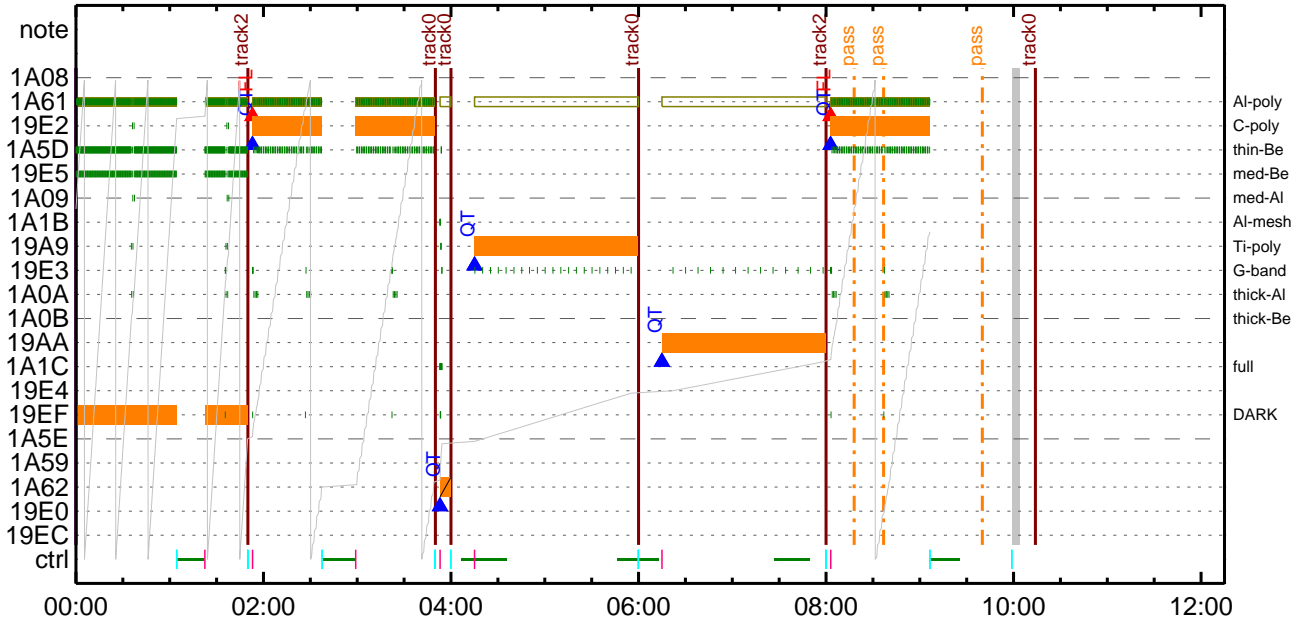
### CMDI #0748 2014/11/26



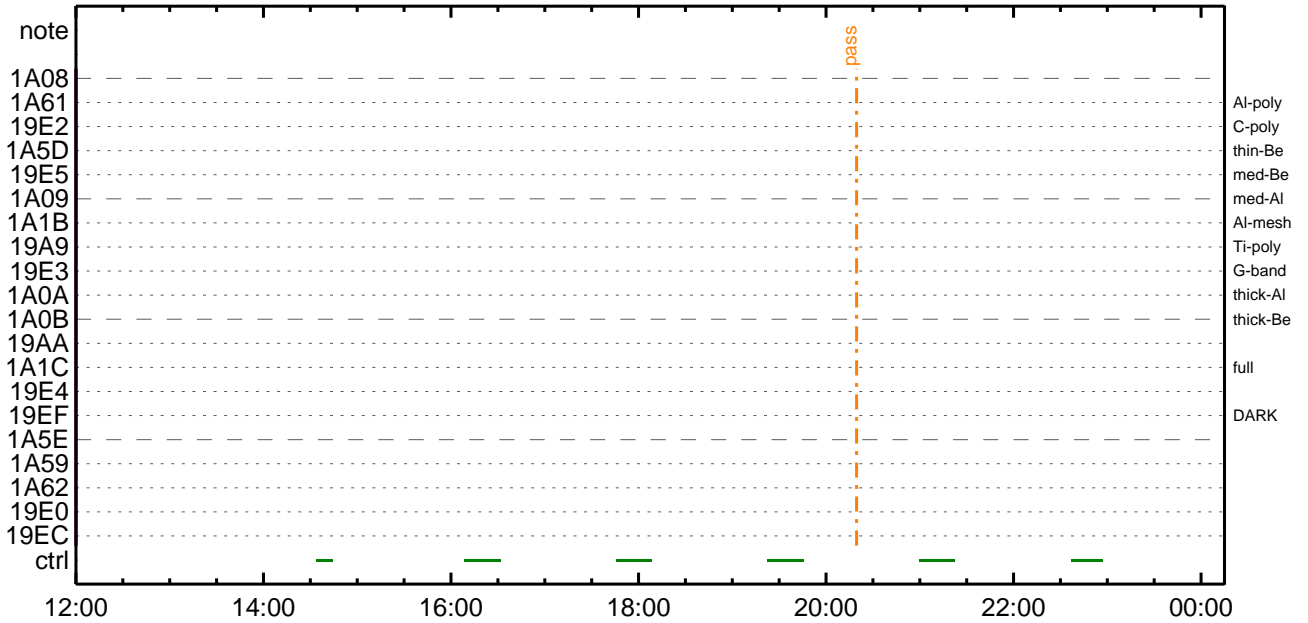
CMDI #0748 2014/11/26



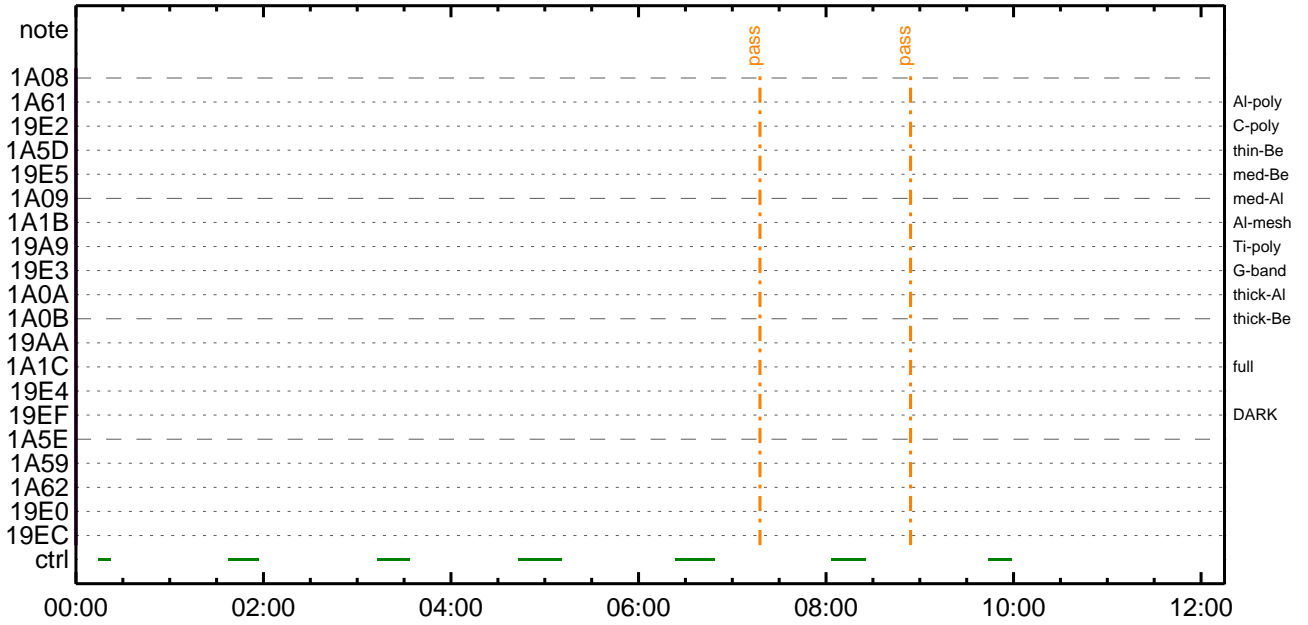
CMDI #0748 2014/11/27



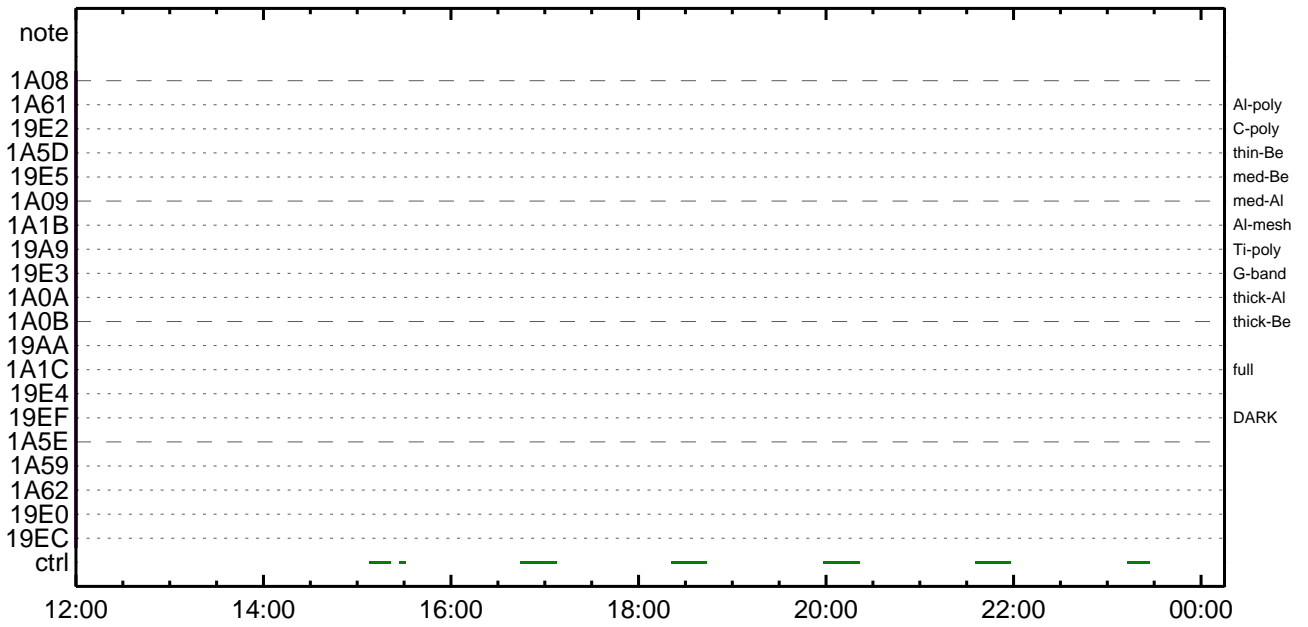
CMDI #0748 2014/11/27



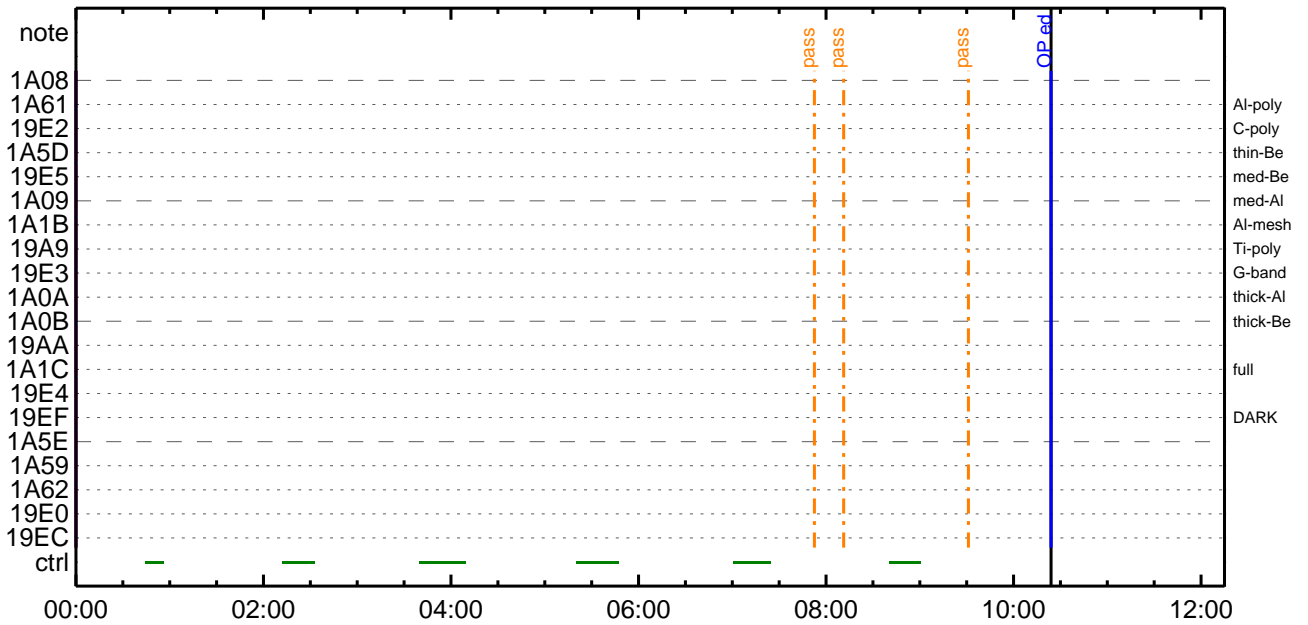
CMDI #0748 2014/11/28



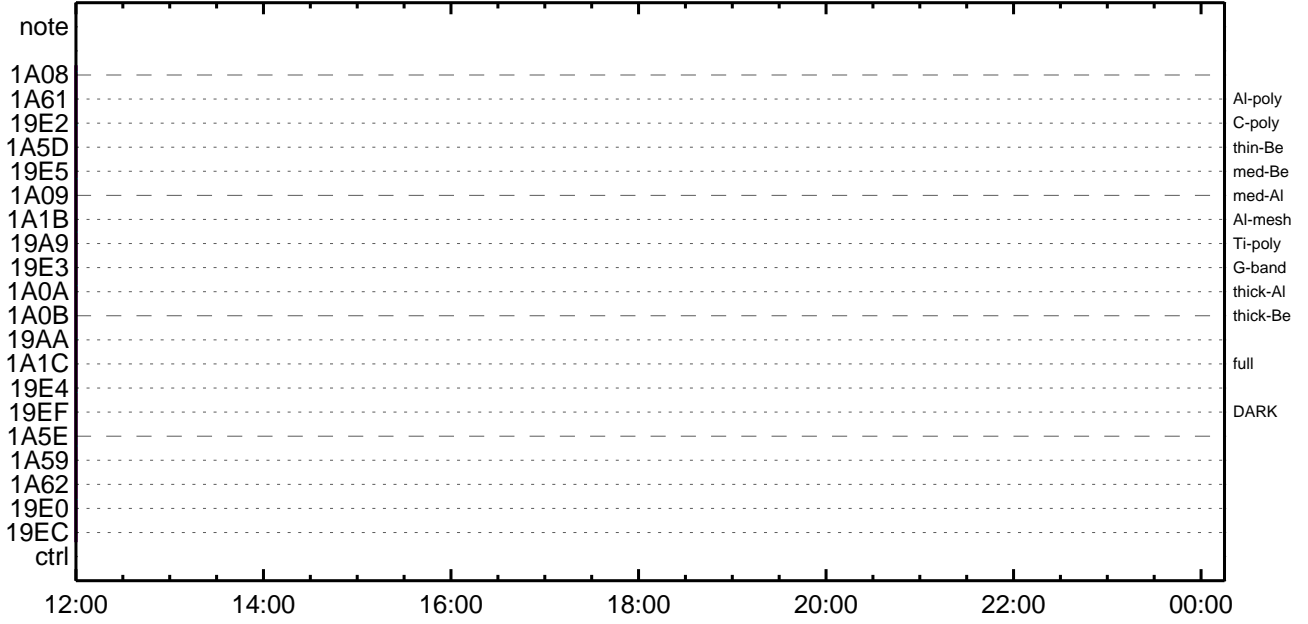
CMDI #0748 2014/11/28



CMDI #0748 2014/11/29



CMDI #0748 2014/11/29







0096 C.  
0097 C.  
0098 C. \*\*\*\*\*  
0099 C. OP/OGY1;Y6i;Y6Yx  
0100 C. \*\*\*\*\*  
0101 C.  
0102 C. iãOP/OGY1;Y6i;ã  
0103 S. OP op-776:OP  
0104 (  
0105 S. OG og-776:OG  
0106 (  
0107 C.  
0108 C. iãNMOG&OPîî°èY6Y6Yx;ã  
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)  
0110 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0111 BC (20 00 7f 01 02)  
0112 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 40  
0113 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0114 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0115 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0116 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0117 +. DC 01-22 DHU\_MODE\_CHNG  
0118 BC (07 0b f8)  
0119 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0120 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0121 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0122 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0123 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0124 C. Y6Y6Y6Y6Y6i>òð³îçš  
0125 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0126 C. RAM ID=NMOGîî°è¹ç·è²îOKòð³îçš  
0127 C.  
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)  
0129 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0130 BC (20 80 7f 01 02)  
0131 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 41  
0132 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0133 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0134 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0135 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0136 +. DC 01-22 DHU\_MODE\_CHNG  
0137 BC (07 0b f8)  
0138 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0139 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0140 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0141 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0142 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0143 C. Y6Y6Y6Y6Y6i>òð³îçš  
0144 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0145 C. RAM ID=NMOGîî°è¹ç·è²îOKòð³îçš  
0146 C.  
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
0148 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0149 BC (21 00 41 01 02)  
0150 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 42  
0151 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0152 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 65  
0153 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0154 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0155 +. DC 01-22 DHU\_MODE\_CHNG  
0156 BC (07 0b f8)  
0157 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0158 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0159 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0160 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0161 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0162 C. Y6Y6Y6Y6Y6i>òð³îçš  
0163 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0164 C. RAM ID=NMOG,RAM ID=OPîî°è¹ç·è²îOKòð³îçš  
0165 C.  
0166 C. \*\*\*\*\* òè²¼òîî°è¹ç·è²îOKòð³îçš \*\*\*\*\*  
0167 C. DHUîî°è¹ç·è²îOKòð³îçš  
0168 +. DC 01-22 DHU\_MODE\_CHNG  
0169 BC (02 0a f8)  
0170 C. çç[HK1\_PKT\_FORM\_NO] EQ 2  
0171 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.5S  
0172 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32K  
0173 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0174 C.  
0175 C. \*\*\*\*\*  
0176 C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 C. \*\*\*\*\*  
0178 C.  
0179 C. NOTICE ;š OPOG UPLOADò-Á÷ç@NGîî°è¹ç·è²îOKòð³îçš  
0180 C. òè²¼òîî°è¹ç·è²îOKòð³îçš  
0181 C.  
0182 C. TIY³Y6Y6Y6Y6òð³îçš(UT)  
0183 +. TI 2014-11-25 09:20:00.0  
0184 DC 01-B3 DHU\_OP\_STOP  
0185 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0186 C.  
0187 +. TI 2014-11-25 09:20:01.0  
0188 DC 01-B4 DHU\_OP\_COPY  
0189 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0190 C.  
0191 +. TI 2014-11-25 09:20:01.0  
0192 DC 01-B5 DHU\_OPOG\_COPY  
0193 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP







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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-267:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-288:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¥ç¥Ã¥×¥í;¼¥É°âÊSTS_CHKððOFFðËð¹ðë
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHKððONðËð¹ðë
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700[ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2014-11-25 09:24:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 . C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** XRT START *****
0174 C.
0175 +. DC 07-F0 MDP_XRT_CTRL_MANU
0176 BC (c1)
0177 + DC 07-F0 MDP_XRT_MODE_STBY
0178 BC (c3)
0179 . C. ----- Success Verify ? OK / NG____
0180 C.
0181 C. XRT Obs. Table Upload
0182 . S. RAM ram-291:MDP_OBS_X
0183 ( )
0184 C.
0185 +. DC 07-F0 MDP_DUMP_XRTTBL
0186 BC (84 07 00 00 00 3a d4)
0187 . C. ----- Comparison Check ? OK / ERR ____
0188 C.
0189 C.
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 01 b1 b1 04 04)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 02 b1 b1 08 08)

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0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 03 b1 b1 08 08)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 04 b1 b1 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 05 85 83 06 06)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 06 c0 c0 10 10)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 07 80 80 20 20)
0204 + DC 07-F0 MDP_XRT_ROI_SET
0205 BC (cd 08 40 c0 10 10)
0206 + DC 07-F0 MDP_XRT_ROI_SET
0207 BC (cd 09 40 40 10 10)
0208 + DC 07-F0 MDP_XRT_ROI_SET
0209 BC (cd 0a c0 40 10 10)
0210 + DC 07-F0 MDP_XRT_ROI_SET
0211 BC (cd 0b 80 80 20 08)
0212 + DC 07-F0 MDP_XRT_ROI_SET
0213 BC (cd 0c 80 80 08 20)
0214 + DC 07-F0 MDP_XRT_ROI_SET
0215 BC (cd 0d 85 83 06 06)
0216 + DC 07-F0 MDP_XRT_ROI_SET
0217 BC (cd 0e 85 83 08 08)
0218 + DC 07-F0 MDP_XRT_ROI_SET
0219 BC (cd 0f 80 80 06 06)
0220 + DC 07-F0 MDP_XRT_ROI_SET
0221 BC (cd 10 80 80 08 08)
0222 + DC 07-F0 MDP_XRT_FLD_ENA
0223 BC (d8)
0224 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0225 BC (c8)
0226 + DC 07-F0 MDP_XRT_AEC_RESET
0227 BC (d0)
0228 + DC 07-F0 MDP_XRT_ARS_DIS
0229 BC (d5)
0230 + DC 07-F0 MDP_XRT_FLD_RESET
0231 BC (da)
0232 . C. ----- Success Verify ? OK / NG ____
0233 C.
0234 C.
0235 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0236 C.
0237 +. DC 07-F0 MDP_XRT_MODE_OBSV
0238 BC (c2)
0239 +. TI 2014-11-25 09:24:02.0
0240 DC 07-F0 MDP_XRT_MODE_OBSV
0241 BC (c2)
0242 . C. ----- Success Verify ? OK / NG ____
0243 C.
0244 C. ***** XRT END *****
0245 C.
0246 . C. ***** MDP ^ûÃîñî»ô¼ÿñÈÂð¹ñèDCBC•x²è *****
0247 C. (¼á°îÿÓÿÁÿÈÿPÿËÿËÿâÿçÿëñÈ¼ññ¼Ä»Û¹ñè)
0248 . S. DC-BC dcbc-402:DCBC
0249 (MDP_known_event)
0250 C.
0251 C.
0252 . C. ***** ÿÐÿ¹•î Daily±;îññè´ð¹ñèDCBC•x²è *****
0253 . S. DC-BC dcbc-153:DCBC
0254 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0255 C.
0256 C.
0257 . C. ¡ãLOSÿÁÿSÿËÿËÿ¼Ä»Û;ã
0258 C.
0259 . C. ***** LOS *****
0260 C.

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\*\*\* OP Sequence for XRT \*\*\*

2014/11/25	09:35:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	55	58	02	24
2014/11/25	10:21:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	4c	72	02	24
2014/11/25	10:51:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	43	8d	02	24
2014/11/25	11:21:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	3a	a7	02	24
2014/11/25	11:51:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	31	ca	02	24
2014/11/25	12:21:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	28	e5	02	24
2014/11/25	12:51:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	20	00	02	24
2014/11/25	13:21:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	17	1a	02	24
2014/11/25	13:51:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	0e	35	02	24
2014/11/25	14:21:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	05	58	02	24
2014/11/25	15:22:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	fd	59	02	24
2014/11/25	15:52:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	f4	73	02	24
2014/11/25	16:58:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	eb	8e	fd	dc
2014/11/25	17:28:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	e2	a8	02	24
2014/11/25	17:58:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/11/25	18:08:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02	00	00	00	00
2014/11/25	22:19:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	d9	cb	02	24
2014/11/25	23:21:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	d0	e6	02	24
2014/11/25	23:51:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	c8	01	02	24
2014/11/26	00:47:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	bf	1b	02	24
2014/11/26	01:17:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	b6	36	02	24
2014/11/26	02:23:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00	ad	59	02	24
2014/11/26	02:53:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02	00	00	00	00
2014/11/26	06:15:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_447_OG [0x1bf]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2014/11/26	08:45:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	04	00	00	00	00
2014/11/26	12:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/11/26	12:14:56.0	XRT_CTRL_MANU_449_OG [0x1c1]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/11/26	12:15:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2014/11/26	12:17:32.0	XRT_FOCUS_POSITION_432_OG [0x1b0]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/11/26	12:17:52.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	14		
2014/11/26	12:17:54.0	XRT_FLD_DIS_437_OG [0x1b5]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2014/11/26	12:17:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2014/11/26	12:17:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/11/26	12:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/11/26	12:24:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/11/26	12:24:56.0	XRT_CTRL_MANU_449_OG [0x1c1]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/11/26	12:25:00.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2014/11/26	12:27:32.0	XRT_FOCUS_POSITION_432_OG [0x1b0]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/11/26	12:27:52.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	0f		
2014/11/26	12:27:54.0	XRT_FLD_DIS_437_OG [0x1b5]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2014/11/26	12:27:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2014/11/26	12:27:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/11/26	12:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/11/26	12:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/11/26	12:34:56.0	XRT_CTRL_MANU_449_OG [0x1c1]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/11/26	12:35:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]							

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2014/11/26	12:37:32.0	XRT_FOCUS_POSITION_432_OG [0x1b0]	AOCU_NM	5	02-76	00	d1	07	d1	07
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2014/11/26	12:37:52.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2014/11/26	12:37:54.0	XRT_FLD_DIS_437_OG [0x1b5]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/11/26	12:37:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/11/26	12:37:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/26	12:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/26	12:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	12:44:56.0	XRT_CTRL_MANU_449_OG [0x1c1]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	12:45:00.0	AOCS_OrE-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00	d1	07	2e	f9
2014/11/26	12:47:32.0	XRT_FOCUS_POSITION_432_OG [0x1b0]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/11/26	12:47:52.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2014/11/26	12:47:54.0	XRT_FLD_DIS_437_OG [0x1b5]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/11/26	12:47:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/11/26	12:47:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/26	12:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/26	12:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	12:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	12:54:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/11/26	12:55:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	00	00	00	00
2014/11/26	12:55:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/11/26	12:57:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/11/26	12:57:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/26	12:57:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2014/11/26	12:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/26	13:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	13:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	13:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/11/26	13:05:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	02	00	00	00	00
2014/11/26	13:05:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/11/26	13:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/11/26	13:05:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/11/26	13:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/26	13:05:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/26	13:07:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2014/11/26	13:07:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2014/11/26	13:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/26	14:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	14:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	14:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/26	14:01:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/26	14:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/26	14:05:00.0	XRT_Custom_430_OG [0x1ae]								
2014/11/26	14:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/26	15:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	15:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/26	15:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/26	15:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								



2014/11/26	15:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/26	15:56:30.0	XRT_Custom_430_OG [0x1ae]							
2014/11/26	15:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/26	17:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	17:09:32.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	17:09:34.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/26	17:09:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/26	17:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/26	17:33:00.0	XRT_Custom_430_OG [0x1ae]							
2014/11/26	17:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/26	18:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	18:46:32.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	18:46:34.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/26	18:46:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/26	18:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/26	19:10:00.0	XRT_Custom_430_OG [0x1ae]							
2014/11/26	19:11:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/26	20:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	20:23:32.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	20:23:34.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/26	20:23:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/26	20:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/26	20:47:00.0	XRT_Custom_430_OG [0x1ae]							
2014/11/26	20:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/26	22:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	22:01:02.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	22:01:04.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/26	22:01:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/26	22:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/26	22:23:00.0	XRT_Custom_430_OG [0x1ae]							
2014/11/26	22:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/26	23:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	23:38:32.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	23:38:34.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/26	23:38:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/26	23:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/26	23:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	23:49:56.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/26	23:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/11/26	23:50:00.0	AOCs_OrE-point_Start_28_OG [0x0b2]							
			AOCU_NM	5	02-76	03 00 00 00 00			
2014/11/26	23:50:18.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/11/26	23:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/11/26	23:50:22.0	XRT_AEC_RESET_413_OG [0x19d]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/11/26	23:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/11/26	23:50:26.0	XRT_FLD_RESET_407_OG [0x197]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/26	23:52:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06			
2014/11/26	23:52:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 13			
2014/11/26	23:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/27	01:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							

2014/11/27	01:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/27	01:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/27	01:04:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/27	01:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/27	01:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/27	01:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2014/11/27	01:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0			
2014/11/27	01:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/27	01:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/27	01:50:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
2014/11/27	01:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/11/27	01:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCS_OrE-point_Start_16_OG [0x0a6]	5	02-76	02 00 00 00 00			
2014/11/27	01:50:22.0	XRT_AEC_RESET_413_OG [0x19d]	AOCU_NM						
2014/11/27	01:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8			
2014/11/27	01:50:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/11/27	01:52:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/11/27	01:52:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/11/27	01:53:00.5	XRT_CTRL_AUTO_408_OG [0x198]	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5			
2014/11/27	02:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/11/27	02:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FLD_RESET_407_OG [0x197]	1	07-F0	da			
2014/11/27	02:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/27	02:37:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	XRT_QT_PROG_SET_441_OG [0x1b9]	2	07-F0	c4 12			
2014/11/27	02:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2014/11/27	02:58:00.0	XRT_Custom_430_OG [0x1ae]	XRT_FL_PROG_SET_401_OG [0x191]	2	07-F0	c5 13			
2014/11/27	02:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13			
2014/11/27	03:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0			
2014/11/27	03:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/27	03:49:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
2014/11/27	03:50:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/27	03:50:18.0	XRT_FLD_DIS_422_OG [0x1a6]	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
2014/11/27	03:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/27	03:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	XRT_FOCUS_POSITION_403_OG [0x193]	4	07-F8	22 ff aa 00			
2014/11/27	03:52:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	AOCS_OrE-point_Start_15_OG [0x0a5]	5	02-76	00 00 00 00 00			
2014/11/27	03:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	XRT_FLD_DIS_422_OG [0x1a6]	1	07-F0	d9			
2014/11/27	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/11/27	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0	c9			
2014/11/27	03:59:58.0	XRT_ROI_A_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/11/27	04:00:00.0	AOCS_OrE-point_Start_29_OG [0x0b3]	XRT_QT_PROG_SET_441_OG [0x1b9]	2	07-F0	c4 12			
2014/11/27	04:00:03.0	XRT_FOCUS_POSITION_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2014/11/27	04:00:23.0	XRT_FLD_DIS_409_OG [0x199]	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0			
2014/11/27	04:00:25.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/27	04:00:27.0	XRT_ARS_DIS_440_OG [0x1b8]	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
2014/11/27	04:15:03.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/27	04:15:05.0	XRT_CTRL_AUTO_408_OG [0x198]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
			AOCS_OrE-point_Start_29_OG [0x0b3]	5	02-76	00 ac 00 00 00			
			AOCU_NM						
			XRT_FOCUS_POSITION_443_OG [0x1bb]	4	07-F8	22 fe 97 00			
			XRT_FLD_DIS_409_OG [0x199]	1	07-F0	d9			
			MDP_XRT_FLD_DIS	1	07-F0	d9			
			XRT_FLRCTRL_DIS_428_OG [0x1ac]	1	07-F0	c9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
			XRT_ARS_DIS_440_OG [0x1b8]	1	07-F0	d5			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
			XRT_QT_PROG_SET_417_OG [0x1a1]	2	07-F0	c4 0d			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
			XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			

2014/11/27	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/27	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/27	05:59:58.0	XRT_FOCUS_POSITION_443_OG [0x1bb]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/11/27	06:00:00.0	AOCS_ORe-point_Start_30_OG [0x0b4]	AOCU_NM	5	02-76	00 00 00 54 00
2014/11/27	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/11/27	06:00:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/11/27	06:00:22.0	XRT_ARS_DIS_440_OG [0x1b8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/11/27	06:14:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2014/11/27	06:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/27	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/27	07:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/27	07:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/11/27	08:00:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	02 00 00 00 00
2014/11/27	08:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/11/27	08:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/11/27	08:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/11/27	08:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/11/27	08:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/27	08:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2014/11/27	08:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2014/11/27	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/27	09:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/27	09:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/27	09:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/27	09:06:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/27	09:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/27	09:58:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/27	09:58:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/27	10:14:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 00 00 00 00