

# XRT Timeline to be uploaded on 2014/12/04

Period: 2014/12/04 11:00:00 - 2014/12/09 09:40:00

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Normal mode

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## XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
12/04 11:13:00 - 12/04 17:59:54	Track ( 412.4, -353.8) @ 12/04 11:10:00	# OP start + 10min +AR12222
12/04 18:13:00 - 12/05 06:16:54	Track ( 465.5, -353.0) @ 12/04 18:10:00	#AR12222
12/05 06:30:00 - 12/05 17:39:30	Track ( 553.5, -351.6) @ 12/05 06:27:00	AR12222
12/06 00:33:00 - 12/06 05:49:54	Track ( 668.8, -349.8) @ 12/06 00:30:00	AR12222
12/06 06:03:00 - 12/06 09:39:54	Track ( 700.2, -349.2) @ 12/06 06:00:00	AR12222

**PROG= 12 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 8 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 2-time(s) 2.0sec
  - Seqn= 24 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn= 42 4-time(s) 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
  - Seqn= 62 8-time(s) 270.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi

Term	Pointing (x, y)	Comment
12/04 18:03:00 - 12/04 18:09:54	Fixed ( 0.0, 0.0)	synoptic
12/05 06:20:00 - 12/05 06:26:54	Fixed ( 0.0, 0.0)	synoptic, shifted 17.0 min
12/05 18:06:00 - 12/05 18:12:54	Fixed ( 0.0, 0.0)	synoptic, shifted 3.0 min
12/06 05:53:00 - 12/06 05:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted -10.0 min

**PROG= 03 1-time(s)**

- Subr= 1 1-time(s) 12.0sec
  - Seqn= 33 1-time(s) 4.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 40 1-time(s) 4.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 77 1-time(s) 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 6 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #1A1B: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 33ms

Term	Pointing (x, y)	Comment
12/05 18:16:00 - 12/06 00:29:54	Track ( 54.4, -674.2) @ 12/05 18:13:00	# HOP232 coronal hole jet

**PROG= 05 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 8 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 48 1-time(s) 30.0sec
    - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
- Subr= 3 30-time(s) 2.0sec
  - Seqn= 57 1-time(s) 60.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
12/04 11:13:00 - 12/04 17:59:54	Track ( 412.4, -353.8) @ 12/04 11:10:00	# OP start + 10min +AR12222
12/04 18:13:00 - 12/05 06:16:54	Track ( 465.5, -353.0) @ 12/04 18:10:00	#AR12222
12/05 06:30:00 - 12/05 17:39:30	Track ( 553.5, -351.6) @ 12/05 06:27:00	AR12222
12/05 18:16:00 - 12/06 00:29:54	Track ( 54.4, -674.2) @ 12/05 18:13:00	# HOP232 coronal hole jet
12/06 00:33:00 - 12/06 05:49:54	Track ( 668.8, -349.8) @ 12/06 00:30:00	AR12222
12/06 06:03:00 - 12/06 09:39:54	Track ( 700.2, -349.2) @ 12/06 06:00:00	AR12222

#### PROG= 19 30-time(s)

<b>Subr= 1 20-time(s) 2.0sec</b>												
<b>Seqn= 11 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn=100 1-time(s) 10.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 10 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 15 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

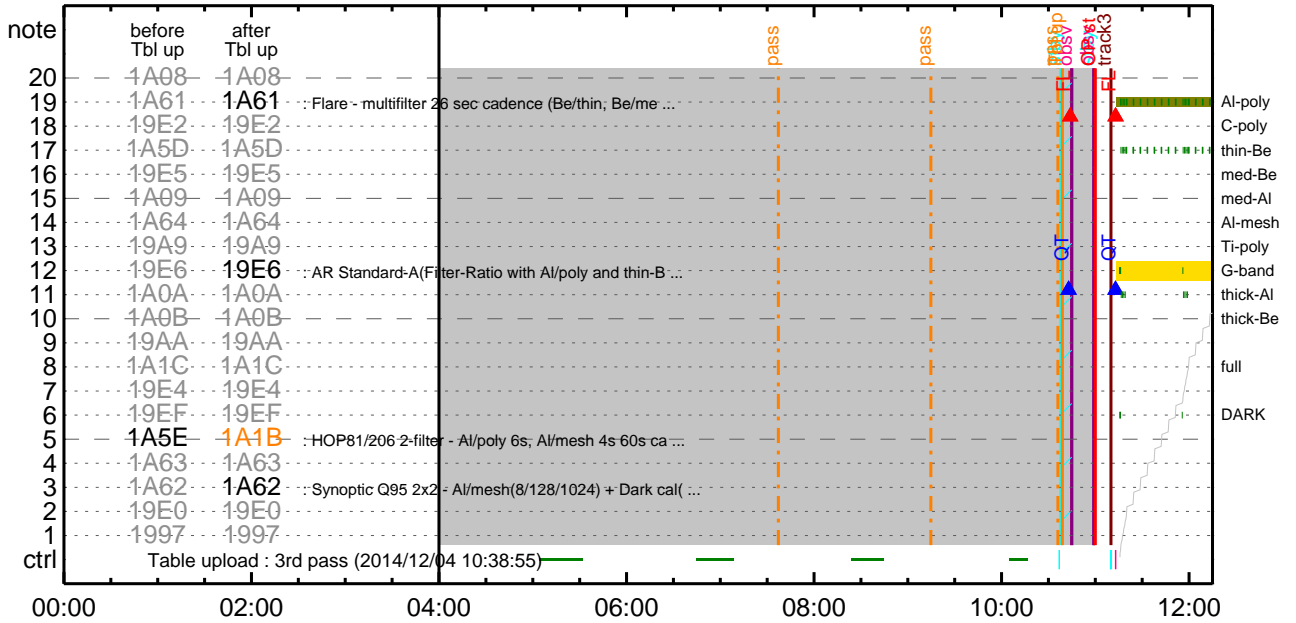
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### Flare Detection

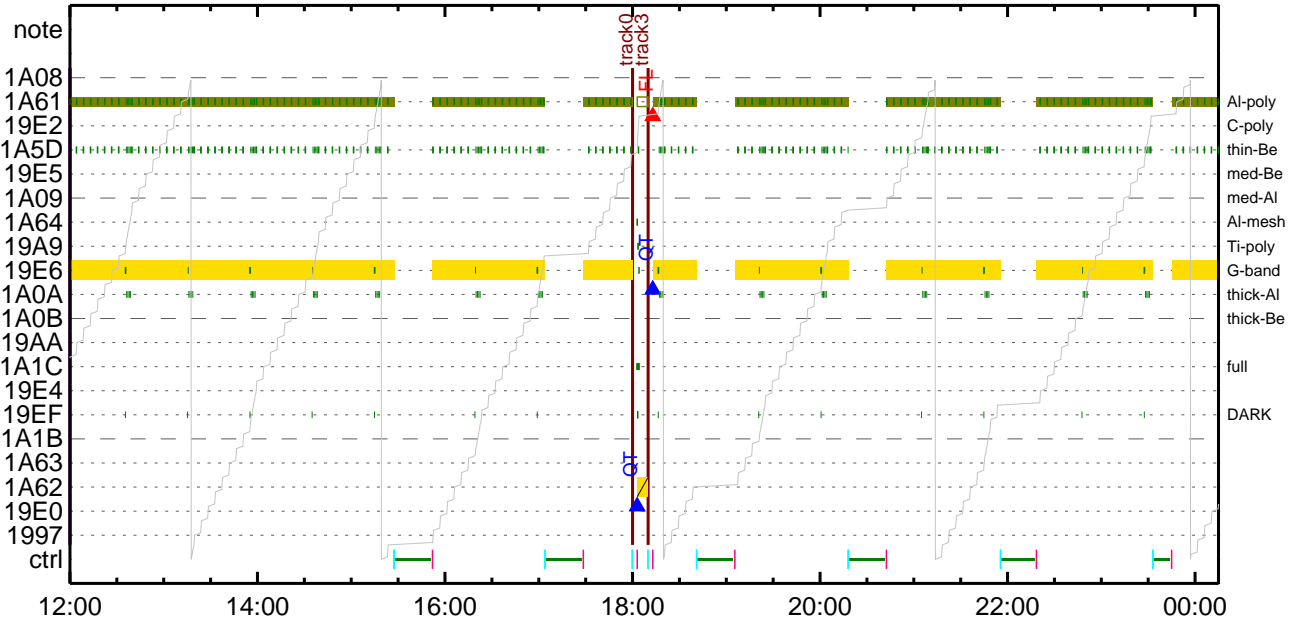
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FLD Patrol												
Term	Pointing (x, y)	Comment										
12/04 18:10:18 - 12/05 06:17:18	Track ( 465.5, -353.0) @ 12/04 18:10:00	#AR12222										
12/05 06:27:18 - 12/05 18:03:18	Track ( 553.5, -351.6) @ 12/05 06:27:00	AR12222										
12/05 18:13:18 - 12/06 05:50:18	Track ( 54.4, -674.2) @ 12/05 18:13:00	# HOP232 coronal hole jet										
12/06 06:00:18 - 12/09 09:40:00	Track ( 700.2, -349.2) @ 12/06 06:00:00	AR12222										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

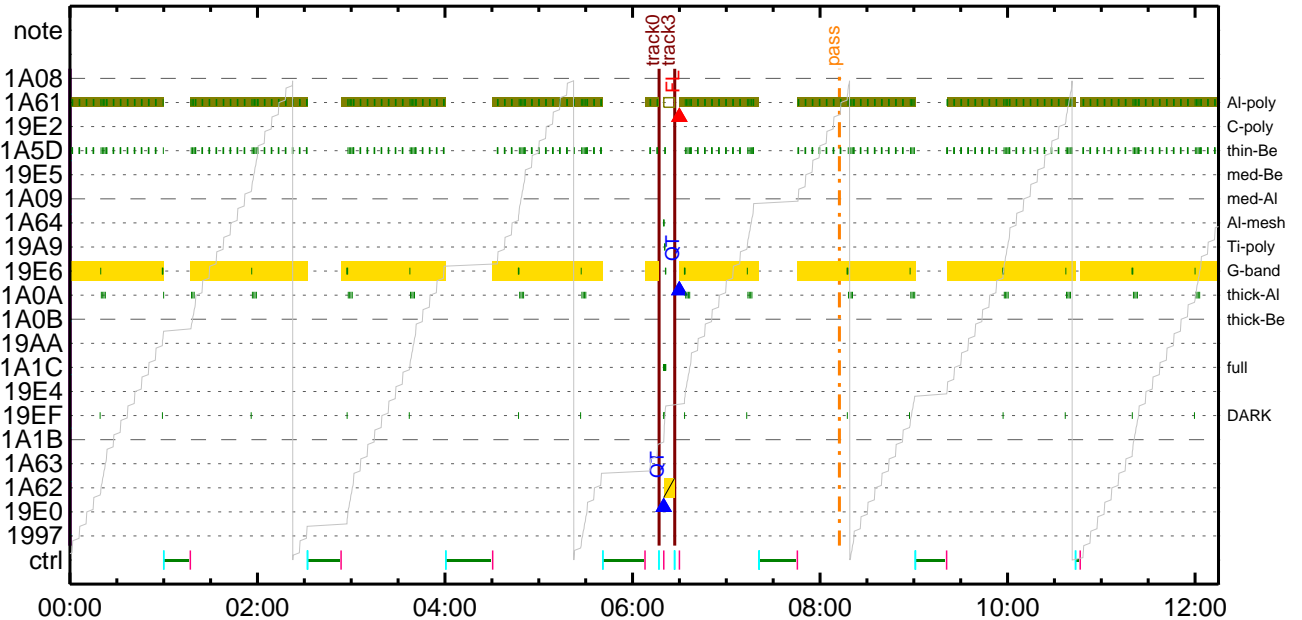
### CMDI #0767 2014/12/04



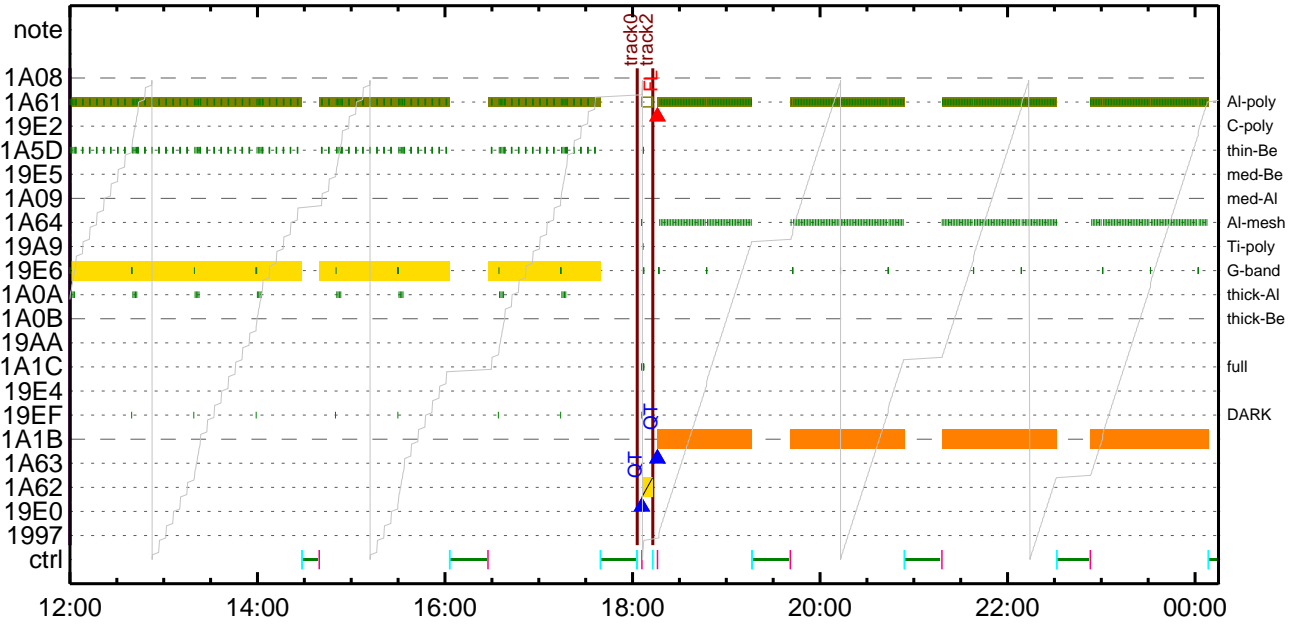
### CMDI #0767 2014/12/04



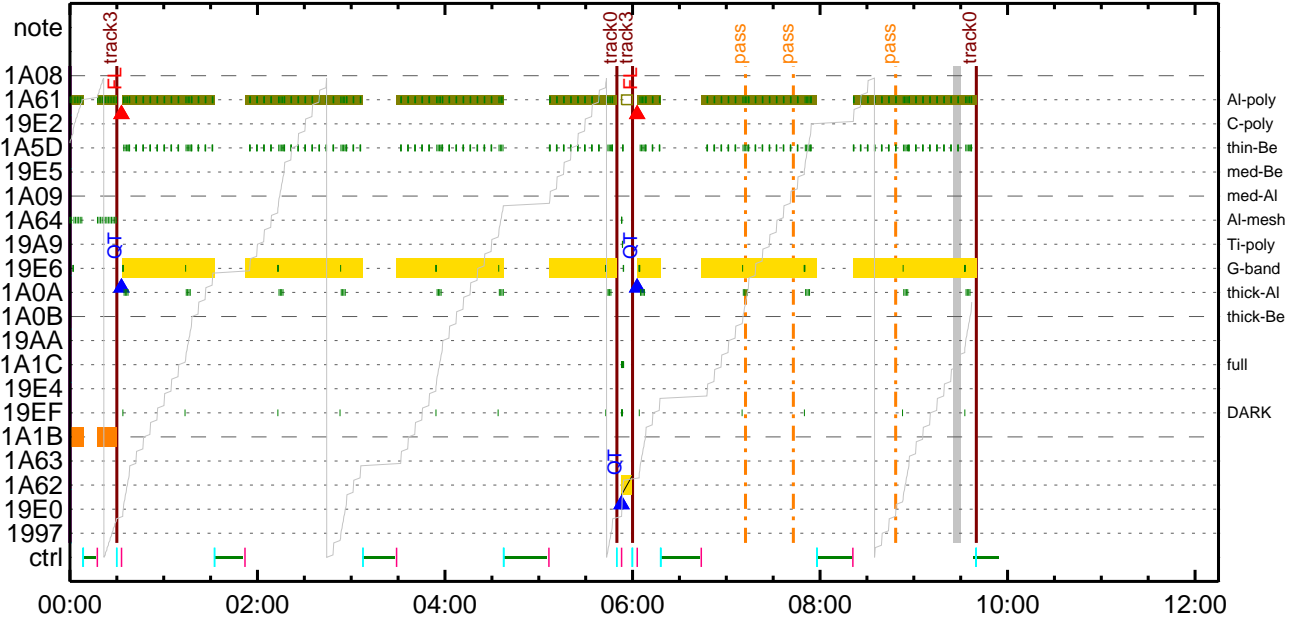
### CMDI #0767 2014/12/05



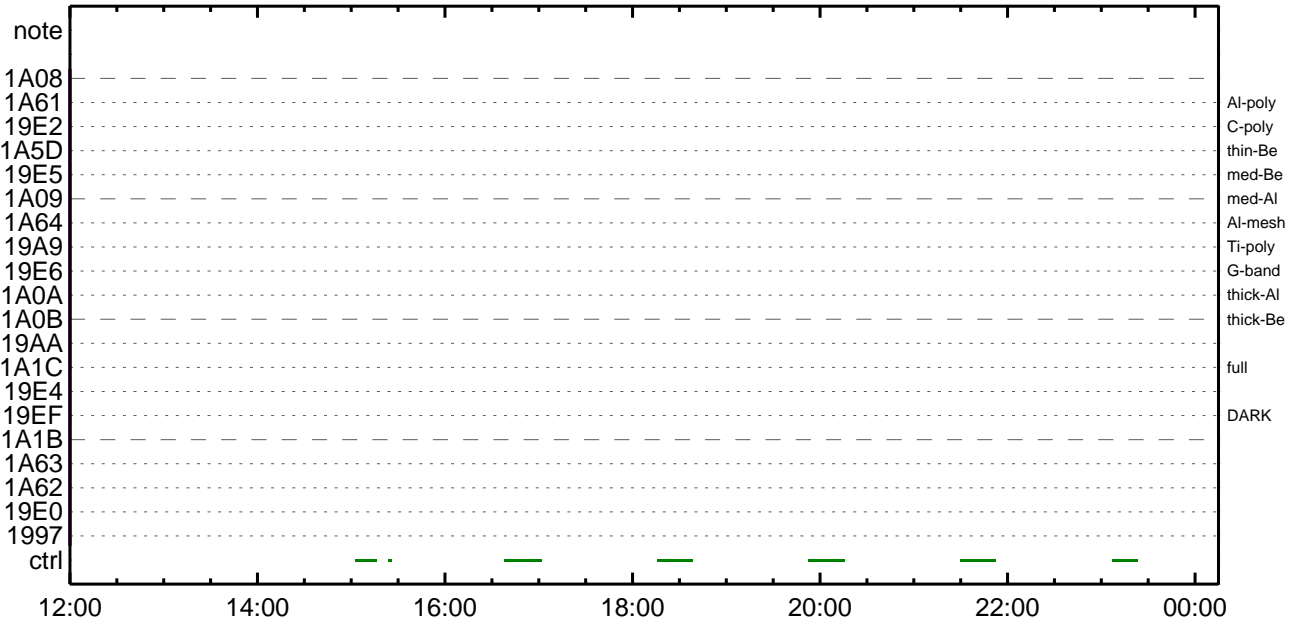
CMDI #0767 2014/12/05



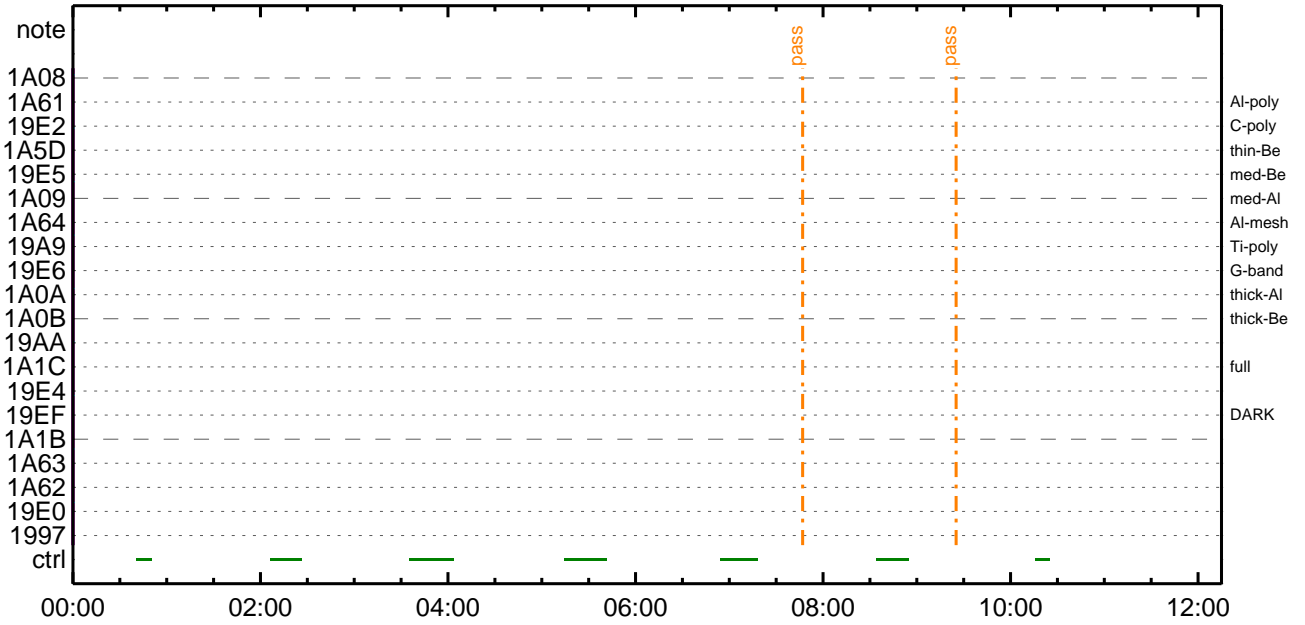
CMDI #0767 2014/12/06



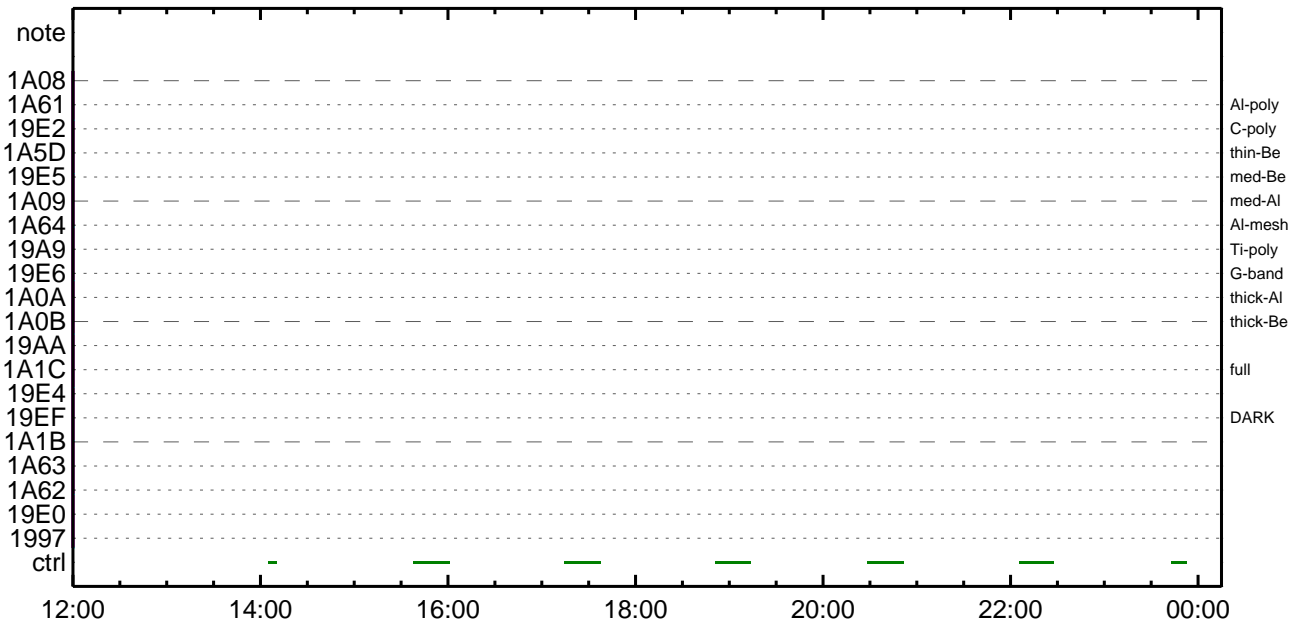
CMDI #0767 2014/12/06



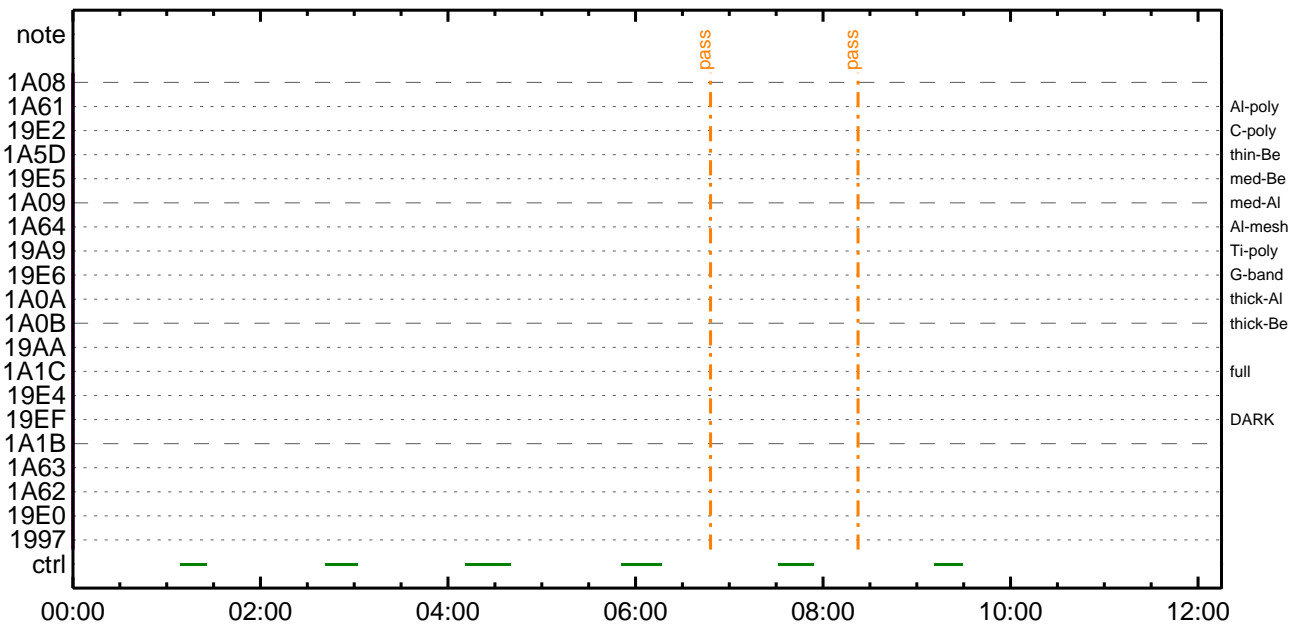
CMDI #0767 2014/12/07



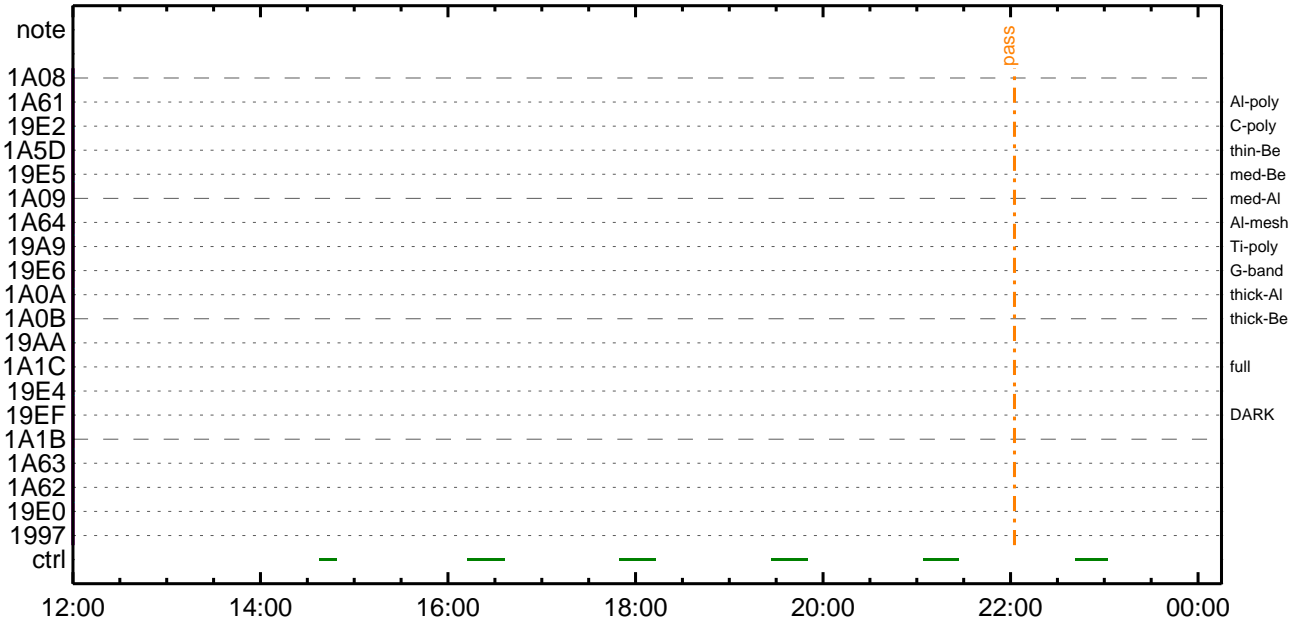
CMDI #0767 2014/12/07



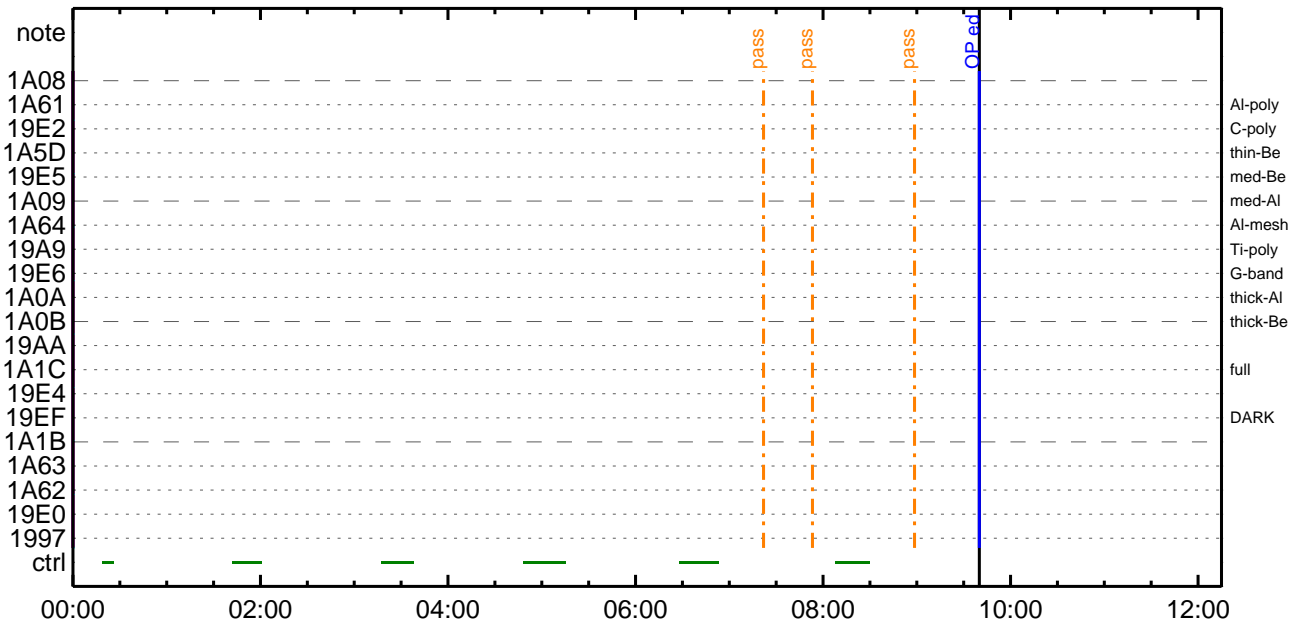
CMDI #0767 2014/12/08



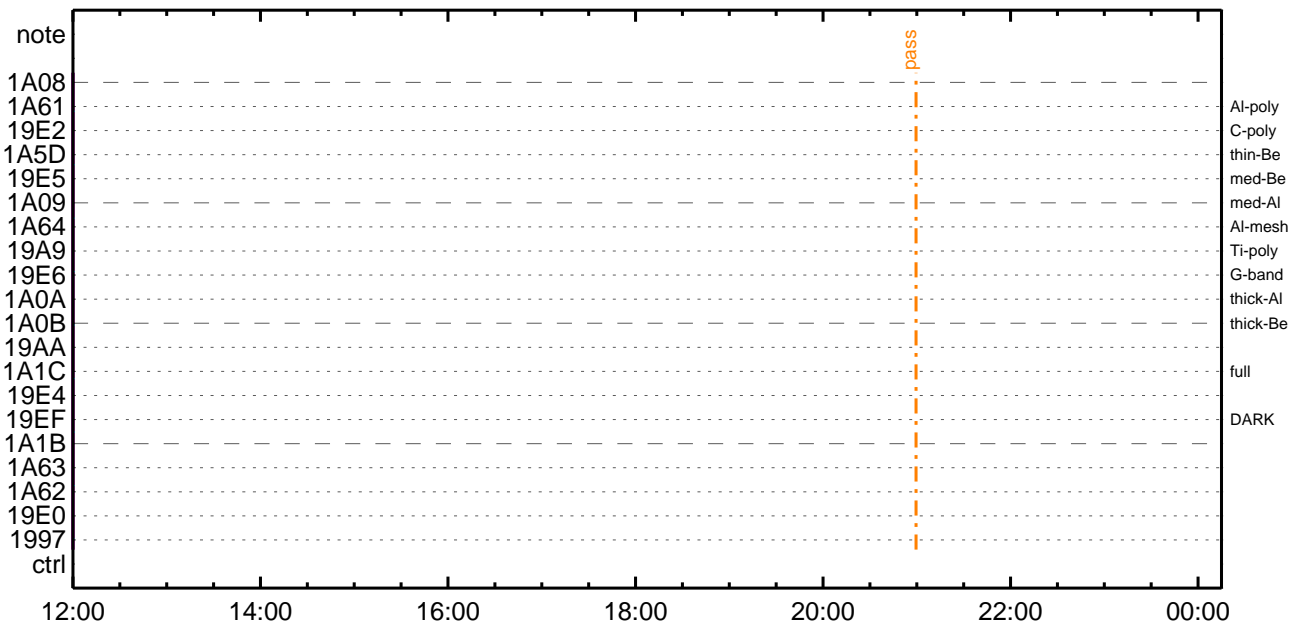
CMDI #0767 2014/12/08



CMDI #0767 2014/12/09



CMDI #0767 2014/12/09





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-795:OP
0104 ( )
0105 S. OG og-795:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y½YÉòðÁDî¿(UT)
0183 +. TI 2014-12-04 10:55:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-12-04 10:55:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-12-04 10:55:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.  
0195 +. TI 2014-12-04 10:59:59.5  
0196 DC 01-B2 DHU\_OP\_START  
0197 C.                    çç[HK1\_TI\_CMD\_NUM]                    EQ            1COUNTUP  
0198 C.  
0199 C. °Ê²¼αîÄê%îíñαîŷÄŷ§ŷÄŷ⁻¹âiü  
0200 C.                    çç[HK1\_TI\_CMD\_ENA/DIS]                   EQ            ENA  
0201 C.                    çç[HK1\_TI\_CMD\_NUM]                    EQ            4  
0202 C.                    çç[HK1\_NEXT\_EXEC\_PIM]                   EQ            DHU  
0203 C.                    çç[HK1\_NEXT\_EXEC\_DC]                   EQ            0xB3  
0204 C.  
0205 C. \*\*\*\*\*  
0206 C. TIîŷ°èŷÄŷÖŷ×  
0207 C. \*\*\*\*\*  
0208 C.  
0209 C. TI\_TBL(0x03AB00-0x03AEFF;§ 1024byte)  
0210 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0211 BC                   (03 ab 03 01 02)  
0212 C.                    çç[HK1\_DMP\_TOP\_ADRS\_1]                   EQ            07  
0213 C.                    çç[HK1\_DMP\_TOP\_ADRS\_0]                   EQ            2B  
0214 C.                    çç[HK1\_DMP\_BLOCK\_NUM]                   EQ            3  
0215 C.                    çç[HK1\_DMP\_REPEAT\_NUM]                   EQ            0  
0216 C.                    çç[HK1\_DMA\_DMP\_PIM]                    EQ            DHU  
0217 +. DC 01-22 DHU\_MODE\_CHNG  
0218 BC                   (07 0b f8)  
0219 C.                    çç[HK1\_PKT\_FORM\_NO]                    EQ            7  
0220 C.                    çç[HK1\_PKT\_GEN\_TIME]                    EQ            0.25 s  
0221 C.                    çç[HK1\_S\_TLM\_BIT\_RATE]                   EQ            32k  
0222 C.                    çç[HK1\_X\_TLM\_BIT\_RATE]                   EQ            4M  
0223 C.                    çç[HK1\_DMP\_CHK\_FLG]                    EQ            EXEC  
0224 C.  
0225 C. ŷÄŷÖŷ×½ªî»αò³îÇ§  
0226 C.                    çç[HK1\_DMP\_CHK\_FLG]                    EQ            NON  
0227 C.  
0228 C. RAM ID=TI\_TBLαîŷ°èŷÄŷÖŷ×½ªî»αò³îÇ§  
0229 C.  
0230 C. DHUŷâ;¼ŷÉ;Ê¼ŷ¼. ŷi;¼ŷÉ;Êαòîãα¹  
0231 +. DC 01-22 DHU\_MODE\_CHNG  
0232 BC                   (02 0a f8)  
0233 C.                    çç[HK1\_PKT\_FORM\_NO]                    EQ            2  
0234 C.                    çç[HK1\_PKT\_GEN\_TIME]                    EQ            0.5S  
0235 C.                    çç[HK1\_S\_TLM\_BIT\_RATE]                   EQ            32K  
0236 C.                    çç[HK1\_X\_TLM\_BIT\_RATE]                   EQ            4M  
0237 C.  
0238 C. \*\*\*\*\*  
0239 C. SOT TI command set  
0240 C. \*\*\*\*\*  
0241 C. Execute, after the success of OP upload.  
0242 +. TI 2014-12-04 10:59:16.0  
0243 DC 07-F0 MDP\_SOT\_MODE\_STBY  
0244 BC                   (41)  
0245 C. -----  
0246 C.       HK1\_TI\_CMD\_NUM                    = 1 CNTUP [ ]  
0247 C. -----  
0248 C. \*\*\*\*\* SOT END \*\*\*\*\*  
0249 C. Stop EIS observation and temporarily disable EIS mode changes  
0250 C.  
0251 C.  
0252 C. \*\*\*\*\* Start EIS operation (TI set) \*\*\*\*\*  
0253 C. Execute, after the success of OP upload.  
0254 C. Set EIS TI-commands  
0255 +. TI 2014-12-04 10:59:30.0  
0256 DC 07-FC EIS\_MODE\_MANU  
0257 BC                   (21 02)  
0258 +. TI 2014-12-04 10:59:40.0  
0259 DC 07-FC EIS\_MODE\_CHG\_DIS  
0260 BC                   (22)  
0261 C.                    [    ] [HK1\_TI\_CMD\_NUM]                   EQ            2 COUNTUP  
0262 C. \*\*\*\*\* End EIS operation (TI set) \*\*\*\*\*  
0263 C.  
0264 C.  
0265 C.  
0266 C. \*\*\*\*\* XRT START \*\*\*\*\*  
0267 C. Execute, after the success of OP upload.  
0268 +. TI 2014-12-04 10:59:00.0  
0269 DC 07-F0 MDP\_XRT\_MODE\_STBY  
0270 BC                   (c3)  
0271 C.                    [    ] [HK1\_TI\_CMD\_NUM]                   EQ            1COUNTUP  
0272 C.  
0273 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0274 C.  
0275 C. \*\*\*\*\* MDP `ûÃîαî»ö¼ŷ¼αÊÄα¹αèDCBC.x²è \*\*\*\*\*  
0276 C. (¼ª°îŷÖŷÄŷÉŷŷŷÉŷáŷçŷèè¼αα¼Ã»Üα¹αè)  
0277 S. DC-BC dcbc-402:DCBC  
0278 (MDP\_known\_event)  
0279 C.  
0280 C.  
0281 C. \*\*\*\*\* ŷÐŷ¹.î Daily±çîñαè'Øα¹αèDCBC.x²è \*\*\*\*\*  
0282 S. DC-BC dcbc-153:DCBC  
0283 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0284 C.  
0285 C.  
0286 C. ;ãLOSŷÄŷ§ŷÄŷ⁻¹¼Ã»Ü;ã  
0287 C.  
0288 C. \*\*\*\*\* LOS \*\*\*\*\*  
0289 C.





(a) Spacecraft Operation Procedure (real-commands)

```
main-797 2014-12-04 12:04:48 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁò•µ°È»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÁ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-262:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2014-12-04 10:59:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 + DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 + DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 + DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 0c)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 13)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2014-12-04 10:59:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃîî»ö¼ÝðÊÂð¹ñèDCBC•x²è *****
0117 C. (%ã°îÿÓÿÄÿÉÿÞÿËÿáÿçÿèñ¼ñ¼Á»Ûñ¹ñè)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑñÉ´Øñ¹ñèDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSÿÁÿ$ÿÄÿ-¼Á»Û;ã
0128 C.
0129 . C. ***** LOS *****
0130 C.
```

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\*\*\* OP Sequence for XRT \*\*\*

2014/12/04	11:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	11:09:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	11:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/12/04	11:10:00.0	AOCS_Orе-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2014/12/04	11:10:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/12/04	11:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/12/04	11:10:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/12/04	11:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/12/04	11:10:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/12/04	11:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2014/12/04	11:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2014/12/04	11:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/12/04	15:27:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	15:27:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	15:27:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/12/04	15:27:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/12/04	15:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/12/04	15:51:00.0	XRT_Custom_430_OG [0x1ae]					
2014/12/04	15:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/12/04	17:04:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	17:04:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	17:04:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/12/04	17:04:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/12/04	17:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/12/04	17:27:30.0	XRT_Custom_430_OG [0x1ae]					
2014/12/04	17:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/12/04	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/12/04	18:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2014/12/04	18:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/12/04	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/12/04	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/12/04	18:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2014/12/04	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/12/04	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/04	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/12/04	18:10:00.0	AOCS_Orе-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2014/12/04	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/12/04	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/12/04	18:10:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/12/04	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/12/04	18:10:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/12/04	18:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2014/12/04	18:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2014/12/04	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]					

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2014/12/04	18:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/04	18:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/04	18:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/04	18:41:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/04	18:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/04	19:04:30.0	XRT_Custom_430_OG [0x1ae]				
2014/12/04	19:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/04	20:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/04	20:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/04	20:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/04	20:18:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/04	20:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/04	20:41:30.0	XRT_Custom_430_OG [0x1ae]				
2014/12/04	20:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/04	21:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/04	21:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/04	21:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/04	21:55:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/04	21:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/04	22:17:30.0	XRT_Custom_430_OG [0x1ae]				
2014/12/04	22:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/04	23:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/04	23:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/04	23:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/04	23:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/04	23:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/04	23:44:00.0	XRT_Custom_430_OG [0x1ae]				
2014/12/04	23:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/05	01:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/05	01:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/05	01:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/05	01:00:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/05	01:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/05	01:16:00.0	XRT_Custom_430_OG [0x1ae]				
2014/12/05	01:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/05	02:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/05	02:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/05	02:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/05	02:32:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/05	02:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/05	02:52:30.0	XRT_Custom_430_OG [0x1ae]				
2014/12/05	02:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/05	04:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/05	04:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/05	04:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/05	04:00:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/05	04:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/05	04:29:30.0	XRT_Custom_430_OG [0x1ae]				
2014/12/05	04:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/05	05:41:00.0	XRT_CTRL_MANU_400_OG [0x190]				

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2014/12/05	05:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05	05:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05	05:41:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/12/05	05:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/12/05	06:07:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/12/05	06:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2014/12/05	06:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2014/12/05	06:16:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/12/05	06:16:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05	06:17:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/12/05	06:17:18.0	XRT_FLD_DIS_422_OG [0x1a6]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/12/05	06:19:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	AOCS_Ore-point_Start_2_OG [0x098]					
2014/12/05	06:19:56.0	XRT_ARS_DIS_423_OG [0x1a7]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/12/05	06:19:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	XRT_FLD_DIS_422_OG [0x1a6]					
2014/12/05	06:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/12/05	06:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FLRCTRL_DIS_405_OG [0x195]					
2014/12/05	06:26:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/12/05	06:26:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_ARS_DIS_423_OG [0x1a7]					
2014/12/05	06:27:00.0	AOCS_Ore-point_Start_1_OG [0x097]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/12/05	06:27:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_QT_PROG_SET_438_OG [0x1b6]					
2014/12/05	06:27:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2014/12/05	06:27:22.0	XRT_AEC_RESET_413_OG [0x19d]	XRT_CTRL_AUTO_408_OG [0x198]					
2014/12/05	06:27:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/12/05	06:27:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05	06:29:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	XRT_CTRL_MANU_402_OG [0x192]					
2014/12/05	06:29:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05	06:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	XRT_FOCUS_POSITION_410_OG [0x19a]					
2014/12/05	07:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/12/05	07:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	AOCS_Ore-point_Start_1_OG [0x097]					
2014/12/05	07:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	AOCU_NM	5	02-76	03 00 00 00 00		
2014/12/05	07:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	XRT_FLD_ENA_411_OG [0x19b]					
2014/12/05	07:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/12/05	07:44:30.0	XRT_Custom_430_OG [0x1ae]	XRT_FLRCTRL_ENA_412_OG [0x19c]					
2014/12/05	07:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/12/05	09:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_AEC_RESET_413_OG [0x19d]					
2014/12/05	09:01:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/12/05	09:01:04.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_ARS_DIS_423_OG [0x1a7]					
2014/12/05	09:01:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/12/05	09:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_FLD_RESET_407_OG [0x197]					
2014/12/05	09:20:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/12/05	09:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_QT_PROG_SET_434_OG [0x1b2]					
2014/12/05	10:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2014/12/05	10:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FL_PROG_SET_401_OG [0x191]					
2014/12/05	10:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2014/12/05	10:43:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	XRT_CTRL_AUTO_408_OG [0x198]					
2014/12/05	10:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/12/05	10:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05	10:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_CTRL_MANU_400_OG [0x190]					
2014/12/05			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05			XRT_CTRL_MANU_402_OG [0x192]					
2014/12/05			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05			XRT_FLD_RESET_415_OG [0x19f]					
2014/12/05			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/12/05			XRT_PREFLR_STRT_439_OG [0x1b7]					
2014/12/05			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/12/05			XRT_Custom_430_OG [0x1ae]					
2014/12/05			XRT_CTRL_AUTO_424_OG [0x1a8]					
2014/12/05			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/12/05			XRT_CTRL_MANU_400_OG [0x190]					
2014/12/05			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05			XRT_CTRL_MANU_402_OG [0x192]					
2014/12/05			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/12/05			XRT_FLD_RESET_415_OG [0x19f]					
2014/12/05			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/12/05			XRT_PREFLR_STRT_439_OG [0x1b7]					
2014/12/05			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/12/05			XRT_Custom_430_OG [0x1ae]					
2014/12/05			XRT_CTRL_AUTO_424_OG [0x1a8]					
2014/12/05			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/12/05			XRT_PREFLR_STOP_419_OG [0x1a3]					
2014/12/05			MDP_XRT_PREFLR_STOP	1	07-F0	e9		



2014/12/05	14:28:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	14:28:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	14:28:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1 07-F0 da
2014/12/05	14:28:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2014/12/05	14:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2014/12/05	14:38:30.0	XRT_Custom_430_OG [0x1ae]	
2014/12/05	14:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2014/12/05	16:03:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	16:03:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	16:03:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1 07-F0 da
2014/12/05	16:03:06.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2014/12/05	16:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2014/12/05	16:26:30.0	XRT_Custom_430_OG [0x1ae]	
2014/12/05	16:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2014/12/05	17:39:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	17:39:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	17:39:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1 07-F0 da
2014/12/05	17:39:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2014/12/05	17:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2014/12/05	18:02:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	18:02:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	18:02:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2014/12/05	18:03:00.0	AOCS_Orpoint_Start_2_OG [0x098] AOCU_NM	5 02-76 00 00 00 00
2014/12/05	18:03:18.0	XRT_FLD_DIS_422_OG [0x1a6] MDP_XRT_FLD_DIS	1 07-F0 d9
2014/12/05	18:05:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2014/12/05	18:05:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1 07-F0 d5
2014/12/05	18:05:58.0	XRT_QT_PROG_SET_438_OG [0x1b6] MDP_XRT_QT_PROG_SET	2 07-F0 c4 03
2014/12/05	18:06:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2014/12/05	18:12:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	18:12:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	18:12:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4 07-F8 22 fe 97 00
2014/12/05	18:13:00.0	AOCS_Orpoint_Start_3_OG [0x099] AOCU_NM	5 02-76 02 00 00 00
2014/12/05	18:13:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1 07-F0 d8
2014/12/05	18:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1 07-F0 c8
2014/12/05	18:13:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1 07-F0 d0
2014/12/05	18:13:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1 07-F0 d5
2014/12/05	18:13:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1 07-F0 da
2014/12/05	18:15:56.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2 07-F0 c4 05
2014/12/05	18:15:58.0	XRT_FL_PROG_SET_401_OG [0x191] MDP_XRT_FL_PROG_SET	2 07-F0 c5 13
2014/12/05	18:16:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2014/12/05	19:16:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	19:16:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2014/12/05	19:16:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1 07-F0 da
2014/12/05	19:16:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2014/12/05	19:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2014/12/05	19:40:00.0	XRT_Custom_430_OG [0x1ae]	
2014/12/05	19:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2014/12/05	20:54:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1

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2014/12/05	20:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/05	20:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/05	20:54:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/12/05	20:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/12/05	21:17:00.0	XRT_Custom_430_OG [0x1ae]					
2014/12/05	21:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/05	22:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/05	22:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/05	22:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/05	22:31:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/12/05	22:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/12/05	22:52:00.0	XRT_Custom_430_OG [0x1ae]					
2014/12/05	22:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/06	00:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	00:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	00:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/06	00:08:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/12/06	00:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/12/06	00:16:30.0	XRT_Custom_430_OG [0x1ae]					
2014/12/06	00:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/06	00:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	00:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	00:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/12/06	00:30:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00	
2014/12/06	00:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/12/06	00:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/12/06	00:30:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/12/06	00:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/12/06	00:30:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/06	00:32:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2014/12/06	00:32:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2014/12/06	00:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/06	01:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	01:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	01:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/06	01:32:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/12/06	01:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/12/06	01:51:00.0	XRT_Custom_430_OG [0x1ae]					
2014/12/06	01:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/06	03:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	03:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	03:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/06	03:07:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/12/06	03:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/12/06	03:28:00.5	XRT_Custom_430_OG [0x1ae]					
2014/12/06	03:29:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/06	04:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	04:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/06	04:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2014/12/06	04:37:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/06	04:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/06	05:05:30.5	XRT_Custom_430_OG [0x1ae]				
2014/12/06	05:06:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/06	05:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	05:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	05:49:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/12/06	05:50:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2014/12/06	05:50:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/12/06	05:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/12/06	05:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/12/06	05:52:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2014/12/06	05:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/06	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	05:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/12/06	06:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2014/12/06	06:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/12/06	06:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/12/06	06:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/12/06	06:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/12/06	06:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/06	06:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2014/12/06	06:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2014/12/06	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/06	06:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	06:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	06:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/06	06:18:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/06	06:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/06	06:43:00.0	XRT_Custom_430_OG [0x1ae]				
2014/12/06	06:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/06	07:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	07:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	07:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/06	07:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/06	08:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/06	08:20:00.0	XRT_Custom_430_OG [0x1ae]				
2014/12/06	08:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/06	09:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	09:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/06	09:40:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00