

XRT Timeline to be uploaded on 2014/12/09

Period: 2014/12/09 09:40:00 - 2014/12/13 12:15:00

* * * * *

Normal mode

* * * * *

XOB #1997: Dark - Open+Thick-Be - 8x8 - 384x384												
Term	Pointing (x, y)		Comment									
12/09 10:04:00 - 12/09 10:24:54	Fixed (936.0, 0.0)	# OP start + 10min, HOP130 (1/15)									
12/09 10:28:00 - 12/09 10:40:54	Fixed (468.0, 0.0)	# (2/15)									
12/09 10:44:00 - 12/09 10:56:54	Fixed (0.0, 0.0)	# (3/15)									
12/09 11:00:00 - 12/09 11:12:54	Fixed (-	468.0, 0.0)	# (4/15)									
12/09 11:16:00 - 12/09 11:28:54	Fixed (-	936.0, 0.0)	# (5/15)									
12/09 11:32:00 - 12/09 11:38:54	Fixed (815.0, 470.0)	# (6/15)									
12/09 11:42:00 - 12/09 11:54:54	Fixed (411.0, 847.0)	# (7/15)									
12/09 11:58:00 - 12/09 12:10:54	Fixed (0.0, 941.0)	# (8/15)									
12/09 12:14:00 - 12/09 12:26:54	Fixed (-	411.0, 847.0)	# (9/15)									
12/09 12:30:00 - 12/09 12:36:54	Fixed (-	815.0, 470.0)	# (10/15)									
12/09 12:40:00 - 12/09 12:46:54	Fixed (815.0, -470.0)	# (11/15)									
12/09 12:50:00 - 12/09 13:02:54	Fixed (411.0, -847.0)	# (12/15)									
12/09 13:06:00 - 12/09 13:18:54	Fixed (0.0, -941.0)	# (13/15)									
12/09 13:22:00 - 12/09 13:34:54	Fixed (-	411.0, -847.0)	# (14/15)									
12/09 13:38:00 - 12/09 13:59:54	Fixed (-	815.0, -471.0)	# (15/15)									
PROG= 01 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 30 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Dark	63ms	Obs	8x8	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A66: FOXSI launch - AR Standard-B(Morphology), Be/thin 24s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1064												
Term	Pointing (x, y)		Comment									
12/09 14:03:00 - 12/09 17:59:54	Track (142.4, -79.8)	# AR 12227 obs									
PROG= 13 Inf-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 8 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 24 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 5-time(s) 2.0sec												
└─ Seqn= 68 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	DPCM	3	0	2.0sec
med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 52 20-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A65: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Al-poly(45/1024), Al-poly+Ti-poly(256/2048), Med-Al(4095)												
Term	Pointing (x, y)		Comment									
12/09 18:07:00 - 12/09 18:14:54	Fixed (0.0, 0.0)	synoptic									
12/10 18:06:30 - 12/10 18:18:24	Fixed (0.0, 0.0)	synoptic, shifted 3.5 min									
PROG= 20 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 33 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 40 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 77 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 2-time(s) 12.0sec												
Seqn= 21 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 37 1-time(s) 4.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 46 1-time(s) 4.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19E5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m

Term	Pointing (x, y)	Comment
12/09 18:18:00 - 12/10 05:24:30	Track (182.0, -79.5) @ 12/09 18:15:00	# AR 12227 obs
12/10 17:03:00 - 12/10 18:03:24	Track (386.9, -77.8) @ 12/10 17:00:00	# AR 12227 obs
12/10 18:21:30 - 12/11 05:33:54	Track (398.2, -77.7) @ 12/10 18:18:30	# AR 12227 obs

PROG= 16 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 8 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec												
Seqn= 24 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62 8-time(s) 240.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A5F: Synoptic Q95 2x2 - Al/mesh(12/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/2048) + Th

Term	Pointing (x, y)	Comment
12/10 06:04:00 - 12/10 06:10:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min
12/11 05:37:00 - 12/11 05:43:54	Fixed (0.0, 0.0)	synoptic, shifted -26.0 min

PROG= 09 1-time(s)

Subr= 1 1-time(s) 12.0sec												
Seqn= 49 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 17 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m

Term	Pointing (x, y)	Comment
12/10 06:14:00 - 12/10 09:59:54	Track (291.2, -78.5) @ 12/10 06:11:00	# AR 12227 obs
12/11 05:47:00 - 12/11 07:42:00	Track (494.0, -77.1) @ 12/11 05:44:00	# AR 12227 obs

PROG= 12 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62 8-time(s) 270.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A1B: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 33ms

Term	Pointing (x, y)	Comment
12/10 10:03:00 - 12/10 16:59:54	Fixed (-24.0, -935.0)	# HOP81 (S-pole)

PROG= 05 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 48 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 30-time(s) 2.0sec													
Seqn= 57 1-time(s) 60.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Flare mode

* * * * *

XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
12/09 10:04:00 - 12/09 10:24:54	Fixed (936.0, 0.0)	# OP start + 10min, HOP130 (1/15)
12/09 10:28:00 - 12/09 10:40:54	Fixed (468.0, 0.0)	# (2/15)
12/09 10:44:00 - 12/09 10:56:54	Fixed (0.0, 0.0)	# (3/15)
12/09 11:00:00 - 12/09 11:12:54	Fixed (-468.0, 0.0)	# (4/15)
12/09 11:16:00 - 12/09 11:28:54	Fixed (-936.0, 0.0)	# (5/15)
12/09 11:32:00 - 12/09 11:38:54	Fixed (815.0, 470.0)	# (6/15)
12/09 11:42:00 - 12/09 11:54:54	Fixed (411.0, 847.0)	# (7/15)
12/09 11:58:00 - 12/09 12:10:54	Fixed (0.0, 941.0)	# (8/15)
12/09 12:14:00 - 12/09 12:26:54	Fixed (-411.0, 847.0)	# (9/15)
12/09 12:30:00 - 12/09 12:36:54	Fixed (-815.0, 470.0)	# (10/15)
12/09 12:40:00 - 12/09 12:46:54	Fixed (815.0, -470.0)	# (11/15)
12/09 12:50:00 - 12/09 13:02:54	Fixed (411.0, -847.0)	# (12/15)
12/09 13:06:00 - 12/09 13:18:54	Fixed (0.0, -941.0)	# (13/15)
12/09 13:22:00 - 12/09 13:34:54	Fixed (-411.0, -847.0)	# (14/15)
12/09 13:38:00 - 12/09 13:59:54	Fixed (-815.0, -471.0)	# (15/15)
12/09 14:03:00 - 12/09 17:59:54	Track (142.4, -79.8) @ 12/09 14:00:00	# AR 12227 obs
12/09 18:18:00 - 12/10 05:24:30	Track (182.0, -79.5) @ 12/09 18:15:00	# AR 12227 obs
12/10 06:14:00 - 12/10 09:59:54	Track (291.2, -78.5) @ 12/10 06:11:00	# AR 12227 obs
12/10 10:03:00 - 12/10 16:59:54	Fixed (-24.0, -935.0)	# HOP81 (S-pole)
12/10 17:03:00 - 12/10 18:03:24	Track (386.9, -77.8) @ 12/10 17:00:00	# AR 12227 obs
12/10 18:21:30 - 12/11 05:33:54	Track (398.2, -77.7) @ 12/10 18:18:30	# AR 12227 obs
12/11 05:47:00 - 12/11 07:42:00	Track (494.0, -77.1) @ 12/11 05:44:00	# AR 12227 obs

PROG= 19 30-time(s)													
Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15 1-time(s) 2.0sec													

	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

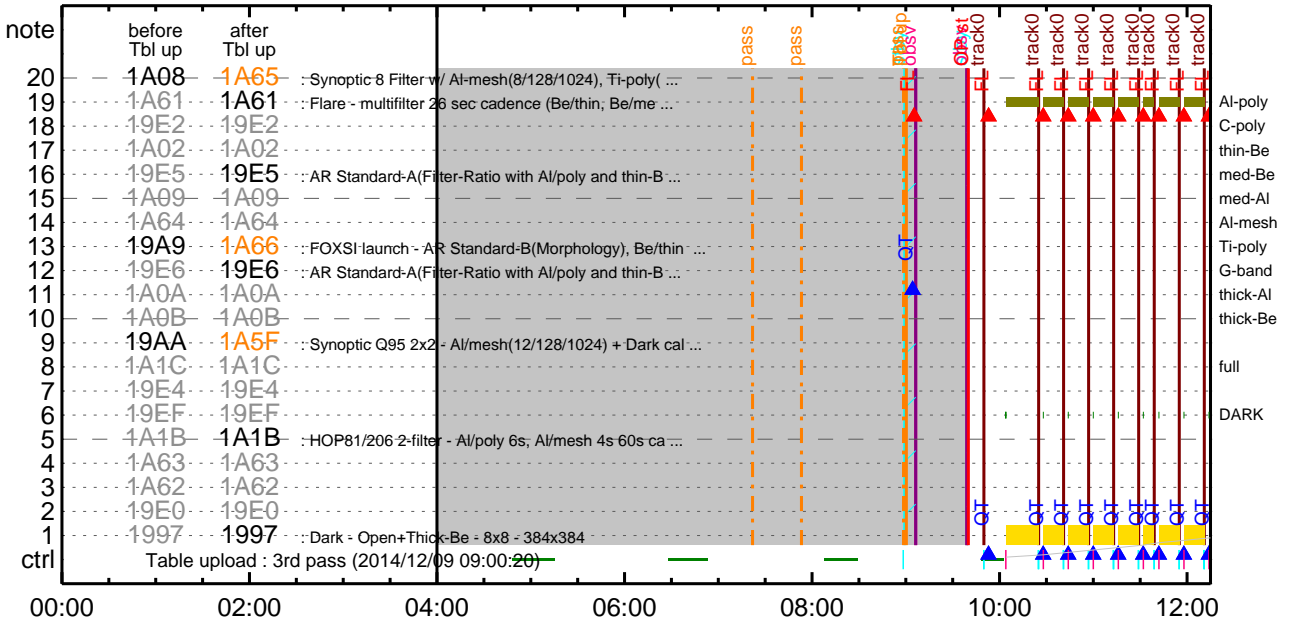
* * * * *

Flare Detection

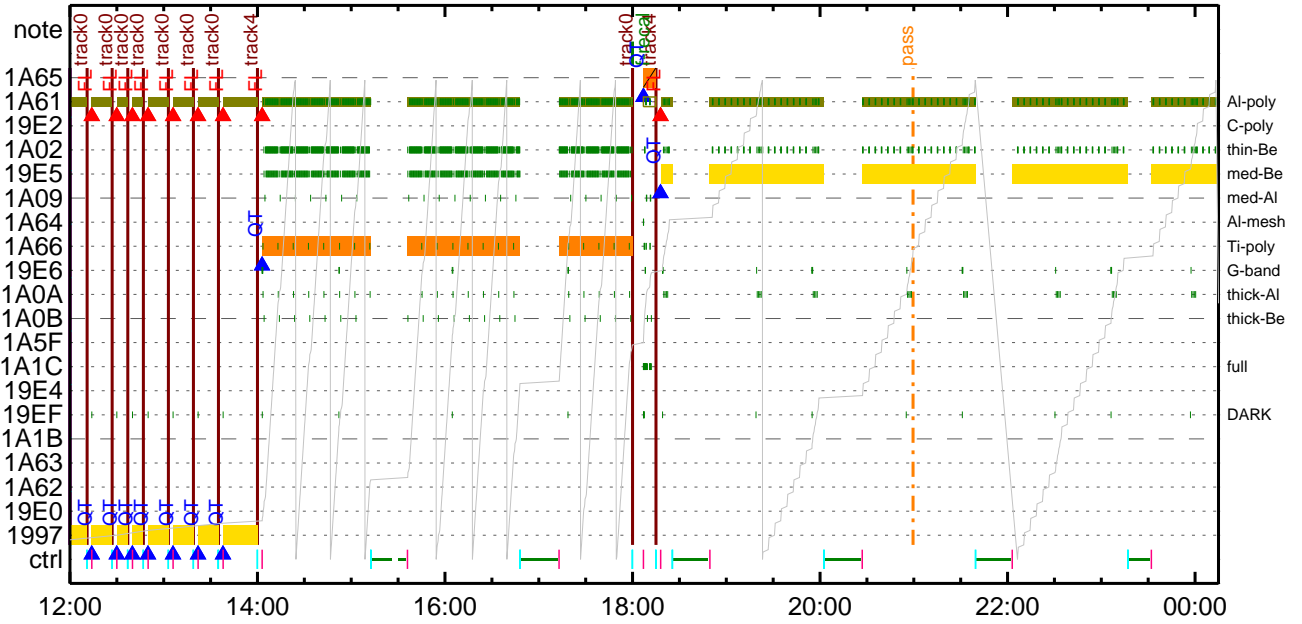
* * * * *

FLD Patrol												
Term	Pointing (x, y)						Comment					
12/09 18:15:18 - 12/10 06:01:18	Track (182.0,	-79.5)	^{12/09 18:15:00}	# AR 12227	obs						
12/10 06:11:18 - 12/10 18:03:48	Track (291.2,	-78.5)	^{12/10 06:11:00}	# AR 12227	obs						
12/10 18:18:48 - 12/11 05:34:18	Track (398.2,	-77.7)	^{12/10 18:18:30}	# AR 12227	obs						
12/11 05:44:18 - 12/13 12:15:00	Track (494.0,	-77.1)	^{12/11 05:44:00}	# AR 12227	obs						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

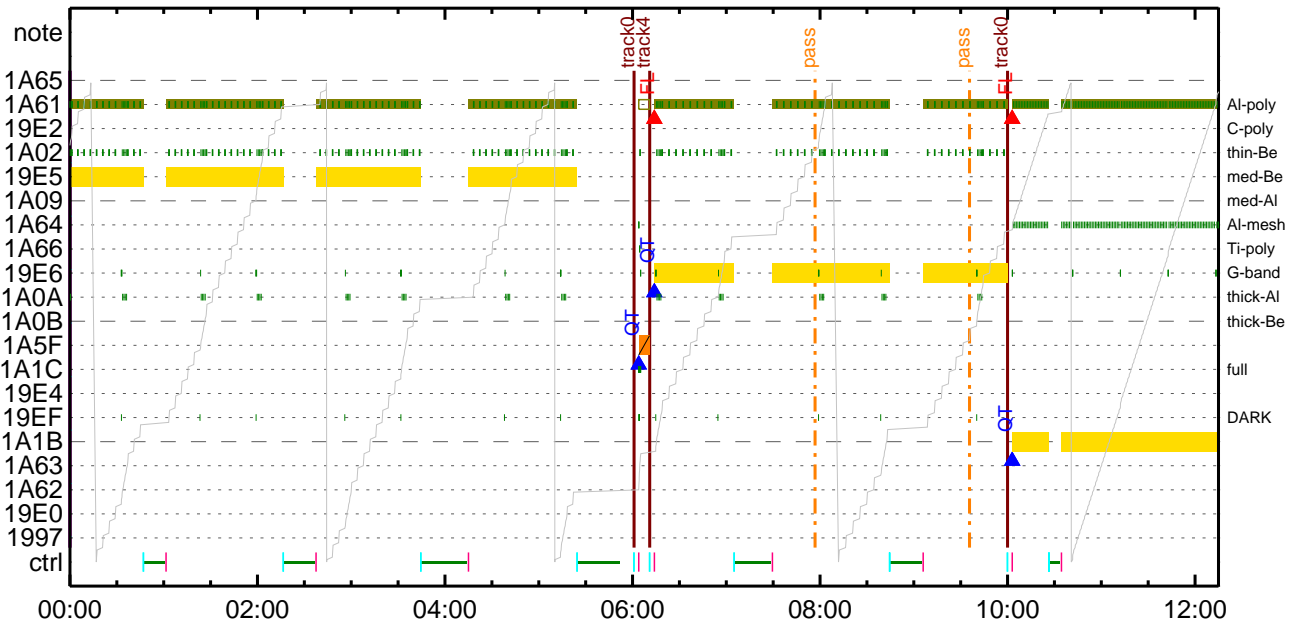
CMDI #0777 2014/12/09



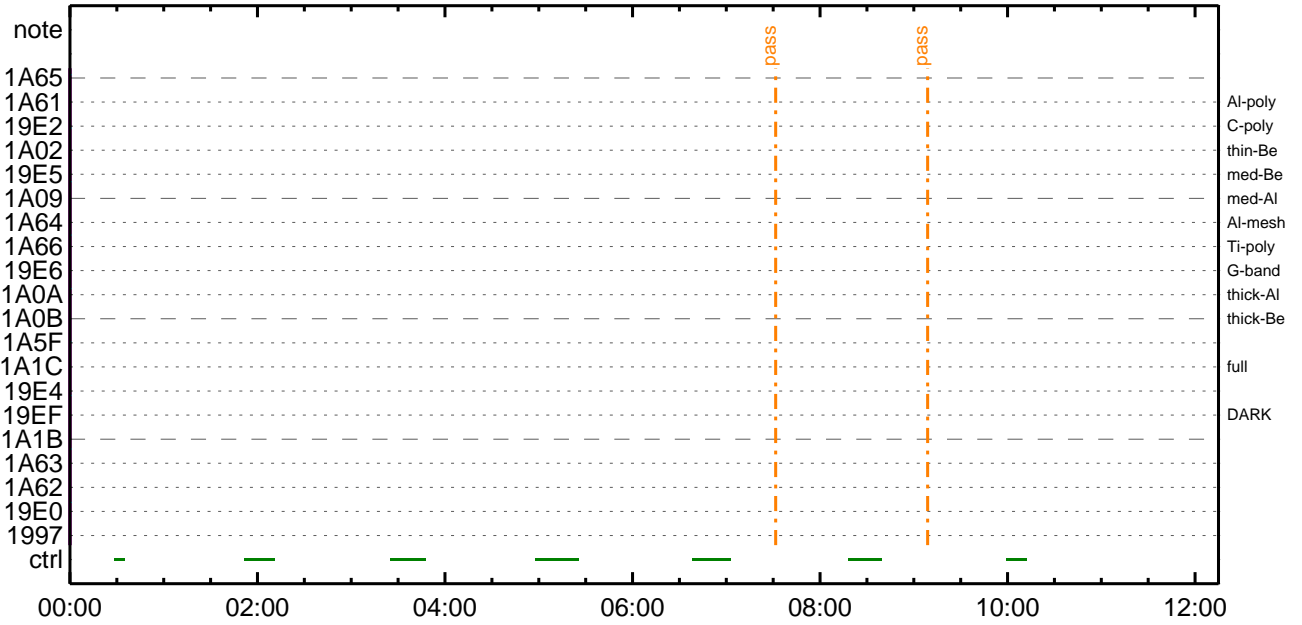
CMDI #0777 2014/12/09



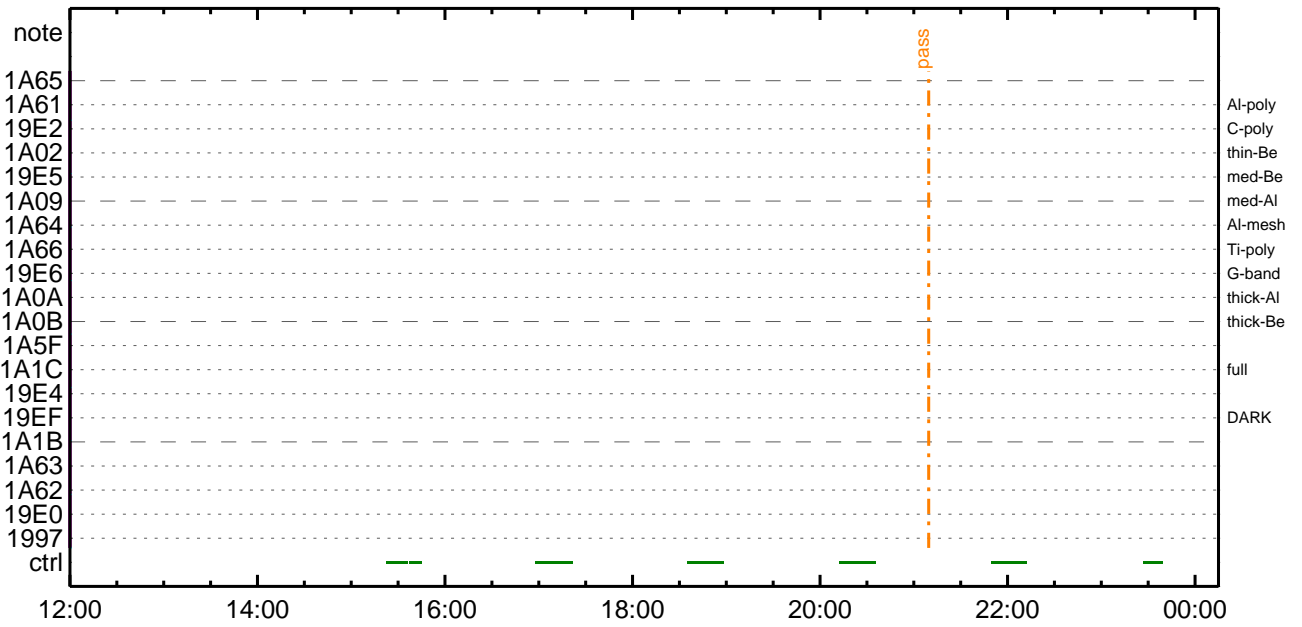
CMDI #0777 2014/12/10



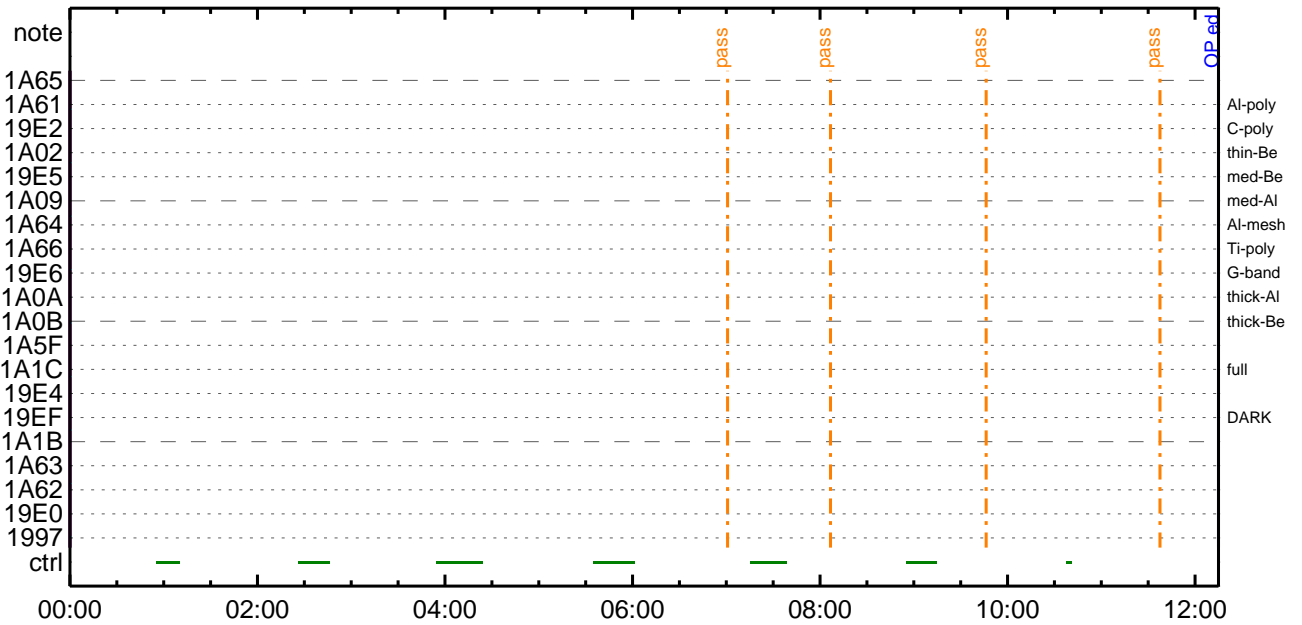
CMDI #0777 2014/12/12



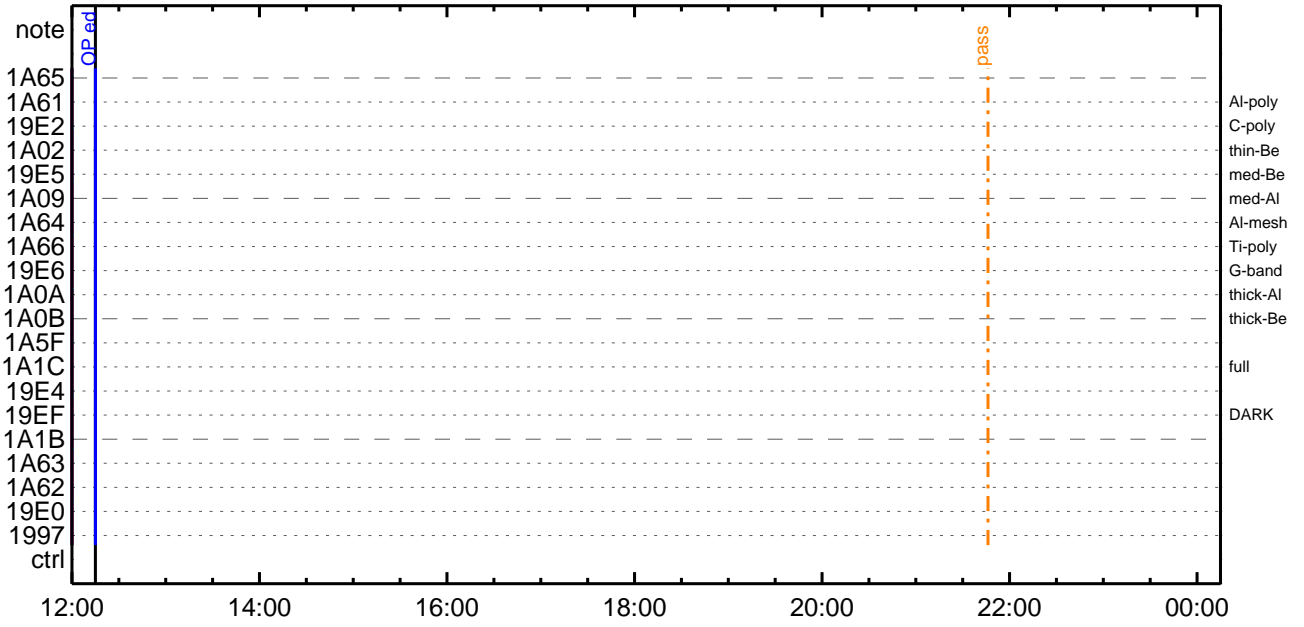
CMDI #0777 2014/12/12



CMDI #0777 2014/12/13



CMDI #0777 2014/12/13




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-803:OP
0104 ( )
0105 S. OG og-803:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îç§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼Ë¹ç•è²îOKò³îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îç§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼Ë¹ç•è²îOKò³îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îç§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼Ë¹ç•è²îOKò³îç§
0165 C.
0166 C. ***** °Ë²¼òî¼Ë¹ç•è²îOKò³îç§ *****
0167 C. DHUYâ;4YE;Ë¼Y½;Yî;4YE;Ëòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î¼Ë¹ç•è²¼òîTI-CMDÁ÷ç²î¼Ë¹Ï²ç•è²¼òî³çç²;f
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YpYóYË²òðÁDîç(UT)
0183 +. TI 2014-12-09 09:35:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-12-09 09:35:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-12-09 09:35:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2014-12-09 09:39:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C.      °Ê²¼□îÄë%îíñ□îŷÄŷ§ŷÄŷ-¹àîŷ
0200 C.      çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C.      *****
0206 C.      TIîŷ°èŷÄŷÖŷ×
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C.      ŷÄŷÖŷ×½ªî»□ð³îç§
0226 C.      çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C.      RAM ID=TI_TBL□îŷ°è¹ç•è²îOK□ð³îç§
0229 C.
0230 C.      DHUŷâ;¼ŷÉ;Ê¼ŷ¼.ŷî;¼ŷÉ;Ê□ðîâ□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C.      *****
0239 C.      SOT TI command set
0240 C.      *****
0241 C.      Execute, after the success of OP upload.
0242 +. TI 2014-12-09 09:39:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C.      -----
0246 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C.      -----
0248 C.      ***** SOT END *****
0249 C.      Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C.      ***** Start EIS operation (TI set) *****
0253 C.      Execute, after the success of OP upload.
0254 C.      Set EIS TI-commands
0255 +. TI 2014-12-09 09:39:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2014-12-09 09:39:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C.      ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2014-12-09 09:39:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 C.      ***** MDP `ûÄî□î»ö¼ŷ□èÄð□¹èDCBC•x²è *****
0276 C.      (¼ª°îŷÖŷÄŷÉŷŷŷÄŷçŷèè¼□□¼Ä»ŷ□¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C.      ***** ŷÐŷ¹•î Daily±çîñèÉ'Ø□¹èDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.      ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»ŷ;ã
0287 C.
0288 C.      ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-804 2014-12-09 13:21:44 94 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Ä»Û;ã
0005 C.
0006 C. YÁY$;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Ä□•μ°Æ»Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈ%μ•ííÈ;È□È¼°ÇÔ□•□¿¼ì¹ç□í;çÄ®, ù□¹□è□ß□çÄ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 + DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 7076263.3 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_CHG_ENA
0056 BC (20)
0057 . C. Verify EIS_MODE_CHG_FLG is ENA
0058 +. DC 07-FC EIS_MODE_MANU
0059 BC (21 02)
0060 . C. Verify EIS in MANUAL mode
0061 C. Estimated OBSTBL upload time is 51s
0062 C. *****
0063 C. EIS START OBSTBL LOAD
0064 C. *****
0065 . S. RAM ram-820:EIS_OBSTBL
0066 ( )
0067 +. DC 07-FC EIS_DUMP_OBSTBL
0068 BC (07 07 07 00 00 70 00)
0069 C.
0070 C. Execute, after the success of OBSTBL upload.
0071 C. Set EIS TI-commands
0072 +. TI 2014-12-09 09:39:50.0
0073 DC 07-FC EIS_MODE_CHG_ENA
0074 BC (20)
0075 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0076 C. *****
0077 C. EIS END OBSTBL LOAD
0078 C. *****
0079 C.
0080 . C. ***** MDP `ûÄí□í»ó¼Y□ÈÄ□¹□èDCBC•×²è *****
0081 C. (¼á°íYóYÁYÉYÏYÉYáYçYè□È¼¼□□¼Ä»Û¹□è)
0082 . S. DC-BC dcbc-402:DCBC
0083 (MDP_known_event)
0084 C.
0085 C.
0086 . C. ***** YDÝ¹•Ï Daily±¿íÑ□È´Ø¹□èDCBC•×²è *****
0087 . S. DC-BC dcbc-153:DCBC
0088 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 C.
0090 C.
0091 . C. ;ãLOSÁY$YÁY~¼Ä»Û;ã
0092 C.
0093 . C. ***** LOS *****
0094 C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-264:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-284:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¥ç¥Ã¥×¥í;¼¥É°âÊSTS_CHKðOFFðËð¹ðë
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHKðONðËð¹ðë
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700[ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2014-12-09 09:39:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 . C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** XRT START *****
0174 C.
0175 +. DC 07-F0 MDP_XRT_CTRL_MANU
0176 BC (c1)
0177 + DC 07-F0 MDP_XRT_MODE_STBY
0178 BC (c3)
0179 . C. ----- Success Verify ? OK / NG____
0180 C.
0181 C. XRT Obs. Table Upload
0182 . S. RAM ram-291:MDP_OBS_X
0183 ( )
0184 C.
0185 +. DC 07-F0 MDP_DUMP_XRTTBL
0186 BC (84 07 00 00 00 3a d4)
0187 . C. ----- Comparison Check ? OK / ERR ____
0188 C.
0189 C.
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 01 b1 b1 04 04)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 02 b1 b1 08 08)

```

```
0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 03 b1 b1 08 08)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 04 b1 b1 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 05 85 83 06 06)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 06 85 83 06 06)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 07 85 83 08 08)
0204 + DC 07-F0 MDP_XRT_ROI_SET
0205 BC (cd 08 80 80 06 06)
0206 + DC 07-F0 MDP_XRT_ROI_SET
0207 BC (cd 09 80 80 20 20)
0208 + DC 07-F0 MDP_XRT_ROI_SET
0209 BC (cd 0a 80 80 20 08)
0210 + DC 07-F0 MDP_XRT_ROI_SET
0211 BC (cd 0b 80 80 08 20)
0212 + DC 07-F0 MDP_XRT_ROI_SET
0213 BC (cd 0f 80 80 06 06)
0214 + DC 07-F0 MDP_XRT_ROI_SET
0215 BC (cd 10 80 80 08 08)
0216 + DC 07-F0 MDP_XRT_FLD_ENA
0217 BC (d8)
0218 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0219 BC (c8)
0220 + DC 07-F0 MDP_XRT_AEC_RESET
0221 BC (d0)
0222 + DC 07-F0 MDP_XRT_ARS_DIS
0223 BC (d5)
0224 + DC 07-F0 MDP_XRT_FLD_RESET
0225 BC (da)
0226 + DC 07-F0 MDP_XRT_QT_PROG_SET
0227 BC (c4 0c)
0228 + DC 07-F0 MDP_XRT_FL_PROG_SET
0229 BC (c5 13)
0230 . C. ----- Success Verify ? OK / NG ____
0231 C.
0232 C.
0233 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0234 C.
0235 + DC 07-F0 MDP_XRT_MODE_OBSV
0236 BC (c2)
0237 + TI 2014-12-09 09:39:02.0
0238 DC 07-F0 MDP_XRT_MODE_OBSV
0239 BC (c2)
0240 . C. ----- Success Verify ? OK / NG ____
0241 C.
0242 C. ***** XRT END *****
0243 C.
0244 . C. ***** MDP 'ũÃîãî»ö¼ÝªÊÄðª¹ªēDCBC•x²è *****
0245 C. (%ã°îÝÔÝÄÝËÝÏÝÄÝ¼ÝèªE¼ª¼Ä»Ûª¹ªè)
0246 . S. DC-BC dcbc-402:DCBC
0247 (MDP_known_event)
0248 C.
0249 C.
0250 . C. ***** ÝDÝ¹·İ Daily±¿İÑªË´Øª¹ªēDCBC•x²è *****
0251 . S. DC-BC dcbc-153:DCBC
0252 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0253 C.
0254 C.
0255 . C. ;ãLOSÝÄÝ§ÝÄÝ¹¼Ä»Û;ã
0256 C.
0257 . C. ***** LOS *****
0258 C.
```

Dec 09, 14 13:21

XRT_OGLIST_0777.chk

Page 1/10

*** OP Sequence for XRT ***

2014/12/09	09:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	09:49:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	09:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/09	09:50:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 ac cd				
2014/12/09	09:50:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/09	09:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/09	09:50:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	09:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	09:50:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	09:52:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/12/09	09:52:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/09	10:03:00.0	XRT_Custom_430_OG [0x1ae]							
2014/12/09	10:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	10:24:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	10:24:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	10:24:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/09	10:25:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 d6 67				
2014/12/09	10:25:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/09	10:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/09	10:25:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	10:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	10:25:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	10:27:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/12/09	10:27:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/09	10:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	10:40:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	10:40:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	10:40:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/09	10:41:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/12/09	10:41:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/09	10:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/09	10:41:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	10:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	10:41:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	10:43:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/12/09	10:43:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/09	10:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	10:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	10:56:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	10:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/09	10:57:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 29 99				
2014/12/09	10:57:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/09	10:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/09	10:57:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	10:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	10:57:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

Dec 09, 14 13:21

XRT_OGLIST_0777.chk

Page 2/10

2014/12/09	10:59:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2014/12/09	10:59:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2014/12/09	11:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/09	11:12:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/09	11:12:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/09	11:12:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2014/12/09	11:13:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 00	00 53 33
2014/12/09	11:13:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/12/09	11:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/12/09	11:13:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/12/09	11:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/12/09	11:13:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/09	11:15:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2014/12/09	11:15:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2014/12/09	11:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/09	11:28:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/09	11:28:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/09	11:28:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2014/12/09	11:29:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 d6	36 b7 8e
2014/12/09	11:29:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/12/09	11:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/12/09	11:29:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/12/09	11:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/12/09	11:29:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/09	11:31:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2014/12/09	11:31:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2014/12/09	11:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/09	11:38:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/09	11:38:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/09	11:38:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2014/12/09	11:39:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 b4	b5 db 75
2014/12/09	11:39:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/12/09	11:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/12/09	11:39:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/12/09	11:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/12/09	11:39:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/09	11:41:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2014/12/09	11:41:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2014/12/09	11:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/09	11:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/09	11:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/09	11:54:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2014/12/09	11:55:00.0	AOCS_OrE-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 ac	5b 00 00
2014/12/09	11:55:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/12/09	11:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/12/09	11:55:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/12/09	11:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]					

Dec 09, 14 13:21

XRT_OGLIST_0777.chk

Page 3/10

2014/12/09	11:55:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/09	11:57:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2014/12/09	11:57:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2014/12/09	11:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/09	12:10:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/09	12:10:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/09	12:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/12/09	12:11:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	b4	b5	24 8b
2014/12/09	12:11:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/12/09	12:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/12/09	12:11:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/12/09	12:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/12/09	12:11:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/09	12:13:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2014/12/09	12:13:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2014/12/09	12:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/09	12:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/09	12:26:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/09	12:26:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/12/09	12:27:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	d6	36	48 72
2014/12/09	12:27:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/12/09	12:27:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/12/09	12:27:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/12/09	12:27:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/12/09	12:27:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/09	12:29:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2014/12/09	12:29:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2014/12/09	12:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/09	12:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/09	12:36:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/09	12:36:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/12/09	12:37:00.0	AOCS_OrE-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	29	ca	b7 8e
2014/12/09	12:37:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/12/09	12:37:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/12/09	12:37:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/12/09	12:37:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/12/09	12:37:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/09	12:39:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2014/12/09	12:39:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2014/12/09	12:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/09	12:46:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/09	12:46:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/09	12:46:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/12/09	12:47:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	4b	4b	db 75
2014/12/09	12:47:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/12/09	12:47:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			

Dec 09, 14 13:21

XRT_OGLIST_0777.chk

Page 4/10

2014/12/09	12:47:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	12:47:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	12:47:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	12:49:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/12/09	12:49:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/09	12:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	13:02:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	13:02:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	13:02:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/09	13:03:00.0	AOCS_OrE-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 53 a5 00 00				
2014/12/09	13:03:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/09	13:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/09	13:03:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	13:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	13:03:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	13:05:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/12/09	13:05:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/09	13:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	13:18:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	13:18:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	13:18:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/09	13:19:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 4b 4b 24 8b				
2014/12/09	13:19:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/09	13:19:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/09	13:19:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	13:19:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	13:19:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	13:21:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/12/09	13:21:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/09	13:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	13:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	13:34:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	13:34:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/09	13:35:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 29 db 48 72				
2014/12/09	13:35:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/09	13:35:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/09	13:35:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	13:35:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	13:35:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	13:37:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/12/09	13:37:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/09	13:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	13:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/09	14:00:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2014/12/09	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							

2014/12/09	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/09	14:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	14:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	14:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/12/09	14:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2014/12/09	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	15:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	15:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	15:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	15:12:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/09	15:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/09	15:35:00.0	XRT_Custom_430_OG [0x1ae]								
2014/12/09	15:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	16:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	16:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	16:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	16:48:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/09	16:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/09	17:12:00.0	XRT_Custom_430_OG [0x1ae]								
2014/12/09	17:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	17:59:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78	00			
2014/12/09	18:00:00.0	AOCS_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2014/12/09	18:03:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	00
2014/12/09	18:04:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/12/09	18:06:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/12/09	18:06:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	18:06:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2014/12/09	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	18:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	18:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	00
2014/12/09	18:15:00.0	AOCS_Orе-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	04	00	00	00	00
2014/12/09	18:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/09	18:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/09	18:15:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/09	18:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/09	18:15:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	18:17:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2014/12/09	18:17:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2014/12/09	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/09	18:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	18:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/09	18:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/09	18:25:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				

Dec 09, 14 13:21

XRT_OGLIST_0777.chk

Page 6/10

2014/12/09	18:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/09	18:48:30.0	XRT_Custom_430_OG [0x1ae]			
2014/12/09	18:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/09	20:02:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/09	20:02:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/09	20:02:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/09	20:02:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/09	20:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/09	20:26:00.0	XRT_Custom_430_OG [0x1ae]			
2014/12/09	20:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/09	21:39:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/09	21:39:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/09	21:39:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/09	21:39:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/09	21:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/09	22:02:01.0	XRT_Custom_430_OG [0x1ae]			
2014/12/09	22:03:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/09	23:17:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/09	23:17:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/09	23:17:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/09	23:17:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/09	23:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/09	23:31:00.0	XRT_Custom_430_OG [0x1ae]			
2014/12/09	23:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/10	00:47:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	00:47:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	00:47:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/10	00:47:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/10	00:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/10	01:00:30.0	XRT_Custom_430_OG [0x1ae]			
2014/12/10	01:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/10	02:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	02:16:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	02:16:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/10	02:16:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/10	02:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/10	02:36:30.0	XRT_Custom_430_OG [0x1ae]			
2014/12/10	02:37:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/10	03:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	03:44:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	03:44:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/10	03:44:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/10	03:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/10	04:14:00.0	XRT_Custom_430_OG [0x1ae]			
2014/12/10	04:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/10	05:24:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	05:24:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	05:24:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/10	05:24:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/10	05:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

Dec 09, 14 13:21

XRT_OGLIST_0777.chk

Page 7/10

2014/12/10	06:00:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	06:00:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	06:00:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/12/10	06:01:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/12/10	06:01:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/12/10	06:03:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/12/10	06:03:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/10	06:03:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2014/12/10	06:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	06:10:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	06:10:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	06:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/10	06:11:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2014/12/10	06:11:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/10	06:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/10	06:11:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/10	06:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/10	06:11:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/10	06:13:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2014/12/10	06:13:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/10	06:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	07:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	07:05:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	07:05:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/10	07:05:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/10	07:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/10	07:28:31.0	XRT_Custom_430_OG [0x1ae]							
2014/12/10	07:29:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	08:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	08:44:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	08:44:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/10	08:44:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/10	08:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/10	09:05:00.0	XRT_Custom_430_OG [0x1ae]							
2014/12/10	09:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/10	10:00:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00 53 1a 02 24				
2014/12/10	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/10	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/10	10:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/10	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/10	10:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/10	10:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2014/12/10	10:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/10	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	10:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							

Dec 09, 14 13:21

XRT_OGLIST_0777.chk

Page 8/10

2014/12/10	10:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	10:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	10:26:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/10	10:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/10	10:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/10	10:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	14:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	14:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	14:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/10	14:14:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/10	14:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/10	14:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	14:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	15:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	15:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	15:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	15:47:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/10	15:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/10	16:10:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/10	16:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	16:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	17:00:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/10	17:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00				
2014/12/10	17:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/10	17:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/10	17:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/10	17:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/10	17:02:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/10	17:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2014/12/10	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/10	17:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	17:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	17:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	17:24:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/10	17:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/10	17:47:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/10	17:48:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/10	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	18:03:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/10	18:03:30.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/12/10	18:03:48.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00				
2014/12/10	18:06:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/12/10	18:06:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/12/10	18:06:28.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/10	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2014/12/10			MDP_XRT_CTRL_AUTO	1	07-F0	c0				

2014/12/10	18:18:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	18:18:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	18:18:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/12/10	18:18:30.0	AOCS_Ore-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	04 00 00 00 00
2014/12/10	18:18:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2014/12/10	18:18:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/12/10	18:18:52.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2014/12/10	18:18:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2014/12/10	18:18:56.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/10	18:21:26.0	XRT_QT_PROG_SET_406_OG [0x196] MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2014/12/10	18:21:28.0	XRT_FL_PROG_SET_401_OG [0x191] MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2014/12/10	18:21:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/10	19:01:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	19:01:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	19:01:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/10	19:01:06.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/10	19:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/10	19:24:01.0	XRT_Custom_430_OG [0x1ae]			
2014/12/10	19:25:01.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/10	20:38:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	20:38:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	20:38:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/10	20:38:06.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/10	20:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/10	21:01:30.0	XRT_Custom_430_OG [0x1ae]			
2014/12/10	21:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/10	22:15:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	22:15:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	22:15:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/10	22:15:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/10	22:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/10	22:37:00.0	XRT_Custom_430_OG [0x1ae]			
2014/12/10	22:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/10	23:53:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	23:53:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/10	23:53:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/10	23:53:06.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/10	23:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/11	00:02:00.0	XRT_Custom_430_OG [0x1ae]			
2014/12/11	00:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/11	01:17:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/11	01:17:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/11	01:17:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/11	01:17:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/11	01:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/11	01:35:30.0	XRT_Custom_430_OG [0x1ae]			
2014/12/11	01:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/11	02:51:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/11	02:51:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1

Dec 09, 14 13:21

XRT_OGLIST_0777.chk

Page 10/10

2014/12/11	02:51:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/11	02:51:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/11	02:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/11	03:12:30.0	XRT_Custom_430_OG [0x1ae]							
2014/12/11	03:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/11	04:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	04:21:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	04:21:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/11	04:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/11	04:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/11	04:50:00.0	XRT_Custom_430_OG [0x1ae]							
2014/12/11	04:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/11	05:33:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	05:33:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	05:33:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/12/11	05:34:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/12/11	05:34:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/12/11	05:36:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/12/11	05:36:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/11	05:36:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2014/12/11	05:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/11	05:43:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	05:43:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	05:43:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/12/11	05:44:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2014/12/11	05:44:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/12/11	05:44:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/12/11	05:44:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/12/11	05:44:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/12/11	05:44:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/11	05:46:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2014/12/11	05:46:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2014/12/11	05:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/11	06:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	06:01:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	06:01:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/11	06:01:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/11	06:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/11	06:27:00.0	XRT_Custom_430_OG [0x1ae]							
2014/12/11	06:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/12/11	07:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	07:42:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/12/11	07:42:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/12/11	07:42:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/12/11	07:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/12/11	09:24:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				