

XRT Timeline to be uploaded on 2014/12/30

Period: 2014/12/30 09:12:00 - 2015/01/08 10:14:00

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Normal mode

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XOB #1A1C: CME watch - 4x4 - AEC 2 - Al-poly - G-band (2x2,8ms) - Leak (33ms) - 100s cad													
Term		Pointing (x, y)		Comment									
12/30 09:44:32 - 12/30 12:10:49		Track (-118.5, -120.2) @ 12/30 09:33:30		SP mosaic obs. at DC 1/2									
12/30 12:13:55 - 12/30 14:45:30		Track (161.9, -120.0) @ 12/30 12:10:55		SP mosaic obs. at DC 2/2									
PROG= 05 Inf.-time(s)													
Subr= 1		60-time(s)		100.0sec									
Seqn= 78		1-time(s)		4.0sec									
Al-poly/Open		thin-Be/Open		close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0 2.0sec
Subr= 2		1-time(s)		2.0sec									
Seqn= 6		1-time(s)		2.0sec									
Open/G-band		Open/G-band		open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #19E2: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly cont													
Term		Pointing (x, y)		Comment									
12/30 15:03:00 - 12/30 17:57:30		Track (-439.6, -195.8) @ 12/30 15:00:00		AR12251 tracking									
12/30 18:44:00 - 12/31 06:06:24		Track (-409.6, -194.8) @ 12/30 18:41:00		AR12251 tracking									
12/31 06:19:30 - 12/31 17:59:54		Track (-311.9, -192.0) @ 12/31 06:16:30		AR12251 tracking									
12/31 18:13:00 - 01/01 06:08:54		Track (-207.5, -189.6) @ 12/31 18:10:00		AR12251 tracking									
01/01 06:22:00 - 01/01 17:31:30		Track (-98.1, -187.7) @ 01/01 06:19:00		AR12251 tracking									
01/01 18:10:00 - 01/02 05:41:54		Track (9.4, -186.4) @ 01/01 18:07:00		AR12251 tracking									
01/02 05:55:00 - 01/02 17:43:54		Track (116.4, -185.9) @ 01/02 05:52:00		AR12251 tracking									
01/02 17:57:00 - 01/03 05:59:54		Track (224.5, -186.0) @ 01/02 17:54:00		AR12251 tracking									
01/03 18:14:00 - 01/04 06:18:54		Track (431.7, -188.4) @ 01/03 18:11:00		AR12251 tracking									
PROG= 06 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 8		2-time(s)		2.0sec									
Open/G-band		Open/G-band		close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0 2.0sec
Subr= 2		2-time(s)		2.0sec									
Seqn= 24		1-time(s)		2.0sec									
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0 2.0sec
Open/G-band		Open/G-band		open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0 2.0sec
Seqn= 42		4-time(s)		2.0sec									
Al-poly/Open		thin-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0 2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec
Seqn= 62		20-time(s)		90.0sec									
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec
Al-poly/Open		thin-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1 2.0sec
Al-poly/Open		thin-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2 2.0sec
Al-poly/Open		thin-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-p															
Term		Pointing (x, y)		Comment											
12/30 18:28:00 - 12/30 18:40:54		Fixed (0.0, 0.0)		synoptic, shifted 21.0 min											
12/31 06:09:30 - 12/31 06:16:24		Fixed (0.0, 0.0)		synoptic, shifted 6.5 min											
12/31 18:03:00 - 12/31 18:09:54		Fixed (0.0, 0.0)		synoptic											
01/01 06:12:00 - 01/01 06:18:54		Fixed (0.0, 0.0)		synoptic, shifted 9.0 min											
01/01 18:00:00 - 01/01 18:06:54		Fixed (0.0, 0.0)		synoptic, shifted -3.0 min											
01/02 05:45:00 - 01/02 05:51:54		Fixed (0.0, 0.0)		synoptic, shifted -18.0 min											
01/02 17:47:00 - 01/02 17:53:54		Fixed (0.0, 0.0)		synoptic, shifted -16.0 min											
01/03 18:04:00 - 01/03 18:10:54		Fixed (0.0, 0.0)		synoptic, shifted 1.0 min											
01/04 06:22:00 - 01/04 07:23:00		Fixed (0.0, 0.0)		synoptic, shifted 19.0 min											
PROG= 03 1-time(s)															
Subr= 1		1-time(s)		12.0sec											
Seqn= 33		1-time(s)		4.0sec											
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 5		1-time(s)		2.0sec											
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)			DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)			DPCM	0	0	2.0sec
Seqn= 40		1-time(s)		4.0sec											
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A6B: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(4512/1024/1024)

Term			Pointing (x, y)				Comment								
01/03 06:03:00 - 01/03 06:09:54			Fixed (		0.0, 0.0)		synoptic								
PROG= 01 1-time(s)															
Subr= 1		1-time(s)		12.0sec											
Seqn= 33		1-time(s)		4.0sec											
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 5		1-time(s)		2.0sec											
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)			Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)			DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)			DPCM	0	0	2.0sec
Seqn= 40		1-time(s)		4.0sec											
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 77		1-time(s)		2.0sec											
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 6		1-time(s)		2.0sec											
└ Open/G-band		Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)			Q=90	0	0	2.0sec
└ Open/G-band		Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Subr= 2		1-time(s)		12.0sec											
Seqn= 46		1-time(s)		2.0sec											
└ Open/thick-Be		Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
Seqn= 25		1-time(s)		4.0sec											
└ Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
└ Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
Seqn= 21		1-time(s)		4.0sec											
└ Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
Seqn= 37		1-time(s)		2.0sec											
└ med-Al/Open		med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
└ med-Al/Open		med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
Seqn= 25		1-time(s)		4.0sec											
└ Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
└ Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
Seqn= 21		1-time(s)		4.0sec											
└ Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
Seqn= 37		1-time(s)		2.0sec											
└ med-Al/Open		med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
└ med-Al/Open		med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
Seqn= 46		1-time(s)		2.0sec											
└ Open/thick-Be		Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	384x384 (1064, 1048)			Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval	

XOB #19C7: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 180s cadence, G-band - 384x384 45ms

Term			Pointing (x, y)							Comment						
01/03 06:13:00 - 01/03 18:00:54			Track (		0.0, -810.9)		@ 01/03 06:10:00			South Corona Hole Observation						
PROG= 11 Inf.-time(s)																
┌	Subr= 1		1-time(s)		2.0sec											
	└ Seqn= 90		2-time(s)		2.0sec											
			Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
	Subr= 2		1-time(s)		2.0sec											
	└ Seqn= 82		1-time(s)		30.0sec											
			Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec
	Subr= 3		25-time(s)		2.0sec											
	└ Seqn= 57		1-time(s)		180.0sec											
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec
			Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

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Flare mode

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XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
12/30 09:44:32 - 12/30 12:10:49	Track (-118.5, -120.2) @ 12/30 09:33:30	SP mosaic obs. at DC 1/2
12/30 12:13:55 - 12/30 14:45:30	Track (161.9, -120.0) @ 12/30 12:10:55	SP mosaic obs. at DC 2/2
12/30 15:03:00 - 12/30 17:57:30	Track (-439.6, -195.8) @ 12/30 15:00:00	AR12251 tracking
12/30 18:44:00 - 12/31 06:06:24	Track (-409.6, -194.8) @ 12/30 18:41:00	AR12251 tracking
12/31 06:19:30 - 12/31 17:59:54	Track (-311.9, -192.0) @ 12/31 06:16:30	AR12251 tracking
12/31 18:13:00 - 01/01 06:08:54	Track (-207.5, -189.6) @ 12/31 18:10:00	AR12251 tracking
01/01 06:22:00 - 01/01 17:31:30	Track (-98.1, -187.7) @ 01/01 06:19:00	AR12251 tracking
01/01 18:10:00 - 01/02 05:41:54	Track (9.4, -186.4) @ 01/01 18:07:00	AR12251 tracking
01/02 05:55:00 - 01/02 17:43:54	Track (116.4, -185.9) @ 01/02 05:52:00	AR12251 tracking
01/02 17:57:00 - 01/03 05:59:54	Track (224.5, -186.0) @ 01/02 17:54:00	AR12251 tracking
01/03 06:13:00 - 01/03 18:00:54	Track (0.0, -810.9) @ 01/03 06:10:00	South Corona Hole Observation
01/03 18:14:00 - 01/04 06:18:54	Track (431.7, -188.4) @ 01/03 18:11:00	AR12251 tracking
PROG= 19 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 15 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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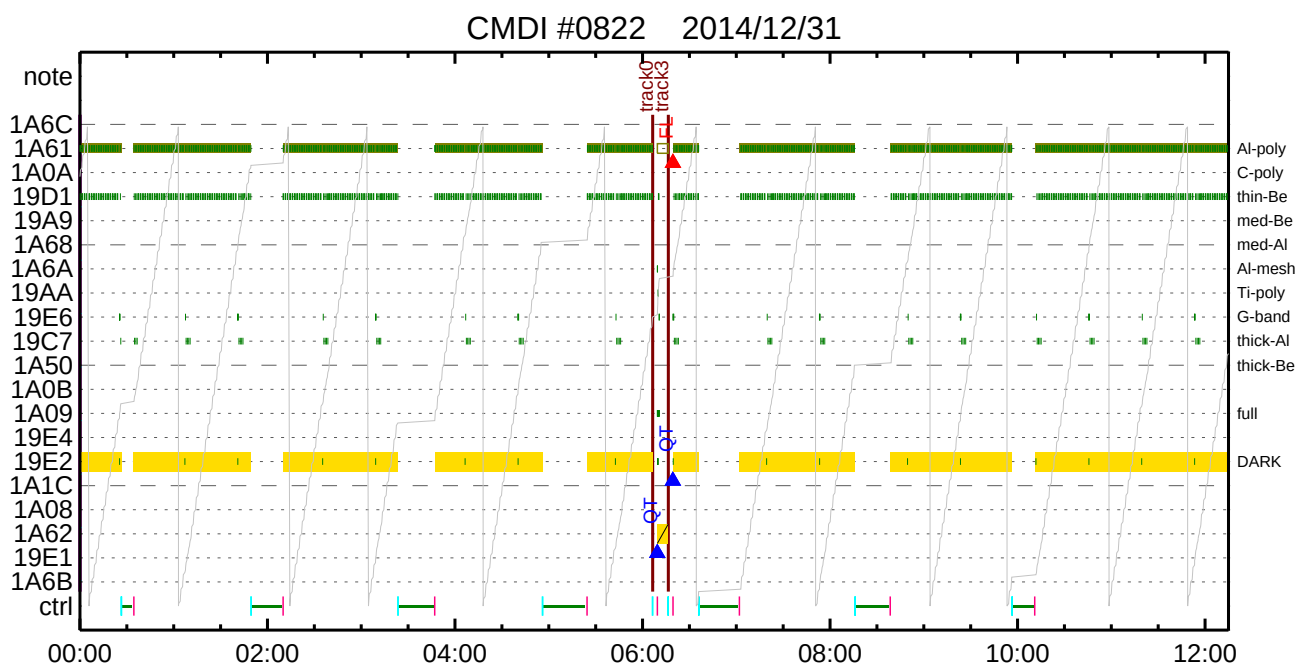
Flare Detection

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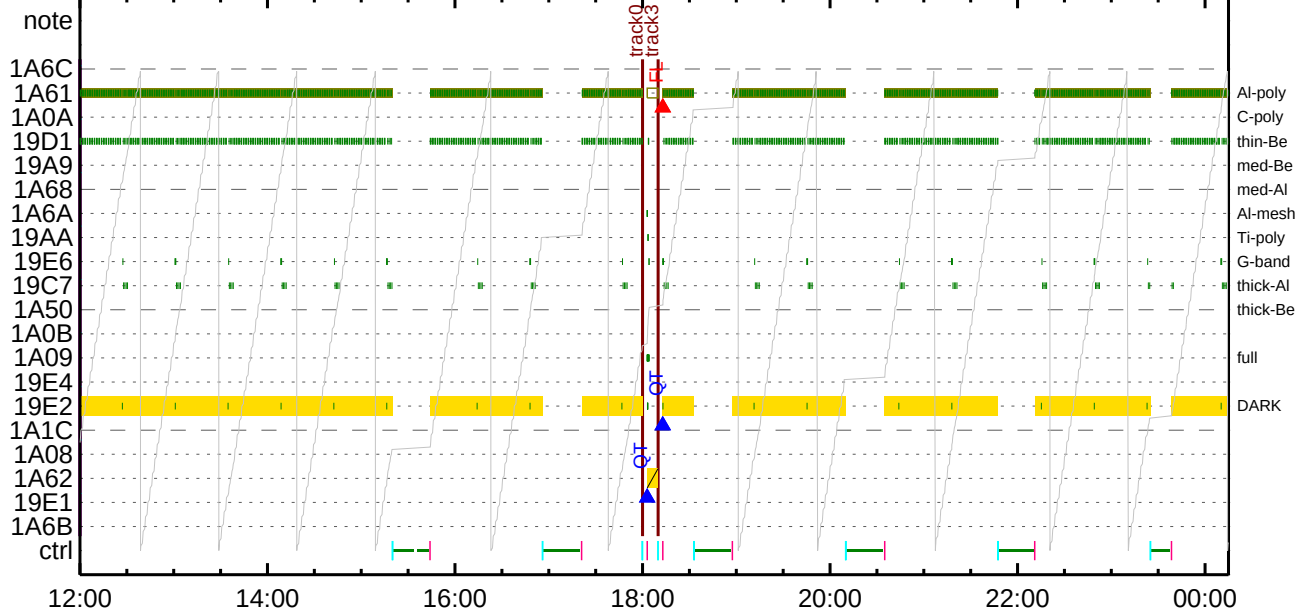
FLD Patrol											
Term	Pointing (x, y)							Comment			
12/30 18:41:18 - 12/31 06:06:48	Track (-409.6, -194.8) @ 12/30 18:41:00							AR12251 tracking			
12/31 06:16:48 - 12/31 18:00:18	Track (-311.9, -192.0) @ 12/31 06:16:30							AR12251 tracking			
12/31 18:10:18 - 01/01 06:09:18	Track (-207.5, -189.6) @ 12/31 18:10:00							AR12251 tracking			
01/01 06:19:18 - 01/01 17:57:18	Track (-98.1, -187.7) @ 01/01 06:19:00							AR12251 tracking			
01/01 18:07:18 - 01/02 05:42:18	Track (9.4, -186.4) @ 01/01 18:07:00							AR12251 tracking			
01/02 05:52:18 - 01/02 17:44:18	Track (116.4, -185.9) @ 01/02 05:52:00							AR12251 tracking			
01/02 17:54:18 - 01/03 06:00:18	Track (224.5, -186.0) @ 01/02 17:54:00							AR12251 tracking			
01/03 06:10:18 - 01/03 18:01:18	Track (0.0, -810.9) @ 01/03 06:10:00							South Corona Hole Observation			
01/03 18:11:18 - 01/04 06:19:18	Track (431.7, -188.4) @ 01/03 18:11:00							AR12251 tracking			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

note	before Tbl up	after Tbl up	ctrl
20	1A65	1A6C	
19	1A61	1A61	Al-poly
18	1A0A	1A0A	C-poly
17	19D1	19D1	thin-Be
16	19A9	19A9	med-Be
15	1A68	1A68	med-Al
14	1A6A	1A6A	Al-mesh
13	19AA	19AA	Ti-poly
12	19E6	19E6	G-band
11	19F9	19C7	thick-Al
10	1A50	1A50	thick-Be
9	1A0B	1A0B	
8	1A09	1A09	
7	19E4	19E4	full
6	19E2	19E2	
5	1A1B	1A1C	DARK
4	1A08	1A08	
3	1A62	1A62	
2	19E1	19E1	
1	1A6B	1A6B	

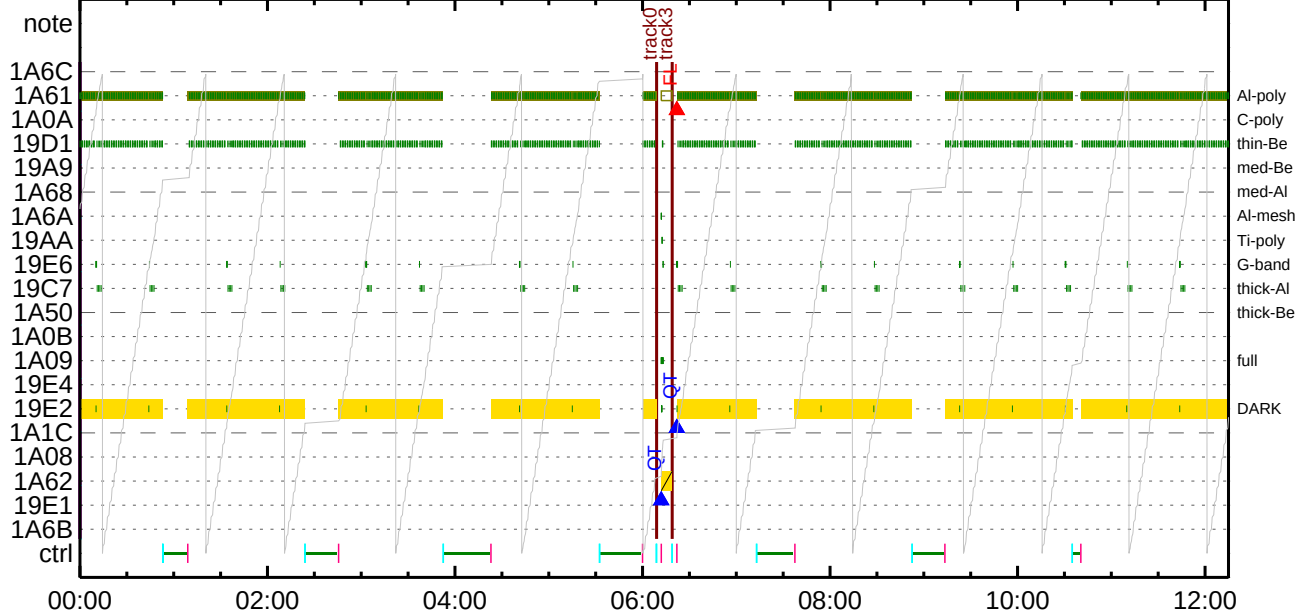
Table upload : 3rd pass (2014/12/30 06:32:20)



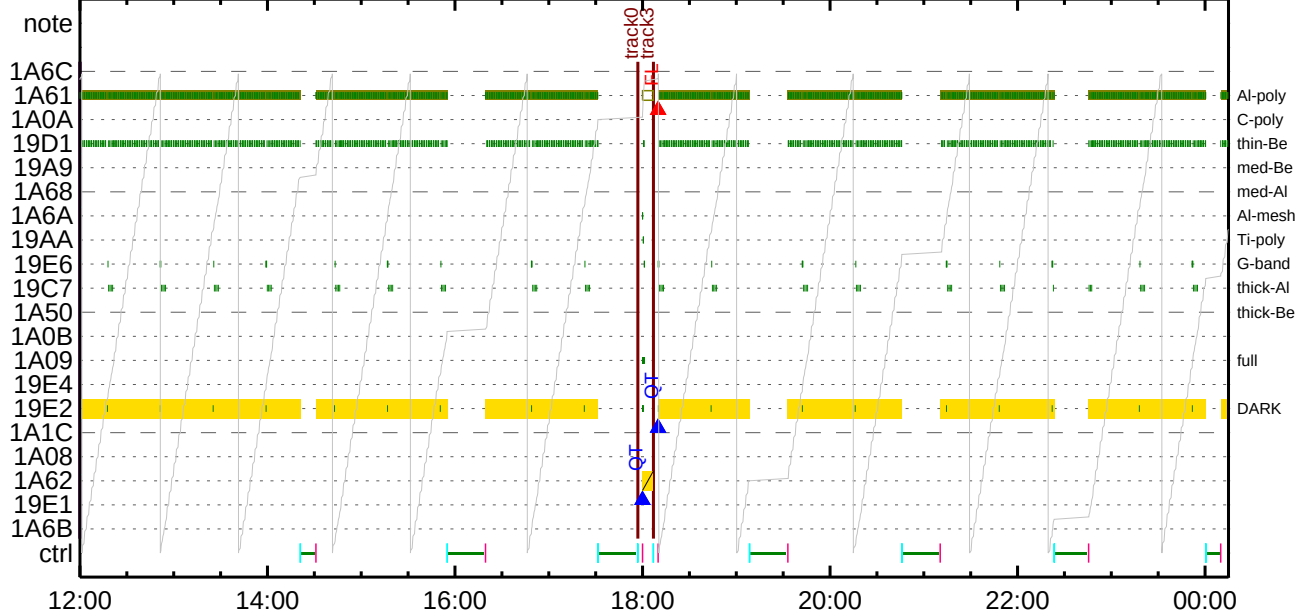
CMDI #0822 2014/12/31



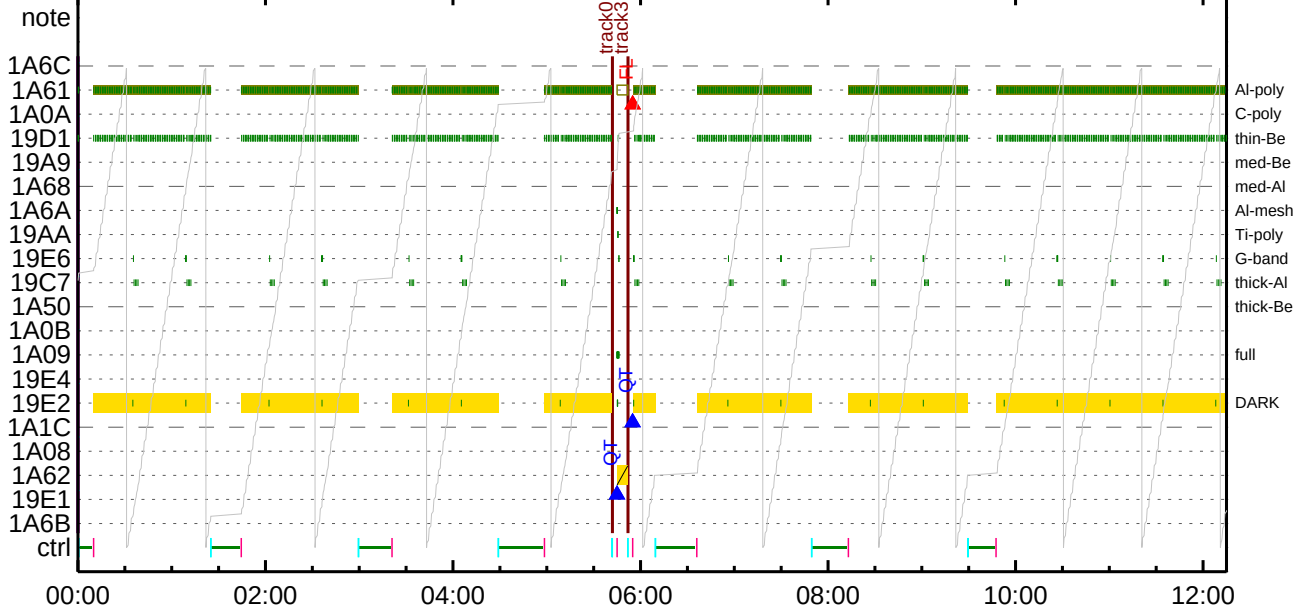
CMDI #0822 2015/01/01



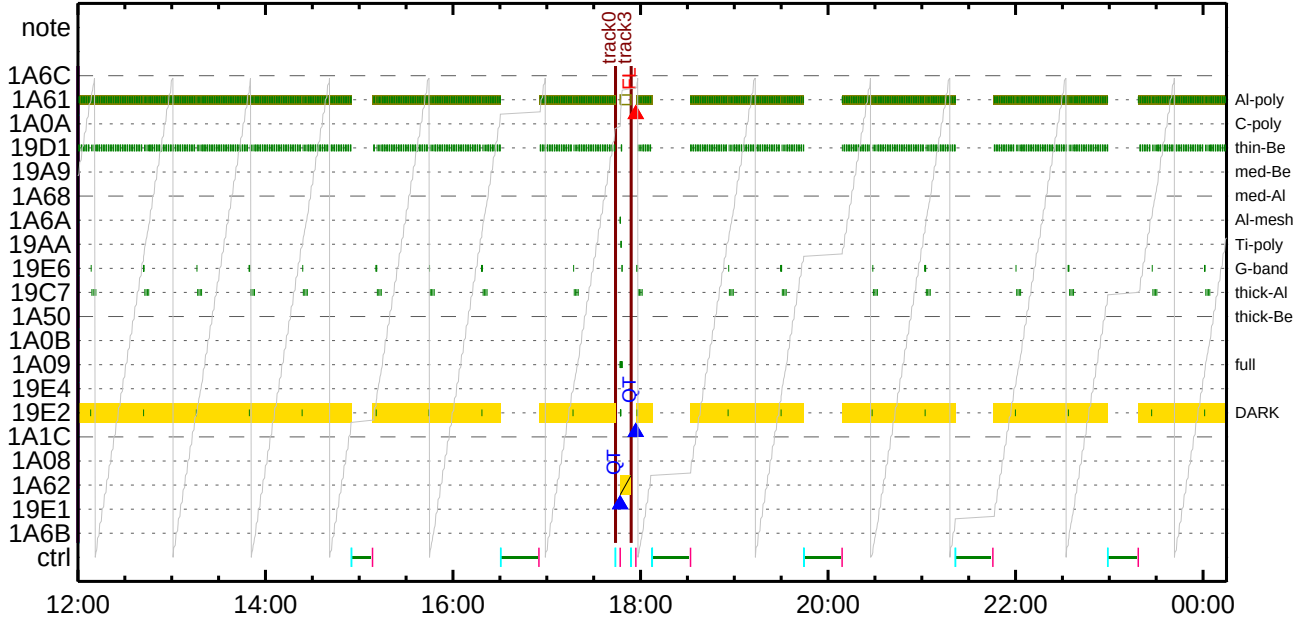
CMDI #0822 2015/01/01



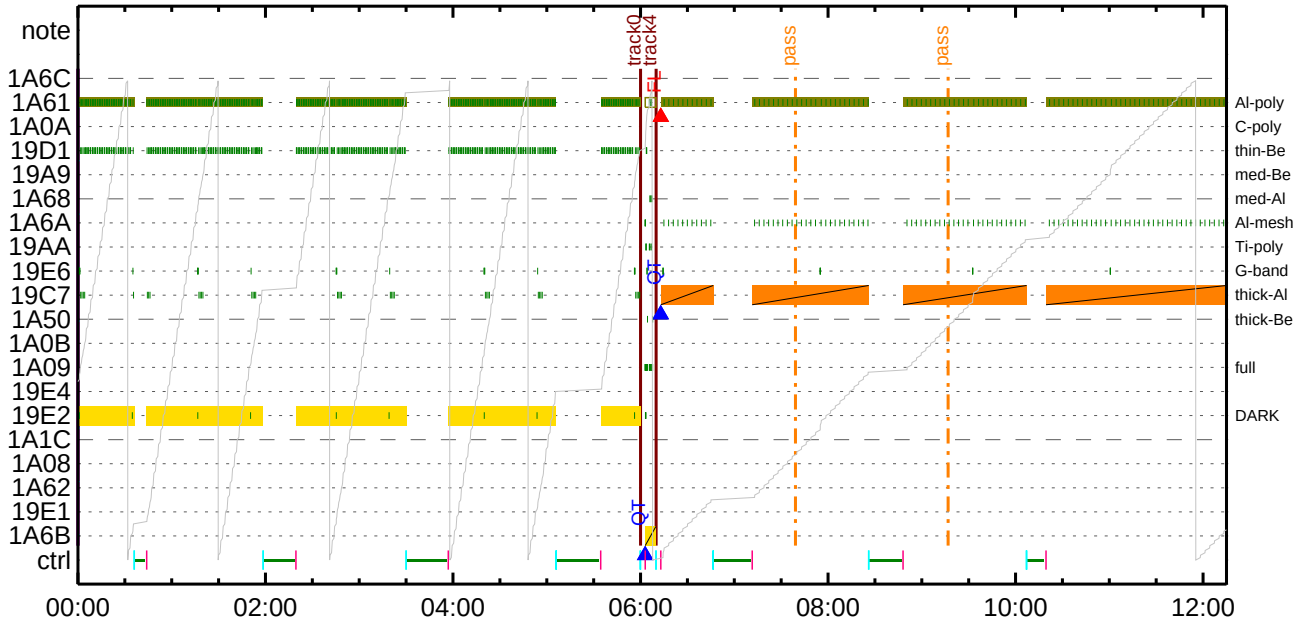
CMDI #0822 2015/01/02



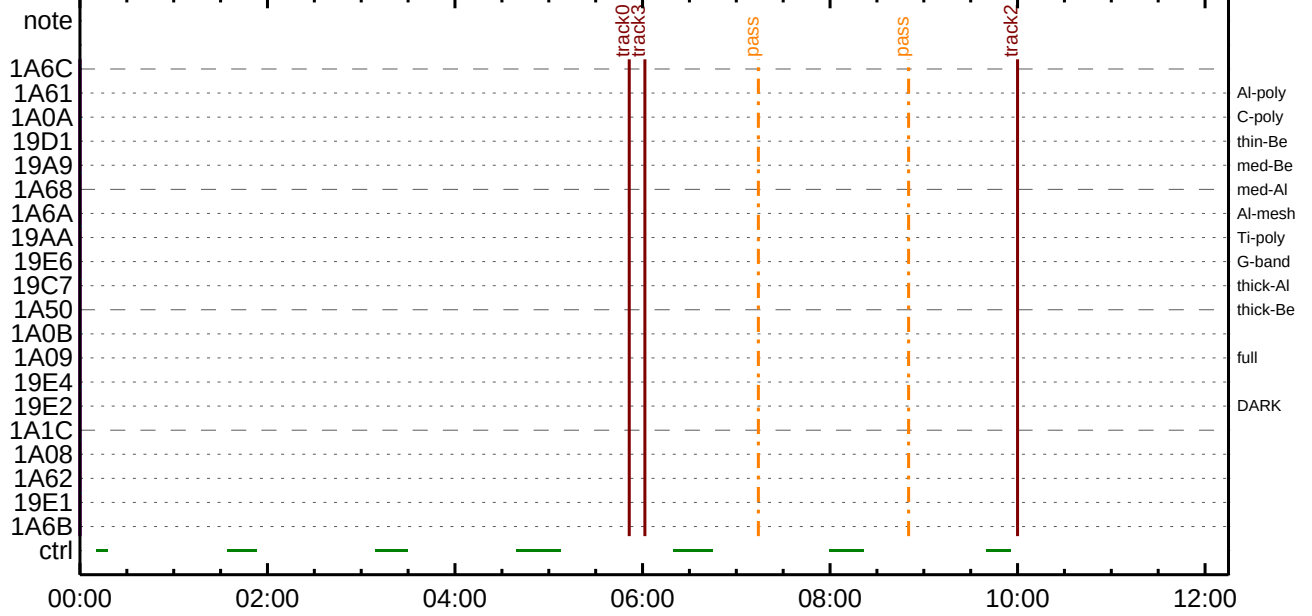
CMDI #0822 2015/01/02



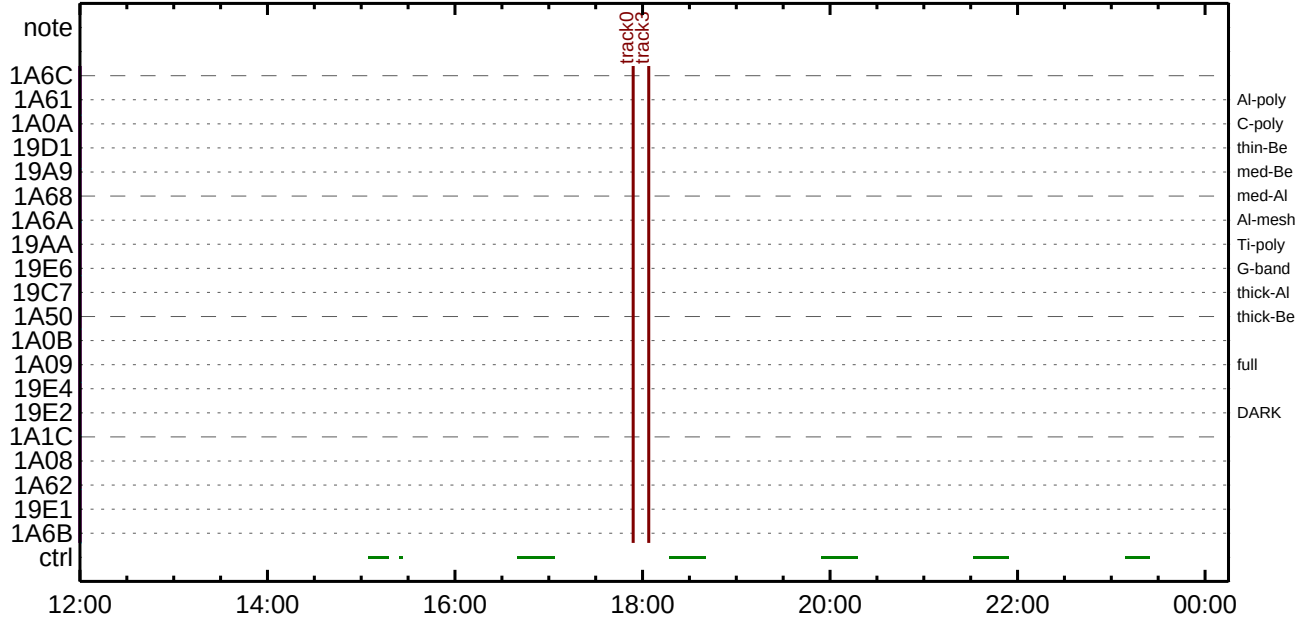
CMDI #0822 2015/01/03



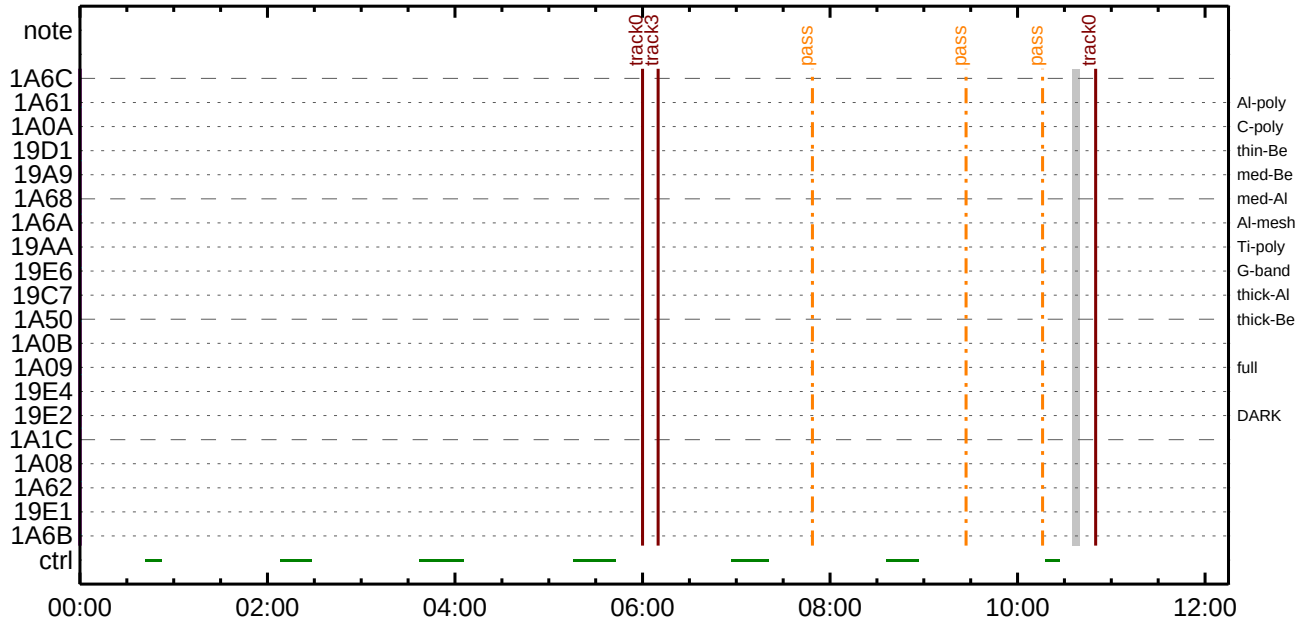
CMDI #0822 2015/01/05



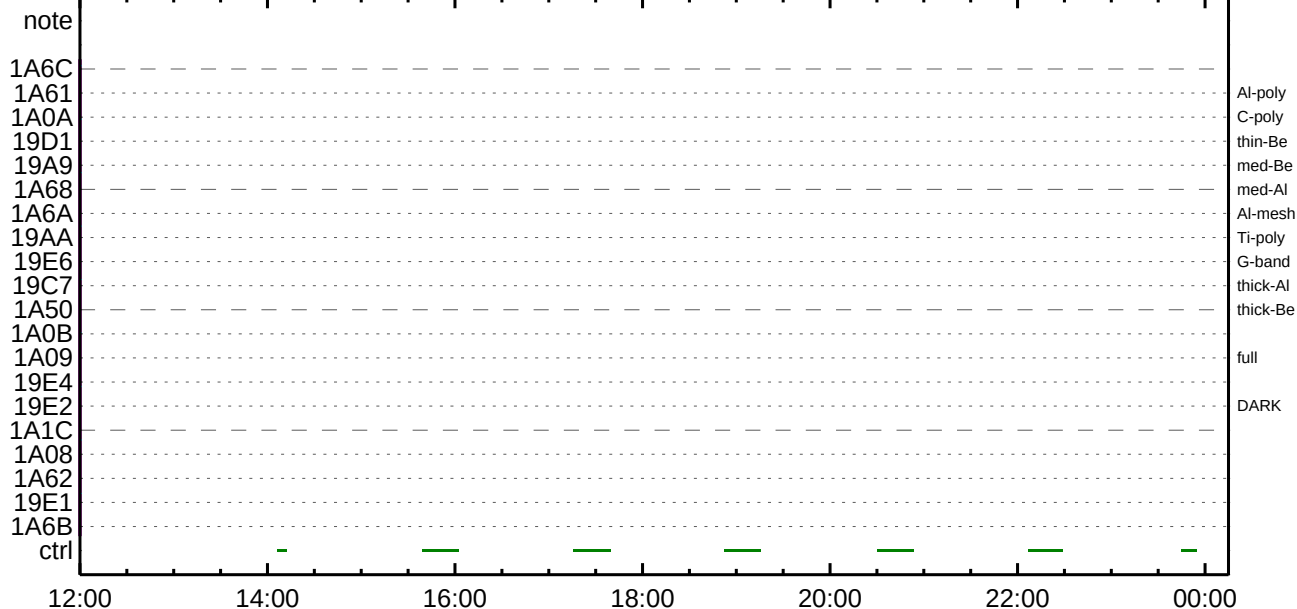
CMDI #0822 2015/01/05



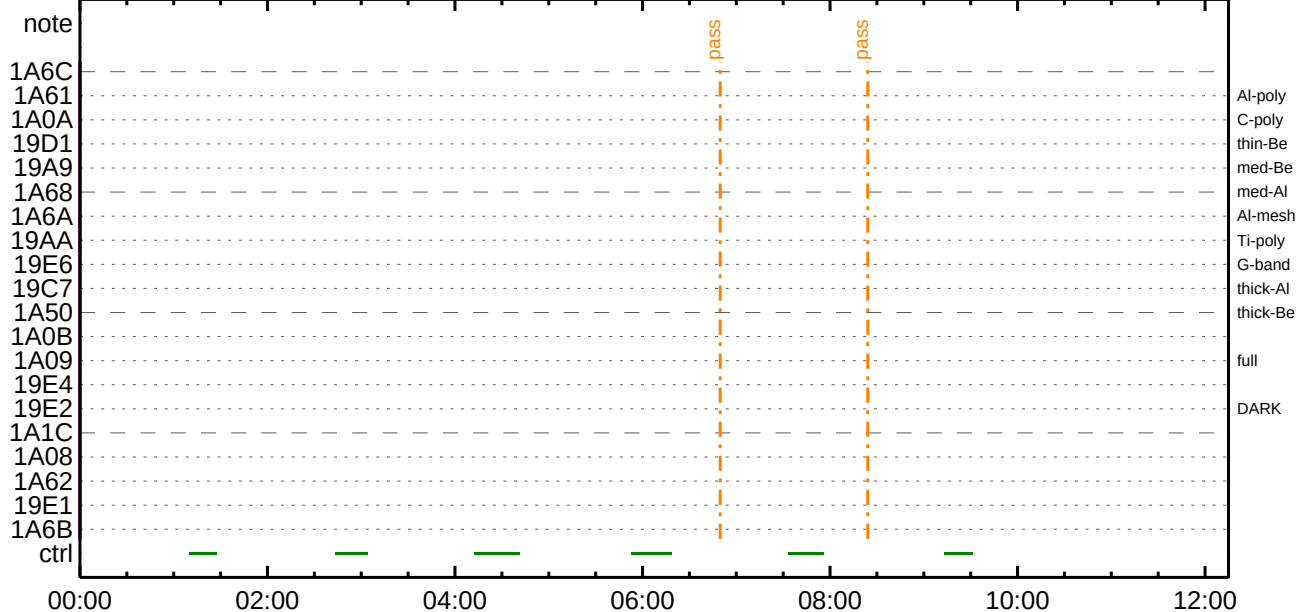
CMDI #0822 2015/01/06



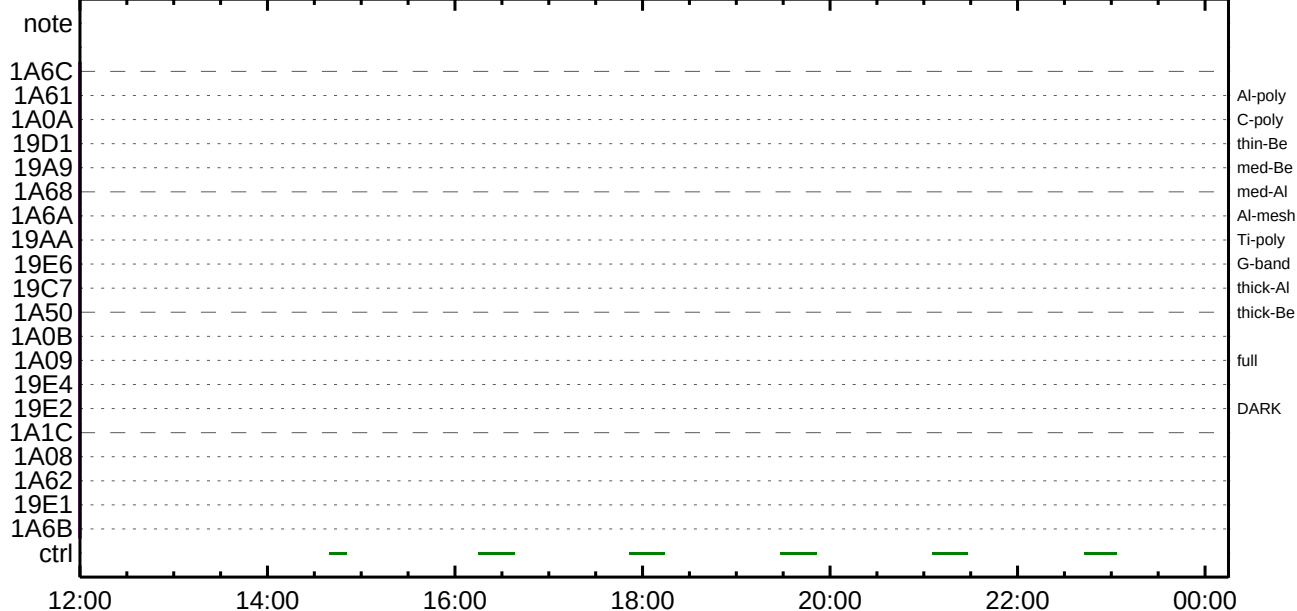
CMDI #0822 2015/01/06



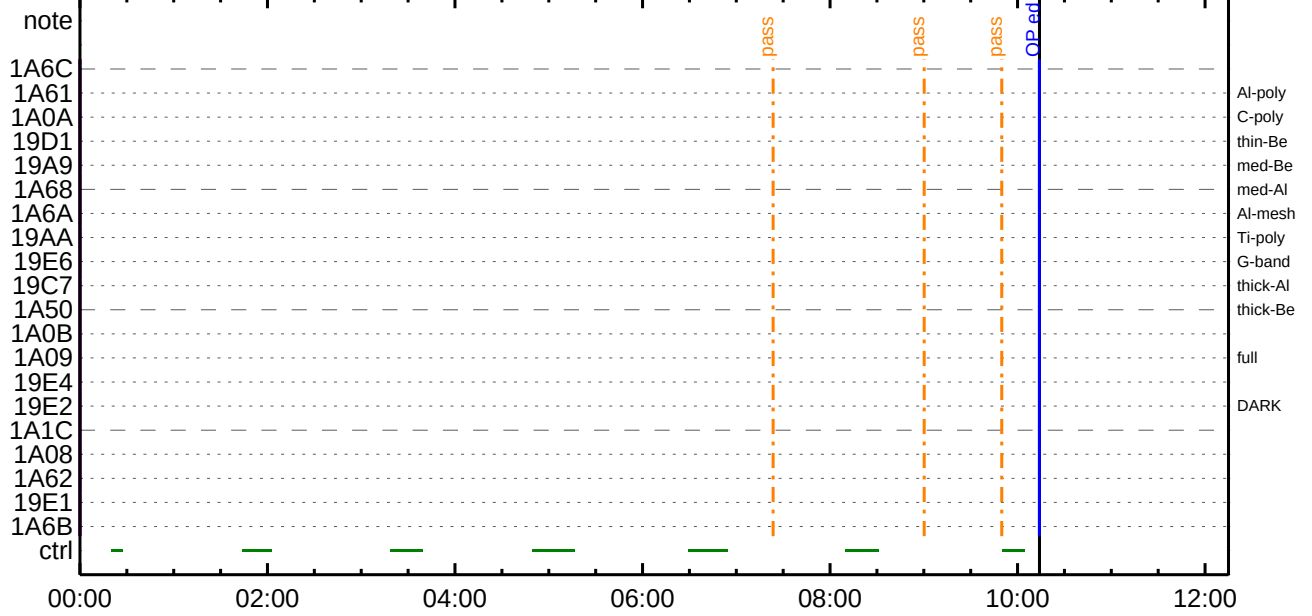
CMDI #0822 2015/01/07



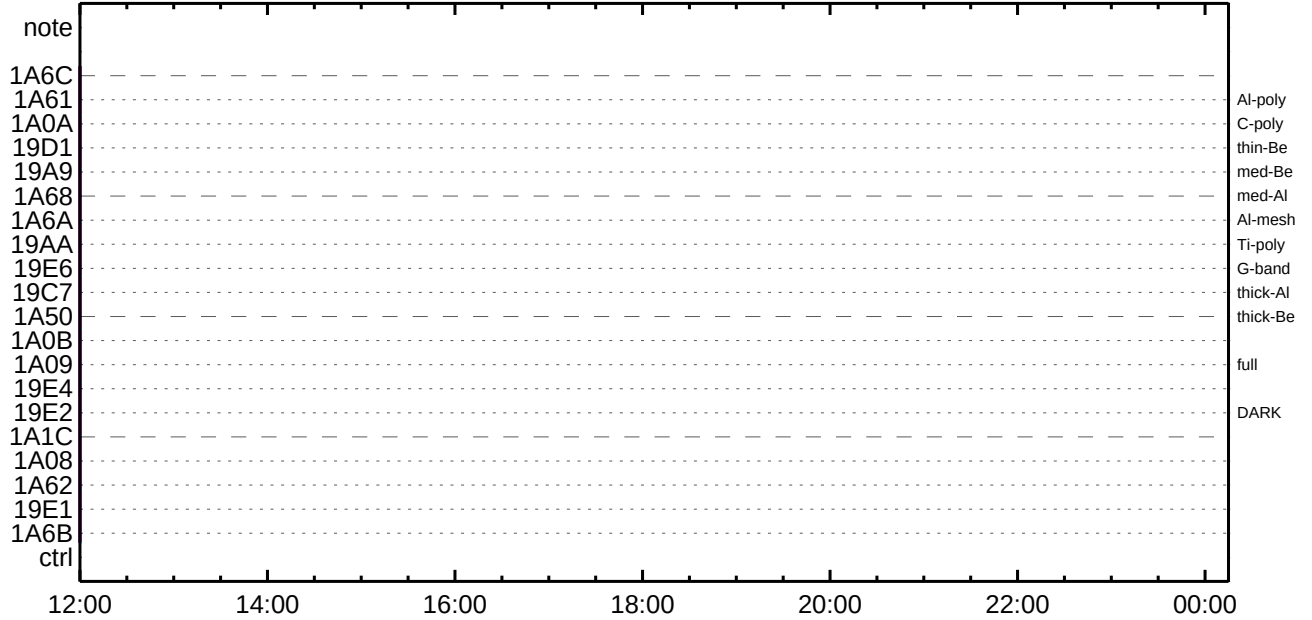
CMDI #0822 2015/01/07



CMDI #0822 2015/01/08



CMDI #0822 2015/01/08



0194	C.			
0195	+. TI	2014-12-30 09:11:59.5		
0196	DC	01-B2 DHU_OP_START		
0197	C.	☐☐[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0198	C.			
0199	C.	°Ê¼☐İÄê%İİŇ☐İ¥Á¥\$¥Ä¥¹äİÜ		
0200	C.	☐☐[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0201	C.	☐☐[HK1_TI_CMD_NUM]	EQ	4
0202	C.	☐☐[HK1_NEXT_EXEC_PIM]	EQ	DHU
0203	C.	☐☐[HK1_NEXT_EXEC_DC]	EQ	0xB3
0204	C.			
0205	C.	*****		
0206	C.	TIİİ°è¥Ä¥0¥x		
0207	C.	*****		
0208	C.			
0209	C.	TI_TBL(0x03AB00-0x03AEFF;S 1024byte)		
0210	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0211	BC	(03 ab 03 01 02)		
0212	C.	☐☐[HK1_DMP_TOP_ADRS_1]	EQ	07
0213	C.	☐☐[HK1_DMP_TOP_ADRS_0]	EQ	2B
0214	C.	☐☐[HK1_DMP_BLOCK_NUM]	EQ	3
0215	C.	☐☐[HK1_DMP_REPEAT_NUM]	EQ	0
0216	C.	☐☐[HK1_DMA_DMP_PIM]	EQ	DHU
0217	+. DC	01-22 DHU_MODE_CHNG		
0218	BC	(07 0b f8)		
0219	C.	☐☐[HK1_PKT_FORM_NO]	EQ	7
0220	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.25 s
0221	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32k
0222	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0223	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	EXEC
0224	C.			
0225	C.	¥Ä¥0¥x¥½ªİ»☐0³İÇ\$		
0226	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	NON
0227	C.			
0228	C.	RAM ID=TI_TBL☐İ¥Ê¹Ç•è²İOK☐0³İÇ\$		
0229	C.			
0230	C.	DHU¥âİ;¥ÉİÊ¼Ý½, ¥İİ;¥ÊİÊ☐0İä☐¹		
0231	+. DC	01-22 DHU_MODE_CHNG		
0232	BC	(02 0a f8)		
0233	C.	☐☐[HK1_PKT_FORM_NO]	EQ	2
0234	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.5S
0235	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32K
0236	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0237	C.			
0238	C.	*****		
0239	C.	SOT TI command set		
0240	C.	*****		
0241	C.	Execute, after the success of OP upload.		
0242	+. TI	2014-12-30 09:11:16.0		
0243	DC	07-F0 MDP_SOT_MODE_STBY		
0244	BC	(41)		
0245	C.	-----		
0246	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0247	C.	-----		
0248	C.	***** SOT END *****		
0249	C.	Stop EIS observation and temporarily disable EIS mode changes		
0250	C.			
0251	C.			
0252	C.	***** Start EIS operation (TI set) *****		
0253	C.	Execute, after the success of OP upload.		
0254	C.	Set EIS TI-commands		
0255	+. TI	2014-12-30 09:11:30.0		
0256	DC	07-FC EIS_MODE_MANU		
0257	BC	(21 02)		
0258	+. TI	2014-12-30 09:11:40.0		
0259	DC	07-FC EIS_MODE_CHG_DIS		
0260	BC	(22)		
0261	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0262	C.	***** End EIS operation (TI set) *****		
0263	C.			
0264	C.			
0265	C.			
0266	C.	***** XRT START *****		
0267	C.	Execute, after the success of OP upload.		
0268	+. TI	2014-12-30 09:11:00.0		
0269	DC	07-F0 MDP_XRT_MODE_STBY		
0270	BC	(c3)		
0271	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0272	C.			
0273	C.	***** XRT END *****		
0274	C.			
0275	C.	***** MDP ´ÜÄİ☐İ»0¼Ý☐ĒÄĐ☐¹☐ēDCBC•x²è *****		
0276	C.	(%â°İ¥0¥Ä¥Ê¥½¥Ê¥â¥Ç¥ē☐Ē¼☐☐¥Ä»Ü☐¹☐ē)		
0277	S.	DC-BC dcbc-402:DCBC		
0278		(MDP_known_event)		
0279	C.			
0280	C.			
0281	C.	***** ¥Đ¥¹•İ Daily±çİŇ☐Ē´Đ☐¹☐ēDCBC•x²è *****		
0282	S.	DC-BC dcbc-153:DCBC		
0283		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0284	C.			
0285	C.			
0286	C.	İäLOS¥Ä¥\$¥Ä¥¼Ä»Üİä		
0287	C.			
0288	C.	***** LOS *****		
0289	C.			

(a) Spacecraft Operation Procedure (real-commands)

```
main-846 2014-12-30 13:05:27 168 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿Aα•μ°Æ»İ×AÇ¿İ¥Ç¥A¥×¥ıı;¼¥ÉiĒĒē½μ•İİĒiĒē¼°ÇÖα•α¿¼ı¹Ç¿İjÇÀ®, ùα¹αēαβαÇÁ÷¿®α•αĒααα³αĒi£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 C.
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 552468.1 +- 1.0 (s) [ ]
0052 C.
0053 C.
0054 . C. *****
0055 C. SOT table upload
0056 C. *****
0057 . C. < Stop FG table >
0058 +. DC 07-F0 MDP_FG_CTRL_MANU
0059 BC (51)
0060 C. -----
0061 C. MDP_FG_CTRL_MODE = MANU [ ]
0062 C. -----
0063 C.
0064 . C. <Upload FG Observation Table>
0065 . S. RAM ram-269:MDP_OBS_F
0066 C.
0067 C.
0068 . C. < Dump RAMID=MDP_OBS_F >
0069 +. DC 07-F0 MDP_DUMP_FGTBL
0070 BC (82 07 00 00 00 38 b8)
0071 C. -----
0072 C. MDP_OBS_F verify = OK/NG [ ]
0073 C. -----
0074 C.
0075 . C. < Stop SP table >
0076 +. DC 07-F0 MDP_SP_CTRL_MANU
0077 BC (61)
0078 C. -----
0079 C. MDP_SP_CTRL_MODE = MANU [ ]
0080 C. -----
0081 C.
0082 . C. <Upload SP Observation Table>
0083 . S. RAM ram-289:MDP_OBS_S
0084 C.
0085 C.
0086 . C. < Dump RAMID=MDP_OBS_S >
0087 +. DC 07-F0 MDP_DUMP_SPTBL
0088 BC (83 07 00 00 00 38 b8)
0089 C. -----
0090 C. MDP_OBS_S verify = OK/NG [ ]
0091 C. -----
0092 C.
0093 . C. < Upload DPL table >
0094 C.
0095 C. ¥Ç¥Ã¥×¥ıı;¼¥ÉİÁ°αĒSTS_CHKαòOFFαĒēα¹αē
```

```

0096 C.
0097 . S. RAM ram-271:MDP_DPL
0098 ( )
0099 C.
0100 . C. < Dump RAMID=MDP_DPL >
0101 +. DC 07-F0 MDP_DUMP_FGTBL
0102 BC (82 07 00 38 b8 00 40)
0103 C. -----
0104 C. MDP_DPL verify = OK [ ]
0105 C. -----
0106 C.
0107 C. STS_CHK=0N=Ë=ë
0108 C.
0109 . C. < Update MDP DSC PAR1 >
0110 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0111 BC (4c)
0112 C. MDP_CMD_CODE = F04C0700[ ]
0113 C. MDP_CMD_CNT (count-up 1) [ ]
0114 C. -----
0115 C.
0116 . C.
0117 C. *****
0118 C. SOT TI command set
0119 C. *****
0120 C. Execute, after the success of TBL upload.
0121 +. TI 2014-12-30 09:11:18.0
0122 DC 07-F0 MDP_SOT_MODE_OBSV
0123 BC (40)
0124 C. -----
0125 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0126 C. -----
0127 C.
0128 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0129 +. DC 07-FC EIS_MODE_CHG_ENA
0130 BC (20)
0131 . C. Verify EIS_MODE_CHG_FLG is ENA
0132 +. DC 07-FC EIS_MODE_MANU
0133 BC (21 02)
0134 . C. Verify EIS in MANUAL mode
0135 . C. Estimated OBSTBL upload time is 49s
0136 C. *****
0137 C. EIS START OBSTBL LOAD
0138 C. *****
0139 . S. RAM ram-820:EIS_OBSTBL
0140 ( )
0141 +. DC 07-FC EIS_DUMP_OBSTBL
0142 BC (07 07 07 00 00 70 00)
0143 C.
0144 C. Execute, after the success of OBSTBL upload.
0145 C. Set EIS TI-commands
0146 +. TI 2014-12-30 09:11:50.0
0147 DC 07-FC EIS_MODE_CHG_ENA
0148 BC (20)
0149 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0150 C. *****
0151 C. EIS END OBSTBL LOAD
0152 C. *****
0153 C.
0154 . C. ***** MDP ´0ÃÎ=Î»0%Y=ÊÃ0=1=èDCBC•x²è *****
0155 C. (%ã°i¥0¥Ã¥Ê¥P¥Ê¥ã¥¢¥è=Ê½¼=¼¥Ã»Û=1=è)
0156 . S. DC-BC dcbc-402:DCBC
0157 (MDP_known_event)
0158 C.
0159 C.
0160 . C. ***** ¥0¥1•Î Daily±¿ÎÑ=Ë´0=1=èDCBC•x²è *****
0161 . S. DC-BC dcbc-153:DCBC
0162 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0163 C.
0164 C.
0165 . C. iãLOS¥Á¥S¥Ã¥¬¼Ã»Ûiã
0166 C.
0167 . C. ***** LOS *****
0168 C.

```



```
0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ()
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 0c)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 13)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2014-12-30 09:11:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP '0AÎ0Î»0%Ý0ÊÂ0010èDCBC•x2è *****
0169 C. (%â°i¥0¥Á¥Ê¥Ð¥Ê¥â¥Ç¥è0Ê½%00¥Â»Û010è)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥Ð¥1•İ Daily±¿ÎÑ0Ê'0010èDCBC•x2è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥S¥Ã¥¬¼Â»Û;ä
0180 C.
0181 . C. ***** LOS *****
0182 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-848 2014-12-30 13:05:27 29 33 SOLAR-B MAIN //
0001      C.
0002      . C. ***** AOS *****
0003      C.
0004      . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005      C.
0006      C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008      C.
0009      . C. ***** AOCS : Reload orbital element (send every contact) *****
0010      C.   ĀījĒ¿□Å□•μ°£»Ī×AÇ□Ī¥¢¥Á¥×¥īj¼¥ĒjĒĒē½μ•īĪĒjĒēĒ¼°ÇŌ□•□¿¼ī¹Ç□ĪjÇÀ®, ù□¹□ē□□□ÇÁ÷¿®□•□Ē□□□³□Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012      C.
0013      C.
0014      C.
0015      . C. ***** MDP `ŪĀĪ□Ī»Ō¼Ÿ□ĒĒĀŌ□¹□ēDCBC•×²ē *****
0016      C. (%ā°Ī¥Ō¥Ā¥Ē¥P¥Ē¥ā¥¢¥ē□Ē¼¼□□Ā»Ū□¹□ē)
0017      . S. DC-BC dcbc-402:DCBC
0018      (MDP_known_event)
0019      C.
0020      C.
0021      . C. ***** ¥Ō¥¹•Ī Daily±¿ĪĪŃ□Ē'Ō□¹□ēDCBC•×²ē *****
0022      . S. DC-BC dcbc-153:DCBC
0023      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024      C.
0025      C.
0026      . C. iãLOS¥Á¥S¥Ã¥¼Á»Üjã
0027      C.
0028      . C. ***** LOS *****
0029      C.
```

Dec 30, 14 13:05

XRT_OGLIST_0822.chk

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*** OP Sequence for XRT ***

2014/12/30	09:19:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	09:19:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	09:19:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/30	09:19:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/30	09:22:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2014/12/30	09:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/30	09:33:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 00 00 0b 64
2014/12/30	09:37:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	09:37:28.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	09:37:30.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2014/12/30	09:41:30.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/12/30	09:41:50.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/12/30	09:41:52.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/12/30	09:41:54.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/12/30	09:41:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/12/30	09:41:58.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/30	09:44:28.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2014/12/30	09:44:30.0	XRT_FL_PROG_SET_401_OG [0x191]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2014/12/30	09:44:32.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/30	12:10:49.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	12:10:51.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	12:10:53.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/12/30	12:10:55.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 00 00 f4 9c
2014/12/30	12:11:13.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/12/30	12:11:15.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/12/30	12:11:17.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/12/30	12:11:19.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/12/30	12:11:21.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/30	12:13:51.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2014/12/30	12:13:53.0	XRT_FL_PROG_SET_401_OG [0x191]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2014/12/30	12:13:55.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/30	14:45:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	14:45:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	14:45:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/30	14:45:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/30	14:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/30	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/12/30	15:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2014/12/30	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/12/30	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/12/30	15:00:22.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/12/30	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/12/30	15:00:26.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/30	15:02:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]			

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2014/12/30	15:02:58.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2014/12/30	15:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2014/12/30	16:20:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/30	16:20:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	16:20:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	16:20:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/30	16:23:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/12/30	16:44:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/12/30	16:45:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2014/12/30	17:57:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/30	17:57:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	17:57:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	17:57:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/30	18:00:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/12/30	18:20:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/12/30	18:20:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	18:20:58.0	XRT_FOCUS_RECALIBRATE_445_0G [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	18:21:00.0	AOCs_ORe-point_Start_4_0G [0x09a]	XRT_FOCUS_RECAL	2	07-F8	78	00
2014/12/30	18:24:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	AOCU_NM	5	02-76	00	00 00 00 00
2014/12/30	18:25:18.0	XRT_FLD_DIS_422_0G [0x1a6]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2014/12/30	18:27:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/12/30	18:27:56.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/12/30	18:27:58.0	XRT_QT_PROG_SET_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/12/30	18:28:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2014/12/30	18:40:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/30	18:40:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	18:40:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	18:41:00.0	AOCs_ORe-point_Start_1_0G [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/12/30	18:41:18.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	03	00 00 00 00
2014/12/30	18:41:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/12/30	18:41:22.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/12/30	18:41:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/12/30	18:41:26.0	XRT_FLD_RESET_407_0G [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/12/30	18:43:56.0	XRT_QT_PROG_SET_442_0G [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/30	18:43:58.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2014/12/30	18:44:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2014/12/30	19:34:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/30	19:34:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	19:34:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	19:34:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/12/30	19:37:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/12/30	19:58:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/12/30	19:59:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2014/12/30	21:12:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/12/30	21:12:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	21:12:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/12/30	21:12:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2014/12/30	21:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/30	21:35:01.0	XRT_Custom_430_OG [0x1ae]			
2014/12/30	21:36:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/30	22:49:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	22:49:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/30	22:49:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/30	22:49:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/30	22:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/30	23:09:30.0	XRT_Custom_430_OG [0x1ae]			
2014/12/30	23:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	00:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	00:26:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	00:26:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/31	00:26:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/31	00:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/31	00:33:30.0	XRT_Custom_430_OG [0x1ae]			
2014/12/31	00:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	01:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	01:49:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	01:49:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/31	01:49:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/31	01:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/31	02:09:00.0	XRT_Custom_430_OG [0x1ae]			
2014/12/31	02:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	03:23:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	03:23:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	03:23:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/31	03:23:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/31	03:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/31	03:46:00.0	XRT_Custom_430_OG [0x1ae]			
2014/12/31	03:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	04:56:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	04:56:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	04:56:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/31	04:56:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/31	04:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/31	05:23:30.5	XRT_Custom_430_OG [0x1ae]			
2014/12/31	05:24:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	06:06:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	06:06:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	06:06:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/12/31	06:06:30.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00
2014/12/31	06:06:48.0	XRT_FLD_DIS_422_OG [0x1a6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/12/31	06:09:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/12/31	06:09:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/12/31	06:09:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2014/12/31	06:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	06:16:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	06:16:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	06:16:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			

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2014/12/31	06:16:30.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	03	00	00	00
2014/12/31	06:16:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/12/31	06:16:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/12/31	06:16:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/12/31	06:16:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/12/31	06:16:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/31	06:19:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2014/12/31	06:19:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2014/12/31	06:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/31	06:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	06:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	06:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/31	06:36:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/12/31	06:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/12/31	07:01:00.0	XRT_Custom_430_OG [0x1ae]							
2014/12/31	07:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/31	08:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	08:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	08:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/31	08:16:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/12/31	08:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/12/31	08:37:30.0	XRT_Custom_430_OG [0x1ae]							
2014/12/31	08:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/31	09:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	09:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	09:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/31	09:56:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/12/31	09:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/12/31	10:10:00.0	XRT_Custom_430_OG [0x1ae]							
2014/12/31	10:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/31	15:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	15:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	15:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/31	15:20:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/12/31	15:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/12/31	15:43:00.0	XRT_Custom_430_OG [0x1ae]							
2014/12/31	15:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/31	16:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	16:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	16:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/12/31	16:56:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/12/31	16:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/12/31	17:20:00.0	XRT_Custom_430_OG [0x1ae]							
2014/12/31	17:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/12/31	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/12/31	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2014/12/31	18:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00	00	00
2014/12/31	18:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							

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2014/12/31	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/12/31	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/12/31	18:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/12/31	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2014/12/31	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/12/31	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2014/12/31	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/12/31	18:10:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/12/31	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/12/31	18:10:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/12/31	18:12:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/31	18:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2014/12/31	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2014/12/31	18:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	18:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	18:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	18:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/31	18:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/31	18:56:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/31	18:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2014/12/31	20:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	20:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	20:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	20:10:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/31	20:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/31	20:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/31	20:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2014/12/31	21:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	21:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	21:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	21:47:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/31	21:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/31	22:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/31	22:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2014/12/31	23:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/12/31	23:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	23:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/12/31	23:25:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/12/31	23:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/12/31	23:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/12/31	23:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2015/01/01	00:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	00:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	00:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	00:53:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01			MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2015/01/01	00:56:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	01:08:00.0	XRT_Custom_430_0G [0x1ae]			
2015/01/01	01:09:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	02:24:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	02:24:02.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	02:24:04.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	02:24:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/01	02:27:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	02:44:30.0	XRT_Custom_430_0G [0x1ae]			
2015/01/01	02:45:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	03:52:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	03:52:32.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	03:52:34.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	03:52:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/01	03:55:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	04:22:00.0	XRT_Custom_430_0G [0x1ae]			
2015/01/01	04:23:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	05:32:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	05:32:32.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	05:32:34.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	05:32:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/01	05:35:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	05:59:00.0	XRT_Custom_430_0G [0x1ae]			
2015/01/01	06:00:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	06:08:54.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	06:08:56.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	06:08:58.0	XRT_FOCUS_POSITION_403_0G [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/01	06:09:00.0	AOCS_Ore-point_Start_4_0G [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/01/01	06:09:18.0	XRT_FLD_DIS_422_0G [0x1a6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/01	06:11:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/01	06:11:56.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/01	06:11:58.0	XRT_QT_PROG_SET_438_0G [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/01/01	06:12:00.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	06:18:54.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	06:18:56.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	06:18:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/01	06:19:00.0	AOCS_Ore-point_Start_1_0G [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2015/01/01	06:19:18.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/01	06:19:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/01	06:19:22.0	XRT_AEC_RESET_413_0G [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/01	06:19:24.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/01	06:19:26.0	XRT_FLD_RESET_407_0G [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	06:21:56.0	XRT_QT_PROG_SET_442_0G [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/01/01	06:21:58.0	XRT_FL_PROG_SET_401_0G [0x191]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/01/01	06:22:00.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	07:13:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	07:13:02.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	07:13:04.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	07:13:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]			

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2015/01/01	07:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/01	07:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	07:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	08:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	08:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	08:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	08:52:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/01	08:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	09:12:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	09:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	10:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	10:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	10:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	10:35:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/01	10:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	10:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	10:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	14:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	14:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	14:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	14:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/01	14:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	14:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	14:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	15:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	15:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	15:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	15:55:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/01	15:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	16:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	16:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	17:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	17:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	17:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/01	17:31:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/01	17:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/01	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	17:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	17:56:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/01	17:57:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2015/01/01	17:57:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/01	17:59:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/01	17:59:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/01	17:59:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/01/01	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/01	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	18:06:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/01	18:06:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/01	18:07:00.0	AOCS_Ore-point_Start_1_OG [0x097]				

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2015/01/01	18:07:18.0	XRT_FLD_ENA_411_0G [0x19b]	5	02-76 03 00 00 00 00
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/01/01	18:07:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	1	07-F0 c8
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/01/01	18:07:22.0	XRT_AEC_RESET_413_0G [0x19d]	1	07-F0 d0
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/01/01	18:07:24.0	XRT_ARS_DIS_423_0G [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/01	18:07:26.0	XRT_FLD_RESET_407_0G [0x197]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/01	18:09:56.0	XRT_QT_PROG_SET_442_0G [0x1ba]	2	07-F0 c4 06
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 06
2015/01/01	18:09:58.0	XRT_FL_PROG_SET_401_0G [0x191]	2	07-F0 c5 13
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2015/01/01	18:10:00.0	XRT_CTRL_AUTO_408_0G [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/01	19:08:30.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/01	19:08:32.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/01	19:08:34.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/01	19:08:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/01	19:11:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/01	19:32:00.0	XRT_Custom_430_0G [0x1ae]		
2015/01/01	19:33:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/01	20:46:00.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/01	20:46:02.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/01	20:46:04.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/01	20:46:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/01	20:49:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/01	21:09:30.0	XRT_Custom_430_0G [0x1ae]		
2015/01/01	21:10:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/01	22:23:30.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/01	22:23:32.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/01	22:23:34.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/01	22:23:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/01	22:26:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/01	22:44:30.0	XRT_Custom_430_0G [0x1ae]		
2015/01/01	22:45:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	00:00:30.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	00:00:32.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	00:00:34.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	00:00:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	00:03:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/02	00:09:00.0	XRT_Custom_430_0G [0x1ae]		
2015/01/02	00:10:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	01:25:00.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	01:25:02.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	01:25:04.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	01:25:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	01:28:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/02	01:43:30.0	XRT_Custom_430_0G [0x1ae]		
2015/01/02	01:44:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	02:59:30.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	02:59:32.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	02:59:34.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	02:59:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	03:02:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/02	03:20:00.0	XRT_Custom_430_0G [0x1ae]		

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2015/01/02	03:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	04:29:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	04:29:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	04:29:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	04:29:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	04:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/02	04:57:30.0	XRT_Custom_430_OG [0x1ae]		
2015/01/02	04:58:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	05:41:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	05:41:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	05:41:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/01/02	05:42:00.0	AOCS_ORe-point_Start_4_OG [0x09a]		
		AOCU_NM	5	02-76 00 00 00 00 00
2015/01/02	05:42:18.0	XRT_FLD_DIS_422_OG [0x1a6]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2015/01/02	05:44:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2015/01/02	05:44:56.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/02	05:44:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 03
2015/01/02	05:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	05:51:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	05:51:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	05:51:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/01/02	05:52:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 03 00 00 00 00
2015/01/02	05:52:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/01/02	05:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/01/02	05:52:22.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/01/02	05:52:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/02	05:52:26.0	XRT_FLD_RESET_407_OG [0x197]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	05:54:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 06
2015/01/02	05:54:58.0	XRT_FL_PROG_SET_401_OG [0x191]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2015/01/02	05:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	06:09:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	06:09:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	06:09:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	06:09:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	06:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/02	06:35:00.0	XRT_Custom_430_OG [0x1ae]		
2015/01/02	06:36:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	07:49:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	07:49:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	07:49:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	07:49:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	07:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/02	08:12:00.0	XRT_Custom_430_OG [0x1ae]		
2015/01/02	08:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	09:29:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	09:29:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	09:29:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	09:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	09:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9

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2015/01/02	09:46:30.0	XRT_Custom_430_OG [0x1ae]		
2015/01/02	09:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	14:55:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	14:55:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	14:55:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	14:55:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	14:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/02	15:07:30.0	XRT_Custom_430_OG [0x1ae]		
2015/01/02	15:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	16:30:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	16:30:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	16:30:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	16:30:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	16:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/02	16:54:00.0	XRT_Custom_430_OG [0x1ae]		
2015/01/02	16:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	17:43:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	17:43:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	17:43:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/01/02	17:44:00.0	AOCS_Ore-point_Start_4_OG [0x09a]		
		AOCU_NM	5	02-76 00 00 00 00 00
2015/01/02	17:44:18.0	XRT_FLD_DIS_422_OG [0x1a6]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2015/01/02	17:46:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2015/01/02	17:46:56.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/02	17:46:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 03
2015/01/02	17:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	17:53:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	17:53:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	17:53:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/01/02	17:54:00.0	AOCS_Ore-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 03 00 00 00 00
2015/01/02	17:54:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/01/02	17:54:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/01/02	17:54:22.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/01/02	17:54:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/02	17:54:26.0	XRT_FLD_RESET_407_OG [0x197]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	17:56:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 06
2015/01/02	17:56:58.0	XRT_FL_PROG_SET_401_OG [0x191]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2015/01/02	17:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	18:07:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	18:07:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	18:07:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	18:07:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	18:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/02	18:31:00.0	XRT_Custom_430_OG [0x1ae]		
2015/01/02	18:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/02	19:44:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	19:44:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/02	19:44:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/02	19:44:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/02	19:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		

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2015/01/02	20:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/02	20:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/02	21:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/02	21:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/02	21:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/02	21:21:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/02	21:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/02	21:44:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/02	21:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/02	22:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/02	22:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/02	22:59:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/02	22:59:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/02	23:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/02	23:17:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/02	23:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/03	00:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	00:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	00:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	00:36:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/03	00:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/03	00:43:00.0	XRT_Custom_430_OG [0x1ae]				
2015/01/03	00:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/03	01:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	01:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	01:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	01:58:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/03	02:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/03	02:18:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/03	02:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/03	03:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	03:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	03:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	03:30:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/03	03:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/03	03:56:00.0	XRT_Custom_430_OG [0x1ae]				
2015/01/03	03:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/03	05:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	05:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	05:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	05:06:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/03	05:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/03	05:33:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/03	05:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/03	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/03	06:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2015/01/03	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/03	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]				

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2015/01/03	06:02:56.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/03	06:02:58.0	XRT_QT_PROG_SET_425_0G [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/01/03	06:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	06:09:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	06:09:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	06:09:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/03	06:10:00.0	AOCS_ORe-point_Start_5_0G [0x09b]	AOCU_NM	5	02-76	04 00 00 00 00
2015/01/03	06:10:18.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/03	06:10:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/03	06:10:22.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/03	06:10:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/03	06:10:26.0	XRT_FLD_RESET_407_0G [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	06:12:56.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2015/01/03	06:12:58.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/01/03	06:13:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	06:46:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	06:46:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	06:46:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	06:46:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/03	06:49:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/03	07:10:30.0	XRT_Custom_430_0G [0x1ae]				
2015/01/03	07:11:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	08:26:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	08:26:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	08:26:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	08:26:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/03	08:29:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/03	08:47:00.0	XRT_Custom_430_0G [0x1ae]				
2015/01/03	08:48:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	10:07:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	10:07:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	10:07:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	10:07:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/03	10:10:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/03	10:18:30.0	XRT_Custom_430_0G [0x1ae]				
2015/01/03	10:19:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	15:29:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	15:29:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	15:29:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	15:29:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/03	15:32:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/03	15:53:00.0	XRT_Custom_430_0G [0x1ae]				
2015/01/03	15:54:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/03	17:06:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	17:06:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/03	17:06:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/03	17:06:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/03	17:09:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2015/01/03	17:29:30.0	XRT_Custom_430_OG [0x1ae]		
2015/01/03	17:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/03	18:00:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	18:00:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	18:00:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/01/03	18:01:00.0	AOCS_ORe-point_Start_4_OG [0x09a]		
		AOCU_NM	5	02-76 00 00 00 00 00
2015/01/03	18:01:18.0	XRT_FLD_DIS_422_OG [0x1a6]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2015/01/03	18:03:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2015/01/03	18:03:56.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/03	18:03:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 03
2015/01/03	18:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/03	18:10:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	18:10:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	18:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/01/03	18:11:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 03 00 00 00 00
2015/01/03	18:11:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/01/03	18:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/01/03	18:11:22.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/01/03	18:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/03	18:11:26.0	XRT_FLD_RESET_407_OG [0x197]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/03	18:13:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 06
2015/01/03	18:13:58.0	XRT_FL_PROG_SET_401_OG [0x191]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2015/01/03	18:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/03	18:43:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	18:43:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	18:43:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/03	18:43:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/03	18:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/03	19:06:00.0	XRT_Custom_430_OG [0x1ae]		
2015/01/03	19:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/03	20:20:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	20:20:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	20:20:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/03	20:20:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/03	20:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/03	20:43:30.0	XRT_Custom_430_OG [0x1ae]		
2015/01/03	20:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/03	21:57:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	21:57:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	21:57:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/03	21:57:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/03	22:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/03	22:19:30.0	XRT_Custom_430_OG [0x1ae]		
2015/01/03	22:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/03	23:34:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	23:34:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/03	23:34:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/03	23:34:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/03	23:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		

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2015/01/03	23:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/03	23:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/04	01:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/04	01:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/04	01:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/04	01:01:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/04	01:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/04	01:17:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/04	01:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/04	02:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/04	02:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/04	02:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/04	02:33:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/04	02:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/04	02:54:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/04	02:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/04	04:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/04	04:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/04	04:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/04	04:02:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/04	04:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/04	04:31:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/04	04:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/04	05:43:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/04	05:43:02.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/04	05:43:04.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/04	05:43:06.5	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/04	05:46:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/04	06:09:00.0	XRT_Custom_430_OG [0x1ae]				
2015/01/04	06:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/01/04	06:18:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/04	06:18:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/04	06:18:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/04	06:19:00.0	AOCs_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2015/01/04	06:19:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/04	06:21:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/04	06:21:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/04	06:21:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/01/04	06:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/04	06:29:00.0	AOCs_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2015/01/04	07:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/04	07:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/04	07:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/04	07:23:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/04	07:24:26.0	XRT_TCIB_XRT_S_HTR_A_ENA_426_OG [0x1aa]	TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2015/01/04	07:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/04	18:05:00.0	AOCs_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2015/01/04	18:15:00.0	AOCs_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2015/01/05	05:51:30.0	AOCs_Ore-point_Start_4_OG [0x09a]				

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2015/01/05	06:01:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00	00	00	00
2015/01/05	10:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	03	00	00	00	00
2015/01/05	17:54:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	02	00	00	00	00
2015/01/05	18:04:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00	00	00	00
2015/01/06	06:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03	00	00	00	00
2015/01/06	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00	00	00	00	00
2015/01/06	10:50:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03	00	00	00	00
			AOCU_NM	5	02-76	00	00	00	00	00