

# XRT Timeline to be uploaded on 2015/01/06

Period: 2015/01/06 10:40:00 - 2015/01/10 10:20:00

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Normal mode

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## XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
01/07 13:23:00 - 01/07 13:29:54	Fixed ( -528.4, -528.4)	XRT quad pointing
<b>PROG= 04 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 88 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
01/07 13:33:00 - 01/07 13:39:54	Fixed ( 528.4, -528.4)	XRT quad pointing
<b>PROG= 08 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 31 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
01/07 13:43:00 - 01/07 13:49:54	Fixed ( 528.4, 528.4)	XRT quad pointing
<b>PROG= 18 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 81 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
01/07 13:53:00 - 01/07 13:59:54	Fixed ( -528.4, 528.4)	XRT quad pointing
<b>PROG= 09 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 28 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 23</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 3</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 12</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 14</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)**

Term	Pointing (x, y)	Comment
01/07 14:07:00 - 01/07 17:51:30	Track ( 559.3, -67.3) @ 01/07 14:00:00	Track AR 12253
01/07 18:28:00 - 01/08 05:59:54	Track ( 592.6, -68.6) @ 01/07 18:25:00	Track AR 12253
01/08 06:13:00 - 01/08 09:47:54	Track ( 675.9, -72.6) @ 01/08 06:10:00	Track AR 12253

**PROG= 10** 1-time(s)

<b>Subr= 1</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 8</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 24</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 42</b>		<b>4-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 62</b>		<b>15-time(s)</b>	<b>120.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1A6B: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45)**

Term	Pointing (x, y)	Comment
01/07 18:18:00 - 01/07 18:24:54	Fixed ( 0.0, 0.0)	synoptic, shifted 15.0 min

**PROG= 01** 1-time(s)

<b>Subr= 1</b>		<b>1-time(s)</b>	<b>12.0sec</b>									
<b>Seqn= 33</b>		<b>1-time(s)</b>	<b>4.0sec</b>									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 5</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 40</b>		<b>1-time(s)</b>	<b>4.0sec</b>									
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 77</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 6</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>	<b>12.0sec</b>									
<b>Seqn= 46</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 25</b>		<b>1-time(s)</b>	<b>4.0sec</b>									
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 21</b>		<b>1-time(s)</b>	<b>4.0sec</b>									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 37</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 25</b>		<b>1-time(s)</b>	<b>4.0sec</b>									

Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 21 1-time(s) 4.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 37 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 46 1-time(s) 2.0sec</b>												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi**

Term	Pointing (x, y)	Comment
01/08 06:03:00 - 01/08 06:09:54	Fixed ( 0.0, 0.0)	synoptic
<b>PROG= 03 1-time(s)</b>		
<b>Subr= 1 1-time(s) 12.0sec</b>		
<b>Seqn= 33 1-time(s) 4.0sec</b>		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 5 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
<b>Seqn= 40 1-time(s) 4.0sec</b>		
Open/Ti-poly	Open/Ti-poly	close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 77 1-time(s) 2.0sec</b>		
thin-Be/Open	thin-Be/Open	close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band	open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
01/07 14:07:00 - 01/07 17:51:30	Track ( 559.3, -67.3) @ 01/07 14:00:00	Track AR 12253
01/07 18:28:00 - 01/08 05:59:54	Track ( 592.6, -68.6) @ 01/07 18:25:00	Track AR 12253
01/08 06:13:00 - 01/08 09:47:54	Track ( 675.9, -72.6) @ 01/08 06:10:00	Track AR 12253
<b>PROG= 19 30-time(s)</b>		
<b>Subr= 1 20-time(s) 2.0sec</b>		
<b>Seqn= 11 1-time(s) 2.0sec</b>		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn=100 1-time(s) 10.0sec</b>		
thin-Be/Open	med-Be/Open	close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 10 1-time(s) 2.0sec</b>		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn= 15 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

**Flare Detection**

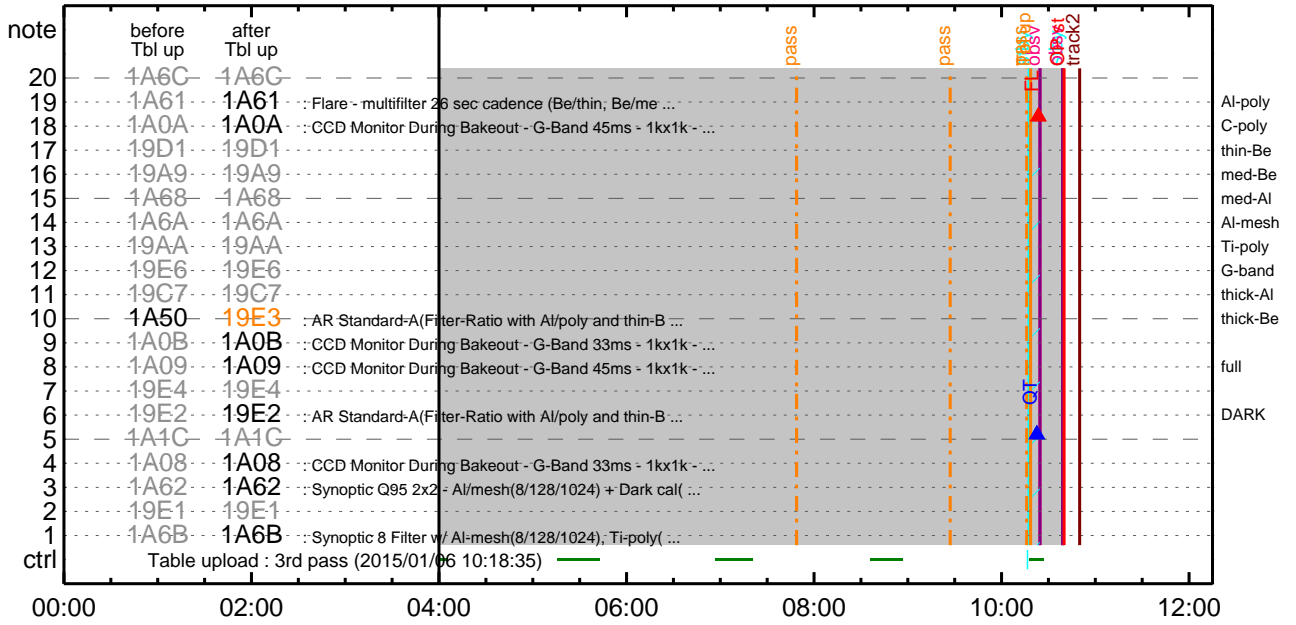
\* \* \* \* \*

**FLD Patrol**

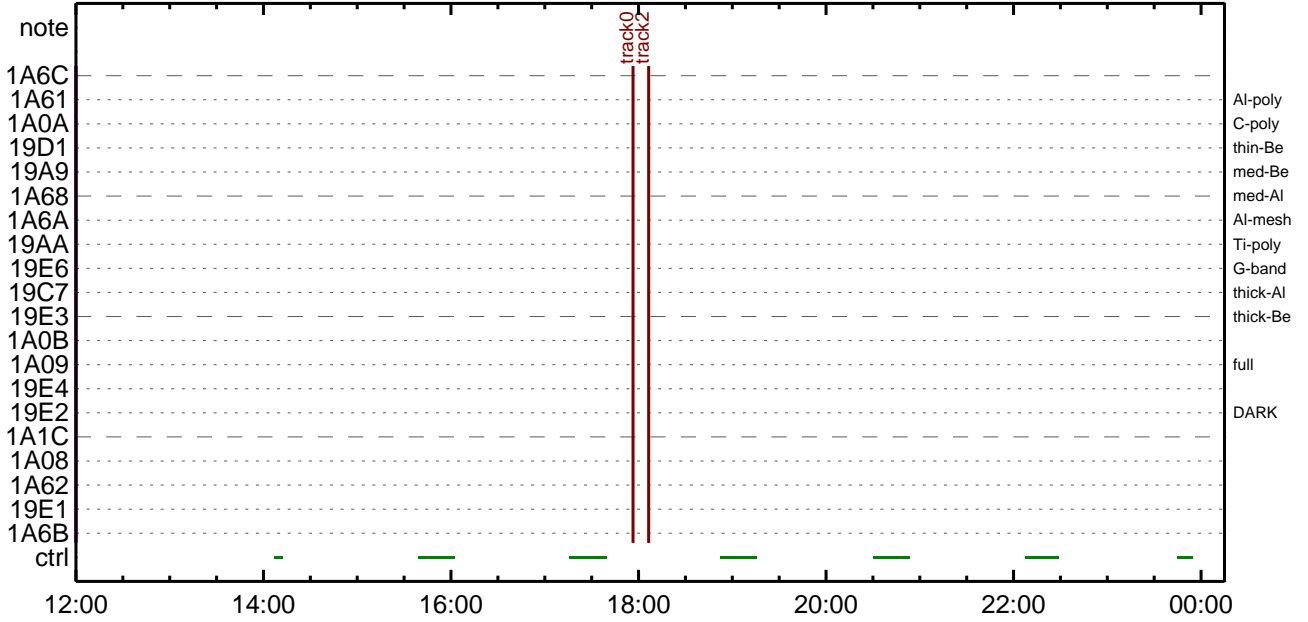
Term	Pointing (x, y)	Comment
01/07 14:04:18 - 01/07 18:15:18	Track ( 559.3, -67.3) @ 01/07 14:00:00	Track AR 12253
01/07 18:25:18 - 01/08 06:00:18	Track ( 592.6, -68.6) @ 01/07 18:25:00	Track AR 12253

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

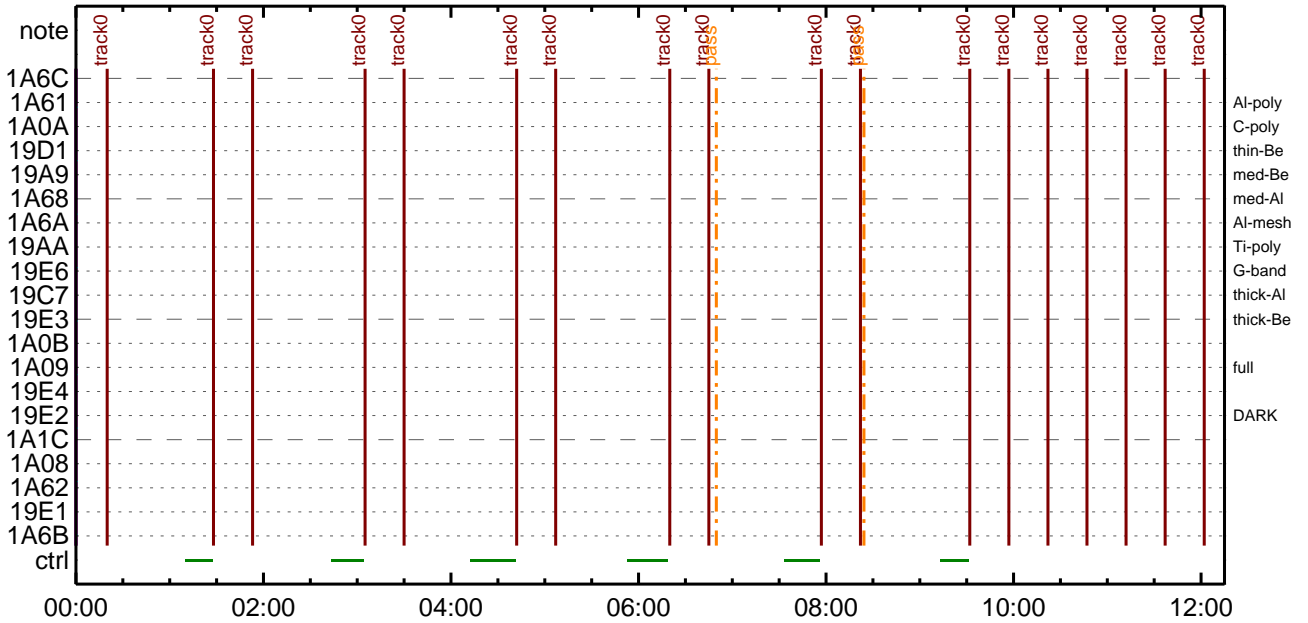
### CMDI #0832 2015/01/06



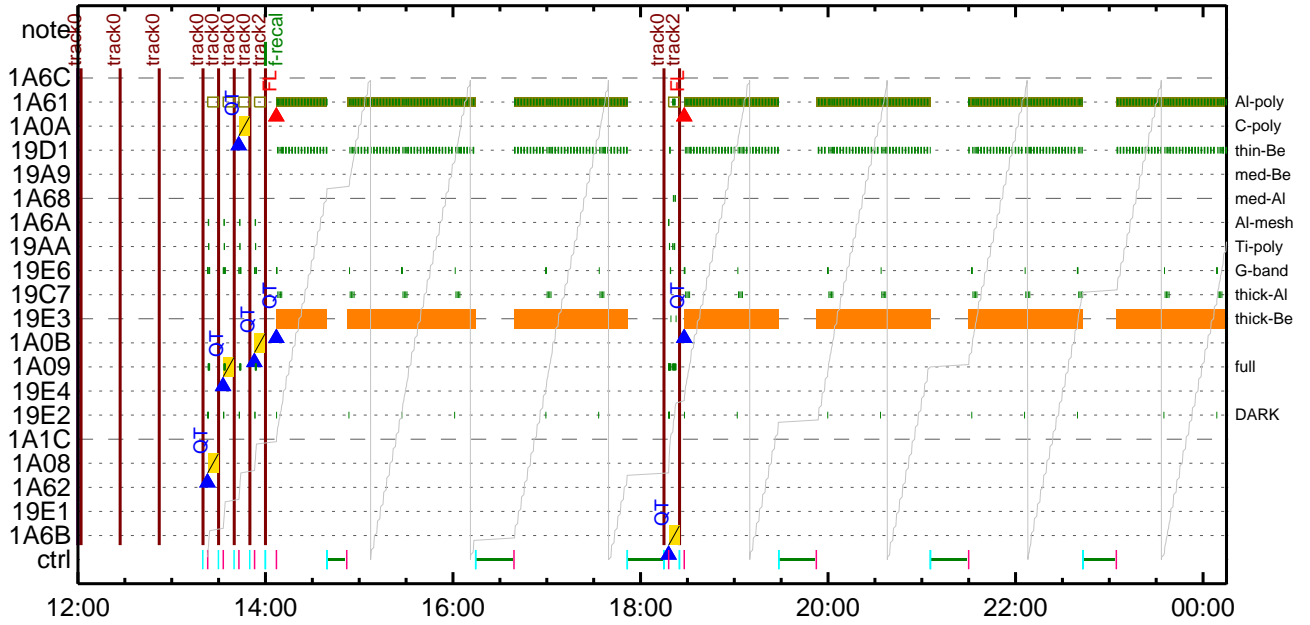
### CMDI #0832 2015/01/06



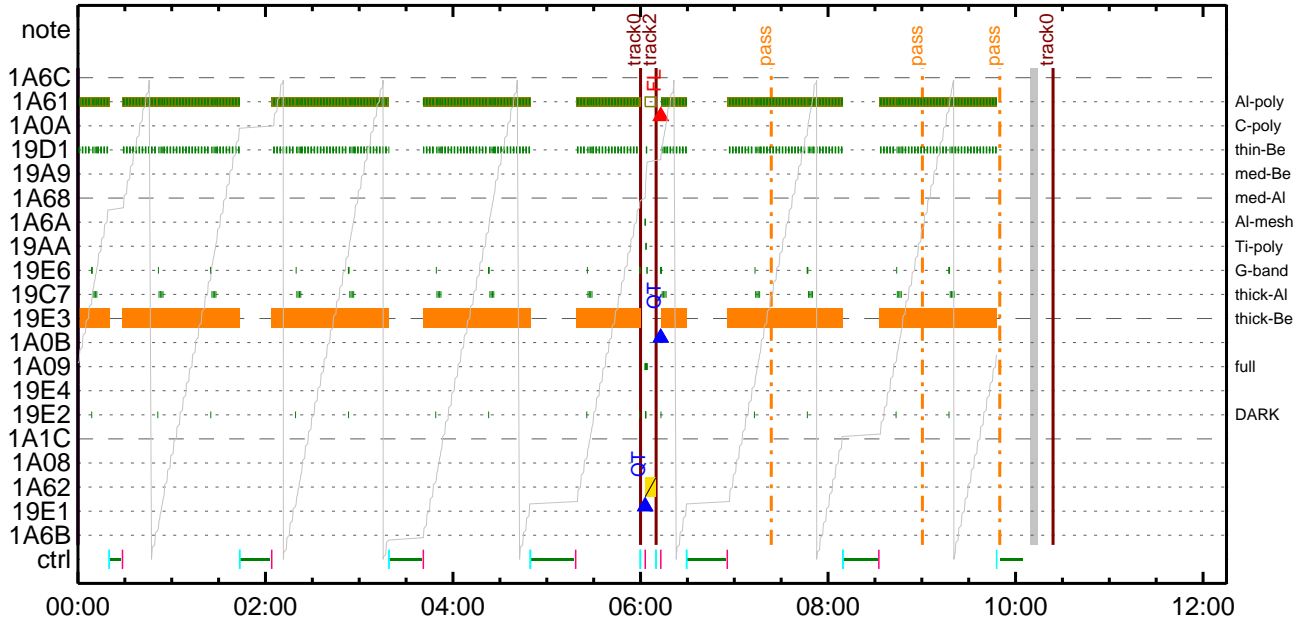
### CMDI #0832 2015/01/07



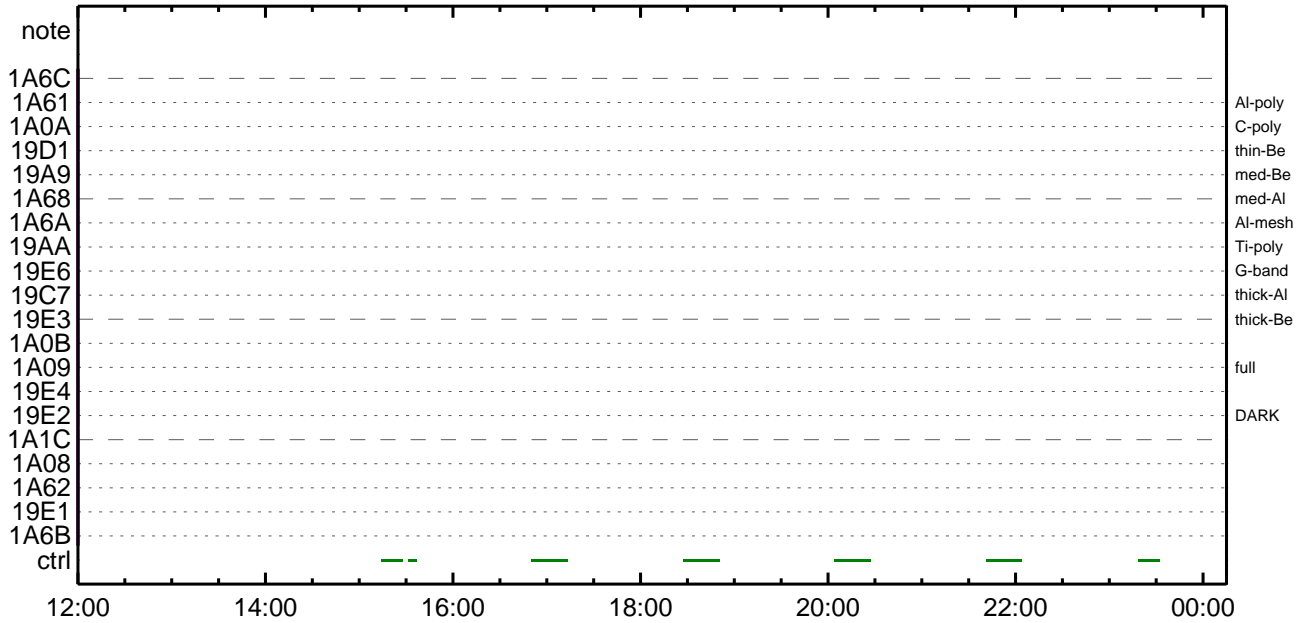
CMDI #0832 2015/01/07



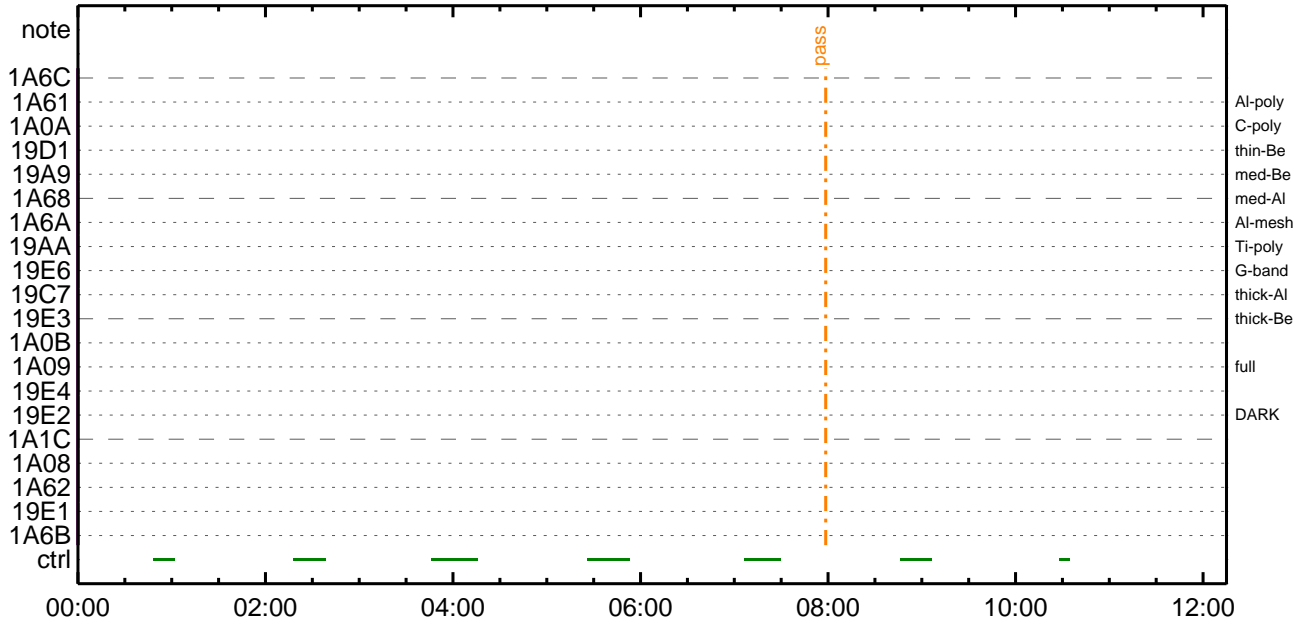
CMDI #0832 2015/01/08



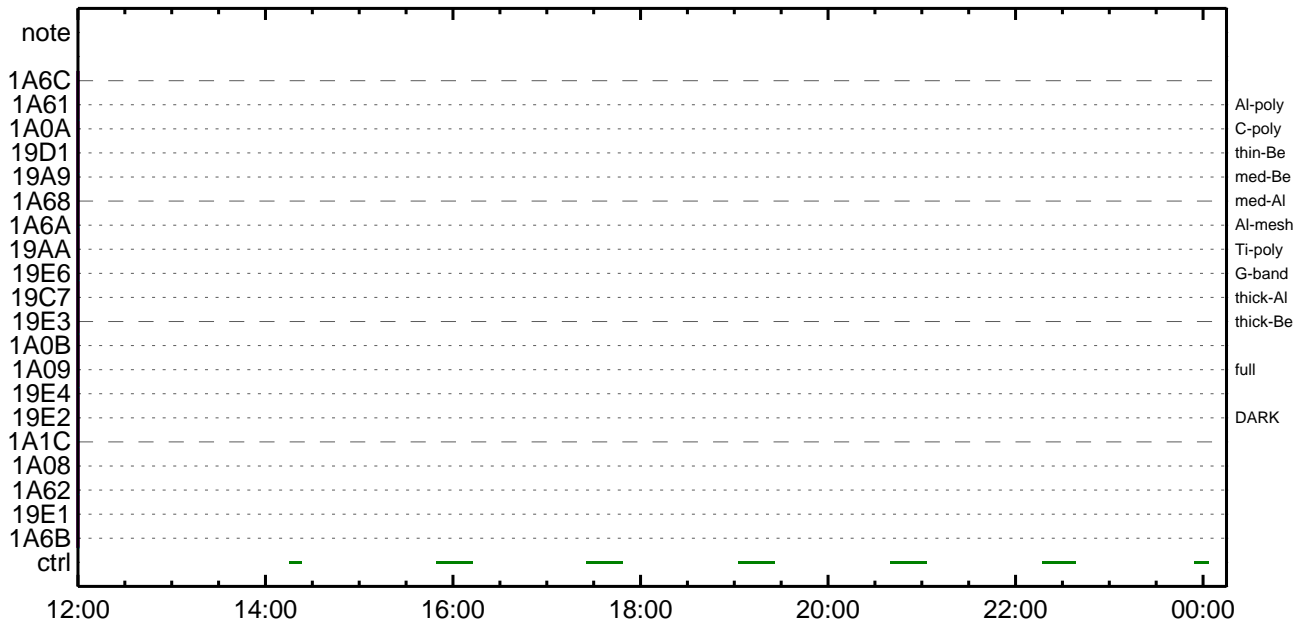
CMDI #0832 2015/01/08



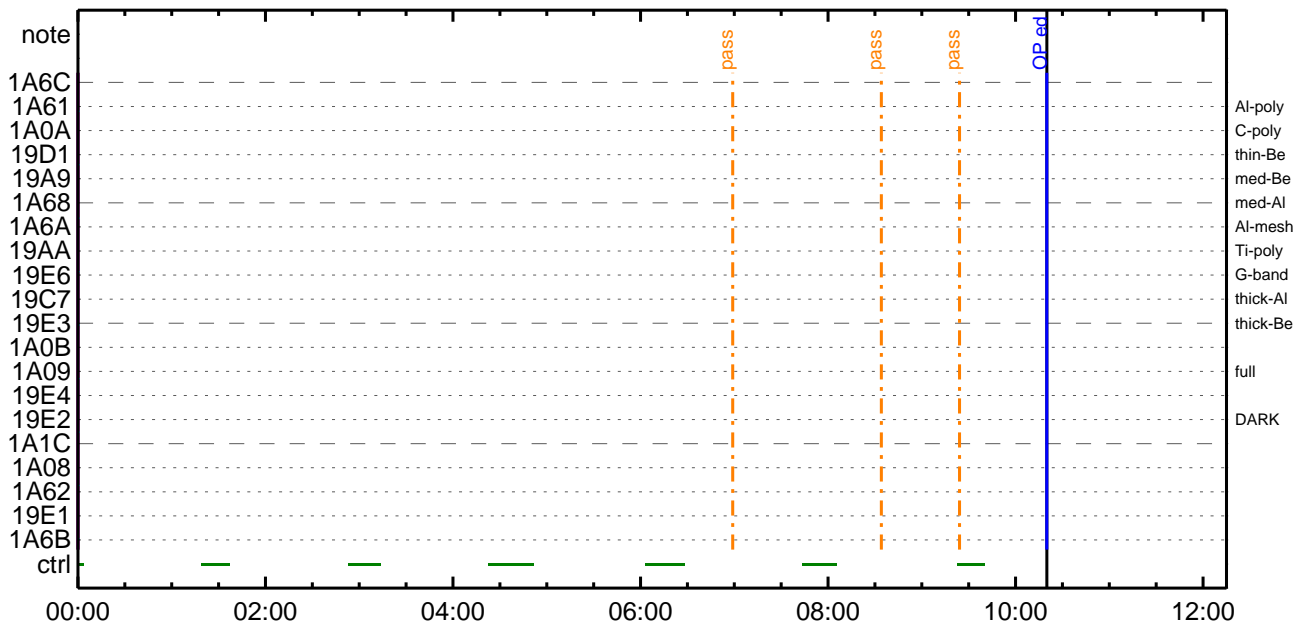
CMDI #0832 2015/01/09



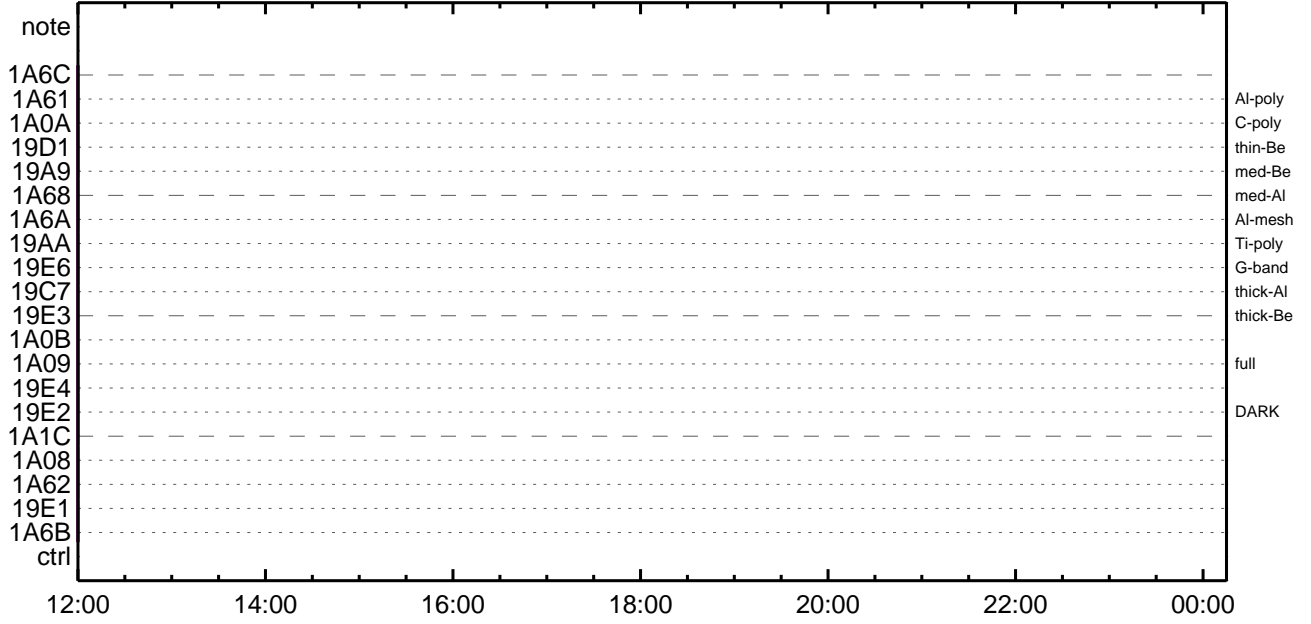
CMDI #0832 2015/01/09



CMDI #0832 2015/01/10



CMDI #0832 2015/01/10





(a) Spacecraft Operation Procedure (real-commands)

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main-854 2015-01-06 13:01:22 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÄÝSYÄÝ-¼Ä»Ü;ä
0005 C.
0006 C. ÝÄÝß;¼Ý³ÝÞÝÓÝÉÄ+¿©
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOC : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¿¿ãð•µ°Ë>Í×ÁÇ¿ÝçÝÄÝ×Ýí;¼ÝÉ;ËÈÈ¼µ•ííË;ËãÈ¼°ÇÓã•¿¼ì¹çãí;çÀ®, ùã¹ãÈãðãÇÄ+¿©ã•ðËãã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿@µ;ON
0016 C. *****
0017 C. ç" °ËÀ, í×ËÝãáLÒãðãÇãí»p´Òãð¹íí, ñ.; çËÓí×ãÈXÁÓONãí¹ÒãÈãíãÈãã³ãÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÝDÝÓÝÉÝíÝÄÝ-¾ÒÄÖã-°ÁÄêã•¿¿;ç°Ë²¼ãí°ËÀ, ¼ê¼çãð¼Ä¹Òã¹ãÈ;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ËÀ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëã•¿¿ã¼ì¹çãí;ç°Ë²¼ãí°ËÀ¹Òã»°;çDCBC-150ãØ¿ËÈã;f
0035 C.
0036 . C. ;ãPT1°ËÀ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ë, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ë, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ë, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝÓÝÉÝÉÄÚÄØ;ËÄ•Ä°²óÈð;Ë, áãí°ËÀ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ë, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ë, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ë, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÀ, ñ-¼«Ë°Áã»ßã•¿¿;ã;ç°Ë²¼ãð¼Ä¹Òã¹ãÈ;f
0055 C. ÝçÝÓÝÉÝÉÄÚÄØãÄ•Ä°²óÈðã-¶ãã¼ì¹çãí°í»ã¹ãÈãðãÇÄÖãÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ËÀ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëã•¿¿ã¼ì¹çãí;ç°Ë²¼ãí°ËÀ¹Òã»°;çDCBC-151ãØ¿ËÈã;f
0061 C.
0062 . C. ;ãPT2°ËÀ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ë, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ë, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ë, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝÓÝÉÝÉÄÚÄØ;ËÄ•Ä°²óÈð;Ë, áãí°ËÀ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ë, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ë, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ë, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÀ, Äã»ß;çXÁ+¿@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÀ, Äã»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿@µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF

```

```
0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;Y4E;YAY6YX
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;Y4E;ã
0103 . S. OP      op-854:OP
0104 ( )
0105 . S. OG      og-854:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPîî°èYAY6YX;ã
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 C.           çç[HK1_DMP_TOP_ADRS_1]      EQ      40
0113 C.           çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0114 C.           çç[HK1_DMP_BLOCK_NUM]      EQ     127
0115 C.           çç[HK1_DMP_REPEAT_NUM]     EQ      0
0116 C.           çç[HK1_DMA_DMP_PIM]        EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 C.           çç[HK1_PKT_FORM_NO]        EQ      7
0120 C.           çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0121 C.           çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0122 C.           çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0123 C.           çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0124 . C. YAY6YX×½ªî»ò³îÇS
0125 C.           çç[HK1_DMP_CHK_FLG]      EQ     NON
0126 . C. RAM ID=NMOGαî½Ē¹ç.è²îOKαò³îÇS
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 C.           çç[HK1_DMP_TOP_ADRS_1]      EQ     41
0132 C.           çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0133 C.           çç[HK1_DMP_BLOCK_NUM]      EQ     127
0134 C.           çç[HK1_DMP_REPEAT_NUM]     EQ      0
0135 C.           çç[HK1_DMA_DMP_PIM]        EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 C.           çç[HK1_PKT_FORM_NO]        EQ      7
0139 C.           çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0140 C.           çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0141 C.           çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0142 C.           çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0143 . C. YAY6YX×½ªî»ò³îÇS
0144 C.           çç[HK1_DMP_CHK_FLG]      EQ     NON
0145 . C. RAM ID=NMOGαî½Ē¹ç.è²îOKαò³îÇS
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 C.           çç[HK1_DMP_TOP_ADRS_1]      EQ     42
0151 C.           çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0152 C.           çç[HK1_DMP_BLOCK_NUM]      EQ     65
0153 C.           çç[HK1_DMP_REPEAT_NUM]     EQ      0
0154 C.           çç[HK1_DMA_DMP_PIM]        EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 C.           çç[HK1_PKT_FORM_NO]        EQ      7
0158 C.           çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0159 C.           çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0160 C.           çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0161 C.           çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0162 . C. YAY6YX×½ªî»ò³îÇS
0163 C.           çç[HK1_DMP_CHK_FLG]      EQ     NON
0164 . C. RAM ID=NMOG,RAM ID=OPαî½Ē¹ç.è²îOKαò³îÇS
0165 C.
0166 . C. ***** οĒ²¼αî½Ē¹ç.è²îOKαò³îÇS *****
0167 C. DHUYâ;Y4E;Ē½Y½;Yi;Y4E;Ēòîãα¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 C.           çç[HK1_PKT_FORM_NO]        EQ      2
0171 C.           çç[HK1_PKT_GEN_TIME]      EQ     0.5S
0172 C.           çç[HK1_S_TLM_BIT_RATE]    EQ     32K
0173 C.           çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE |s OPOG UPLOADα-Ā÷çîNGNİ½¹ç;ç°Ē²¼αîTI-CMDĀ÷çî½Ā¹Ēα.αĒααα³αĒ;f
0180 C.   αPαç;çSETαĒDUMPαĒ±ºiYNY¹ç¹Ēα|α³αĒ;f
0181 C.
0182 . C. TIY³YpY6YĒòðĀĒîç(UT)
0183 +. TI 2015-01-06 10:35:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.           çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0186 C.
0187 +. TI 2015-01-06 10:35:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.           çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0190 C.
0191 +. TI 2015-01-06 10:35:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.           çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
```

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0194 C.
0195 +. TI 2015-01-06 10:39:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.         ¤¤[HK1_TI_CMD_NUM]               EQ      1COUNTUP
0198 C.
0199 C. °È²¼ºíÄè%îíñºîîŷÄŷ§ŷÄŷ¹àîŭ
0200 C.         ¤¤[HK1_TI_CMD_ENA/DIS]           EQ      ENA
0201 C.         ¤¤[HK1_TI_CMD_NUM]               EQ      4
0202 C.         ¤¤[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.         ¤¤[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204 C.
0205 C. *****
0206 C. TÍîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.         ¤¤[HK1_DMP_TOP_ADRS_1]           EQ      07
0213 C.         ¤¤[HK1_DMP_TOP_ADRS_0]           EQ      2B
0214 C.         ¤¤[HK1_DMP_BLOCK_NUM]            EQ      3
0215 C.         ¤¤[HK1_DMP_REPEAT_NUM]           EQ      0
0216 C.         ¤¤[HK1_DMA_DMP_PIM]              EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.         ¤¤[HK1_PKT_FORM_NO]              EQ      7
0220 C.         ¤¤[HK1_PKT_GEN_TIME]              EQ      0.25 s
0221 C.         ¤¤[HK1_S_TLM_BIT_RATE]           EQ      32k
0222 C.         ¤¤[HK1_X_TLM_BIT_RATE]           EQ      4M
0223 C.         ¤¤[HK1_DMP_CHK_FLG]              EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»º³îÇ§
0226 C.         ¤¤[HK1_DMP_CHK_FLG]              EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLºîîºÈ¹Ç•è²îOKºº³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼.ŷî;¼ŷÈ;Èººîäº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.         ¤¤[HK1_PKT_FORM_NO]              EQ      2
0234 C.         ¤¤[HK1_PKT_GEN_TIME]              EQ      0.5S
0235 C.         ¤¤[HK1_S_TLM_BIT_RATE]           EQ      32K
0236 C.         ¤¤[HK1_X_TLM_BIT_RATE]           EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2015-01-06 10:39:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2015-01-06 10:39:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.         [      ] [HK1_TI_CMD_NUM]         EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2015-01-06 10:39:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C. HK1_TI_CMD_NUM       = 1 CNTUP [      ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2015-01-06 10:39:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.         [      ] [HK1_TI_CMD_NUM]         EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîãî»ö¼ŷºÈÃºº¹ºèDCBC•x²è *****
0276 C. (¼ªºîŷÖŷÄŷÈŷŷŷŷÄŷÇŷèºÈ¼ºº¼ª»ŭº¹ºè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹.İ Daily±çîñºÈºº¹ºèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ¹¼ª»ŭ;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-856 2015-01-06 13:01:22 162 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Ä»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÄµ°È»Í×ÁÇ¿íY¿Y×Yí;¼YÉ;ÈÈè¼µ·íÉ;ÈèÈ¼°ÇÖã·¿¼i¹¿ãÍ;¿Ä®, ùã¹ãèãããÇÁ+¿®ã·èããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-261:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. Y¿YÁY×Yí;¼YÉãîÁ°ãÉSTS_CHKãðOFFãÈã¹ãè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2015-01-06 10:39:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG_____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR _____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
    
```

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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 85 83 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 c0 c0 10 10)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 20 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 40 c0 10 10)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0b 40 40 10 10)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0c c0 40 10 10)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 0d 80 80 20 08)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 0e 80 80 08 20)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 0f 80 80 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 10 80 80 08 08)
0120 + DC 07-F0 MDP_XRT_FLD_ENA
0121 BC (d8)
0122 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0123 BC (c8)
0124 + DC 07-F0 MDP_XRT_AEC_RESET
0125 BC (d0)
0126 + DC 07-F0 MDP_XRT_ARS_DIS
0127 BC (d5)
0128 + DC 07-F0 MDP_XRT_FLD_RESET
0129 BC (da)
0130 + DC 07-F0 MDP_XRT_QT_PROG_SET
0131 BC (c4 06)
0132 + DC 07-F0 MDP_XRT_FL_PROG_SET
0133 BC (c5 13)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C.
0137 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0138 C.
0139 + DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 + TI 2015-01-06 10:39:02.0
0142 DC 07-F0 MDP_XRT_MODE_OBSV
0143 BC (c2)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C. ***** XRT END *****
0147 C.
0148 . C. ***** MDP ^ôÃîñî»ô%ŶñÊÄñ¹ñèDCBC•x²è *****
0149 C. (%ã°îŷÓŷÄŷÈŷPŷÈŷŷâŷçŷèñÊ%¼ñ¼Ä»Ûñ¹ñè)
0150 . S. DC-BC dcbc-402:DCBC
0151 (MDP_known_event)
0152 C.
0153 C.
0154 . C. ***** ŷDŷ¹.İ Daily±;îÑñÈ´Øñ¹ñèDCBC•x²è *****
0155 . S. DC-BC dcbc-153:DCBC
0156 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0157 C.
0158 C.
0159 . C. ;ãLOSŷÄŷSŷÄŷ~¼Ä»Û;ã
0160 C.
0161 . C. ***** LOS *****
0162 C.

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\*\*\* OP Sequence for XRT \*\*\*

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2015/01/06 10:50:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCu_NM                    5 02-76 02 00 00 00 00
2015/01/06 17:56:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCu_NM                    5 02-76 00 00 00 00 00
2015/01/06 18:06:30.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCu_NM                    5 02-76 02 00 00 00 00
2015/01/07 00:20:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCu_NM                    5 02-76 00 55 58 02 24
2015/01/07 01:28:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCu_NM                    5 02-76 00 4c 72 02 24
2015/01/07 01:53:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCu_NM                    5 02-76 00 43 8d 02 24
2015/01/07 03:05:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCu_NM                    5 02-76 00 3a a7 02 24
2015/01/07 03:30:00.0 AOCs_OrE-point_Start_7_OG [0x09d]
                        AOCu_NM                    5 02-76 00 31 ca 02 24
2015/01/07 04:42:00.0 AOCs_OrE-point_Start_8_OG [0x09e]
                        AOCu_NM                    5 02-76 00 28 e5 02 24
2015/01/07 05:07:00.0 AOCs_OrE-point_Start_9_OG [0x09f]
                        AOCu_NM                    5 02-76 00 20 00 02 24
2015/01/07 06:20:00.0 AOCs_OrE-point_Start_10_OG [0x0a0]
                        AOCu_NM                    5 02-76 00 17 1a 02 24
2015/01/07 06:45:00.0 AOCs_OrE-point_Start_11_OG [0x0a1]
                        AOCu_NM                    5 02-76 00 0e 35 02 24
2015/01/07 07:24:26.0 XRT_TCIB_XRT_S_HTR_A_DIS_447_OG [0x1bf]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2015/01/07 07:57:00.0 AOCs_OrE-point_Start_12_OG [0x0a2]
                        AOCu_NM                    5 02-76 00 05 58 02 24
2015/01/07 08:22:00.0 AOCs_OrE-point_Start_13_OG [0x0a3]
                        AOCu_NM                    5 02-76 00 fd 59 02 24
2015/01/07 09:32:00.0 AOCs_OrE-point_Start_14_OG [0x0a4]
                        AOCu_NM                    5 02-76 00 f4 73 02 24
2015/01/07 09:57:00.0 AOCs_OrE-point_Start_15_OG [0x0a5]
                        AOCu_NM                    5 02-76 00 eb 8e 02 24
2015/01/07 10:22:00.0 AOCs_OrE-point_Start_16_OG [0x0a6]
                        AOCu_NM                    5 02-76 00 e2 a8 02 24
2015/01/07 10:47:00.0 AOCs_OrE-point_Start_17_OG [0x0a7]
                        AOCu_NM                    5 02-76 00 d9 cb 02 24
2015/01/07 11:12:00.0 AOCs_OrE-point_Start_18_OG [0x0a8]
                        AOCu_NM                    5 02-76 00 d0 e6 02 24
2015/01/07 11:37:00.0 AOCs_OrE-point_Start_19_OG [0x0a9]
                        AOCu_NM                    5 02-76 00 c8 01 02 24
2015/01/07 12:02:00.0 AOCs_OrE-point_Start_20_OG [0x0aa]
                        AOCu_NM                    5 02-76 00 bf 1b 02 24
2015/01/07 12:27:00.0 AOCs_OrE-point_Start_21_OG [0x0ab]
                        AOCu_NM                    5 02-76 00 b6 36 02 24
2015/01/07 12:52:00.0 AOCs_OrE-point_Start_22_OG [0x0ac]
                        AOCu_NM                    5 02-76 00 ad 59 02 24
2015/01/07 13:19:54.0 XRT_CTRL_MANU_414_OG [0x19e]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/01/07 13:20:00.0 AOCs_OrE-point_Start_23_OG [0x0ad]
                        AOCu_NM                    5 02-76 00 2e f9 2e f9
2015/01/07 13:22:32.0 XRT_FOCUS_POSITION_432_OG [0x1b0]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/01/07 13:22:52.0 XRT_QT_PROG_SET_433_OG [0x1b1]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 04
2015/01/07 13:22:54.0 XRT_FLD_DIS_437_OG [0x1b5]
                        MDP_XRT_FLD_DIS            1 07-F0 d9
2015/01/07 13:22:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2015/01/07 13:22:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS            1 07-F0 d5
2015/01/07 13:23:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2015/01/07 13:29:54.0 XRT_CTRL_MANU_414_OG [0x19e]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/01/07 13:30:00.0 AOCs_OrE-point_Start_24_OG [0x0ae]
                        AOCu_NM                    5 02-76 00 2e f9 d1 07
2015/01/07 13:32:32.0 XRT_FOCUS_POSITION_432_OG [0x1b0]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/01/07 13:32:52.0 XRT_QT_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 08
2015/01/07 13:32:54.0 XRT_FLD_DIS_437_OG [0x1b5]
                        MDP_XRT_FLD_DIS            1 07-F0 d9
2015/01/07 13:32:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2015/01/07 13:32:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS            1 07-F0 d5
2015/01/07 13:33:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2015/01/07 13:39:54.0 XRT_CTRL_MANU_414_OG [0x19e]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/01/07 13:40:00.0 AOCs_OrE-point_Start_25_OG [0x0af]
                        AOCu_NM                    5 02-76 00 d1 07 d1 07
2015/01/07 13:42:32.0 XRT_FOCUS_POSITION_432_OG [0x1b0]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/01/07 13:42:52.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 12
2015/01/07 13:42:54.0 XRT_FLD_DIS_437_OG [0x1b5]
                        MDP_XRT_FLD_DIS            1 07-F0 d9
2015/01/07 13:42:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]

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2015/01/07	13:42:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/01/07	13:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/07	13:49:54.0	XRT_CTRL_MANU_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	13:50:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	00 d1 07 2e f9				
2015/01/07	13:52:32.0	XRT_FOCUS_POSITION_432_OG [0x1b0]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/01/07	13:52:52.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2015/01/07	13:52:54.0	XRT_FLD_DIS_437_OG [0x1b5]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/01/07	13:52:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/01/07	13:52:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/01/07	13:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/07	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	13:59:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78 00				
2015/01/07	14:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00				
2015/01/07	14:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/01/07	14:04:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/01/07	14:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/01/07	14:04:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/01/07	14:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/01/07	14:04:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/07	14:06:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2015/01/07	14:06:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/01/07	14:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/07	14:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	14:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	14:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/07	14:39:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/01/07	14:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/01/07	14:51:00.0	XRT_Custom_430_OG [0x1ae]								
2015/01/07	14:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/07	16:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	16:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	16:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/07	16:14:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/01/07	16:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/01/07	16:38:00.0	XRT_Custom_430_OG [0x1ae]								
2015/01/07	16:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/07	17:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	17:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	17:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/07	17:51:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/01/07	17:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/01/07	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	18:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	18:14:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/01/07	18:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/01/07	18:15:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9				

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2015/01/07	18:17:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/01/07	18:17:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/01/07	18:17:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2015/01/07	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/07	18:24:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	18:24:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	18:24:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/01/07	18:25:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2015/01/07	18:25:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/01/07	18:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/01/07	18:25:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/01/07	18:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/01/07	18:25:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/07	18:27:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2015/01/07	18:27:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/01/07	18:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/07	19:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	19:28:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	19:28:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/07	19:28:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/01/07	19:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/01/07	19:51:30.0	XRT_Custom_430_OG [0x1ae]							
2015/01/07	19:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/07	21:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	21:05:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	21:05:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/07	21:05:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/01/07	21:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/01/07	21:29:00.0	XRT_Custom_430_OG [0x1ae]							
2015/01/07	21:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/07	22:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	22:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/07	22:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/07	22:43:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/01/07	22:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/01/07	23:03:30.0	XRT_Custom_430_OG [0x1ae]							
2015/01/07	23:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/08	00:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/08	00:20:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/08	00:20:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/08	00:20:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/01/08	00:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/01/08	00:27:30.0	XRT_Custom_430_OG [0x1ae]							
2015/01/08	00:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/08	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/08	01:43:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/08	01:43:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/08	01:43:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/01/08	01:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2015/01/08	02:03:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/08	02:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/01/08	03:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/08	03:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	03:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/08	03:19:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/08	03:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/08	03:40:00.0	XRT_Custom_430_OG [0x1ae]					
2015/01/08	03:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/01/08	04:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/08	04:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	04:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/08	04:49:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/08	04:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/08	05:17:30.0	XRT_Custom_430_OG [0x1ae]					
2015/01/08	05:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/01/08	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/08	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/01/08	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/01/08	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/01/08	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/01/08	06:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/01/08	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/01/08	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/08	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/01/08	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00	
2015/01/08	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/01/08	06:10:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/01/08	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/01/08	06:10:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/01/08	06:12:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/08	06:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2015/01/08	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2015/01/08	06:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/08	06:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	06:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	06:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/08	06:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/08	06:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/08	06:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/01/08	08:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/08	08:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	08:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/08	08:09:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/08			MDP_XRT_PREFLR_STRT	1	07-F0	e8	

