

Period: 2015/01/10 10:20:00 - 2015/01/20 10:53:00

XOB #1A12: AR Standard-B(Morphology), Be-thin w/ 24s cad and Be-med PFB, multifilter context w/ 20 min cad, G-band test w/ 60 min cad, 384x384, 1064														
Term		Pointing (x, y)					Comment							
01/10 14:33:00 - 01/10 17:37:24		Track (-88.6, 209.9) @ 01/10 14:30:00					# HOP 274 (EFR)							
01/11 14:33:00 - 01/11 17:59:54		Track (135.7, 211.3) @ 01/11 14:30:00					# HOP 274 (EFR)							
01/12 14:35:00 - 01/12 17:35:30		Track (352.6, 208.7) @ 01/12 14:30:00					# HOP 274 (EFR)							
PROG= 14 Inf.time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 8		1-time(s)		2.0sec										
Open/G-band		Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Seqn= 24		1-time(s)		2.0sec										
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Subr= 2		3-time(s)		2.0sec										
Seqn= 16		1-time(s)		2.0sec										
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
C-poly/Open		C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
med-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
med-Al/Open		Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Seqn= 4		50-time(s)		2.0sec										

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	8.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	8.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	8.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thin-Be(16/362/2048)

Term	Pointing (x, y)	Comment
01/10 17:40:30 - 01/10 17:47:24	Fixed (0.0, 0.0)	synoptic, shifted -22.5 min
01/11 05:59:00 - 01/11 06:05:54	Fixed (0.0, 0.0)	synoptic, shifted -4.0 min
01/11 18:03:00 - 01/11 18:09:54	Fixed (0.0, 0.0)	synoptic
01/12 06:16:00 - 01/12 06:22:54	Fixed (0.0, 0.0)	synoptic, shifted 13.0 min
01/12 18:03:00 - 01/12 18:09:54	Fixed (0.0, 0.0)	synoptic
01/13 05:48:30 - 01/13 05:55:24	Fixed (0.0, 0.0)	synoptic, shifted -14.5 min

PROG= 03

1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 33 1-time(s) 4.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 40 1-time(s) 4.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 77 1-time(s) 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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XOB #19E5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
01/10 17:50:30 - 01/11 05:55:54	Track (574.8, 154.6) @ 01/10 17:47:30	# Track AR 12257.
01/11 06:09:00 - 01/11 14:29:54	Track (664.0, 149.9) @ 01/11 06:06:00	# AR cont.
01/11 18:13:00 - 01/12 06:12:54	Track (742.1, 144.5) @ 01/11 18:10:00	# AR cont.
01/12 06:26:00 - 01/12 14:24:30	Track (810.8, 138.3) @ 01/12 06:23:00	# AR cont.
01/12 18:17:00 - 01/13 05:45:24	Track (866.3, 131.5) @ 01/12 18:10:00	# AR cont.
01/13 05:58:30 - 01/13 07:53:30	Track (910.3, 124.2) @ 01/13 05:55:30	# AR cont.

PROG= 15

Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 8 2-time(s) 2.0sec

Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
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Subr= 2 2-time(s) 2.0sec

Seqn= 24 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 42 4-time(s) 2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 62 8-time(s) 240.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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Flare mode

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XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)

Term	Pointing (x, y)	Comment
01/10 14:33:00 - 01/10 17:37:24	Track (-88.6, 209.9) @ 01/10 14:30:00	# HOP 274 (EFR)
01/10 17:50:30 - 01/11 05:55:54	Track (574.8, 154.6) @ 01/10 17:47:30	# Track AR 12257.
01/11 06:09:00 - 01/11 14:29:54	Track (664.0, 149.9) @ 01/11 06:06:00	# AR cont.
01/11 14:33:00 - 01/11 17:59:54	Track (135.7, 211.3) @ 01/11 14:30:00	# HOP 274 (EFR)
01/11 18:13:00 - 01/12 06:12:54	Track (742.1, 144.5) @ 01/11 18:10:00	# AR cont.
01/12 06:26:00 - 01/12 14:24:30	Track (810.8, 138.3) @ 01/12 06:23:00	# AR cont.

01/12 14:35:00 - 01/12 17:35:30 Track (352.6, 208.7) @ 01/12 14:30:00 # HOP 274 (EFR)
01/12 18:17:00 - 01/13 05:45:24 Track (866.3, 131.5) @ 01/12 18:10:00 # AR cont.
01/13 05:58:30 - 01/13 07:53:30 Track (910.3, 124.2) @ 01/13 05:55:30 # AR cont.

PROG= 19 30-time(s)												
Subr= 1		20-time(s)			2.0sec							
Seqn= 11		1-time(s)			2.0sec							
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Seqn=100		1-time(s)			10.0sec							
		thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2 0 2.0sec
		med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
		Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Subr= 2		1-time(s)			2.0sec							
Seqn= 10		1-time(s)			2.0sec							
		med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Seqn= 11		1-time(s)			2.0sec							
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Seqn= 15		1-time(s)			2.0sec							
		Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0 0 2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

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Active Region Search

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NOT USED

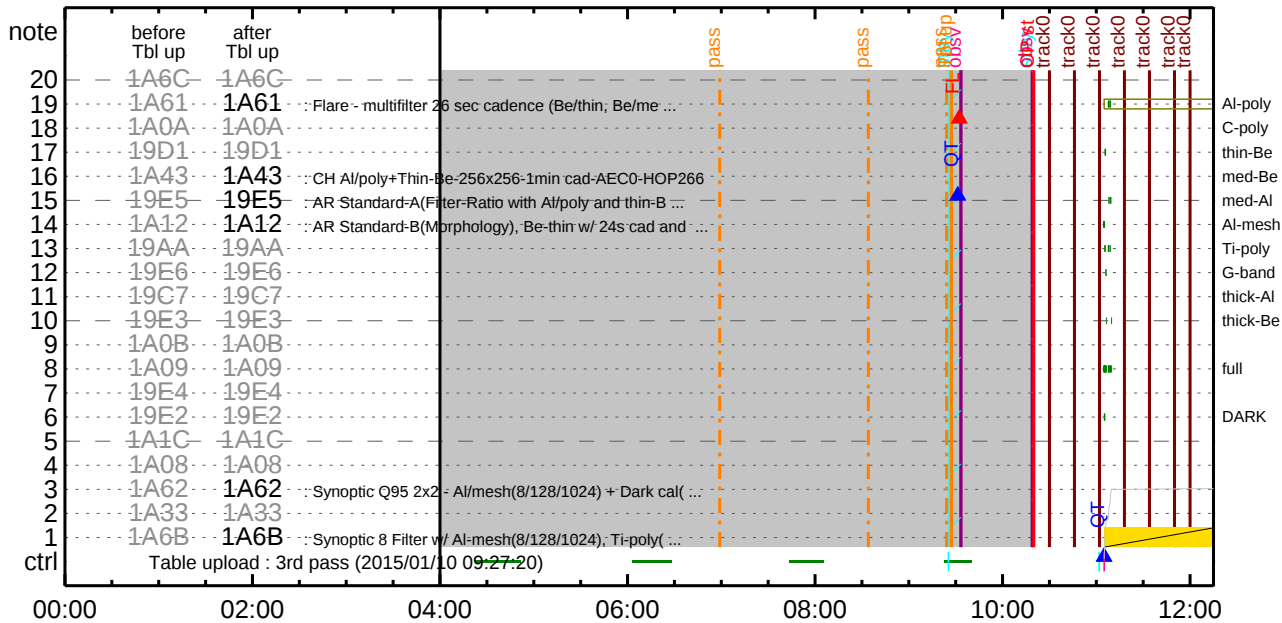
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Flare Detection

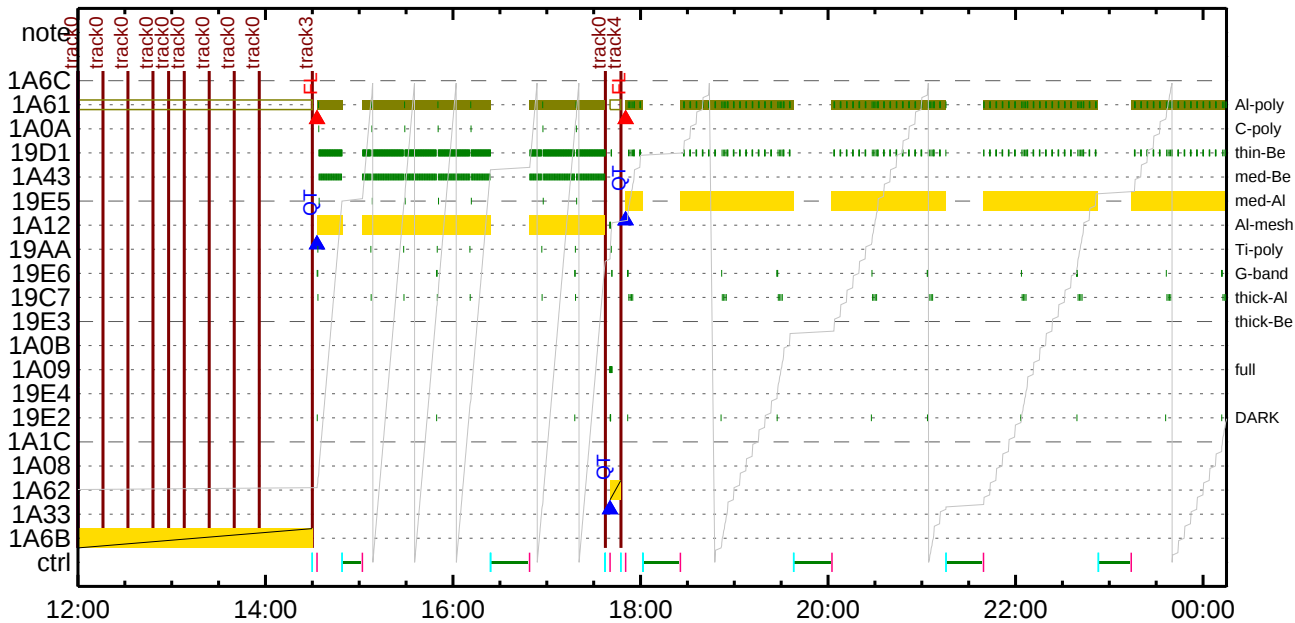
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FLD Patrol												
Term		Pointing (x, y)								Comment		
01/10 14:30:18 - 01/10 17:37:48		Track (-88.6, 209.9) @ 01/10 14:30:00 # HOP 274 (EFR)										
01/10 17:47:48 - 01/11 05:56:18		Track (574.8, 154.6) @ 01/10 17:47:30 # Track AR 12257.										
01/11 06:06:18 - 01/11 18:00:18		Track (664.0, 149.9) @ 01/11 06:06:00 # AR cont.										
01/11 18:10:18 - 01/12 06:13:18		Track (742.1, 144.5) @ 01/11 18:10:00 # AR cont.										
01/12 06:23:18 - 01/12 18:00:18		Track (810.8, 138.3) @ 01/12 06:23:00 # AR cont.										
01/12 18:14:18 - 01/13 05:45:48		Track (866.3, 131.5) @ 01/12 18:10:00 # AR cont.										
01/13 05:55:48 - 01/20 10:53:00		Track (910.3, 124.2) @ 01/13 05:55:30 # AR cont.										
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	80sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

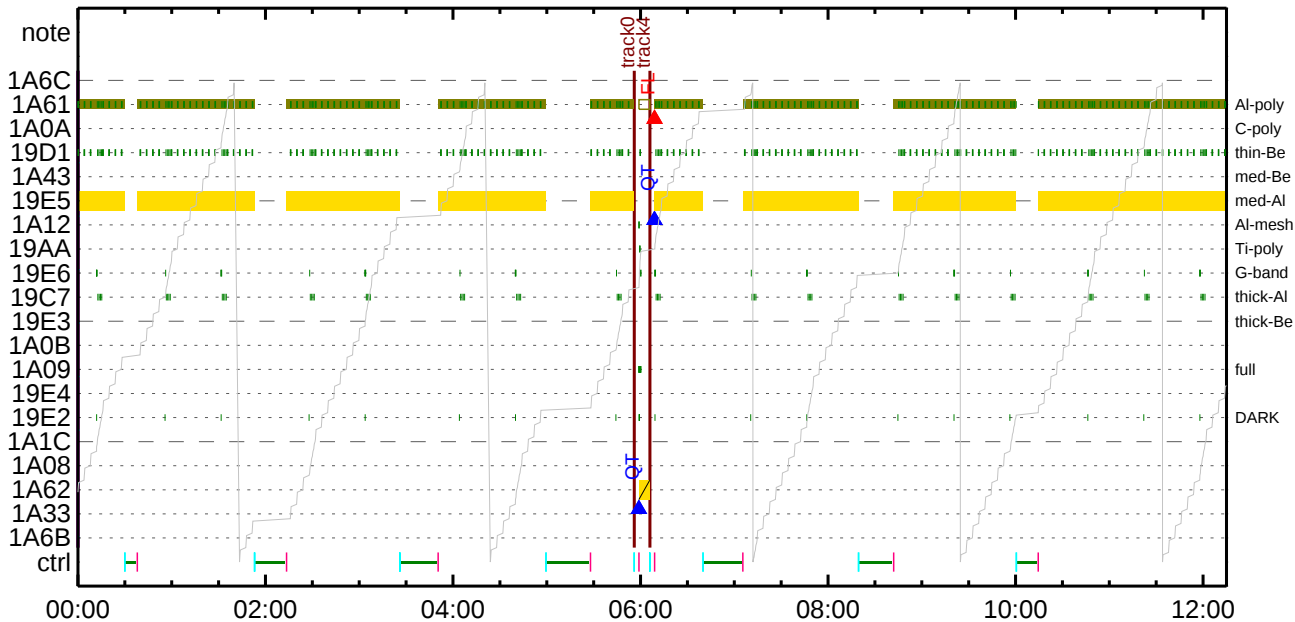
CMDI #0845 2015/01/10



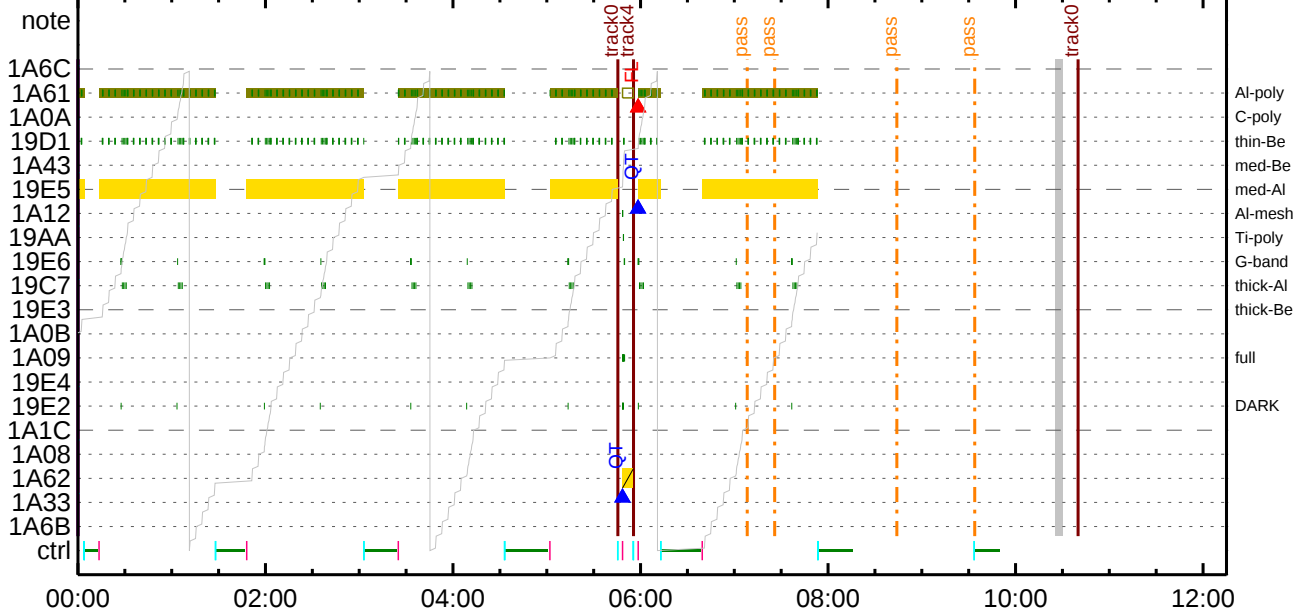
CMDI #0845 2015/01/10



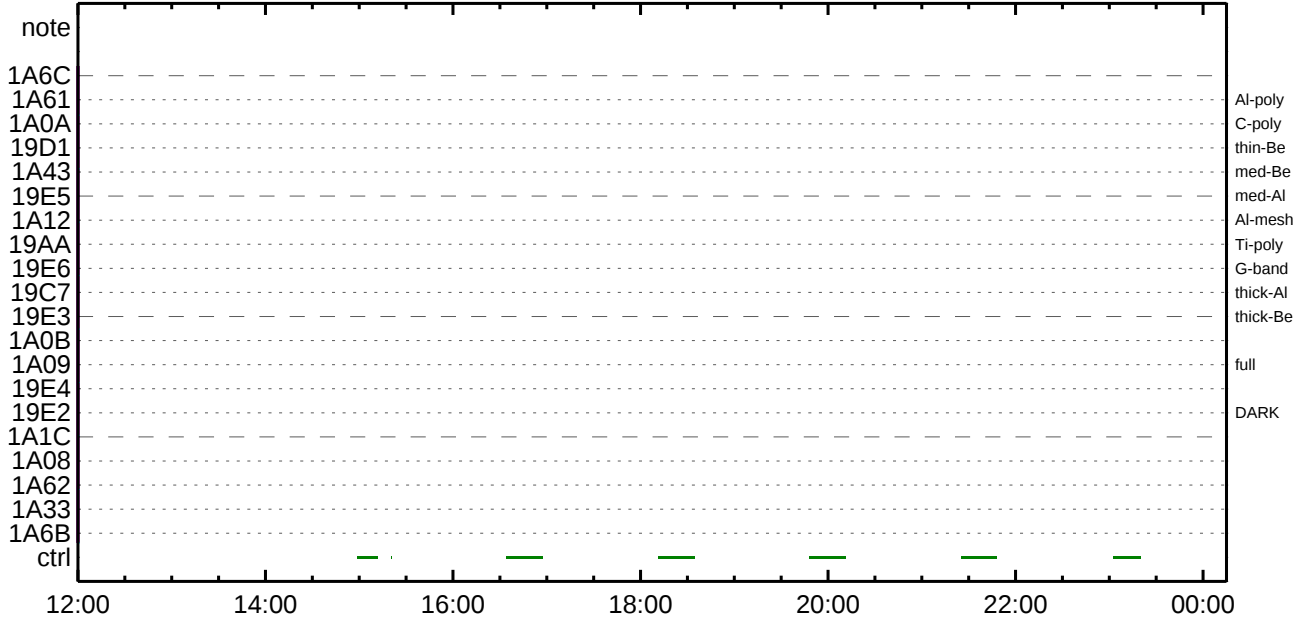
CMDI #0845 2015/01/11



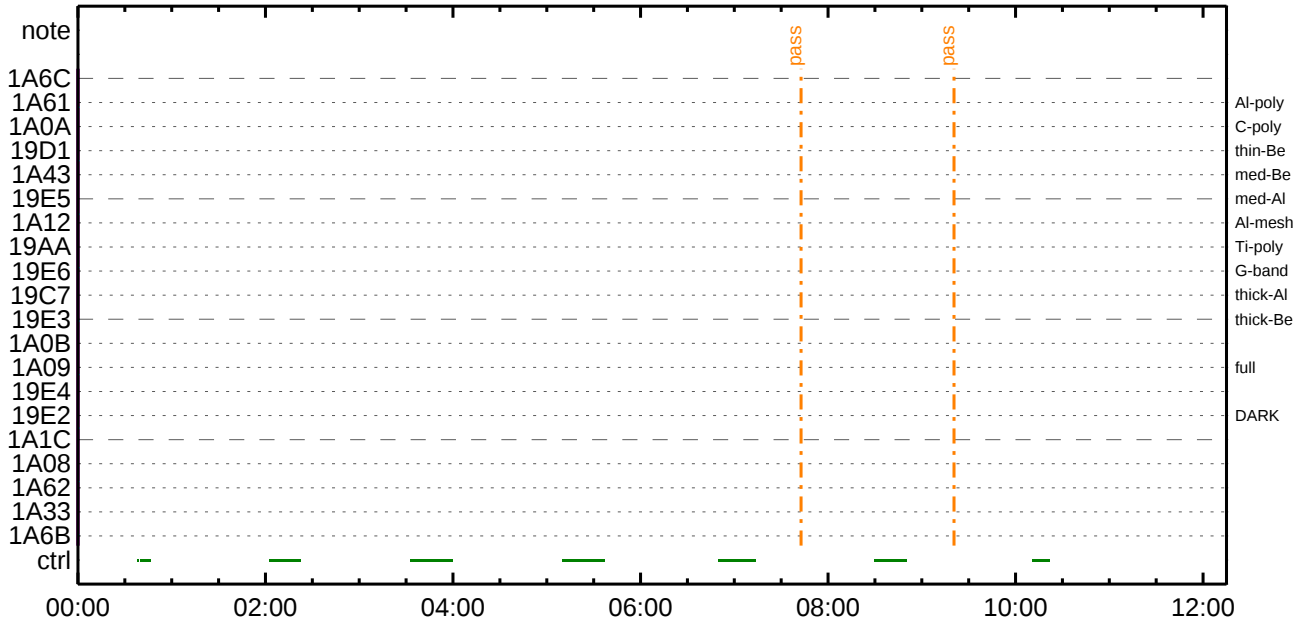
CMDI #0845 2015/01/13



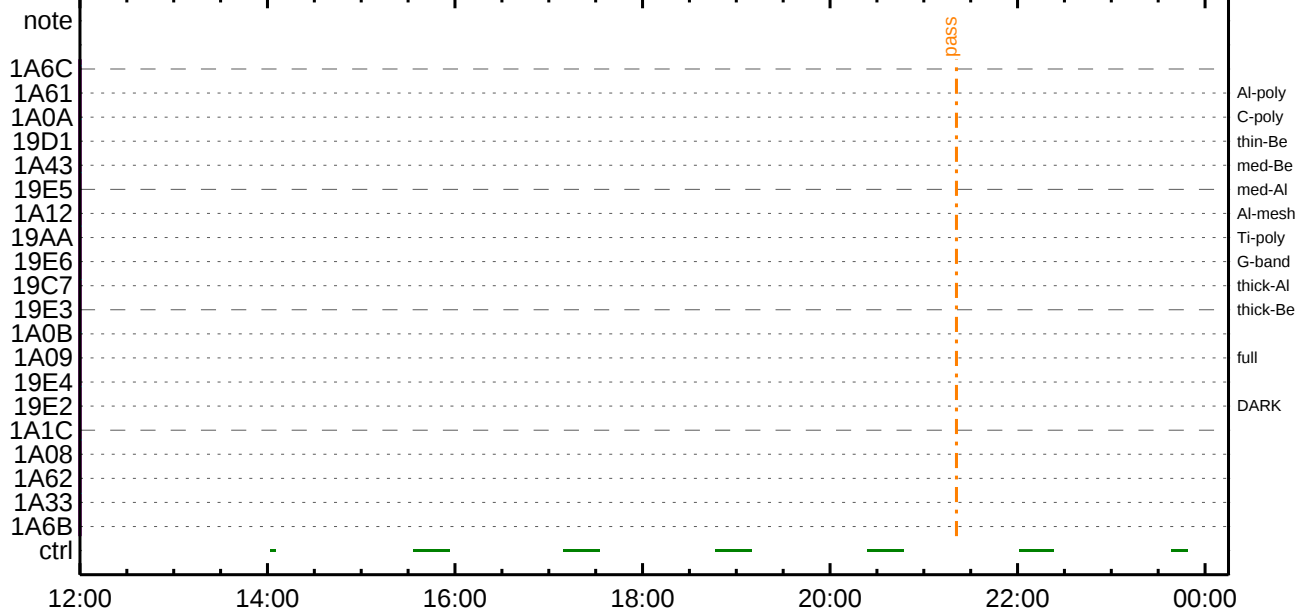
CMDI #0845 2015/01/13



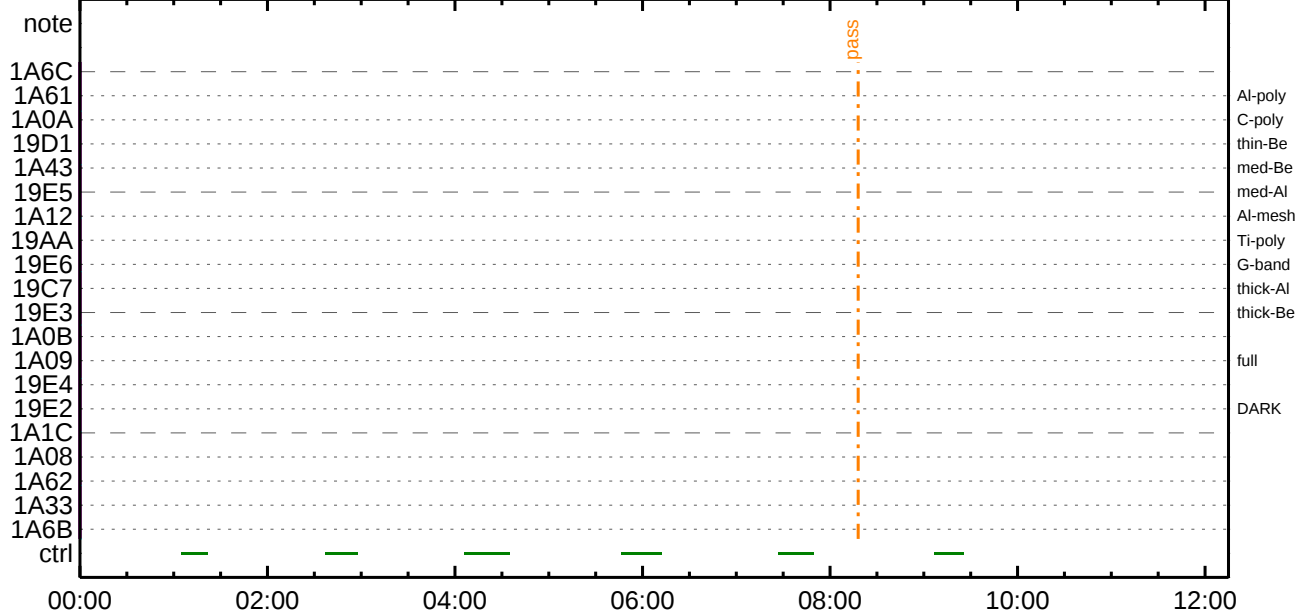
CMDI #0845 2015/01/14



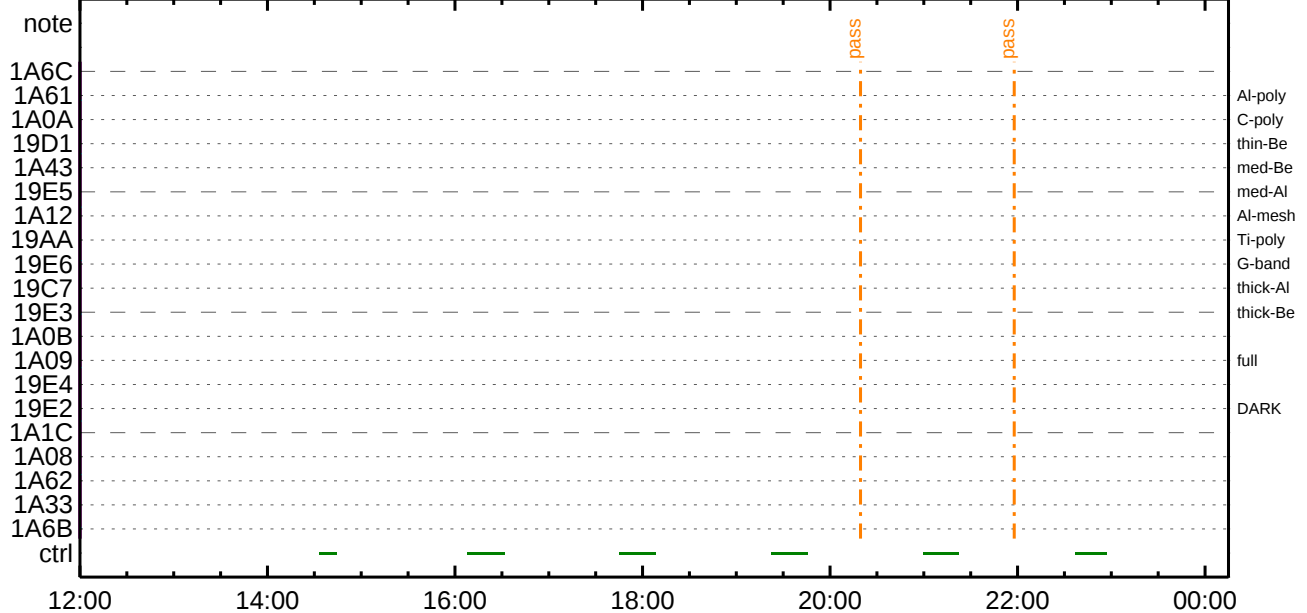
CMDI #0845 2015/01/14



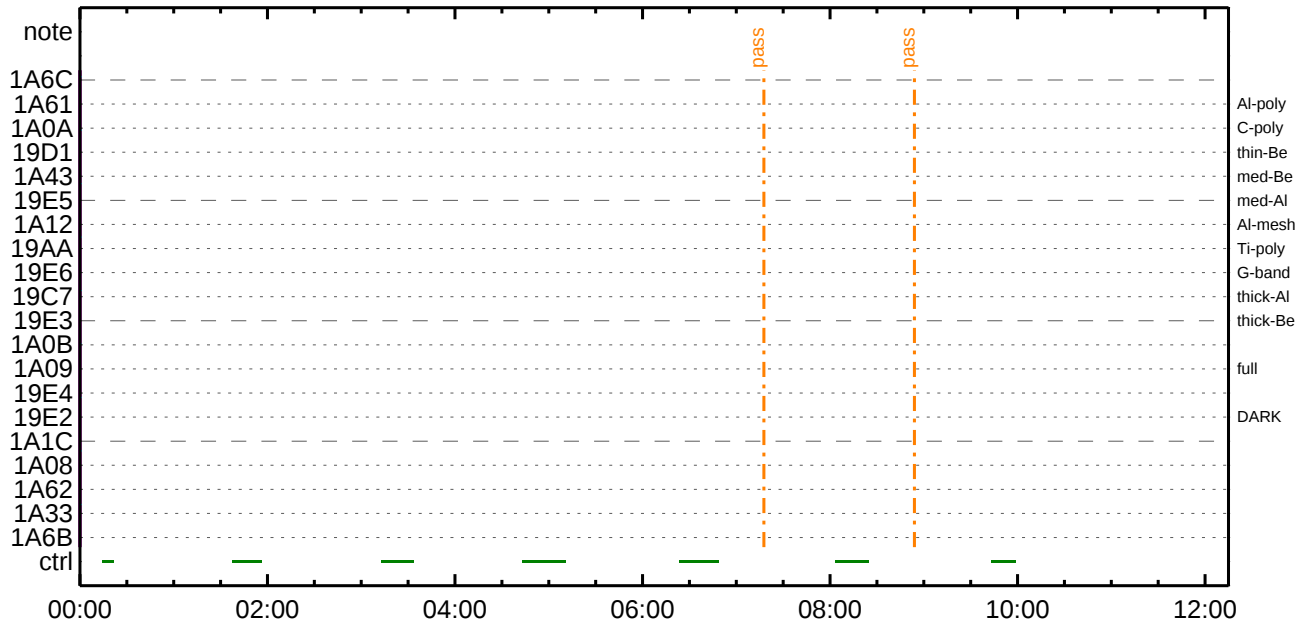
CMDI #0845 2015/01/15



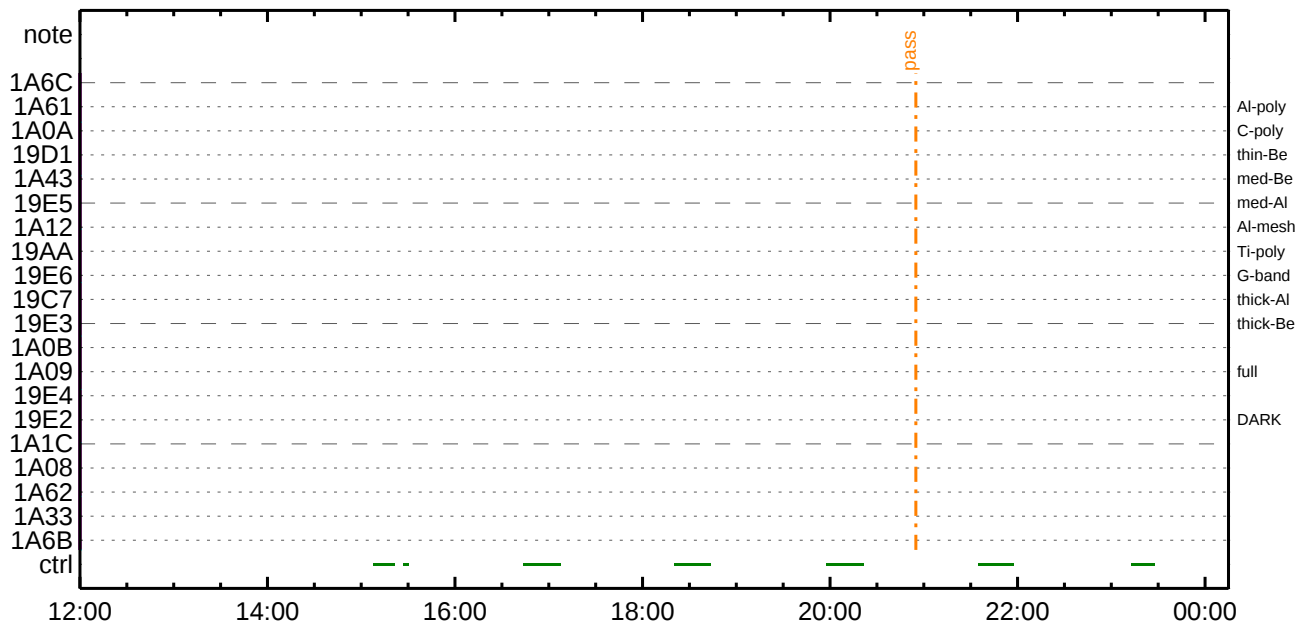
CMDI #0845 2015/01/15



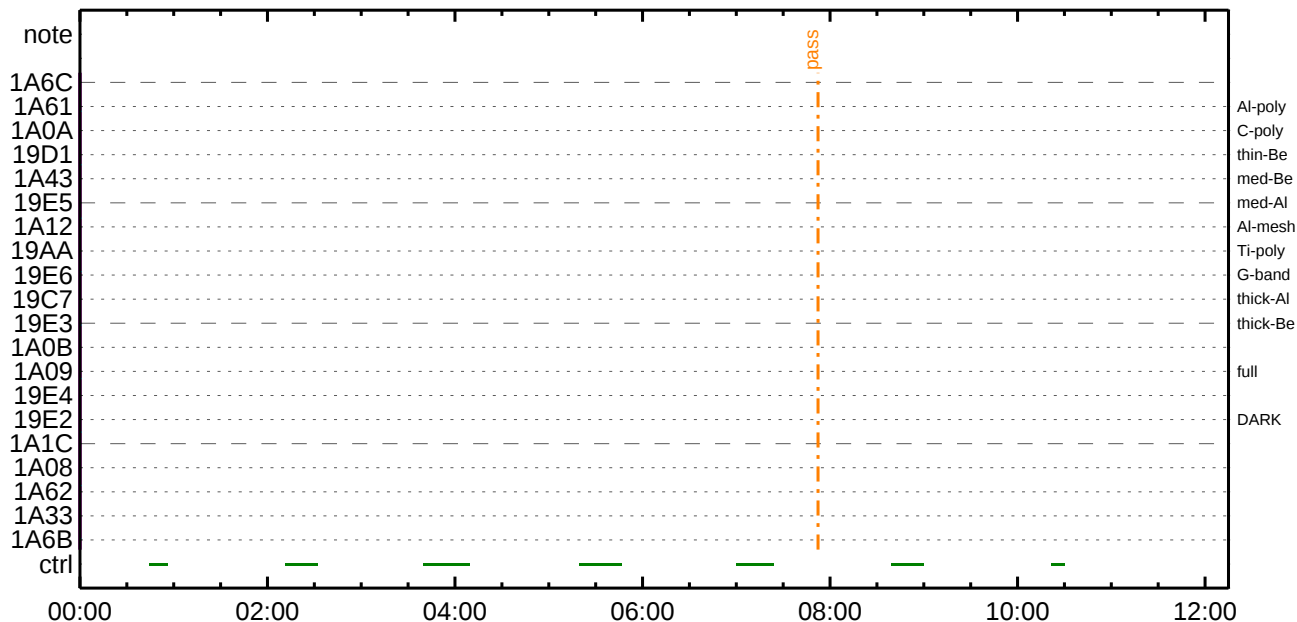
CMDI #0845 2015/01/16



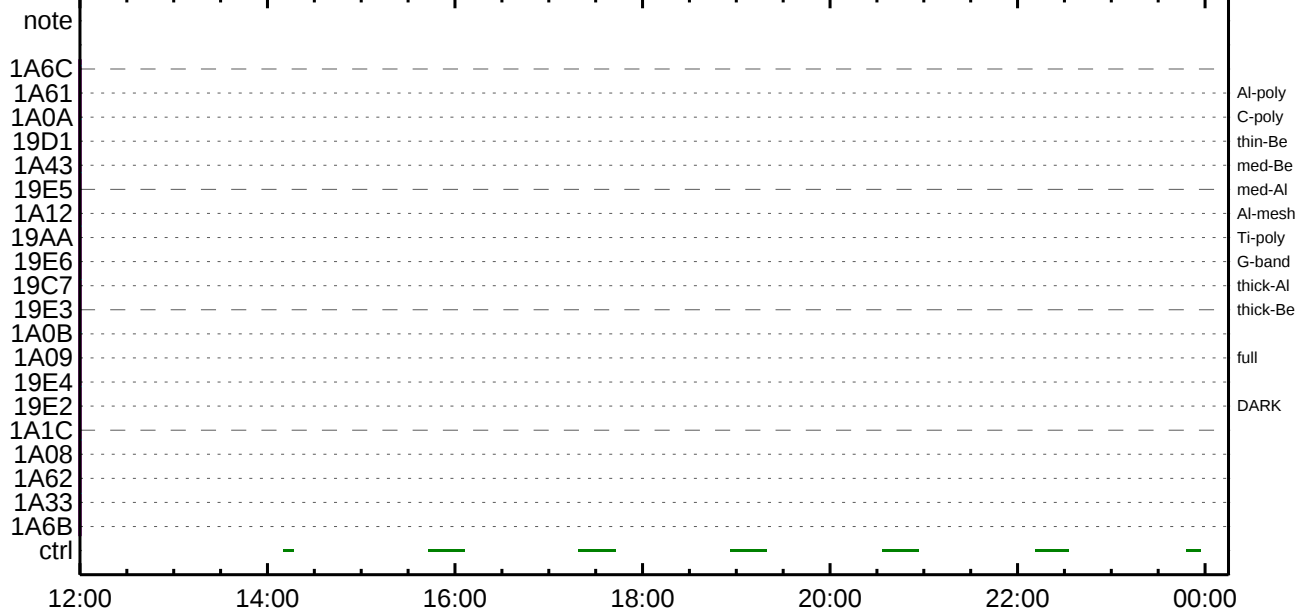
CMDI #0845 2015/01/16



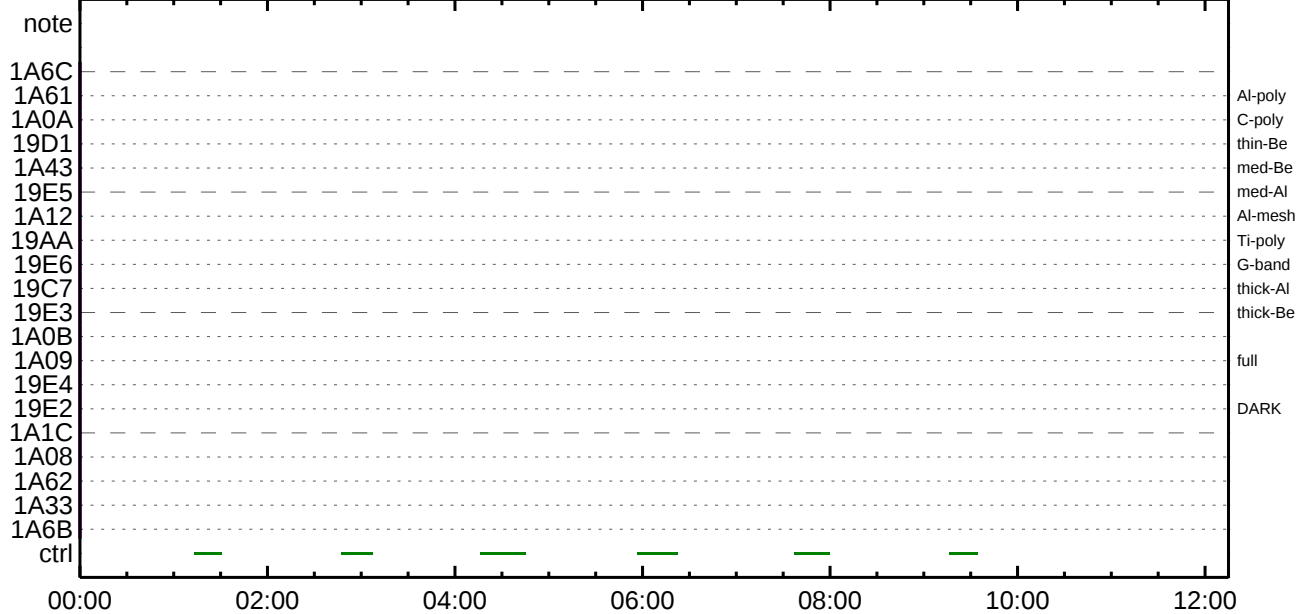
CMDI #0845 2015/01/17



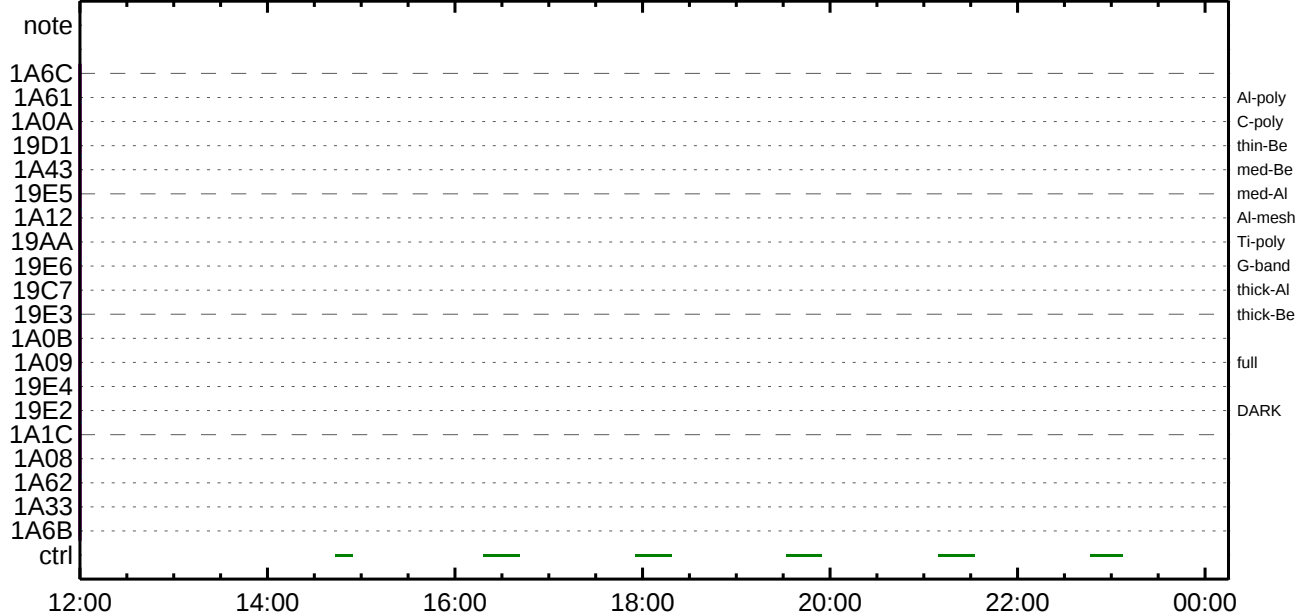
CMDI #0845 2015/01/17



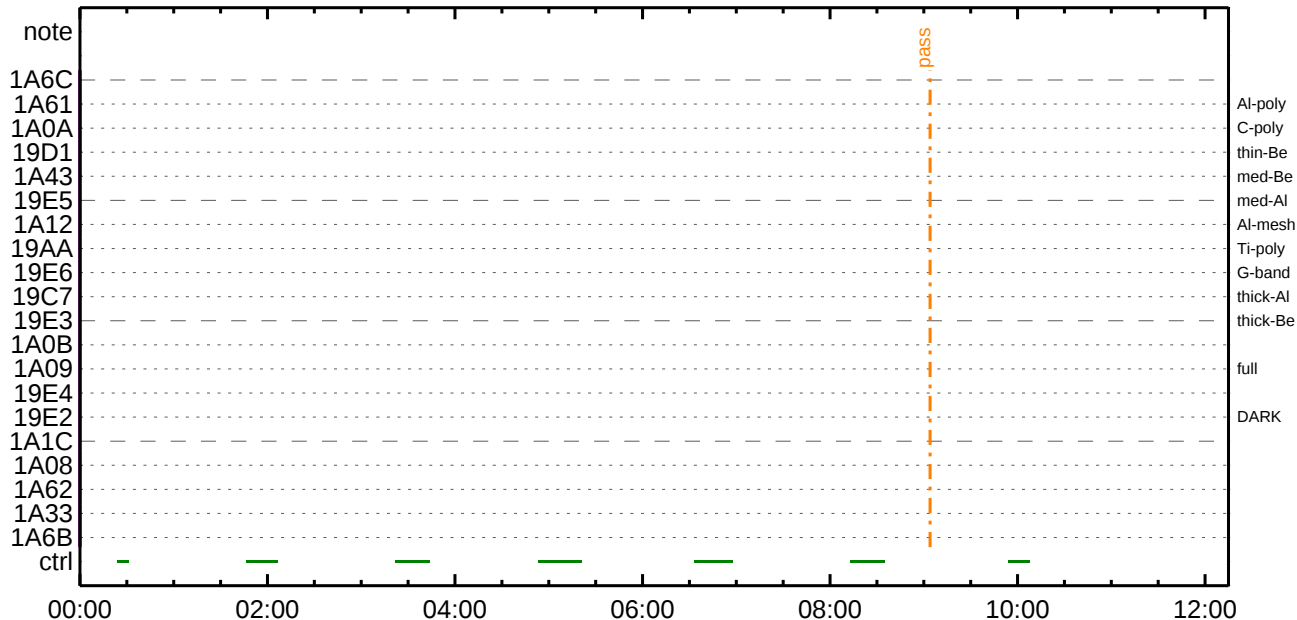
CMDI #0845 2015/01/18



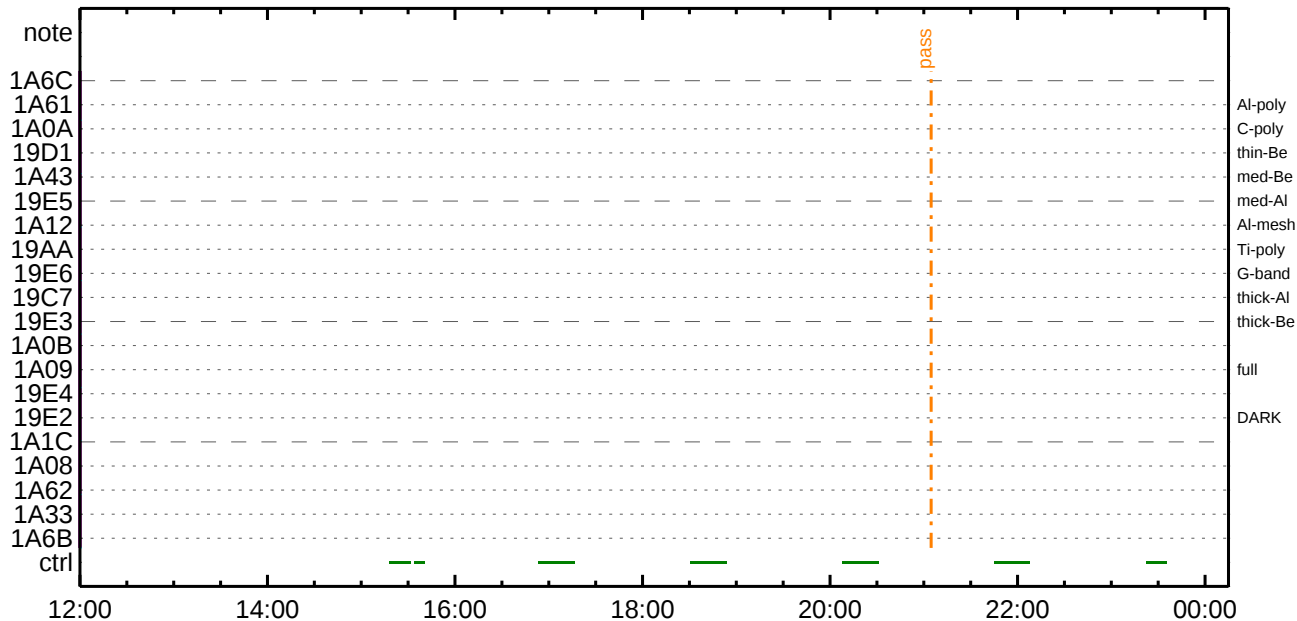
CMDI #0845 2015/01/18



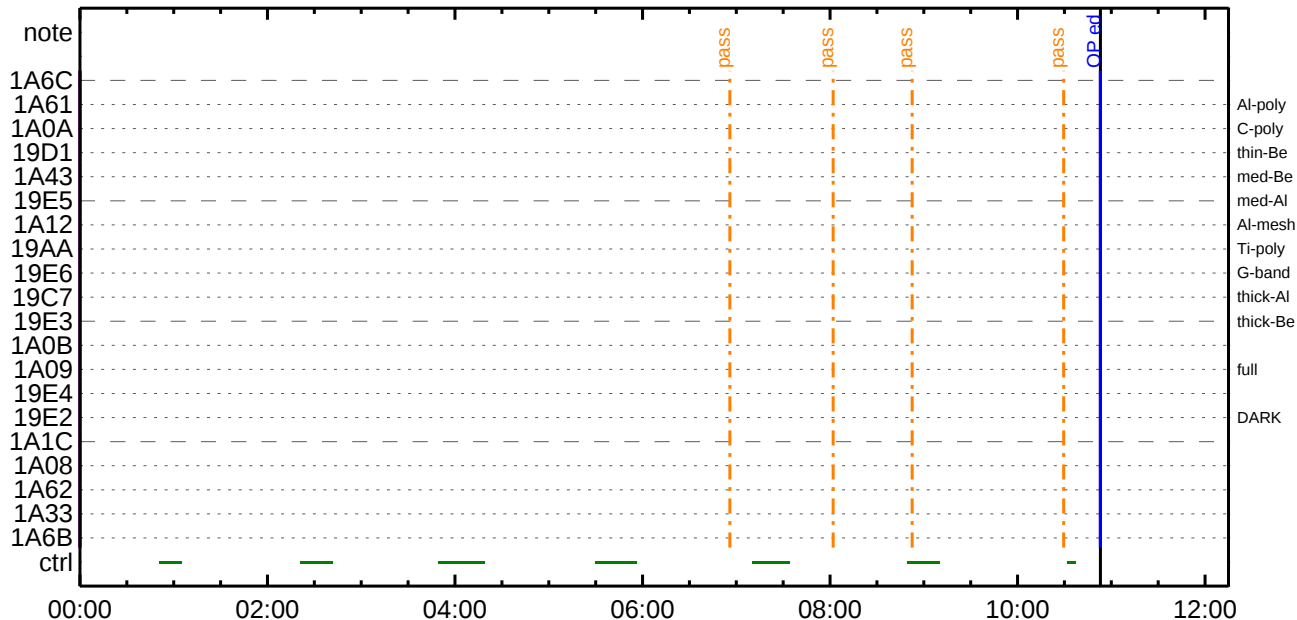
CMDI #0845 2015/01/19



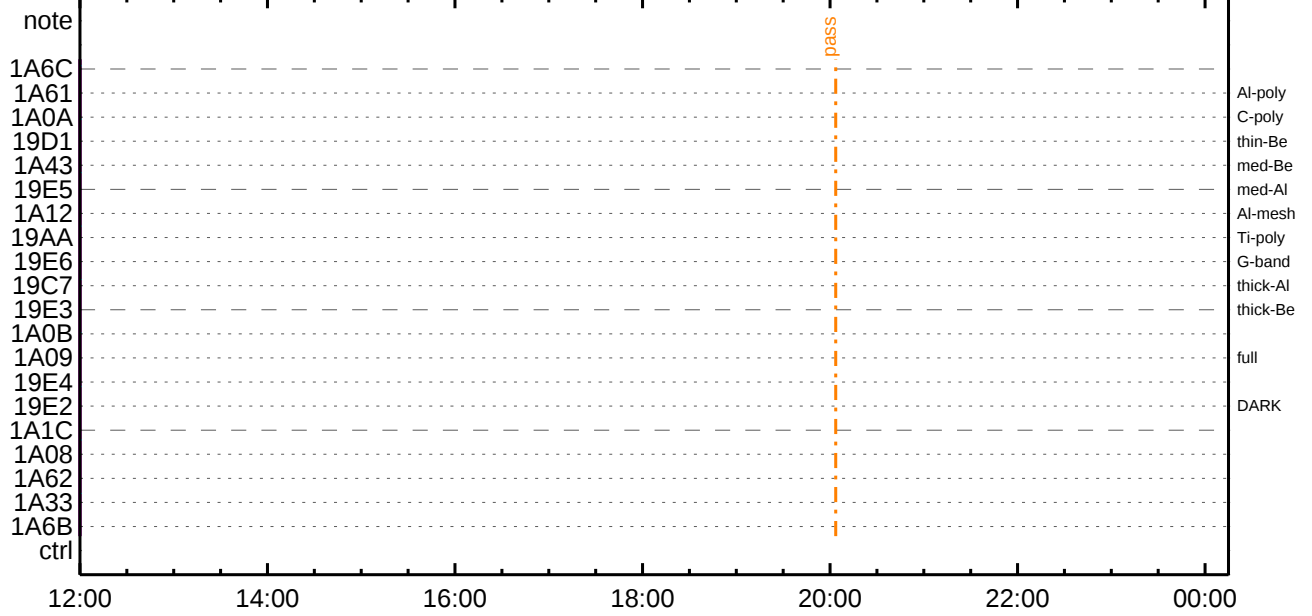
CMDI #0845 2015/01/19



CMDI #0845 2015/01/20



CMDI #0845 2015/01/20



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main-863 2015-01-10 13:15:20 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ¡ãAOS¥Á¥$¥Ã¥¬¼Å»Ü¡ä
0005 C.
0006 C. ¥Ä¥ß¡;¼¥³¥Ð¥Ó¥ÉÄ÷¿®
+ DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Ä¡¡Ë²¿Àª•µ°Æ»Î×ΑÇ²¡Ïφ¥Ä¥×¥¡¡;¼¥Ê¡ËÊè½µ•îÊË¡ËòÊ¼°ÇÒª•²¿¼¹Ç²¡¡ ÇÀ®, ù²¹²è²ª²ÇÄ÷¿®ª•²Ê²ª²³²È¡£
+ DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷¿®µ¡ON
0016 C. *****
0017 C. φ´ °ÆÄ, Î×ÊÝ²àLOSª²Ç²¡»þ´Ö²ð¹íî, ª•¡çÊÔÍ×ªEXÄÓON²ï¹Ô²Ê²¹²Ê²ª²³²È¡£
0018 C.
+ DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. φφ[HK1_XPA_ON/OFF] EQ ON
0025 C. φφ[HK1_XPA_PWR_HI/L0] EQ HI
0026 C. φφ[HK1_XMOD_ON/OFF] EQ ON
0027 C. φφ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥Ó¥É¥¡¥Ã¥¬¾ÒÄÕª¬¼Äêª•²¿²è¡¡φ°Ê²¼²²Ì°ÆÄ, ¼ê½Ç²ò¼Ä¹Ô²¹²è¡¡£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼¹°ÆÄ,
0033 C. *****
0034 C. φ´ RESTART¡ÊPT1¡Ëª•²¿²è¼¹¹Ç²¡¡¡φ°Ê²¼²²Ì¼Ä¹Ôª»ª°¡φDCBC-150ºØ¿Ê²à¡£
0035 C.
0036 . C. ¡ãPT1°ÆÄ, ³«»î¡ä
0037 + DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. φφ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¡¼Ú)
0043 C. φφ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¼Ú)
0044 C. φφ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¼Ú)
0045 C.
0046 . C. ¡ã¥φ¥ó¥Æ¥ÉÄÜÄØ¡ÊÄ•Ä°²óÊò¡Ë, ä²¹°ÆÄ, °Æ³«¡ä
+ DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. φφ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¡¼Ú)
0050 C. φφ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¼Ú)
0051 C. φφ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, º¬¼«Æ°Ää»βª•²¿, ä¡¡φ°Ê²¼²²Ì¼Ä¹Ô²¹²è¡£
0055 C. ¥φ¥ó¥Æ¥ÉÄÜÄØªÄÄ•Ä°²óÊòª¬¼á²¼¹¹Ç²¡¡'î»²¹²è²ª²ÇÄÔªÄ£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼¹°ÆÄ,
0059 C. *****
0060 C. φ´ RESTART¡ÊPT2¡Ëª•²¿²è¼¹¹Ç²¡¡¡φ°Ê²¼²²Ì¼Ä¹Ôª»ª°¡φDCBC-151ºØ¿Ê²à¡£
0061 C.
0062 . C. ¡ãPT2°ÆÄ, ³«»î¡ä
0063 + DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. φφ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¡¼Ú)
0069 C. φφ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¼Ú)
0070 C. φφ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¼Ú)
0071 C.
0072 . C. ¡ã¥φ¥ó¥Æ¥ÉÄÜÄØ¡ÊÄ•Ä°²óÊò¡Ë, ä²¹°ÆÄ, °Æ³«¡ä
0073 + DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. φφ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¡¼Ú)
0076 C. φφ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¼Ú)
0077 C. φφ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ß¡¡XÄ÷¿®µ¡OFF
0081 C. *****
0082 C.
0083 . C. ¡ãDR°ÆÄ, Ää»ß¡ä
0084 + DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. φφ[HK1_REP_STA/STP] EQ STOP
0087 C. φφ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. φφ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ¡ãXÄ÷¿®µ¡OFF¡ä
0091 + DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. φφ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. φφ[HK1_XPA_ON/OFF] EQ OFF
```

[illegible]

```

0194 C.
0195 +. TI 2015-01-10 10:19:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.
0198 C.
0199 C. °Ê²¼□İÄê¾İİÑ□İ¥Á¥$¥Ä¥¯¹àİÜ
0200 C.
0201 C.
0202 C.
0203 C.
0204 C.
0205 C. *****
0206 C. Tİİİ°è¥Ä¥Ó¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.
0213 C.
0214 C.
0215 C.
0216 C.
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.
0220 C.
0221 C.
0222 C.
0223 C.
0224 C.
0225 C.
0226 C.
0227 C.
0228 C. RAM ID=TI_TBL□İ¾Ê¹Ç•è²İOK□ð³İÇ$
0229 C.
0230 C. DHU¥âı¼¥ÊıÊ¾¥½, ¥İı¼¥ÊıË□ðİä□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.
0234 C.
0235 C.
0236 C.
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2015-01-10 10:19:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC (21 02)
0247 +. TI 2015-01-10 10:19:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC (22)
0250 C.
0251 C.
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2015-01-10 10:19:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC (c3)
0260 C.
0261 C.
0262 C.
0263 C.
0264 C.
0265 C.
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C.
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C.
0276 C.
0277 C.
0278 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-864 2015-01-10 13:05:10 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Ä»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äi;Ê¿¿A¤•µ°Æ»I×AÇ¤I¥¢¥Á¥×¥i;¼¥ÉjÊÊ½µ•iÍÊjÊ½¼°Ç¿¤•¤¿¼¹¿¤IjÇÄ®, Ü¤¹¤Ê¤P¤ÇÄ÷¿®¤•¤Ê¤¤¤³¤Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆÄ, Í×ÊÝ¤äLOS¤P¤Ç¿¤I»p´Ö¤ð¹ÍÍ, ¤•jÇÊÖÍ×¤ÊXÄÓON¤I¹Ö¤Ê¤I¤Ê¤¤¤³¤Èj£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ¢¢[HK1_XPA_ON/OFF] EQ ON
0025 C. ¢¢[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ¢¢[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¢¢[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥Ó¥É¥i¥Ã¥¼ÖÄÖ¤¬°ÄÄ¤¤•¤¿¤¤j¢°Ê²¼¤I°ÆÄ, ¼Ê½¿¤ð¼Ä¹Ö¤¹¤Êj£
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄI¼I°ÆÄ
0033 C. *****
0034 C. ¨ RESTARTjÊPT1jÊ¤•¤¿¤¤¼¹¿¤Ij¢°Ê²¼¤I¼Ä¹Ö¤¤¤°j¢DCBC-150¤Ø¿Ê¤¤¤j£
0035 C.
0036 . C. iãPT1°ÆÄ, ³«»Ijã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, i¼Ü)
0043 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ü)
0044 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ü)
0045 C.
0046 . C. iã¥¢¥Ó¥Æ¥ÊÄÜÄ¤jÊÄ•Ä²ÓÊðjÊ, ä¤I°ÆÄ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, i¼Ü)
0050 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ü)
0051 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ü)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ¤¬¼«Æ°Ää»B¤•¤¿, äj¢°Ê²¼¤ð¼Ä¹Ö¤¹¤Êj£
0055 C. ¥¢¥Ó¥Æ¥ÊÄÜÄ¤¤¤Ä•Ä²ÓÊð¤¬¤¤¤¼¹¿¤I°I»¤¹¤Ê¤P¤ÇÄÖ¤Äj£
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄI¼I°ÆÄ
0059 C. *****
0060 C. ¨ RESTARTjÊPT2jÊ¤•¤¿¤¤¼¹¿¤Ij¢°Ê²¼¤I¼Ä¹Ö¤¤¤°j¢DCBC-151¤Ø¿Ê¤¤¤j£
0061 C.
0062 . C. iãPT2°ÆÄ, ³«»Ijã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, i¼Ü)
0069 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ü)
0070 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ü)
0071 C.
0072 . C. iã¥¢¥Ó¥Æ¥ÊÄÜÄ¤jÊÄ•Ä²ÓÊðjÊ, ä¤I°ÆÄ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, i¼Ü)
0076 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, i¼Ü)
0077 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, i¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»BjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. iãDR°ÆÄ, Ää»Bjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¢¢[HK1_REP_STA/STP] EQ STOP
0087 C. ¢¢[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¢¢[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. iãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ¢¢[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¢¢[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 58s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2015-01-10 10:19:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÃÎï»ö¼ÝðÊÃð¹æðDCBC•x²è *****
0156 C. (%ã°ï¥Ö¥Ã¥Ê¥ð¥¥¥ã¥¢¥è¥Ê¼¥¥¥Ã»Û¹¥è)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•ï Daily±¿ÎÑæ´ð¹æðDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ¡ãLOS¥Ã¥$¥Ã¥´¼Ã»Û¡ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-865 2015-01-10 13:05:10 100 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀiĒ¿¿Aa•µ°£»İ×AÇ¿İ¥C¥A¥×¥i¼¥ĒjĒĒ¿µ•iĒĒjĒĒ¿°Ç¿•¿¿i¹Ç¿İjÇÀ®, ũ¿¹¿Ē¿¿¿ÇÁ÷¿®¿•¿Ē¿¿¿¿¿Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ()
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 08 08)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 04 04)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_FLD_ENA
0059 BC (d8)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061 BC (c8)
0062 + DC 07-F0 MDP_XRT_AEC_RESET
0063 BC (d0)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 + DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 10)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 13)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2015-01-10 10:19:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG ____
0083 C.
0084 C. ***** XRT END *****
0085 C.
0086 . C. ***** MDP ´ŪĀİ¿Ē»Ō%Ÿ¿ĒĀ¿¿¹¿ĒDCBC•x²è *****
0087 C. (%Ā°İ¥Ō¥Ā¥Ē¥D¥Ē¥Ā¥C¥Ē¿Ē½¿¿¿¼Ā»Ū¿¹¿Ē)
0088 . S. DC-BC dcbc-402:DCBC
0089 (MDP_known_event)
0090 C.
0091 C.
0092 . C. ***** ¥Đ¥¹•İ Daily±¿İŅ¿Ē´Ō¿¹¿ĒDCBC•x²è *****
0093 . S. DC-BC dcbc-153:DCBC
0094 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095 C.
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0096 C.
0097 . C. ; ãLOS¥Á¥S¥Ã¥¼Ã»Ü; ä
0098 C.
0099 . C. ***** LOS *****
0100 C.

Jan 10, 15 13:05

XRT_OGLIST_0845.chk

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*** OP Sequence for XRT ***

2015/01/10	10:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2015/01/10	10:46:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	d6	67
2015/01/10	11:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/10	11:01:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/10	11:01:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/01/10	11:02:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/01/10	11:02:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/01/10	11:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/01/10	11:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/01/10	11:04:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2015/01/10	11:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/10	11:18:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	29	99
2015/01/10	11:34:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	53	33
2015/01/10	11:50:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2015/01/10	12:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2015/01/10	12:16:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	ac	5b	00	00
2015/01/10	12:32:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	b4	b5	24	8b
2015/01/10	12:48:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2015/01/10	12:58:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2015/01/10	13:08:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2015/01/10	13:24:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2015/01/10	13:40:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2015/01/10	13:56:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	29	db	48	72
2015/01/10	14:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/10	14:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/10	14:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/01/10	14:30:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	03	00	00	00	00
2015/01/10	14:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/01/10	14:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/01/10	14:30:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/01/10	14:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/01/10	14:30:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/10	14:32:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/01/10	14:32:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/01/10	14:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/10	14:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/10	14:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/10	14:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/10	14:49:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/01/10	14:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/01/10	15:01:01.0	XRT_Custom_430_OG [0x1ae]							
2015/01/10	15:02:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/01/10	16:24:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/10	16:24:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/01/10	16:24:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/01/10	16:24:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2015/01/10	16:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/10	16:48:00.0	XRT_Custom_430_OG [0x1ae]			
2015/01/10	16:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/10	17:37:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	17:37:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	17:37:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/10	17:37:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/01/10	17:37:48.0	XRT_FLD_DIS_422_OG [0x1a6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/10	17:40:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/10	17:40:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/10	17:40:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/01/10	17:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/10	17:47:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	17:47:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	17:47:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/10	17:47:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/01/10	17:47:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/10	17:47:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/10	17:47:52.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/10	17:47:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/10	17:47:56.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/10	17:50:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/01/10	17:50:28.0	XRT_FL_PROG_SET_401_OG [0x191]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/01/10	17:50:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/10	18:01:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	18:01:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	18:01:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/10	18:01:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/10	18:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/10	18:24:30.0	XRT_Custom_430_OG [0x1ae]			
2015/01/10	18:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/10	19:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	19:38:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	19:38:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/10	19:38:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/10	19:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/10	20:01:30.0	XRT_Custom_430_OG [0x1ae]			
2015/01/10	20:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/10	21:15:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	21:15:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	21:15:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/10	21:15:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/10	21:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/10	21:38:30.0	XRT_Custom_430_OG [0x1ae]			
2015/01/10	21:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/10	22:53:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	22:53:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/10	22:53:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/10	22:53:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			

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			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/10	22:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/10	23:13:00.0	XRT_Custom_430_OG [0x1ae]				
2015/01/10	23:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/11	00:30:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	00:30:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	00:30:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/11	00:30:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/11	00:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/11	00:37:00.0	XRT_Custom_430_OG [0x1ae]				
2015/01/11	00:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/11	01:53:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	01:53:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	01:53:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/11	01:53:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/11	01:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/11	02:12:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/11	02:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/11	03:26:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	03:26:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	03:26:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/11	03:26:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/11	03:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/11	03:49:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/11	03:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/11	04:59:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	04:59:32.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	04:59:34.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/11	04:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/11	05:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/11	05:27:00.0	XRT_Custom_430_OG [0x1ae]				
2015/01/11	05:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/11	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	05:55:56.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	05:55:58.0	XRT_FOCUS_POSITION_403_OG [0x193]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/11	05:56:00.0	AOCS_ORe-point_Start_3_OG [0x099]				
			AOCU_NM	5	02-76	00 00 00 00 00
2015/01/11	05:56:18.0	XRT_FLD_DIS_422_OG [0x1a6]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/11	05:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/11	05:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/11	05:58:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/01/11	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/11	06:05:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	06:05:56.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/11	06:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/11	06:06:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]				
			AOCU_NM	5	02-76	04 00 00 00 00
2015/01/11	06:06:18.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/11	06:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/11	06:06:22.0	XRT_AEC_RESET_413_OG [0x19d]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/11	06:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/11	06:06:26.0	XRT_FLD_RESET_407_OG [0x197]				

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2015/01/11	06:08:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/11	06:08:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2015/01/11	06:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2015/01/11	06:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/11	06:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	06:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	06:40:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/11	06:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/11	07:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/11	07:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/11	08:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	08:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	08:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	08:19:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/11	08:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/11	08:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/11	08:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/11	10:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	10:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	10:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	10:00:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/11	10:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/11	10:13:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/11	10:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/11	14:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	14:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	14:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	14:30:00.0	AOCs_OrE-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/01/11	14:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/01/11	14:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/01/11	14:30:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/01/11	14:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/01/11	14:30:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/01/11	14:32:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/11	14:32:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2015/01/11	14:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2015/01/11	15:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/11	15:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	15:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	15:23:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/11	15:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/11	15:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/11	15:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/11	16:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	16:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	16:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/11	16:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/11	17:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2015/01/11	17:23:30.0	XRT_Custom_430_OG [0x1ae]		
2015/01/11	17:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/11	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/01/11	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 00 00 00 00 00
2015/01/11	18:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2015/01/11	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2015/01/11	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/11	18:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 03
2015/01/11	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/11	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/01/11	18:10:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]		
		AOCU_NM	5	02-76 04 00 00 00 00
2015/01/11	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/01/11	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/01/11	18:10:22.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/01/11	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/11	18:10:26.0	XRT_FLD_RESET_407_OG [0x197]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/11	18:12:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0f
2015/01/11	18:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2015/01/11	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/11	18:36:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	18:36:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	18:36:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/11	18:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/11	18:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/11	19:00:00.0	XRT_Custom_430_OG [0x1ae]		
2015/01/11	19:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/11	20:14:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	20:14:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	20:14:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/11	20:14:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/11	20:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/11	20:37:30.0	XRT_Custom_430_OG [0x1ae]		
2015/01/11	20:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/11	21:51:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	21:51:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	21:51:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/11	21:51:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/11	21:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/11	22:13:30.0	XRT_Custom_430_OG [0x1ae]		
2015/01/11	22:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/11	23:28:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	23:28:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/11	23:28:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/11	23:28:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/11	23:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		

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2015/01/11	23:40:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/11	23:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/01/12	00:56:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	00:56:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	00:56:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/01/12	00:56:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/01/12	00:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9
2015/01/12	01:11:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/12	01:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/01/12	02:27:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	02:27:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	02:27:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/01/12	02:27:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/01/12	02:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9
2015/01/12	02:48:00.0	XRT_Custom_430_OG [0x1ae]				
2015/01/12	02:49:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/01/12	03:56:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	03:56:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	03:56:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/01/12	03:56:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/01/12	03:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9
2015/01/12	04:25:30.0	XRT_Custom_430_OG [0x1ae]				
2015/01/12	04:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/01/12	05:36:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	05:36:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	05:36:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/01/12	05:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/01/12	05:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9
2015/01/12	06:03:00.0	XRT_Custom_430_OG [0x1ae]				
2015/01/12	06:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/01/12	06:12:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	06:12:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	06:12:58.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION		4	07-F8	22 ff aa 00
2015/01/12	06:13:00.0	AOCS_Ore-point_Start_3_OG [0x099]				
		AOCU_NM		5	02-76	00 00 00 00 00
2015/01/12	06:13:18.0	XRT_FLD_DIS_422_OG [0x1a6]				
		MDP_XRT_FLD_DIS		1	07-F0	d9
2015/01/12	06:15:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9
2015/01/12	06:15:56.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS		1	07-F0	d5
2015/01/12	06:15:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]				
		MDP_XRT_QT_PROG_SET		2	07-F0	c4 03
2015/01/12	06:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/01/12	06:22:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	06:22:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/01/12	06:22:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2015/01/12	06:23:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]				
		AOCU_NM		5	02-76	04 00 00 00 00
2015/01/12	06:23:18.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA		1	07-F0	d8
2015/01/12	06:23:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8
2015/01/12	06:23:22.0	XRT_AEC_RESET_413_OG [0x19d]				
		MDP_XRT_AEC_RESET		1	07-F0	d0
2015/01/12	06:23:24.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS		1	07-F0	d5
2015/01/12	06:23:26.0	XRT_FLD_RESET_407_OG [0x197]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/01/12	06:25:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]				

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2015/01/12	06:25:58.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2015/01/12	06:26:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2015/01/12	07:16:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/12	07:16:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	07:16:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	07:16:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/12	07:19:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/12	07:40:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/12	07:41:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2015/01/12	08:56:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/12	08:56:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	08:56:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	08:56:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/12	08:59:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/12	09:16:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/12	09:17:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2015/01/12	10:39:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/12	10:39:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	10:39:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	10:39:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/12	10:42:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/12	10:42:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/12	10:43:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2015/01/12	14:24:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/12	14:24:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	14:24:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	14:24:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/12	14:27:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/12	14:29:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/12	14:29:56.0	XRT_CTRL_MANU_402_0G [0x192]					
2015/01/12	14:29:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	14:30:00.0	AOCs_Or-point_Start_16_0G [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/01/12	14:30:18.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	03	00 00 00 00
2015/01/12	14:30:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/01/12	14:30:22.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/01/12	14:30:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/01/12	14:30:26.0	XRT_FLD_RESET_407_0G [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/01/12	14:32:56.0	XRT_QT_PROG_SET_427_0G [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/12	14:32:58.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2015/01/12	14:34:00.5	XRT_Custom_430_0G [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2015/01/12	14:35:00.5	XRT_CTRL_AUTO_424_0G [0x1a8]					
2015/01/12	15:58:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/01/12	15:58:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	15:58:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/01/12	15:58:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/01/12	16:01:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/01/12	16:22:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/01/12	16:23:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2015/01/12	17:35:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2015/01/12	17:35:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	17:35:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	17:35:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/12	17:38:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/12	17:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/12	17:59:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	17:59:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	18:00:00.0	AOCs_0Re-point_Start_3_0G [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/12	18:00:18.0	XRT_FLD_DIS_422_0G [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00
2015/01/12	18:02:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/12	18:02:56.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/12	18:02:58.0	XRT_QT_PROG_SET_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/12	18:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/01/12	18:09:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/12	18:09:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	18:09:58.0	XRT_FOCUS_RECALIBRATE_445_0G [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	18:10:00.0	AOCs_0Re-point_Start_17_0G [0x0a7]	XRT_FOCUS_RECAL	2	07-F8	78 00
2015/01/12	18:13:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	AOCU_NM	5	02-76	04 00 00 00 00
2015/01/12	18:14:18.0	XRT_FLD_ENA_411_0G [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/12	18:14:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/12	18:14:22.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/12	18:14:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/12	18:14:26.0	XRT_FLD_RESET_407_0G [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/12	18:16:56.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/12	18:16:58.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/01/12	18:17:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/01/12	19:12:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/12	19:12:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	19:12:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	19:12:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/12	19:15:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/12	19:35:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/12	19:36:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430_0G [0x1ae]	1	07-F0	c0
2015/01/12	20:49:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/12	20:49:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	20:49:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	20:49:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/12	20:52:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/12	21:13:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/12	21:14:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430_0G [0x1ae]	1	07-F0	c0
2015/01/12	22:27:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/12	22:27:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	22:27:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/12	22:27:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/12	22:30:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/12	22:48:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/12	22:49:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430_0G [0x1ae]	1	07-F0	c0
2015/01/13	00:04:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2015/01/13	00:04:02.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	00:04:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/13	00:04:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/13	00:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/13	00:12:30.0	XRT_Custom_430_OG [0x1ae]			
2015/01/13	00:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/13	01:28:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	01:28:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	01:28:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/13	01:28:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/13	01:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/13	01:47:00.0	XRT_Custom_430_OG [0x1ae]			
2015/01/13	01:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/13	03:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	03:03:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	03:03:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/13	03:03:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/13	03:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/13	03:24:00.0	XRT_Custom_430_OG [0x1ae]			
2015/01/13	03:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/13	04:33:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	04:33:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	04:33:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/13	04:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/13	04:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/13	05:01:00.0	XRT_Custom_430_OG [0x1ae]			
2015/01/13	05:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/13	05:45:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	05:45:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	05:45:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/13	05:45:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/01/13	05:45:48.0	XRT_FLD_DIS_422_OG [0x1a6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/13	05:48:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/13	05:48:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/13	05:48:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/01/13	05:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/13	05:55:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	05:55:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	05:55:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/13	05:55:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/01/13	05:55:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/13	05:55:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/13	05:55:52.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/13	05:55:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/13	05:55:56.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/13	05:58:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/01/13	05:58:28.0	XRT_FL_PROG_SET_401_OG [0x191]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/01/13	05:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2015/01/13	06:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	06:13:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	06:13:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/13	06:13:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/13	06:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/13	06:38:30.5	XRT_Custom_430_OG [0x1ae]			
2015/01/13	06:39:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/13	07:53:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	07:53:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	07:53:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/13	07:53:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/13	07:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/13	09:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	09:33:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/13	09:33:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/13	09:33:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/13	09:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/13	10:40:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00