

XRT Timeline to be uploaded on 2015/01/29

Period: 2015/01/29 09:12:00 - 2015/02/05 10:40:00

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Normal mode

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XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms																
Term			Pointing (x, y)				Comment									
01/30 14:03:00 - 01/30 14:09:54			Fixed (-528.4, -528.4)				post bake out obs (1/4)									
PROG= 04			1-time(s)													
Subr= 1			1-time(s)				12.0sec									
Seqn= 88			1-time(s)				12.0sec									
Open/G-band			Open/G-band		open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 1536)		Q=90	0	0	2.0sec
Open/G-band			Open/G-band		open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 1536)		Q=90	0	0	2.0sec
Open/thick-Be			Open/thick-Be		close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 1536)		Q=98	0	0	2.0sec
Open/thick-Be			Open/thick-Be		close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 1536)		Q=98	0	0	2.0sec
Subr= 2			1-time(s)				2.0sec									
Seqn= 23			2-time(s)				2.0sec									
Open/Al-mesh			Open/Ti-poly		close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Ti-poly			Open/thick-Al		close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 3			2-time(s)				2.0sec									
Seqn= 12			1-time(s)				2.0sec									
Open/G-band			Open/G-band		close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)		DPCM	0	0	2.0sec
Seqn= 14			1-time(s)				2.0sec									
Open/G-band			Open/G-band		open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter			Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2															
Term			Pointing (x, y)				Comment								
01/30 14:13:00 - 01/30 14:19:54			Fixed (528.4, -528.4)				post bake out obs (2/4)								
PROG= 08 1-time(s)															
Subr= 1		1-time(s)		12.0sec											
Seqn= 31		1-time(s)		12.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 1536)		Q=90	0	0	2.0sec
Open/G-band		Open/G-band		open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 1536)		Q=90	0	0	2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 1536)		Q=98	0	0	2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 1536)		Q=98	0	0	2.0sec
Subr= 2		1-time(s)		2.0sec											
Seqn= 23		2-time(s)		2.0sec											
Open/Al-mesh		Open/Ti-poly		close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 3		2-time(s)		2.0sec											
Seqn= 12		1-time(s)		2.0sec											
Open/G-band		Open/G-band		close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)		DPCM	0	0	2.0sec
Seqn= 14		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter			Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2															
Term			Pointing (x, y)				Comment								
01/30 14:23:00 - 01/30 14:29:54			Fixed (528.4, 528.4)				post bake out obs (3/4)								
PROG= 18 1-time(s)															
Subr= 1		1-time(s)		12.0sec											
Seqn= 81		1-time(s)		12.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 512)		Q=90	0	0	2.0sec
Open/G-band		Open/G-band		open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 512)		Q=90	0	0	2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 512)		Q=98	0	0	2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 512)		Q=98	0	0	2.0sec
Subr= 2		1-time(s)		2.0sec											
Seqn= 23		2-time(s)		2.0sec											
Open/Al-mesh		Open/Ti-poly		close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 3		2-time(s)		2.0sec											
Seqn= 12		1-time(s)		2.0sec											
Open/G-band		Open/G-band		close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)		DPCM	0	0	2.0sec
Seqn= 14		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter			Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms														
Term			Pointing (x, y)			Comment								
01/30 14:33:00 - 01/30 14:39:54			Fixed (-528.4, 528.4)			post bake out obs (4/4)								
PROG= 09 1-time(s)														
└ Subr= 1 1-time(s) 12.0sec														
└ Seqn= 28 1-time(s) 12.0sec														
└ Open/G-band		Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	
└ Open/G-band		Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	

	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 23	2-time(s)	2.0sec											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3	2-time(s)	2.0sec											
Seqn= 12	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 14	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A33: CME watch - 4x4 - AEC 2 - Al-poly - G-band (2x2,8ms) - Leak (33ms) - 5min cad													
Term		Pointing (x, y)		Comment									
01/30 14:43:00 - 01/30 16:56:30		Fixed (-970.0, 140.0)		# DST coordinate obs at E limb									
02/02 18:14:30 - 02/03 00:11:24		Track (645.3, 36.8) @ 02/02 18:11:30		# Quiet sun obs for SOT/EIS									
PROG= 02 Inf.-time(s)													
┌ Subr= 1 20-time(s) 300.0sec													
└ Seqn= 78 1-time(s) 4.0sec													
└ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec													
┌ Subr= 2 1-time(s) 2.0sec													
└ Seqn= 6 1-time(s) 2.0sec													
└ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec													
└ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #19E5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly cont														
Term		Pointing (x, y)				Comment								
01/30 17:21:30 - 01/30 17:59:54		Track (414.6, -76.7) @ 01/30 17:00:00				# track AR 12268								
01/30 18:13:00 - 01/31 06:09:24		Track (424.4, -77.1) @ 01/30 18:10:00				# track AR 12268								
01/31 06:22:30 - 01/31 17:57:24		Track (522.5, -82.3) @ 01/31 06:19:30				# track AR 12268								
01/31 18:10:30 - 02/01 05:41:54		Track (610.9, -88.4) @ 01/31 18:07:30				# track AR 12268								
02/01 05:59:00 - 02/01 17:44:24		Track (690.8, -95.7) @ 02/01 05:52:00				# track AR 12268								
02/01 17:57:30 - 02/02 05:49:54		Track (763.5, -104.1) @ 02/01 17:54:30				# track AR 12268								
02/03 00:14:30 - 02/03 06:19:24		Track (898.4, -129.2) @ 02/03 00:11:30				# track AR 12268								
02/03 06:32:30 - 02/03 08:30:00		Track (917.1, -135.0) @ 02/03 06:29:30				# track AR 12268								
PROG= 15 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 8		2-time(s)		2.0sec										
		Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2		2-time(s)		2.0sec										
Seqn= 24		1-time(s)		2.0sec										
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42		4-time(s)		2.0sec										
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
		thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62		8-time(s)		240.0sec										
		thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
		thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
		thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A6B: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly																
Term		Pointing (x, y)					Comment									
01/30 18:03:00 - 01/30 18:09:54		Fixed (0.0, 0.0)					synoptic									
PROG= 01		1-time(s)														
Subr= 1		1-time(s)					12.0sec									
Seqn= 33		1-time(s)					4.0sec									
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Seqn= 5		1-time(s)					2.0sec									
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)				Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)				Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)				Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)				DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)				DPCM	0	0	2.0sec
Seqn= 40		1-time(s)					4.0sec									
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
└ Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thir

Term	Pointing (x, y)	Comment
01/31 06:12:30 - 01/31 06:19:24	Fixed (0.0, 0.0)	synoptic, shifted 9.5 min
01/31 18:00:30 - 01/31 18:07:24	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min
02/01 05:45:00 - 02/01 05:51:54	Fixed (0.0, 0.0)	synoptic, shifted -18.0 min
02/01 17:47:30 - 02/01 17:54:24	Fixed (0.0, 0.0)	synoptic, shifted -15.5 min
02/02 05:53:00 - 02/02 05:59:54	Fixed (0.0, 0.0)	synoptic, shifted manually
02/02 18:04:30 - 02/02 18:11:24	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min
02/03 06:22:30 - 02/03 06:29:24	Fixed (0.0, 0.0)	synoptic, shifted 19.5 min

PROG= 03	1-time(s)														
Subr= 1	1-time(s)		12.0sec												
Seqn= 33	1-time(s)		4.0sec												
├	Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
├	Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
├	Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 5	1-time(s)		2.0sec												
├	Open/Ti-poly	Open/thick-AI	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
├	Open/Ti-poly	Open/thick-AI	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
├	Open/Ti-poly	Open/thick-AI	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
├	Open/Ti-poly	Open/thick-AI	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec		
├	Open/Ti-poly	Open/thick-AI	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec		
Seqn= 40	1-time(s)		4.0sec												
├	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
├	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
├	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 77	1-time(s)		2.0sec												
├	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
├	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
├	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 6	1-time(s)		2.0sec												
├	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec		
├	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
	Default Filter	Thicker Filter	VLS mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

XOB #1A39: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 30s cadence, G-band - 384x384 33ms

Term	Pointing (x, y)	Comment
02/02 06:03:00 - 02/02 18:01:24	Track (-134.5, -750.6) @ 02/02 06:00:00	# HOP 232

PROG= 17														Inf.-time(s)																																									
Subr= 1														1-time(s)														2.0sec																											
└ Seqn= 8														2-time(s)														2.0sec																											
														Open/G-band														Open/G-band close														Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec													
Subr= 2														1-time(s)														2.0sec																											
└ Seqn= 48														1-time(s)														30.0sec																											
														Open/G-band														Open/G-band open														Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
Subr= 3														60-time(s)														2.0sec																											
└ Seqn= 57														1-time(s)														30.0sec																											

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
01/30 14:43:00 - 01/30 16:56:30	Fixed (-970.0, 140.0)	# DST coordinate obs at E limb
01/30 17:21:30 - 01/30 17:59:54	Track (414.6, -76.7) @ 01/30 17:00:00	# track AR 12268
01/30 18:13:00 - 01/31 06:09:24	Track (424.4, -77.1) @ 01/30 18:10:00	# track AR 12268
01/31 06:22:30 - 01/31 17:57:24	Track (522.5, -82.3) @ 01/31 06:19:30	# track AR 12268
01/31 18:10:30 - 02/01 05:41:54	Track (610.9, -88.4) @ 01/31 18:07:30	# track AR 12268
02/01 05:59:00 - 02/01 17:44:24	Track (690.8, -95.7) @ 02/01 05:52:00	# track AR 12268
02/01 17:57:30 - 02/02 05:49:54	Track (763.5, -104.1) @ 02/01 17:54:30	# track AR 12268
02/02 06:03:00 - 02/02 18:01:24	Track (-134.5, -750.6) @ 02/02 06:00:00	# HOP 232
02/02 18:14:30 - 02/03 00:11:24	Track (645.3, 36.8) @ 02/02 18:11:30	# Quiet sun obs for SOT/EIS
02/03 00:14:30 - 02/03 06:19:24	Track (898.4, -129.2) @ 02/03 00:11:30	# track AR 12268
02/03 06:32:30 - 02/03 08:30:00	Track (917.1, -135.0) @ 02/03 06:29:30	# track AR 12268

PROG= 19 30-time(s)

Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

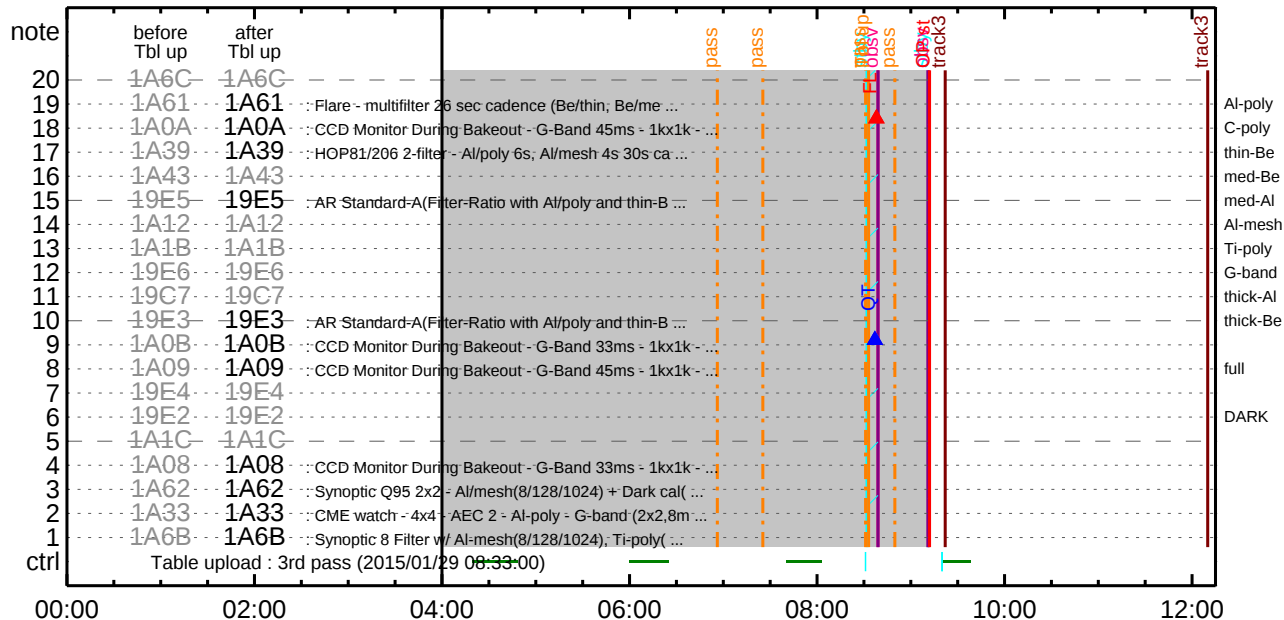
* * * * *

Flare Detection

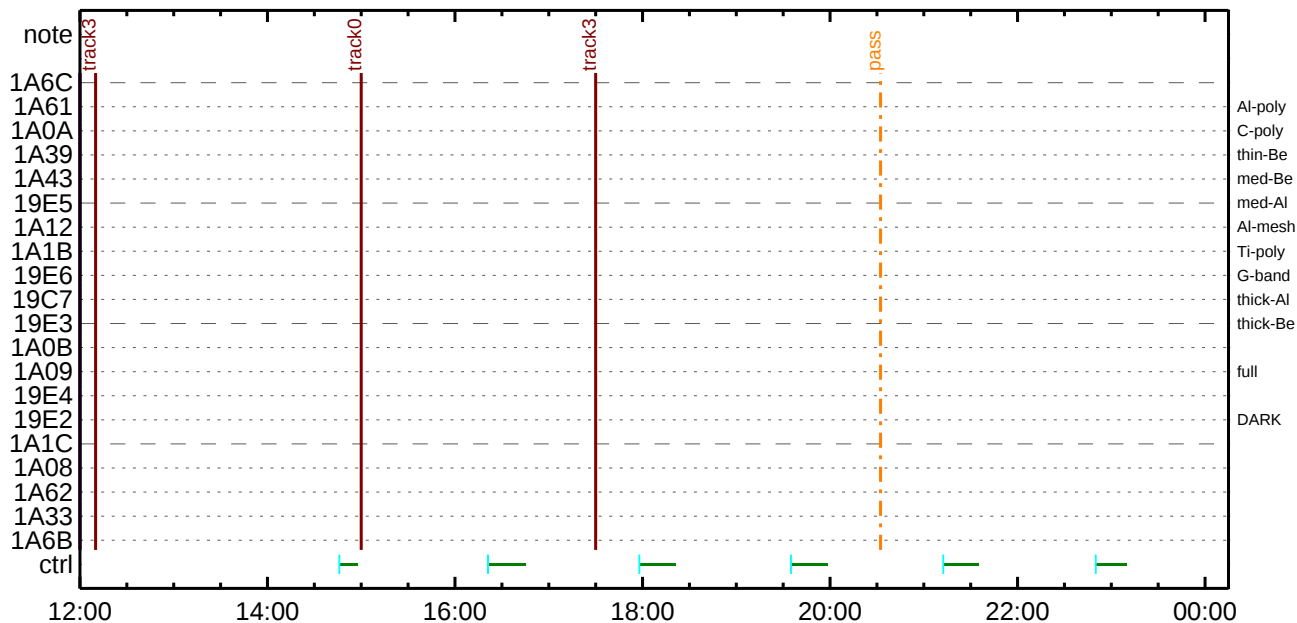
* * * * *

FLD Patrol												
Term	Pointing (x, y)	Comment										
01/30 14:40:18 - 01/30 18:00:18	Fixed (-970.0, 140.0)	# DST coordinate obs at E limb										
01/30 18:10:18 - 01/31 06:09:48	Track (424.4, -77.1) @ 01/30 18:10:00	# track AR 12268										
01/31 06:19:48 - 01/31 17:57:48	Track (522.5, -82.3) @ 01/31 06:19:30	# track AR 12268										
01/31 18:07:48 - 02/01 05:42:18	Track (610.9, -88.4) @ 01/31 18:07:30	# track AR 12268										
02/01 05:56:18 - 02/01 17:44:48	Track (690.8, -95.7) @ 02/01 05:52:00	# track AR 12268										
02/01 17:54:48 - 02/02 05:50:18	Track (763.5, -104.1) @ 02/01 17:54:30	# track AR 12268										
02/02 06:00:18 - 02/02 18:01:48	Track (-134.5, -750.6) @ 02/02 06:00:00	# HOP 232										
02/02 18:11:48 - 02/03 06:19:48	Track (645.3, 36.8) @ 02/02 18:11:30	# Quiet sun obs for SOT/EIS										
02/03 06:29:48 - 02/05 10:40:00	Track (917.1, -135.0) @ 02/03 06:29:30	# track AR 12268										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

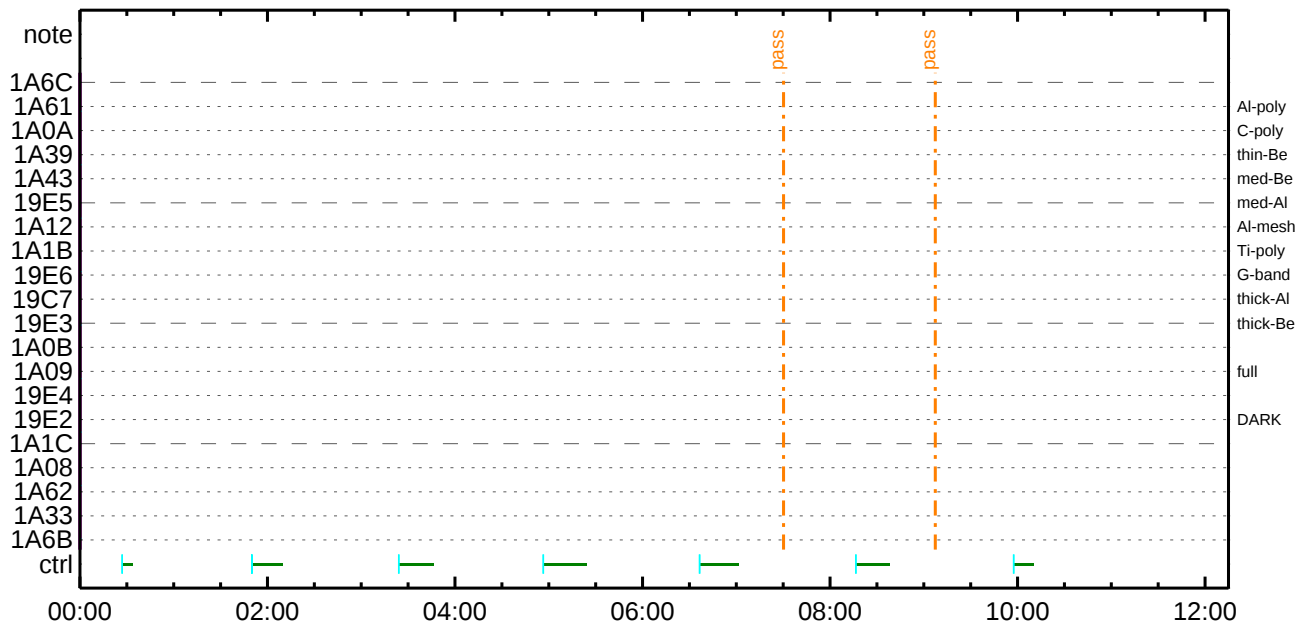
CMDI #0885 2015/01/29



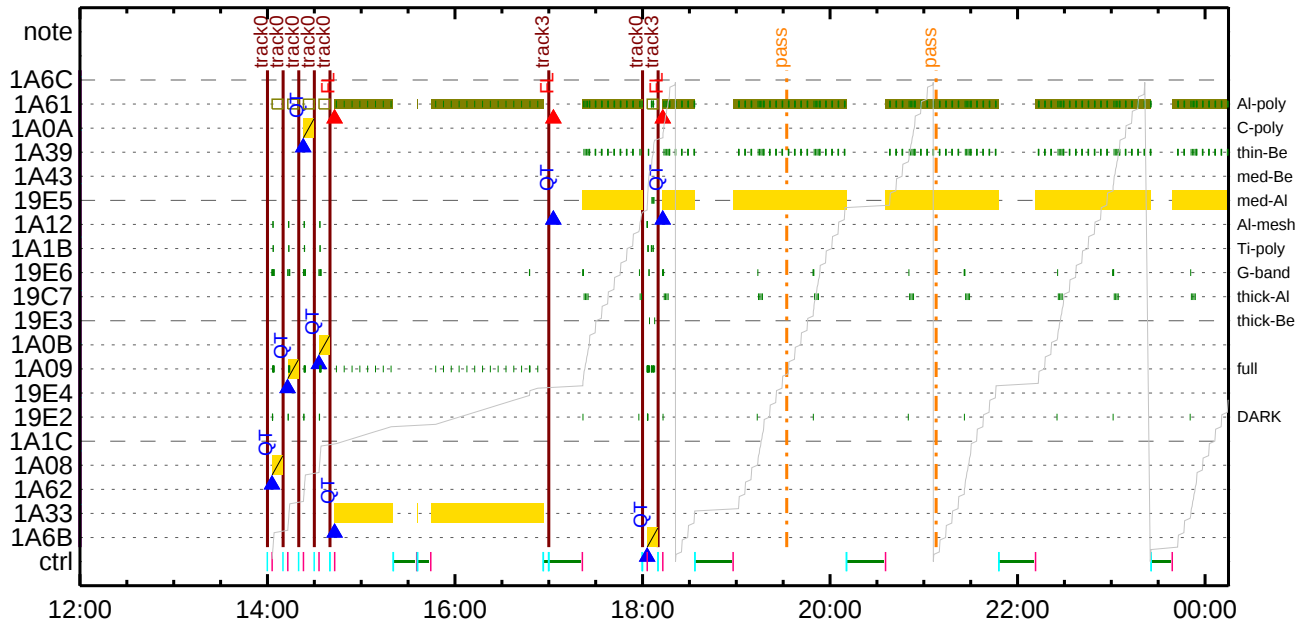
CMDI #0885 2015/01/29



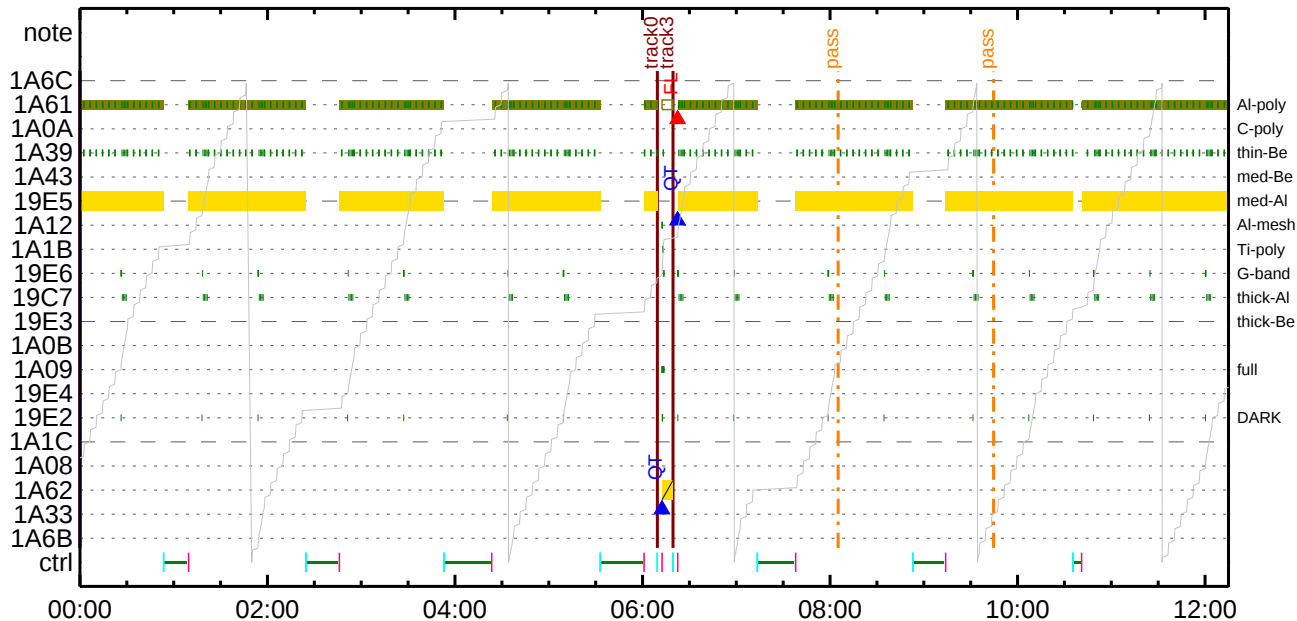
CMDI #0885 2015/01/30



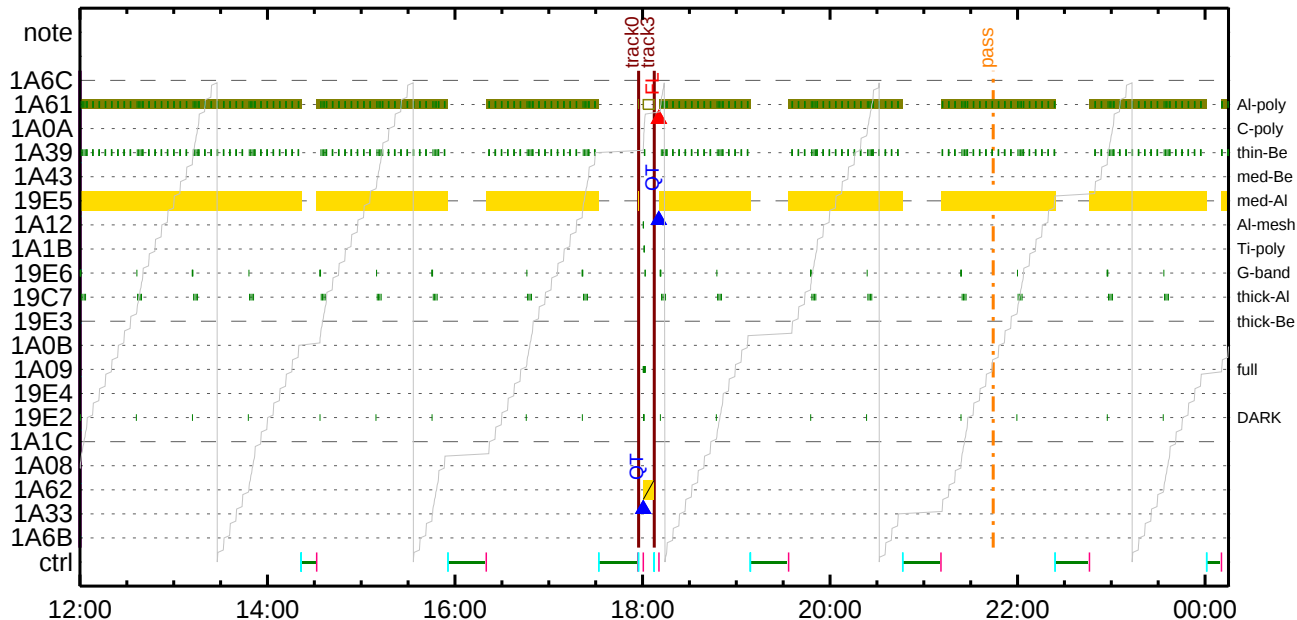
CMDI #0885 2015/01/30



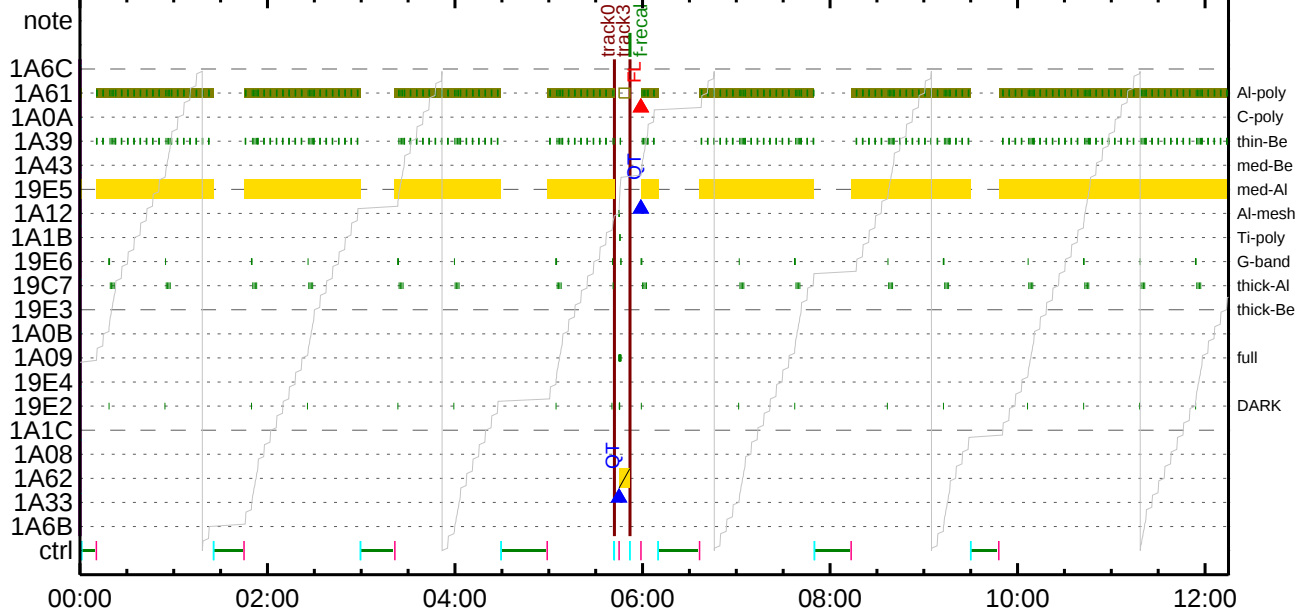
CMDI #0885 2015/01/31



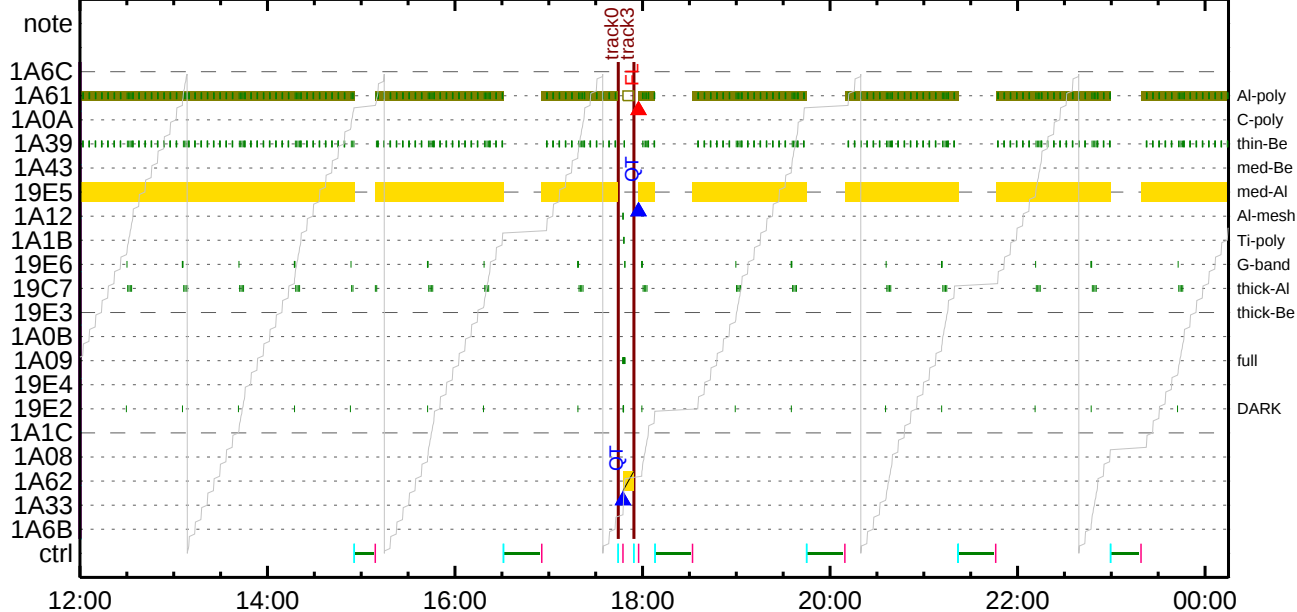
CMDI #0885 2015/01/31



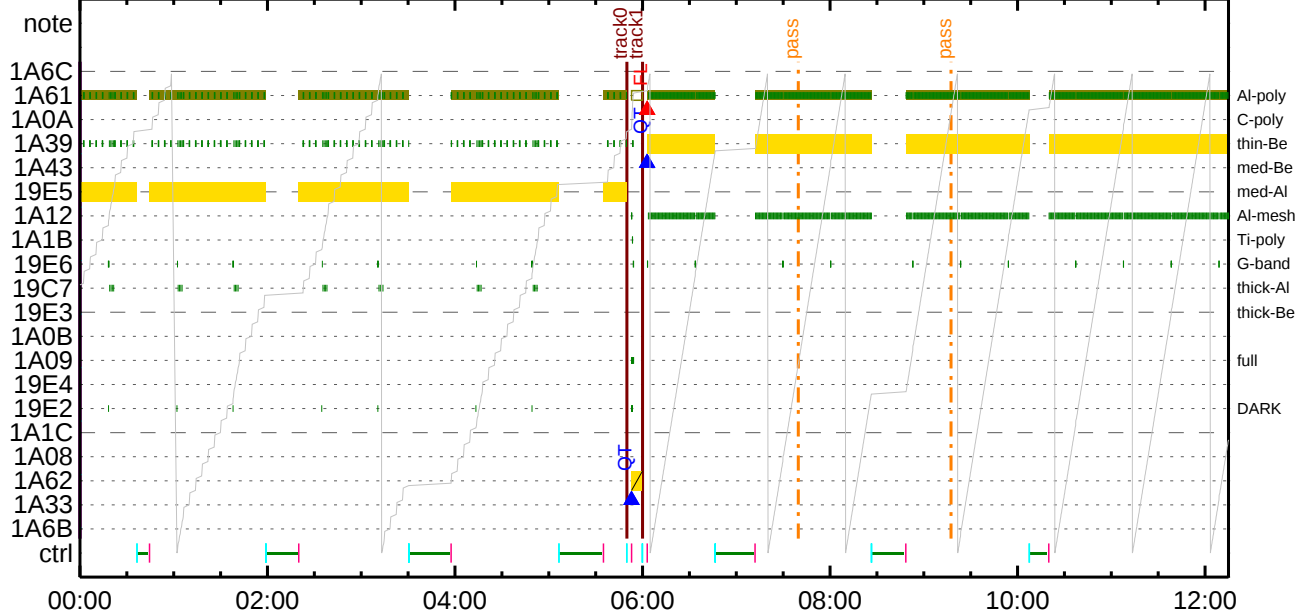
CMDI #0885 2015/02/01



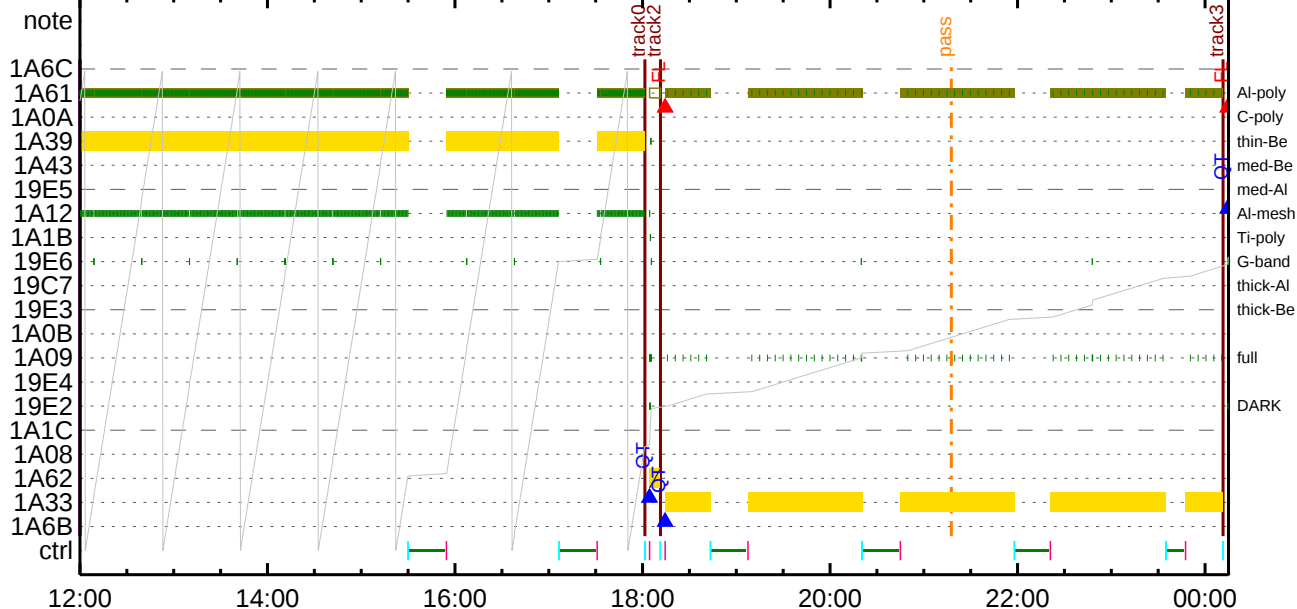
CMDI #0885 2015/02/01



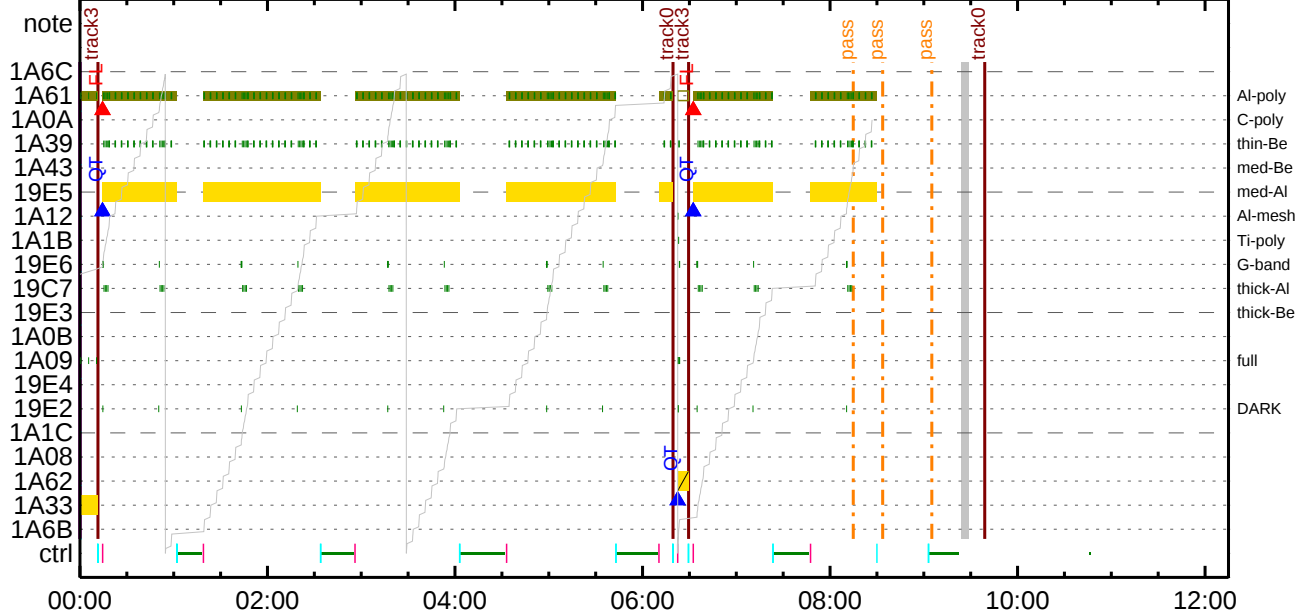
CMDI #0885 2015/02/02



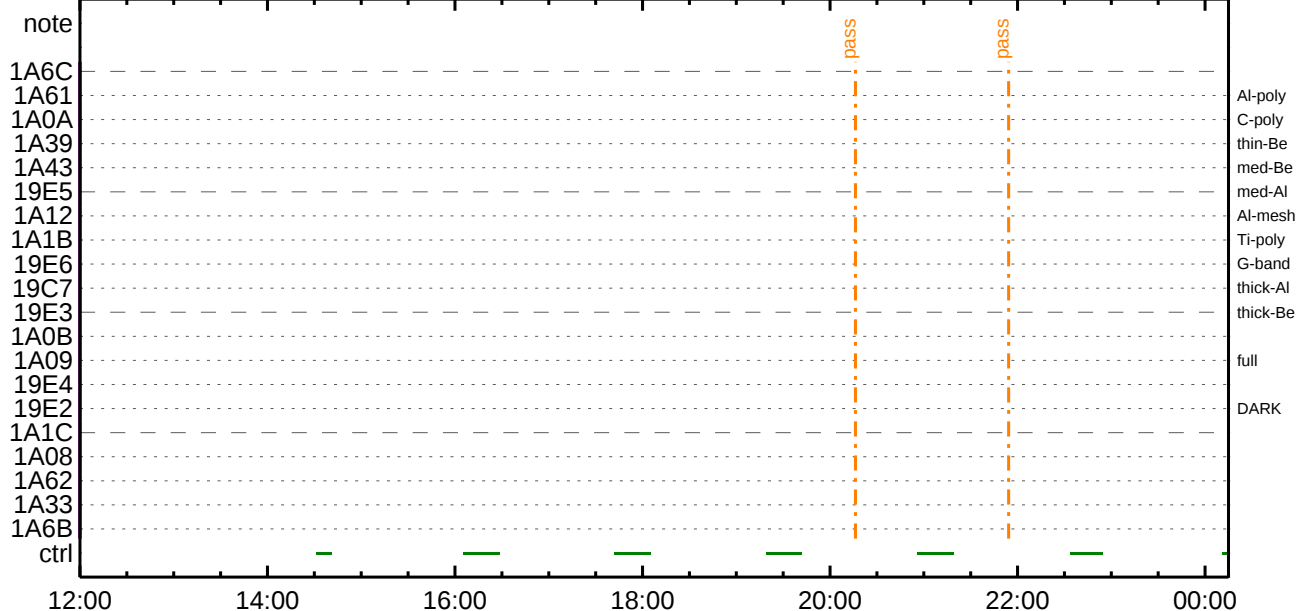
CMDI #0885 2015/02/02



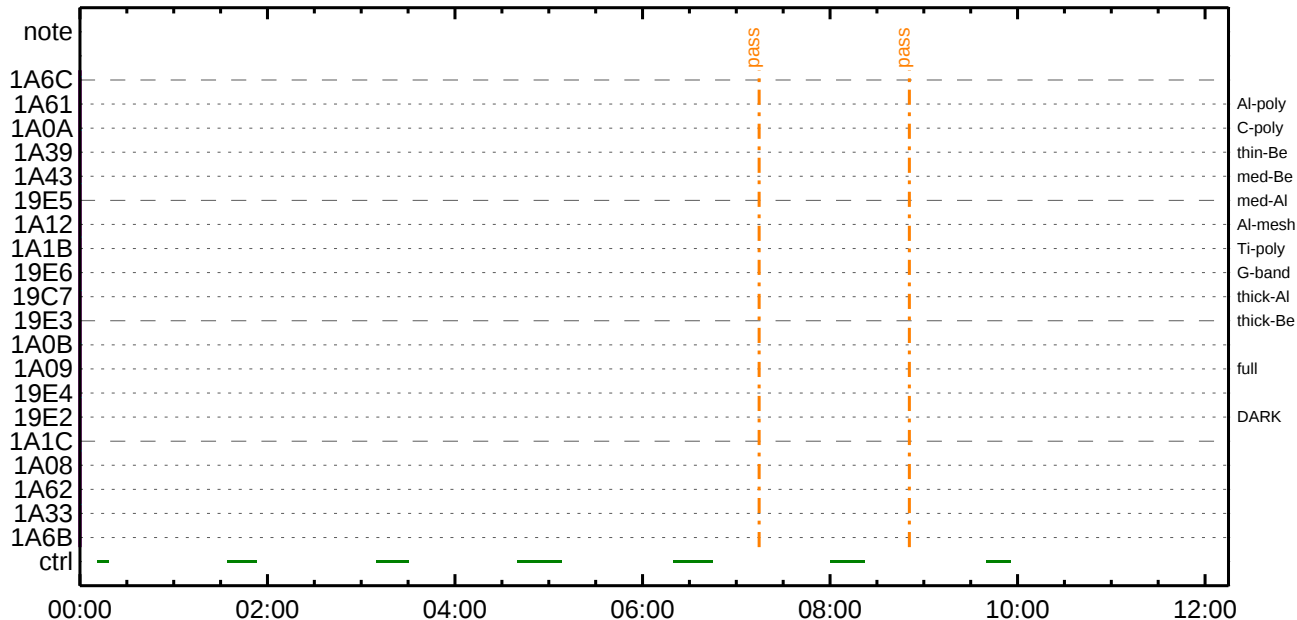
CMDI #0885 2015/02/03



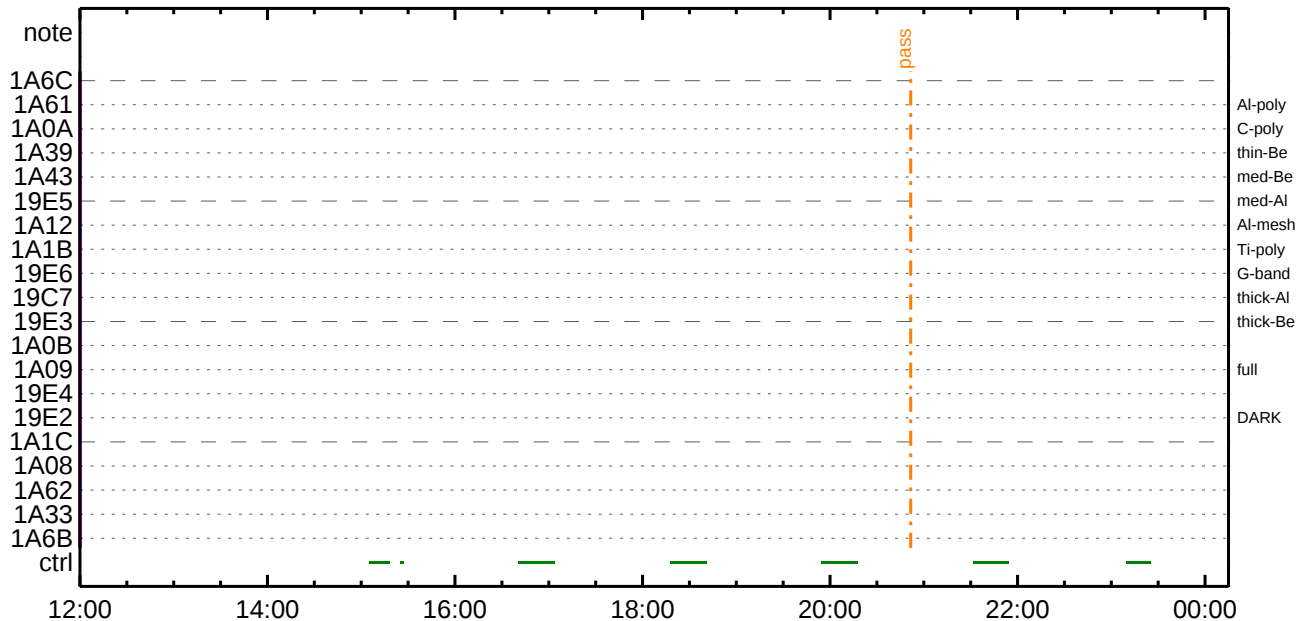
CMDI #0885 2015/02/03



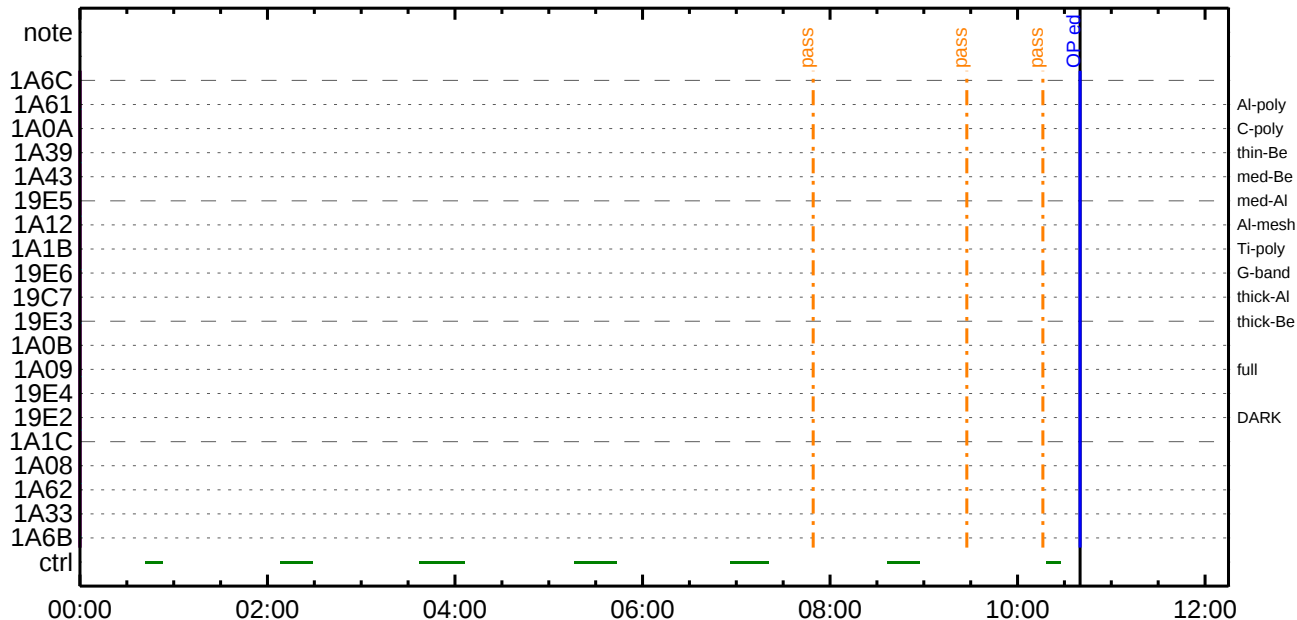
CMDI #0885 2015/02/04



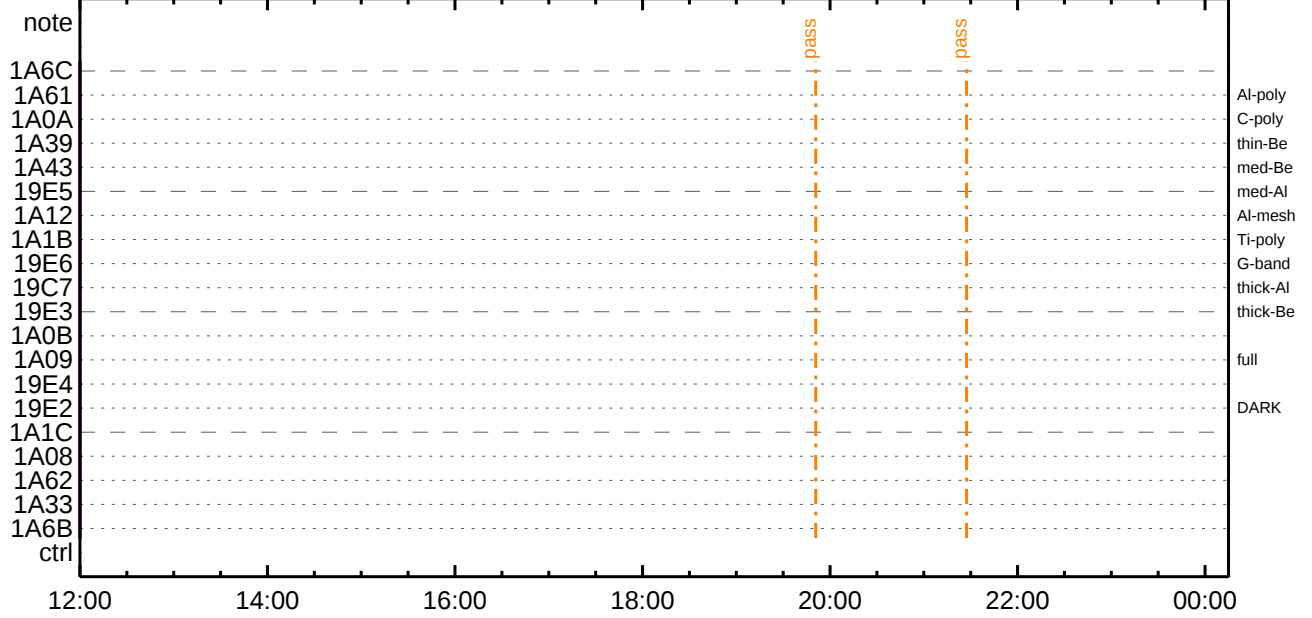
CMDI #0885 2015/02/04



CMDI #0885 2015/02/05



CMDI #0885 2015/02/05



(a) Spacecraft Operation Procedure (real-commands)

```
main-912 2015-01-29 12:53:53 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¿İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼¹Ç¿İjÇÀ®, Ü¤¹¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆĀ, İ×ĒY¤¤ŁOS¤¤¤Ç¿İ»p´Ŏ¤Ŏ¹İĒ, ¤•jÇĒŎİ×¤ĒXĀŎON¤İ¹Ŏ¤Ēİ¤¤¤¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ŏ¥É¥İ¥Á¥¼ŎĀŎ¤¬°ĀĀĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ÆĀ, ¼Ē½Ç¤Ŏ¼Ā¹Ŏ¤¹¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ÆĀ
0033 C. *****
0034 C. ¨ ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-150¤Ŏ¿ĒĒ¤¤jĒ
0035 C.
0036 . C. jãPT1°ÆĀ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0045 C.
0046 . C. jã¥Ç¥Ŏ¥Æ¥ĒĒĀŎjĒĒ•Ā°²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0052 C.
0053 C.
0054 . C. PT1°ÆĀ, ¤¬¼«Æ°ĀĀ»İ¤•¤¿, ĀjÇ°Ē²¼¤Ŏ¼Ā¹Ŏ¤¹¤ĒjĒ
0055 C. ¥Ç¥Ŏ¥Æ¥ĒĒĀŎ¤¤Ā•Ā°²ŎĒŎ¤¬ŎĀ¤¤¼¹Ç¿İ °İ»¤¹¤Ē¤¤¤ÇĀŎ¤ĀjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ÆĀ
0059 C. *****
0060 C. ¨ ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-151¤Ŏ¿ĒĒ¤¤¤jĒ
0061 C.
0062 . C. jãPT2°ÆĀ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0071 C.
0072 . C. jã¥Ç¥Ŏ¥Æ¥ĒĒĀŎjĒĒ•Ā°²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0078 C.
0079 . C. *****
0080 C. DR°ÆĀ, Ā¤»İjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆĀ, Ā¤»İjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

```
0096 . C.  
0097 . C.  
0098 . C. *****  
0099 . C. OP/OG¥¡¼¥É;|¥À¥ó×  
0100 . C. *****  
0101 . C.  
0102 . C. ¡ãOP/OG¥¡¼¥É;ä  
0103 . S. OP op-912:OP  
0104 . ()  
0105 . S. OG og-912:OG  
0106 . ()  
0107 . C.  
0108 . C. ¡ãNMOG&OPÎÏ°è¥À¥ó×;ä  
0109 . C. NMOG(0x200000-0x207FFF;$ 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0111 BC (20 00 7f 01 02)  
0112 . C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40  
0113 . C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0114 . C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127  
0115 . C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0116 . C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0117 BC (07 0b f8)  
0118 . C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0119 . C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0120 . C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0121 . C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0122 . C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0123 . C.  
0124 . C. ¥À¥ó¥×½ªİ»ð³İÇŞ  
0125 . C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0126 . C. RAM ID=NMOG¤I%Ê¹Ç•ë²IOK¤ð³İÇŞ  
0127 . C.  
0128 . C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0130 BC (20 80 7f 01 02)  
0131 . C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41  
0132 . C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0133 . C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127  
0134 . C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0135 . C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0137 BC (07 0b f8)  
0138 . C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0139 . C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0140 . C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0141 . C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0142 . C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0143 . C. ¥À¥ó¥×½ªİ»ð³İÇŞ  
0144 . C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0145 . C. RAM ID=NMOG¤I%Ê¹Ç•ë²IOK¤ð³İÇŞ  
0146 . C.  
0147 . C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0149 BC (21 00 41 01 02)  
0150 . C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42  
0151 . C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0152 . C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65  
0153 . C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0154 . C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0155 BC (07 0b f8)  
0156 . C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0157 . C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0158 . C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0159 . C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0160 . C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0161 . C.  
0162 . C. ¥À¥ó¥×½ªİ»ð³İÇŞ  
0163 . C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0164 . C. RAM ID=NMOG,RAM ID=OP¤I%Ê¹Ç•ë²IOK¤ð³İÇŞ  
0165 . C.  
0166 . C. **** °Ê²¼¤I%A´¶Á°¤ĖĖ-¤ºÁ÷¿® (%âµ-¥À¥ó¥×¾ê½Ç¤ðÄŒ¤æÇ½¤ª¨¤ē%ı¹Ç¤Ç¤â) ****  
0167 . C. DHU¥â¼¥É;î¥Y½, ¥ì¼¥é;î¤òİā¤¹  
+ DC 01-22 DHU_MODE_CHNG  
0169 BC (02 0a f8)  
0170 . C. ¢¢[HK1_PKT_FORM_NO] EQ 2  
0171 . C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S  
0172 . C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K  
0173 . C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0174 . C.  
0175 . C. *****  
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 . C. *****  
0178 . C.  
0179 . C. NOTICE i$ OPOG UPLOAD¤-Ã÷¿NG¤İ%ł¹Ç;¢•Ê²¼¤İTI-CMDÃ÷¿®¤İ%Â¹Ô¤•¤Ė¤¤³¤ē;£  
0180 . C. ¤¤¤¿;¢SET¤ĖDUMP¤İÆ±°ıİÑ¥¹Çç¹Ô¤ı³¤ē;£  
0181 . C.  
0182 . C. Tİ¥³¥¤¥ô¥É¤ðÄĐİ¿(UT)  
+ TI 2015-01-29 09:07:00.0  
0184 DC 01-B3 DHU_OP_STOP  
0185 . C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0186 . C.  
+ TI 2015-01-29 09:07:01.0  
0188 DC 01-B4 DHU_OP_COPY  
0189 . C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0190 . C.  
0191 +. TI 2015-01-29 09:07:01.0  
0192 DC 01-B5 DHU_OPOG_COPY  
0193 . C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

0194	C.			
0195	+. TI	2015-01-29 09:11:59.5		
0196	DC	01-B2 DHU_OP_START		
0197	C.	☐☐[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0198	C.			
0199	C.	°Ê¼☐İÄê%İİŇ☐İ¥Á¥\$¥Ä¥¹äİÜ		
0200	C.	☐☐[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0201	C.	☐☐[HK1_TI_CMD_NUM]	EQ	4
0202	C.	☐☐[HK1_NEXT_EXEC_PIM]	EQ	DHU
0203	C.	☐☐[HK1_NEXT_EXEC_DC]	EQ	0xB3
0204	C.			
0205	C.	*****		
0206	C.	TIİİ°è¥Ä¥0¥×		
0207	C.	*****		
0208	C.			
0209	C.	TI_TBL(0x03AB00-0x03AEFF;S 1024byte)		
0210	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0211	BC	(03 ab 03 01 02)		
0212	C.	☐☐[HK1_DMP_TOP_ADRS_1]	EQ	07
0213	C.	☐☐[HK1_DMP_TOP_ADRS_0]	EQ	2B
0214	C.	☐☐[HK1_DMP_BLOCK_NUM]	EQ	3
0215	C.	☐☐[HK1_DMP_REPEAT_NUM]	EQ	0
0216	C.	☐☐[HK1_DMA_DMP_PIM]	EQ	DHU
0217	+. DC	01-22 DHU_MODE_CHNG		
0218	BC	(07 0b f8)		
0219	C.	☐☐[HK1_PKT_FORM_NO]	EQ	7
0220	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.25 s
0221	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32k
0222	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0223	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	EXEC
0224	C.			
0225	C.	¥Ä¥0¥×½ªİ»☐0³İÇ\$		
0226	C.	☐☐[HK1_DMP_CHK_FLG]	EQ	NON
0227	C.			
0228	C.	RAM ID=TI_TBL☐İ¥Ê¹Ç•è²İOK☐0³İÇ\$		
0229	C.			
0230	C.	DHU¥âİ;¼¥ÉİÊ¼Ý½, ¥İİ;¼¥ÈİÊ☐0İä☐¹		
0231	+. DC	01-22 DHU_MODE_CHNG		
0232	BC	(02 0a f8)		
0233	C.	☐☐[HK1_PKT_FORM_NO]	EQ	2
0234	C.	☐☐[HK1_PKT_GEN_TIME]	EQ	0.5S
0235	C.	☐☐[HK1_S_TLM_BIT_RATE]	EQ	32K
0236	C.	☐☐[HK1_X_TLM_BIT_RATE]	EQ	4M
0237	C.			
0238	C.	*****		
0239	C.	SOT TI command set		
0240	C.	*****		
0241	C.	Execute, after the success of OP upload.		
0242	+. TI	2015-01-29 09:11:16.0		
0243	DC	07-F0 MDP_SOT_MODE_STBY		
0244	BC	(41)		
0245	C.	-----		
0246	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0247	C.	-----		
0248	C.	***** SOT END *****		
0249	C.	Stop EIS observation and temporarily disable EIS mode changes		
0250	C.			
0251	C.			
0252	C.	***** Start EIS operation (TI set) *****		
0253	C.	Execute, after the success of OP upload.		
0254	C.	Set EIS TI-commands		
0255	+. TI	2015-01-29 09:11:30.0		
0256	DC	07-FC EIS_MODE_MANU		
0257	BC	(21 02)		
0258	+. TI	2015-01-29 09:11:40.0		
0259	DC	07-FC EIS_MODE_CHG_DIS		
0260	BC	(22)		
0261	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0262	C.	***** End EIS operation (TI set) *****		
0263	C.			
0264	C.			
0265	C.			
0266	C.	***** XRT START *****		
0267	C.	Execute, after the success of OP upload.		
0268	+. TI	2015-01-29 09:11:00.0		
0269	DC	07-F0 MDP_XRT_MODE_STBY		
0270	BC	(c3)		
0271	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0272	C.			
0273	C.	***** XRT END *****		
0274	C.			
0275	C.	***** MDP `ÜÄİ☐İ»0¼Ý☐ĖÄĐ☐¹☐ēDCBC•×²è *****		
0276	C.	(%â°İ¥0¥Ä¥Ė¥Đ¥Ė¥â¥Ç¥ē☐Ė½☐☐¼A»Ü☐¹☐ē)		
0277	S.	DC-BC dcbc-402:DCBC		
0278		(MDP_known_event)		
0279	C.			
0280	C.			
0281	C.	***** ¥Đ¥¹•İ Daily±çİŇ☐Ė¹Đ☐¹☐ēDCBC•×²è *****		
0282	S.	DC-BC dcbc-153:DCBC		
0283		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0284	C.			
0285	C.			
0286	C.	İäLOS¥Ä¥\$¥Ä¥¼Ä»Üİä		
0287	C.			
0288	C.	***** LOS *****		
0289	C.			

(a) Spacecraft Operation Procedure (real-commands)

```
main-913 2015-01-29 12:53:53 108 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ĀiĒ¿¿Aa•µ°Æ»İ×AÇ¿İ¥Ç¥A¥×¥İj¼¥ÉİĒĒµ•İİĒİĒ°ÇÖa•a¿%İ¹Ç¿İjÇÀ®, ũa¹aĒaBaÇÁ÷¿®a•aĒaBa³aĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 C.
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 C.
0045 . C. ***** Start EIS SAFE RECOVERY operation *****
0046 C. ENABLE MODE CHANGE
0047 +. DC 07-FC EIS_MODE_CHG_ENA
0048 BC (20)
0049 . C. MHC SLIT/SLOT RESOLVER TO AUTO
0050 +. DC 07-FC EIS_RESOLV_AUTO
0051 BC (72 00 99 00 02 00 03)
0052 . C. Wait 5 seconds
0053 . C. ENABLE MHC AUTO SAFE
0054 +. DC 07-FC EIS_MHC_SAFE_ENA
0055 BC (5f c0 9a 00 02 00 01)
0056 . C. Wait 5 seconds
0057 . C. ENABLE MHC MECHANISMS
0058 +. DC 07-FC EIS_MHC_MECH_ENA
0059 BC (54 e8 8e)
0060 . C. Wait 5 seconds
0061 . C. FIND MHC SHUTTER INDEX
0062 +. DC 07-FC EIS_FIND_SHT_INDEX
0063 BC (5a 60 09)
0064 . C. Wait 30 seconds
0065 . C. Verify status type 3 parameters: EIS_MHC_SAFE_STS = NON , EIS_MHC_SAFE_FLG = ENA , EIS_MHC_MECH_FLG
= ENA , EIS_SHUTTER_INDEX = ACT
0066 C.
0067 . C. ***** End EIS SAFE RECOVERY operation *****
0068 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0069 +. DC 07-FC EIS_MODE_CHG_ENA
0070 BC (20)
0071 . C. Verify EIS_MODE_CHG_FLG is ENA
0072 +. DC 07-FC EIS_MODE_MANU
0073 BC (21 02)
0074 . C. Verify EIS in MANUAL mode
0075 . C. Estimated OBSTBL upload time is 46s
0076 C. *****
0077 C. EIS START OBSTBL LOAD
0078 C. *****
0079 . S. RAM ram-820:EIS_OBSTBL
0080 C.
0081 +. DC 07-FC EIS_DUMP_OBSTBL
0082 BC (07 07 07 00 00 70 00)
0083 C.
0084 C. Execute, after the success of OBSTBL upload.
0085 C. Set EIS TI-commands
0086 +. TI 2015-01-29 09:11:50.0
0087 DC 07-FC EIS_MODE_CHG_ENA
0088 BC (20)
0089 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0090 C. *****
0091 C. EIS END OBSTBL LOAD
0092 C. *****
0093 C.
0094 . C. ***** MDP ´ŪĀİİĒ»Ö%ŸaĒĀa¹aĒDCBC•x²Ē *****
```

```
0095 C. (%ā°İ¥Ō¥Ā¥Ė¥Ŧ¥Ė¥ā¥Ŧ¥ē□Ė½¼□□¼Ā»Ū□¹□ē)
0096 . S. DC-BC dcbc-402:DCBC
0097 (MDP_known_event)
0098 C.
0099 C.
0100 . C. ***** ¥Đ¥¹•İ Daily±ĸİÑ□Ė´Ŧ□¹□ēDCBC•x²ē *****
0101 . S. DC-BC dcbc-153:DCBC
0102 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0103 C.
0104 C.
0105 . C. ĩāLOS¥Ā¥S¥Ā¥¬¼Ā»Ūĭä
0106 C.
0107 . C. ***** LOS *****
0108 C.
```



```

0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload FG Observation Table>
0109 S. RAM ram-265:MDP_OBS_F
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. < Upload DPL table >
0120 C.
0121 C. ¥¢¥Ã¥×¥í¼¥É°ÎÁ°¤ESTS_CHK¤òOFF¤Ė¤¹¤Ě
0122 C.
0123 S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHK¤òON¤Ė¤¹¤Ě
0134 C.
0135 C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700 [ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2015-01-29 09:11:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 + DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 C. ----- Success Verify ? OK / NG____
0162 C.
0163 C. XRT Obs. Table Upload
0164 S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 C. ----- Comparison Check ? OK / ERR ____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 05 85 83 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 06 85 83 06 06)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 07 85 83 08 08)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 08 c0 c0 10 10)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 09 80 80 20 20)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0a 40 c0 10 10)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0b 40 40 10 10)

```

```
0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 0c c0 40 10 10)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 0d 80 80 20 08)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 0e 80 80 08 20)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 0f 80 80 06 06)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 10 80 80 08 08)
0204 + DC 07-F0 MDP_XRT_FLD_ENA
0205 BC (d8)
0206 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0207 BC (c8)
0208 + DC 07-F0 MDP_XRT_AEC_RESET
0209 BC (d0)
0210 + DC 07-F0 MDP_XRT_ARS_DIS
0211 BC (d5)
0212 + DC 07-F0 MDP_XRT_FLD_RESET
0213 BC (da)
0214 + DC 07-F0 MDP_XRT_QT_PROG_SET
0215 BC (c4 0a)
0216 + DC 07-F0 MDP_XRT_FL_PROG_SET
0217 BC (c5 13)
0218 . C. ----- Success Verify ? OK / NG ____
0219 C.
0220 C.
0221 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0222 C.
0223 + DC 07-F0 MDP_XRT_MODE_OBSV
0224 BC (c2)
0225 + TI 2015-01-29 09:11:02.0
0226 DC 07-F0 MDP_XRT_MODE_OBSV
0227 BC (c2)
0228 . C. ----- Success Verify ? OK / NG ____
0229 C.
0230 C. ***** XRT END *****
0231 C.
0232 . C. ***** MDP 'ûÃîï»ö¼ýðĒð¹ðĕDCBC•x²è *****
0233 C. (%ã°î¥0¥Ã¥Ē¥ð¥Ē¥ã¥Ĉ¥ĕðĒ¼¼ð¼Ã»Ů¹ðĕ)
0234 . S. DC-BC dcbc-402:DCBC
0235 (MDP_known_event)
0236 C.
0237 C.
0238 . C. ***** ¥Ð¥¹•İ Daily±ĴİŇðĒ'ð¹ðĕDCBC•x²è *****
0239 . S. DC-BC dcbc-153:DCBC
0240 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0241 C.
0242 C.
0243 . C. ¡ãLOS¥Ã¥$¥Ã¥¬¼Ã»Ů¡ã
0244 C.
0245 . C. ***** LOS *****
0246 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-915 2015-01-29 12:53:53 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Åi;Ê¿¿A¿•µ°Æ»I×AÇ¿I¥C¥A¥×¥i;¼¥ÉjÊÊè½µ•iÎÊjÊÊ¼°ÇÔ¿•¿¿¼i¹¿¿IjÇÀ®, ù¿¹¿è¿¿¿¿¿Á÷¿®¿•¿Ê¿¿¿³¿Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP `ÜÃÎ¿Î»Ô¼Ý¿ÊÂÐ¿¹¿èDCBC•×²è *****
0016 C. (%ã°I¥0¥Á¥Ê¥P¥É¥ã¥C¥è¿Ê½¿¿¿¼Å»Ü¿¹¿è)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** ¥Ð¥¹•I Daily±¿ÎÑ¿Ê´Ø¿¹¿èDCBC•×²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. iãLOS¥Á¥S¥Ã¥¼Å»Üjã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

Jan 29, 15 12:54

XRT_OGLIST_0885.chk

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*** OP Sequence for XRT ***

2015/01/29	09:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	09:20:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	09:20:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/29	09:20:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/29	09:22:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 0b 64
2015/01/29	09:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/29	12:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 f4 9c
2015/01/29	14:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	14:46:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	14:46:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/29	14:46:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/29	14:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/29	15:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 f3 8e 56 35
2015/01/29	16:21:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	16:21:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	16:21:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/29	16:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/29	16:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/29	17:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2015/01/29	17:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	17:58:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	17:58:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/29	17:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/29	18:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/29	19:35:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	19:35:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	19:35:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/29	19:35:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/29	19:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/29	21:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	21:12:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	21:12:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/29	21:12:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/29	21:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/29	22:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	22:50:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/29	22:50:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/29	22:50:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/29	22:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	00:27:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	00:27:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	00:27:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	00:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	00:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	01:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	01:50:02.0	XRT_CTRL_MANU_402_OG [0x192]			

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2015/01/30	01:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	01:50:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	01:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	03:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	03:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	03:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	03:24:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	03:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	04:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	04:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	04:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	04:56:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	04:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	06:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	06:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	06:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	06:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	06:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	08:00:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_447_OG [0x1bf]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	08:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	TCIB_XRT_S_HTR_A_DIS	0	04-C0	
2015/01/30	08:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	08:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	08:16:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	08:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	09:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	09:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	09:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	09:57:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	10:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	13:59:54.0	XRT_CTRL_MANU_414_OG [0x19e]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	14:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	14:02:32.0	XRT_FOCUS_POSITION_432_OG [0x1b0]	AOCU_NM	5	02-76	00 2e f9 2e f9
2015/01/30	14:02:52.0	XRT_QT_PROG_SET_433_OG [0x1b1]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/30	14:02:54.0	XRT_FLD_DIS_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/01/30	14:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/30	14:02:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/30	14:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/30	14:09:54.0	XRT_CTRL_MANU_414_OG [0x19e]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/30	14:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	14:12:32.0	XRT_FOCUS_POSITION_432_OG [0x1b0]	AOCU_NM	5	02-76	00 2e f9 d1 07
2015/01/30	14:12:52.0	XRT_QT_PROG_SET_436_OG [0x1b4]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/30	14:12:54.0	XRT_FLD_DIS_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2015/01/30	14:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/30	14:12:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/30	14:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/30	14:19:54.0	XRT_CTRL_MANU_414_OG [0x19e]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/30			MDP_XRT_CTRL_MANU	1	07-F0	c1

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2015/01/30	14:20:00.0	AOCS_0Re-point_Start_7_0G [0x09d]		
		AOCU_NM	5	02-76 00 d1 07 d1 07
2015/01/30	14:22:32.0	XRT_FOCUS_POSITION_432_0G [0x1b0]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/01/30	14:22:52.0	XRT_QT_PROG_SET_441_0G [0x1b9]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 12
2015/01/30	14:22:54.0	XRT_FLD_DIS_437_0G [0x1b5]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2015/01/30	14:22:56.0	XRT_FLRCTRL_DIS_405_0G [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2015/01/30	14:22:58.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/30	14:23:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/30	14:29:54.0	XRT_CTRL_MANU_414_0G [0x19e]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	14:30:00.0	AOCS_0Re-point_Start_8_0G [0x09e]		
		AOCU_NM	5	02-76 00 d1 07 2e f9
2015/01/30	14:32:32.0	XRT_FOCUS_POSITION_432_0G [0x1b0]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/01/30	14:32:52.0	XRT_QT_PROG_SET_448_0G [0x1c0]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 09
2015/01/30	14:32:54.0	XRT_FLD_DIS_437_0G [0x1b5]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2015/01/30	14:32:56.0	XRT_FLRCTRL_DIS_405_0G [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2015/01/30	14:32:58.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/30	14:33:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/30	14:39:54.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	14:39:56.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	14:39:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/01/30	14:40:00.0	AOCS_0Re-point_Start_3_0G [0x099]		
		AOCU_NM	5	02-76 00 f3 8e 56 35
2015/01/30	14:40:18.0	XRT_FLD_ENA_411_0G [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/01/30	14:40:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/01/30	14:40:22.0	XRT_AEC_RESET_413_0G [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/01/30	14:40:24.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/01/30	14:40:26.0	XRT_FLD_RESET_407_0G [0x197]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/30	14:42:56.0	XRT_QT_PROG_SET_431_0G [0x1af]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 02
2015/01/30	14:42:58.0	XRT_FL_PROG_SET_401_0G [0x191]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2015/01/30	14:43:00.0	XRT_CTRL_AUTO_408_0G [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/30	15:20:30.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	15:20:32.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	15:20:34.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/30	15:20:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/30	15:23:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/30	15:34:30.0	XRT_Custom_430_0G [0x1ae]		
2015/01/30	15:35:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/30	15:36:00.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	15:36:02.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	15:36:04.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/30	15:36:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/30	15:39:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/30	15:43:30.0	XRT_Custom_430_0G [0x1ae]		
2015/01/30	15:44:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/01/30	16:56:30.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	16:56:32.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	16:56:34.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/01/30	16:56:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/01/30	16:59:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/01/30	16:59:54.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/01/30	16:59:56.0	XRT_CTRL_MANU_402_0G [0x192]		

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2015/01/30	16:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	17:00:00.0	AOCs_0Re-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/30	17:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2015/01/30	17:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/30	17:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/30	17:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/30	17:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/30	17:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	17:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/01/30	17:20:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/01/30	17:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/30	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	18:00:00.0	AOCs_0Re-point_Start_9_OG [0x09f]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/30	18:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00
2015/01/30	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/30	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/30	18:02:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/30	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/01/30	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/30	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	18:10:00.0	AOCs_0Re-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/30	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2015/01/30	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/30	18:10:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/30	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/30	18:10:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/30	18:12:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	18:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/01/30	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/01/30	18:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/30	18:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	18:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	18:33:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	18:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	18:57:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	18:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2015/01/30	20:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/30	20:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	20:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	20:10:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	20:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	20:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	20:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2015/01/30	21:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/30	21:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2015/01/30	21:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	21:48:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	21:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	22:10:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	22:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/30	23:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	23:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/30	23:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/30	23:25:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/30	23:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/30	23:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/30	23:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	00:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	00:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	00:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	00:53:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	00:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	01:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	01:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	02:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	02:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	02:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	02:24:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	02:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	02:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	02:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	03:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	03:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	03:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	03:53:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	03:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	04:22:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	04:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	05:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	05:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	05:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	05:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	05:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	06:00:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	06:01:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	06:09:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	06:09:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	06:09:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/31	06:09:30.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 00 00 00 00
2015/01/31	06:09:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/31	06:12:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/31	06:12:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/31	06:12:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/01/31	06:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2015/01/31	06:19:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	06:19:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	06:19:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/31	06:19:30.0	AOCS_0Re-point_Start_4_0G [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00
2015/01/31	06:19:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/31	06:19:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/31	06:19:52.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/31	06:19:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/31	06:19:56.0	XRT_FLD_RESET_407_0G [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	06:22:26.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/01/31	06:22:28.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/01/31	06:22:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	07:13:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	07:13:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	07:13:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	07:13:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	07:16:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	07:37:00.0	XRT_Custom_430_0G [0x1ae]				
2015/01/31	07:38:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	08:53:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	08:53:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	08:53:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	08:53:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	08:56:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	09:13:00.0	XRT_Custom_430_0G [0x1ae]				
2015/01/31	09:14:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	10:35:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	10:35:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	10:35:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	10:35:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	10:38:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	10:40:00.0	XRT_Custom_430_0G [0x1ae]				
2015/01/31	10:41:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	14:21:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	14:21:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	14:21:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	14:21:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	14:24:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	14:30:30.0	XRT_Custom_430_0G [0x1ae]				
2015/01/31	14:31:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	15:55:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	15:55:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	15:55:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	15:55:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	15:58:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	16:19:00.0	XRT_Custom_430_0G [0x1ae]				
2015/01/31	16:20:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	17:32:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	17:32:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2015/01/31	17:32:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	17:32:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	17:35:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	17:56:00.0	XRT_Custom_430_0G [0x1ae]				
2015/01/31	17:57:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	17:57:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	17:57:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	17:57:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/01/31	17:57:30.0	AOCS_ORe-point_Start_9_0G [0x09f]	AOCU_NM	5	02-76	00 00 00 00 00
2015/01/31	17:57:48.0	XRT_FLD_DIS_422_0G [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/01/31	18:00:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/01/31	18:00:26.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/31	18:00:28.0	XRT_QT_PROG_SET_438_0G [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/01/31	18:00:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	18:07:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	18:07:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	18:07:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/01/31	18:07:30.0	AOCS_ORe-point_Start_4_0G [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00
2015/01/31	18:07:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/01/31	18:07:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/01/31	18:07:52.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/01/31	18:07:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/01/31	18:07:56.0	XRT_FLD_RESET_407_0G [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	18:10:26.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/01/31	18:10:28.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/01/31	18:10:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	19:09:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	19:09:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	19:09:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	19:09:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	19:12:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	19:32:30.0	XRT_Custom_430_0G [0x1ae]				
2015/01/31	19:33:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	20:46:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	20:46:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	20:46:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	20:46:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	20:49:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	21:10:00.0	XRT_Custom_430_0G [0x1ae]				
2015/01/31	21:11:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/01/31	22:24:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	22:24:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/01/31	22:24:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/01/31	22:24:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/01/31	22:27:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/01/31	22:45:00.0	XRT_Custom_430_0G [0x1ae]				
2015/01/31	22:46:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	00:01:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	00:01:02.0	XRT_CTRL_MANU_402_0G [0x192]				

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2015/02/01	00:01:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	00:01:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	00:04:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/01	00:09:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/01	00:10:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	01:25:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	01:25:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	01:25:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	01:25:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	01:28:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/01	01:44:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/01	01:45:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	02:59:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	02:59:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	02:59:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	02:59:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	03:02:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/01	03:20:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/01	03:21:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	04:29:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	04:29:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	04:29:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	04:29:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	04:32:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/01	04:58:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/01	04:59:00.5	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	05:41:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	05:41:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	05:41:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	05:42:00.0	AOCS_0Re-point_Start_9_0G [0x09f]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/02/01	05:42:18.0	XRT_FLD_DIS_422_0G [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00
2015/02/01	05:44:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/02/01	05:44:56.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/02/01	05:44:58.0	XRT_QT_PROG_SET_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/02/01	05:45:00.5	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/02/01	05:51:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	05:51:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	05:51:58.0	XRT_FOCUS_RECALIBRATE_445_0G [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	05:52:00.0	AOCS_0Re-point_Start_4_0G [0x09a]	XRT_FOCUS_RECAL	2	07-F8	78 00
2015/02/01	05:55:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	AOCU_NM	5	02-76	03 00 00 00 00
2015/02/01	05:56:18.0	XRT_FLD_ENA_411_0G [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/02/01	05:56:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/02/01	05:56:22.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/02/01	05:56:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/02/01	05:56:26.0	XRT_FLD_RESET_407_0G [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/02/01	05:58:56.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	05:58:58.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/02/01	05:59:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2015/02/01	06:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	06:10:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	06:10:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	06:10:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/01	06:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/01	06:35:30.0	XRT_Custom_430_OG [0x1ae]			
2015/02/01	06:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	07:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	07:50:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	07:50:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	07:50:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/01	07:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/01	08:12:30.0	XRT_Custom_430_OG [0x1ae]			
2015/02/01	08:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	09:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	09:30:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	09:30:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	09:30:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/01	09:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/01	09:47:00.0	XRT_Custom_430_OG [0x1ae]			
2015/02/01	09:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	14:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	14:55:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	14:55:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	14:55:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/01	14:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/01	15:08:00.0	XRT_Custom_430_OG [0x1ae]			
2015/02/01	15:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	16:31:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	16:31:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	16:31:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/01	16:31:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/01	16:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/01	16:54:30.0	XRT_Custom_430_OG [0x1ae]			
2015/02/01	16:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	17:44:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	17:44:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	17:44:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/02/01	17:44:30.0	AOCS_Ore-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/02/01	17:44:48.0	XRT_FLD_DIS_422_OG [0x1a6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/02/01	17:47:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/02/01	17:47:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/02/01	17:47:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/02/01	17:47:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/01	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	17:54:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/01	17:54:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/02/01	17:54:30.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2015/02/01	17:54:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8

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2015/02/01	17:54:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/02/01	17:54:52.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/02/01	17:54:54.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/02/01	17:54:56.0	XRT_FLD_RESET_407_OG [0x197]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/02/01	17:57:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0f
2015/02/01	17:57:28.0	XRT_FL_PROG_SET_401_OG [0x191]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2015/02/01	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/02/01	18:08:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/01	18:08:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/01	18:08:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/02/01	18:08:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/02/01	18:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/02/01	18:31:00.0	XRT_Custom_430_OG [0x1ae]		
2015/02/01	18:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/02/01	19:45:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/01	19:45:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/01	19:45:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/02/01	19:45:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/02/01	19:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/02/01	20:08:30.0	XRT_Custom_430_OG [0x1ae]		
2015/02/01	20:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/02/01	21:22:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/01	21:22:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/01	21:22:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/02/01	21:22:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/02/01	21:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/02/01	21:45:00.0	XRT_Custom_430_OG [0x1ae]		
2015/02/01	21:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/02/01	22:59:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/01	22:59:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/01	22:59:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/02/01	22:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/02/01	23:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/02/01	23:18:00.0	XRT_Custom_430_OG [0x1ae]		
2015/02/01	23:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/02/02	00:36:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/02	00:36:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/02	00:36:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/02/02	00:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/02/02	00:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/02/02	00:43:30.0	XRT_Custom_430_OG [0x1ae]		
2015/02/02	00:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/02/02	01:59:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/02	01:59:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/02/02	01:59:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/02/02	01:59:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/02/02	02:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/02/02	02:19:00.0	XRT_Custom_430_OG [0x1ae]		
2015/02/02	02:20:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/02/02	03:30:30.0	XRT_CTRL_MANU_400_OG [0x190]		

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2015/02/02	03:30:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	03:30:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	03:30:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/02	03:33:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/02/02	03:56:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/02/02	03:57:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/02	05:06:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	05:06:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	05:06:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	05:06:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/02	05:09:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/02/02	05:34:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/02/02	05:35:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/02	05:49:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	05:49:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	05:49:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	05:50:00.0	AOCS_OrE-point_Start_9_0G [0x09f]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/02/02	05:50:18.0	XRT_FLD_DIS_422_0G [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/02/02	05:52:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/02/02	05:52:56.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/02/02	05:52:58.0	XRT_QT_PROG_SET_438_0G [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/02/02	05:53:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/02/02	05:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/02	05:59:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	05:59:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	06:00:00.0	AOCS_OrE-point_Start_10_0G [0x0a0]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/02/02	06:00:18.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/02/02	06:00:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/02/02	06:00:22.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/02/02	06:00:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/02/02	06:00:26.0	XRT_FLD_RESET_407_0G [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/02/02	06:02:56.0	XRT_QT_PROG_SET_421_0G [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/02	06:02:58.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2015/02/02	06:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2015/02/02	06:46:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/02	06:46:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	06:46:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	06:46:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/02	06:49:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/02/02	07:11:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/02/02	07:12:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/02	08:26:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	08:26:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	08:26:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/02	08:26:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/02	08:29:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/02/02	08:47:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/02/02	08:48:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2015/02/02	10:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	10:07:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	10:07:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/02	10:07:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/02	10:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/02	10:19:00.0	XRT_Custom_430_OG [0x1ae]			
2015/02/02	10:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/02	15:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	15:30:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	15:30:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/02	15:30:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/02	15:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/02	15:53:30.0	XRT_Custom_430_OG [0x1ae]			
2015/02/02	15:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/02	17:06:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	17:06:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	17:06:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/02	17:06:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/02	17:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/02	17:30:00.0	XRT_Custom_430_OG [0x1ae]			
2015/02/02	17:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/02	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	18:01:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	18:01:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/02/02	18:01:30.0	AOCS_ORe-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/02/02	18:01:48.0	XRT_FLD_DIS_422_OG [0x1a6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/02/02	18:04:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/02/02	18:04:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/02/02	18:04:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/02/02	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/02	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	18:11:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	18:11:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/02/02	18:11:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76	02 00 00 00 00
2015/02/02	18:11:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/02/02	18:11:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/02/02	18:11:52.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/02/02	18:11:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/02/02	18:11:56.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/02	18:14:26.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/02/02	18:14:28.0	XRT_FL_PROG_SET_401_OG [0x191]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/02/02	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/02	18:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	18:43:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	18:43:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/02	18:43:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/02	18:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/02	19:06:30.0	XRT_Custom_430_OG [0x1ae]			
2015/02/02	19:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			

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2015/02/02	20:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/02	20:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	20:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	20:20:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/02	20:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/02	20:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/02	20:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/02/02	21:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/02	21:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	21:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	21:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/02	22:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/02	22:20:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/02	22:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/02/02	23:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/02	23:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	23:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/02	23:35:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/02	23:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/02	23:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/02	23:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/02/03	00:11:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/03	00:11:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/03	00:11:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/03	00:11:30.0	AOCs_0Re-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/02/03	00:11:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2015/02/03	00:11:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/02/03	00:11:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/02/03	00:11:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/02/03	00:11:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/02/03	00:14:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/03	00:14:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/02/03	00:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/02/03	01:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/03	01:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/03	01:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/03	01:02:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/03	01:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/03	01:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/03	01:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/02/03	02:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/03	02:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/03	02:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/02/03	02:34:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/02/03	02:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/02/03	02:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/02/03	02:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/02/03	04:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/02/03	04:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2015/02/03	04:03:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/03	04:03:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/02/03	04:06:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/02/03	04:32:00.0	XRT_Custom_430_0G [0x1ae]					
2015/02/03	04:33:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/03	05:43:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	05:43:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	05:43:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/03	05:43:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/02/03	05:46:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/02/03	06:09:30.5	XRT_Custom_430_0G [0x1ae]					
2015/02/03	06:10:30.5	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/03	06:19:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	06:19:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	06:19:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/02/03	06:19:30.0	AOCS_ORe-point_Start_9_0G [0x09f]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/02/03	06:19:48.0	XRT_FLD_DIS_422_0G [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/02/03	06:22:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/02/03	06:22:26.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/02/03	06:22:28.0	XRT_QT_PROG_SET_438_0G [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/02/03	06:22:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/03	06:29:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	06:29:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	06:29:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/02/03	06:29:30.0	AOCS_ORe-point_Start_4_0G [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/02/03	06:29:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/02/03	06:29:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/02/03	06:29:52.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/02/03	06:29:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/02/03	06:29:56.0	XRT_FLD_RESET_407_0G [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/03	06:32:26.0	XRT_QT_PROG_SET_416_0G [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2015/02/03	06:32:28.0	XRT_FL_PROG_SET_401_0G [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2015/02/03	06:32:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/03	07:23:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	07:23:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	07:23:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/03	07:23:36.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/02/03	07:26:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/02/03	07:46:30.0	XRT_Custom_430_0G [0x1ae]					
2015/02/03	07:47:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/02/03	08:30:00.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	09:03:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	09:03:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/02/03	09:03:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/02/03	09:03:06.0	XRT_PREFLR_STRT_439_0G [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/02/03	09:06:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/02/03	09:39:00.0	AOCS_ORe-point_Start_9_0G [0x09f]	AOCU_NM	5	02-76	00 00 00 00 00	