# XRT Timeline to be uploaded on 2015/02/17

Period: 2015/02/17 09:42:00 - 2015/02/21 10:27:00

* * * * * **Normal mode** * * * * *

**XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 02/18 12:03:00 - 02/18 12:09:54 | Fixed ( -528.4, -528.4) | # Post bakeout 1/4 |

**PROG= 02 1-time(s)**

**Subr= 1 1-time(s) 12.0sec**

**Seqn= 88 1-time(s) 12.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 1024x1024 (1536, 1536) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 1024x1024 (1536, 1536) | Q=90 | 0 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Dark | 32ms | Obs | 1x1 | 1024x1024 (1536, 1536) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Dark | 32ms | Obs | 1x1 | 1024x1024 (1536, 1536) | Q=98 | 0 | 0 | 2.0sec |

**Subr= 2 1-time(s) 2.0sec**

**Seqn= 23 2-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Al-mesh | Open/Ti-poly | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Norm | 1.41s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**Subr= 3 2-time(s) 2.0sec**

**Seqn= 12 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | close | Safe | Norm | 32ms | Obs | 1x1 | 2048x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |

**Seqn= 14 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 8ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 02/18 12:13:00 - 02/18 12:19:54 | Fixed ( 528.4, -528.4) | 2/4 |

**PROG= 17 1-time(s)**

**Subr= 1 1-time(s) 12.0sec**

**Seqn= 31 1-time(s) 12.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 1024x1024 (512, 1536) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 1024x1024 (512, 1536) | Q=90 | 0 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Dark | 32ms | Obs | 1x1 | 1024x1024 (512, 1536) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Dark | 32ms | Obs | 1x1 | 1024x1024 (512, 1536) | Q=98 | 0 | 0 | 2.0sec |

**Subr= 2 1-time(s) 2.0sec**

**Seqn= 23 2-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Al-mesh | Open/Ti-poly | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Norm | 1.41s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**Subr= 3 2-time(s) 2.0sec**

**Seqn= 12 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | close | Safe | Norm | 32ms | Obs | 1x1 | 2048x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |

**Seqn= 14 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 8ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 02/18 12:23:00 - 02/18 12:29:54 | Fixed ( 528.4, 528.4) | 3/4 |

**PROG= 15 1-time(s)**

**Subr= 1 1-time(s) 12.0sec**

**Seqn= 81 1-time(s) 12.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 1024x1024 (512, 512) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 1024x1024 (512, 512) | Q=90 | 0 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Dark | 32ms | Obs | 1x1 | 1024x1024 (512, 512) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Dark | 32ms | Obs | 1x1 | 1024x1024 (512, 512) | Q=98 | 0 | 0 | 2.0sec |

**Subr= 2 1-time(s) 2.0sec**

**Seqn= 23 2-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Al-mesh | Open/Ti-poly | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Norm | 1.41s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**Subr= 3 2-time(s) 2.0sec**

**Seqn= 12 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | close | Safe | Norm | 32ms | Obs | 1x1 | 2048x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |

**Seqn= 14 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 8ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 02/18 12:33:00 - 02/18 12:39:54 | Fixed ( -528.4, 528.4) | 4/4 |

**PROG= 09 1-time(s)**

**Subr= 1 1-time(s) 12.0sec**

**Seqn= 28 1-time(s) 12.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 1024x1024 (1536, 512) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 1024x1024 (1536, 512) | Q=90 | 0 | 0 | 2.0sec |

| | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/thick-Be | Open/thick-Be | close | Safe | Dark | 32ms | Obs | 1x1 | 1024x1024 (1536, 512) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Dark | 32ms | Obs | 1x1 | 1024x1024 (1536, 512) | Q=98 | 0 | 0 | 2.0sec |
| **Subr= 2 1-time(s) 2.0sec** | | | | | | | | | | | | |
| **Seqn= 23 2-time(s) 2.0sec** | | | | | | | | | | | | |
| Open/Al-mesh | Open/Ti-poly | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Norm | 1.41s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Subr= 3 2-time(s) 2.0sec** | | | | | | | | | | | | |
| **Seqn= 12 1-time(s) 2.0sec** | | | | | | | | | | | | |
| Open/G-band | Open/G-band | close | Safe | Norm | 32ms | Obs | 1x1 | 2048x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| **Seqn= 14 1-time(s) 2.0sec** | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 8ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #19E4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 02/18 12:43:00 - 02/18 17:59:54 | Track ( 788.0, 172.0) @ 02/18 12:40:00 | # AR12282 obs |

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|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **PROG= 07 Inf.-time(s)** | | | | | | | | | | | | | |
| **Subr= 1 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| **Seqn= 8 2-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/G-band | Open/G-band | close | Safe | Norm | 44ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| **Subr= 2 2-time(s) 2.0sec** | | | | | | | | | | | | | |
| **Seqn= 24 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| **Seqn= 42 4-time(s) 2.0sec** | | | | | | | | | | | | | |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 5.66s | Obs | 1x1 | 512x512 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Norm | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| **Seqn= 62 10-time(s) 180.0sec** | | | | | | | | | | | | | |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 02/18 18:03:00 - 02/18 18:09:54 | Fixed ( 0.0, 0.0) | synoptic |
| 02/19 06:09:00 - 02/19 06:15:54 | Fixed ( 0.0, 0.0) | synoptic, shifted 6.0 min |

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|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **PROG= 03 1-time(s)** | | | | | | | | | | | | | |
| **Subr= 1 1-time(s) 12.0sec** | | | | | | | | | | | | | |
| **Seqn= 33 1-time(s) 4.0sec** | | | | | | | | | | | | | |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 8ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 125ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 1.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Seqn= 5 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 8x8 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 2048x512 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 512x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| **Seqn= 40 1-time(s) 4.0sec** | | | | | | | | | | | | | |
| Open/Ti-poly | Open/Ti-poly | close | Safe | Norm | 16ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/Ti-poly | close | Safe | Norm | 354ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/Ti-poly | close | Safe | Norm | 2.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Seqn= 77 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 86ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 1.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 5.66s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Seqn= 6 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 8ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 32ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 02/18 18:13:00 - 02/19 05:29:00 | Track ( 816.6, 166.8) @ 02/18 18:10:00 | # Cont, |
| 02/19 06:19:00 - 02/19 09:35:00 | Track ( 871.0, 154.9) @ 02/19 06:16:00 | # Cont, |

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|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **PROG= 10 Inf.-time(s)** | | | | | | | | | | | | | |
| **Subr= 1 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| **Seqn= 8 2-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/G-band | Open/G-band | close | Safe | Norm | 44ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| **Subr= 2 2-time(s) 2.0sec** | | | | | | | | | | | | | |
| **Seqn= 24 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |

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|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 32ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| **Seqn= 42 4-time(s) 2.0sec** | | | | | | | | | | | | |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 5.66s | Obs | 1x1 | 512x512 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Norm | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| **Seqn= 62 15-time(s) 120.0sec** | | | | | | | | | | | | |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 1 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 2 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

* * * * * **Flare mode** * * * * *

**XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 02/18 12:43:00 - 02/18 17:59:54 | Track ( 788.0, 172.0) @ 02/18 12:40:00 | # AR12282 obs |
| 02/18 18:13:00 - 02/19 05:29:00 | Track ( 816.6, 166.8) @ 02/18 18:10:00 | # Cont, |
| 02/19 06:19:00 - 02/19 09:35:00 | Track ( 871.0, 154.9) @ 02/19 06:16:00 | # Cont, |

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **PROG= 19 30-time(s)** | | | | | | | | | | | | |
| **Subr= 1 20-time(s) 2.0sec** | | | | | | | | | | | | |
| **Seqn= 11 1-time(s) 2.0sec** | | | | | | | | | | | | |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 125ms | Obs | 2x2 | 512x512 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec |
| **Seqn=100 1-time(s) 10.0sec** | | | | | | | | | | | | |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 125ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec |
| med-Be/Open | Open/thick-Al | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Be | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| **Subr= 2 1-time(s) 2.0sec** | | | | | | | | | | | | |
| **Seqn= 10 1-time(s) 2.0sec** | | | | | | | | | | | | |
| med-Al/Open | med-Al/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Norm | 2.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| **Seqn= 11 1-time(s) 2.0sec** | | | | | | | | | | | | |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 125ms | Obs | 2x2 | 512x512 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec |
| **Seqn= 15 1-time(s) 2.0sec** | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 44ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 2x2 | 512x512 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

* * * * * **Active Region Search** * * * * *

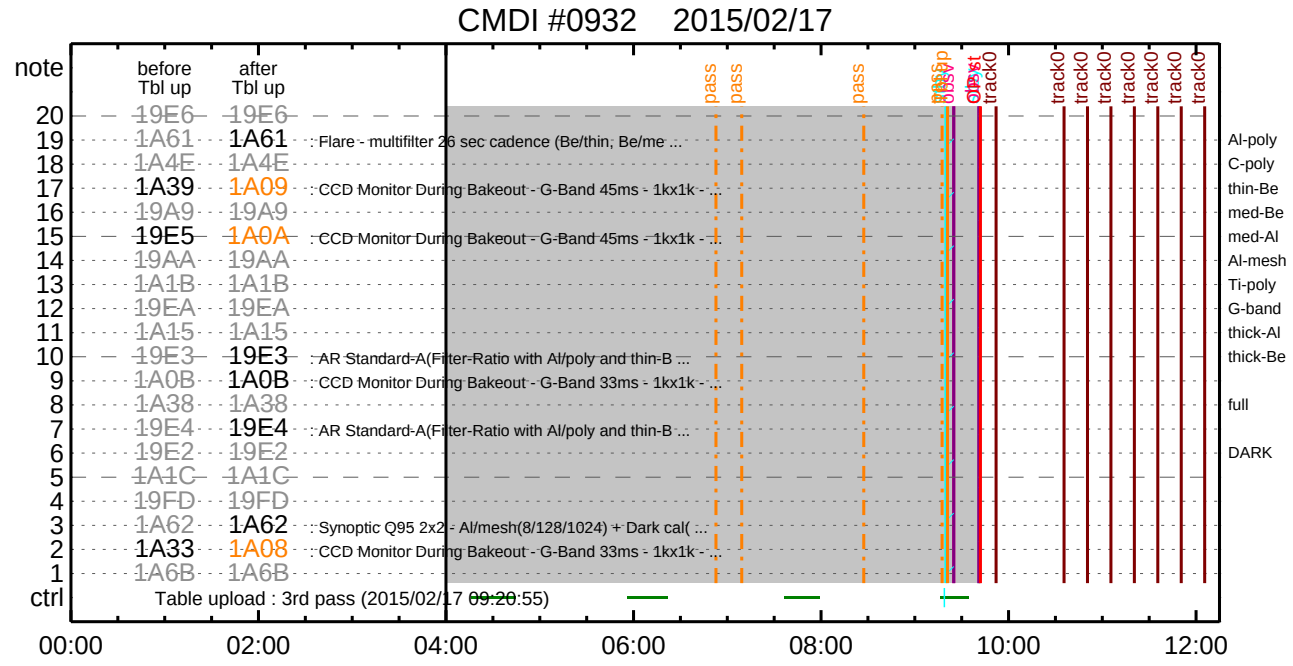
NOT USED

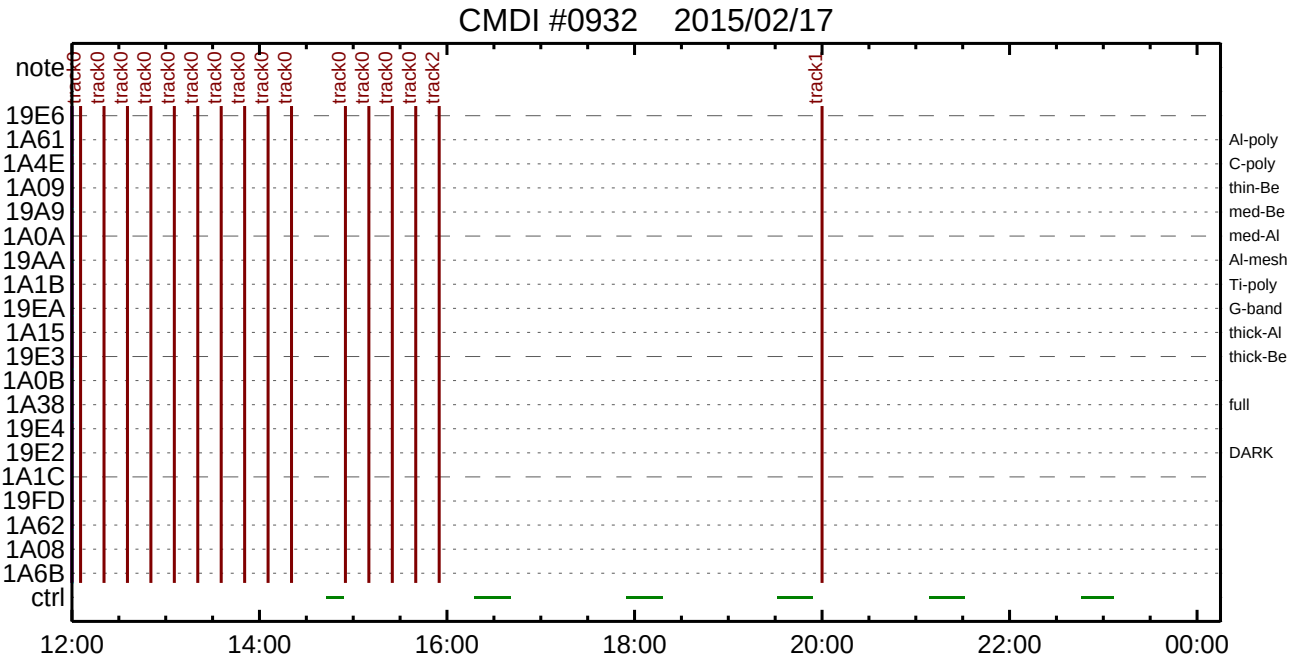
* * * * * **Flare Detection** * * * * *

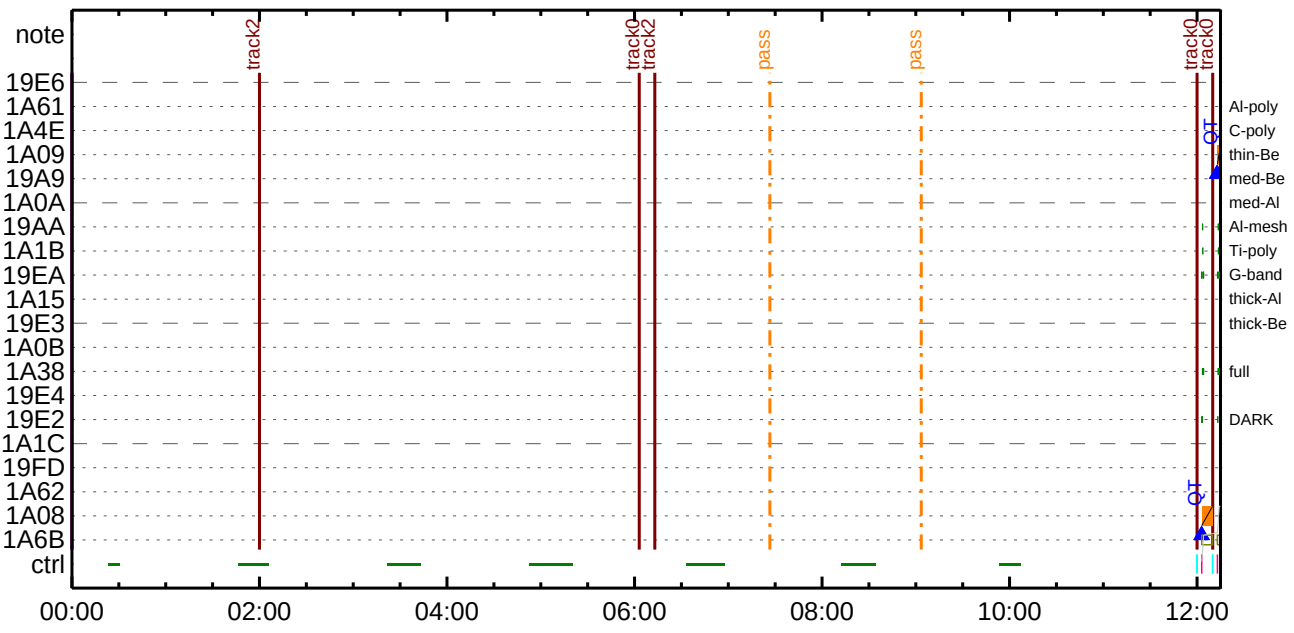
**FLD Patrol**

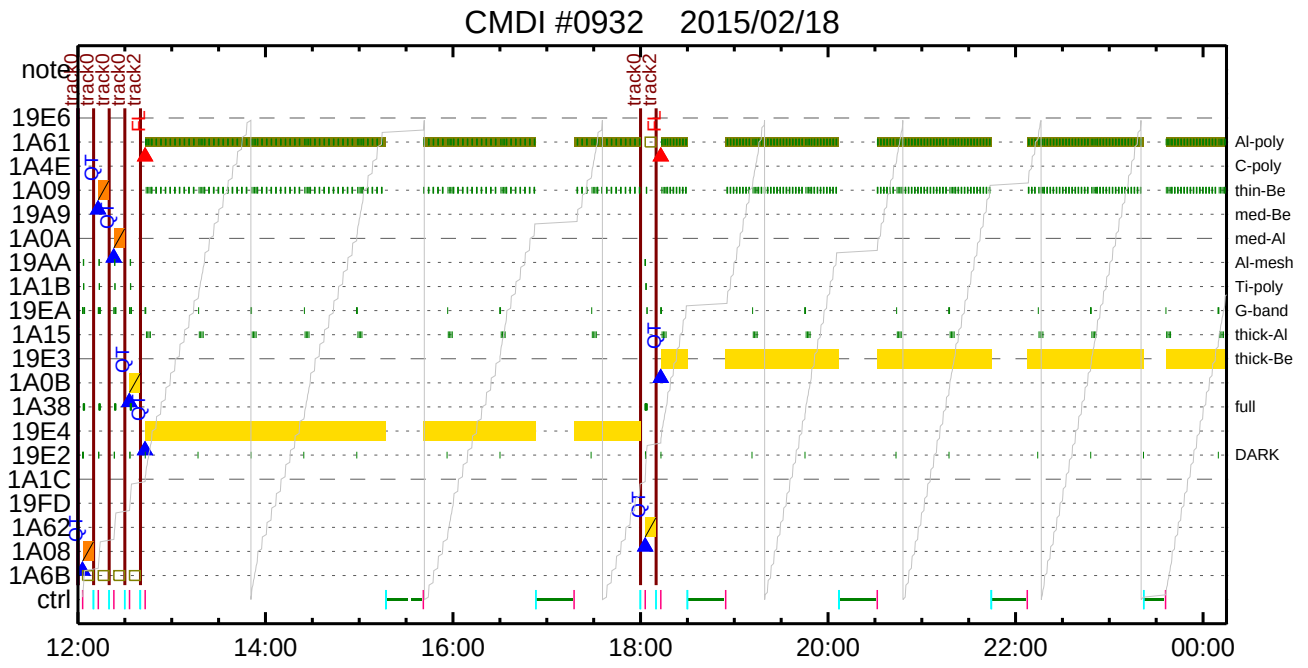
| Term | Pointing (x, y) | Comment |
|---|---|---|
| 02/18 12:40:18 - 02/18 18:00:18 | Track ( 788.0, 172.0) @ 02/18 12:40:00 | # AR12282 obs |
| 02/18 18:10:18 - 02/19 06:06:18 | Track ( 816.6, 166.8) @ 02/18 18:10:00 | # Cont, |
| 02/19 06:16:18 - 02/21 10:27:00 | Track ( 871.0, 154.9) @ 02/19 06:16:00 | # Cont, |

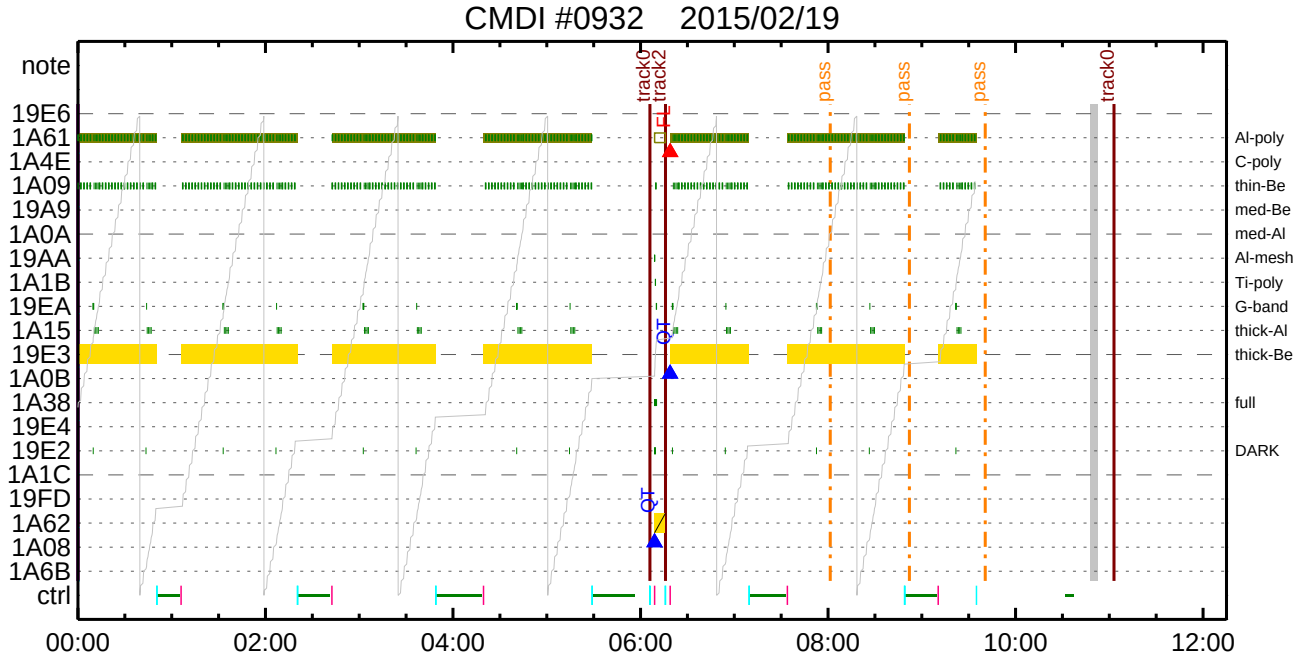
| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Ti-poly | Open/thick-Al | close | Safe | Norm | 8ms | Obs | 8x8 | | Q=50 | | | 30sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

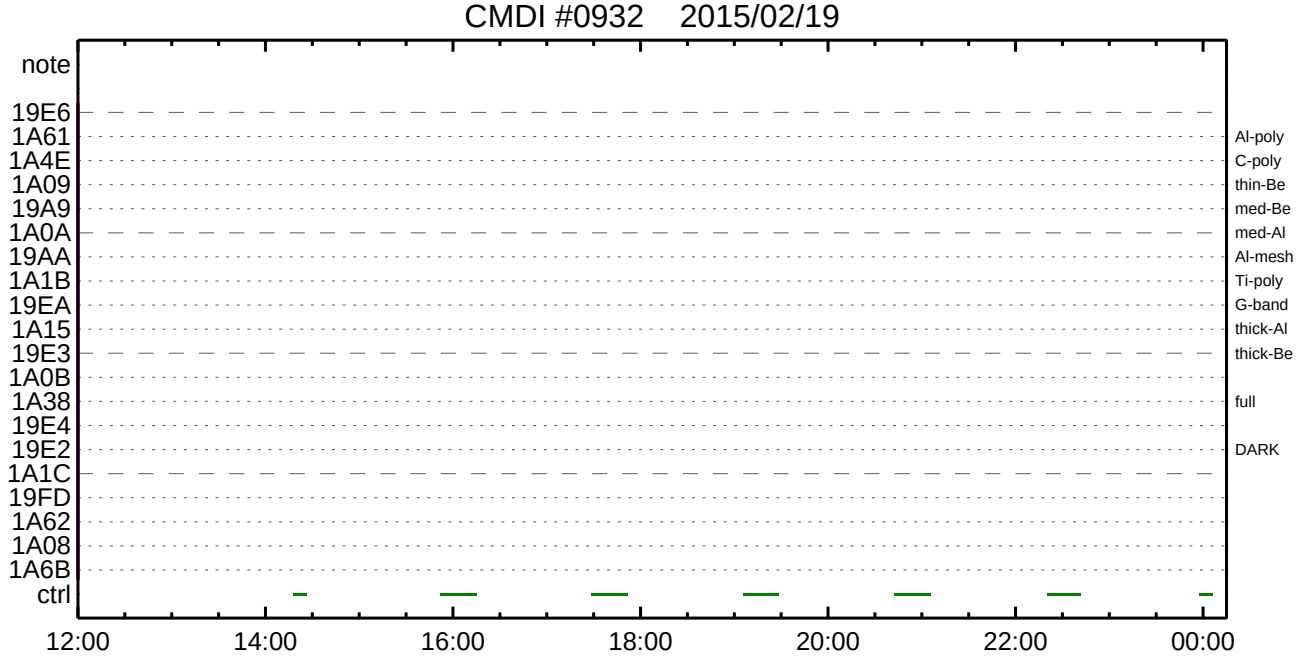

CMDI #0932 2015/02/17
note
before Tbl up
after Tbl up
20 19E6 19E6
19 1A61 1A61 : Flare - multifilter 26 sec cadence (Be/thin; Be/me ...
18 1A4E 1A4E
17 1A39 1A09 : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
16 19A9 19A9
15 19E5 1A0A : CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - ...
14 19AA 19AA
13 1A1B 1A1B
12 19EA 19EA
11 1A15 1A15
10 19E3 19E3 : AR Standard-A(Filter-Ratio with Al/poly and thin-B ...
9 1A0B 1A0B : CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - ...
8 1A38 1A38
7 19E4 19E4 : AR Standard-A(Filter-Ratio with Al/poly and thin-B ...
6 19E2 19E2
5 1A1C 1A1C
4 19FD 19FD
3 1A62 1A62 : Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal( ...
2 1A33 1A08 : CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - ...
1 1A6B 1A6B
ctrl
Table upload : 3rd pass (2015/02/17 09:20:55)
pass
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

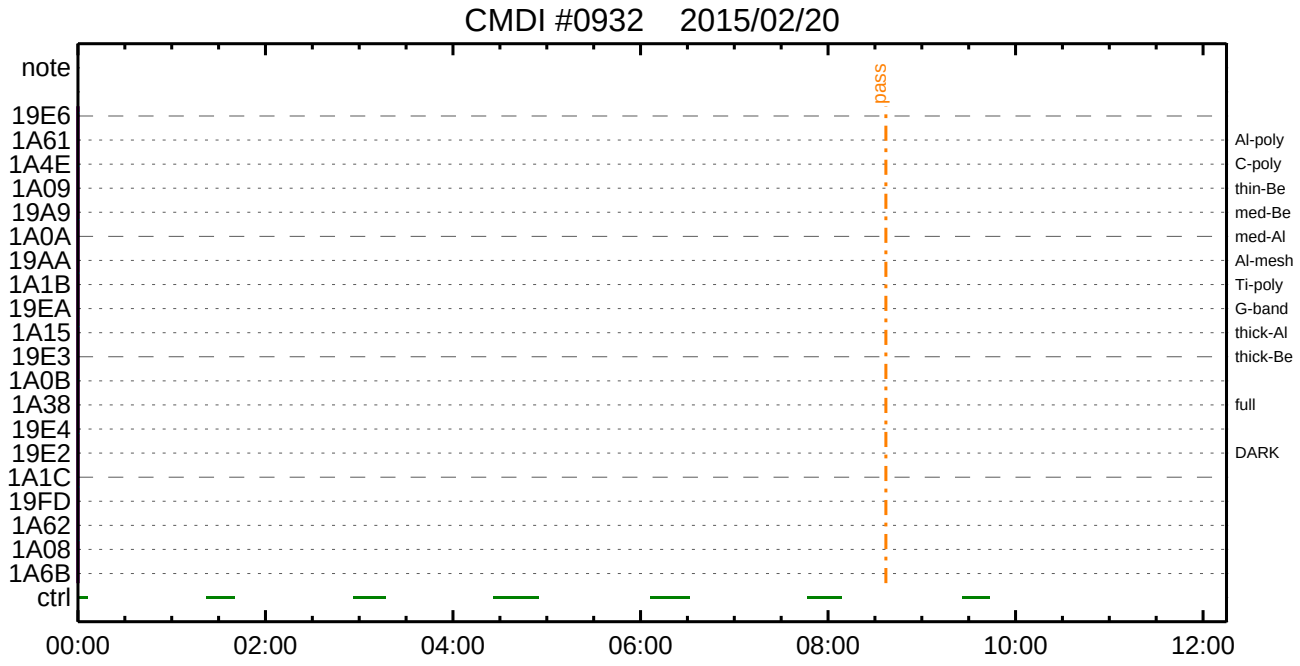

CMDI #0932 2015/02/17
note
track0
track1
track2
19E6
1A61
1A4E
1A09
19A9
1A0A
19AA
1A1B
19EA
1A15
19E3
1A0B
1A38
19E4
19E2
1A1C
19FD
1A62
1A08
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

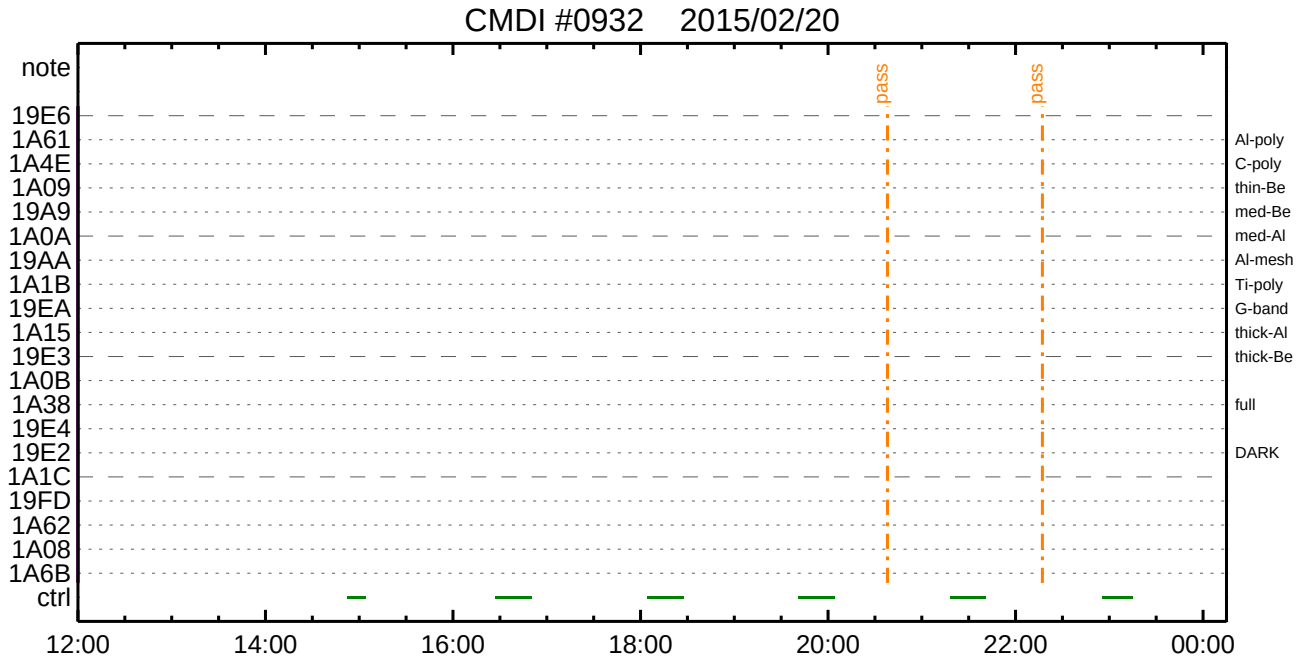

CMDI #0932 2015/02/18
note
track2
track0
pass
19E6
1A61
1A4E
1A09
19A9
1A0A
19AA
1A1B
19EA
1A15
19E3
1A0B
1A38
19E4
19E2
1A1C
19FD
1A62
1A08
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

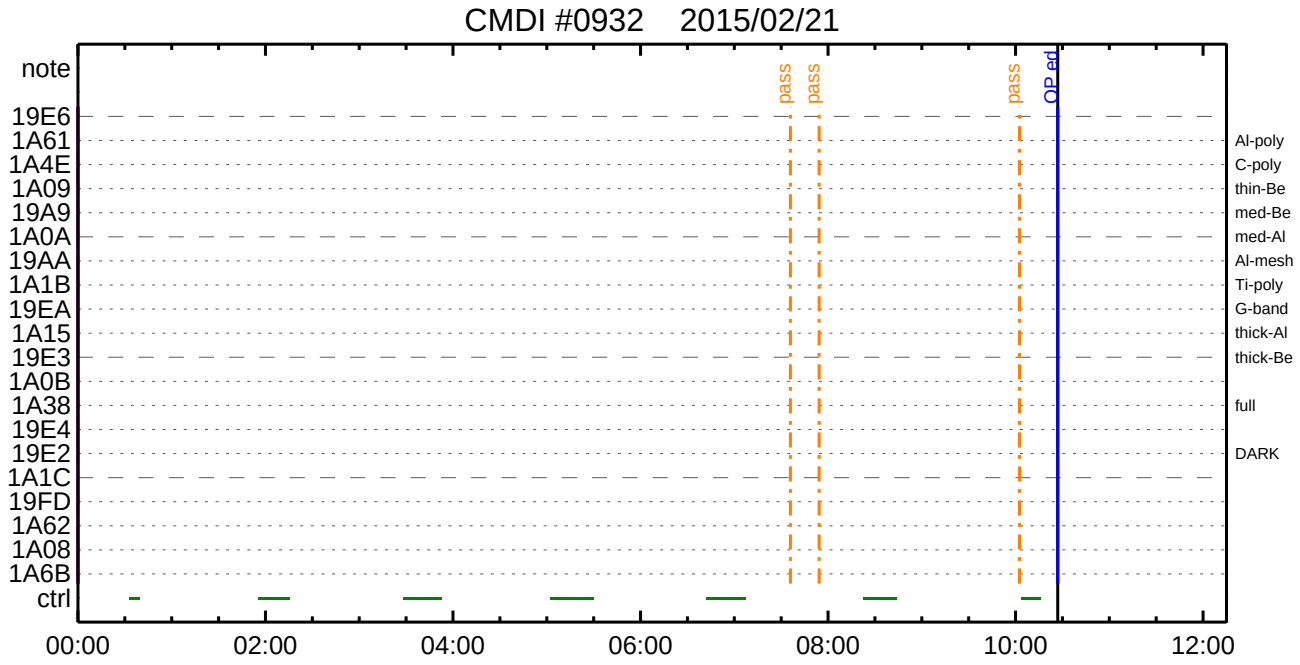

CMDI #0932 2015/02/18
note
19E6
1A61
1A4E
1A09
19A9
1A0A
19AA
1A1B
19EA
1A15
19E3
1A0B
1A38
19E4
19E2
1A1C
19FD
1A62
1A08
1A6B
ctrl
track0
track2
FF
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

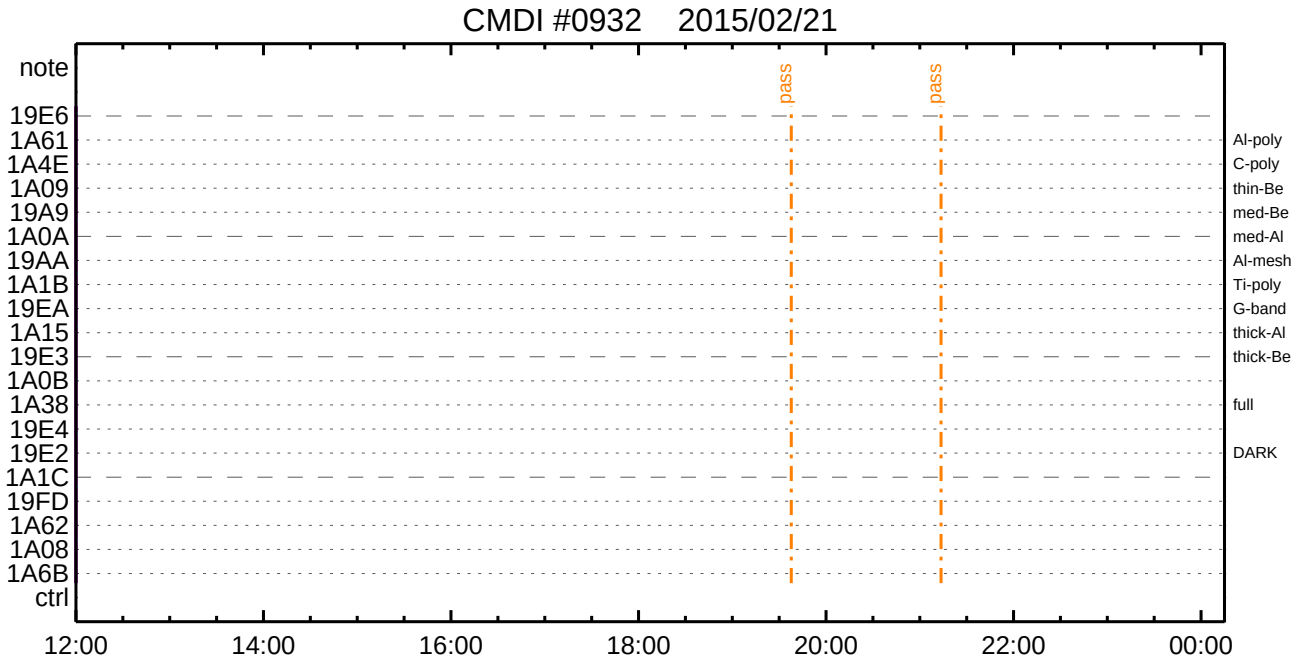

CMDI #0932 2015/02/19
note
19E6
1A61
1A4E
1A09
19A9
1A0A
19AA
1A1B
19EA
1A15
19E3
1A0B
1A38
19E4
19E2
1A1C
19FD
1A62
1A08
1A6B
ctrl
track0
track2
pass
FF
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0932 2015/02/19
note
19E6
1A61
1A4E
1A09
19A9
1A0A
19AA
1A1B
19EA
1A15
19E3
1A0B
1A38
19E4
19E2
1A1C
19FD
1A62
1A08
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0932 2015/02/20
note
19E6
1A61
1A4E
1A09
19A9
1A0A
19AA
1A1B
19EA
1A15
19E3
1A0B
1A38
19E4
19E2
1A1C
19FD
1A62
1A08
1A6B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0932 2015/02/20
note
19E6
1A61
1A4E
1A09
19A9
1A0A
19AA
1A1B
19EA
1A15
19E3
1A0B
1A38
19E4
19E2
1A1C
19FD
1A62
1A08
1A6B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0932 2015/02/21
note
19E6
1A61
1A4E
1A09
19A9
1A0A
19AA
1A1B
19EA
1A15
19E3
1A0B
1A38
19E4
19E2
1A1C
19FD
1A62
1A08
1A6B
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0932 2015/02/21
note
19E6
1A61
1A4E
1A09
19A9
1A0A
19AA
1A1B
19EA
1A15
19E3
1A0B
1A38
19E4
19E2
1A1C
19FD
1A62
1A08
1A6B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-951 2015-02-17 12:17:13 205 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-951:OP
0020     ()
0021   . S. OG    og-951:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2015-02-17 09:37:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2015-02-17 09:37:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2015-02-17 09:37:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2015-02-17 09:41:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ***************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ***************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154     C. ******************************
0155     C.  SOT TI command set
0156     C. ******************************
0157     C. Execute, after the success of OP upload.
0158   +. TI 2015-02-17 09:41:16.0
0159     DC 07-F0 MDP_SOT_MODE_STBY
0160     BC       (41)
0161   . C. ------------------------------------
0162     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0163     C. ------------------------------------
0164     C. ************ SOT END ***********
0165   . C. Stop EIS observation and temporarily disable EIS mode changes
0166     C.
0167     C.
0168     C. ********** Start EIS operation (TI set) **********
0169     C. Execute, after the success of OP upload.
0170     C. Set EIS TI-commands
0171   +. TI 2015-02-17 09:41:30.0
0172     DC 07-FC EIS_MODE_MANU
0173     BC       (21 02)
0174   +. TI 2015-02-17 09:41:40.0
0175     DC 07-FC EIS_MODE_CHG_DIS
0176     BC       (22)
0177   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0178     C. ************ End EIS operation (TI set) ***********
0179     C.
0180     C.
0181     C.
0182     C. ************ XRT START ***********
0183     C. Execute, after the success of OP upload.
0184   +. TI 2015-02-17 09:41:00.0
0185     DC 07-F0 MDP_XRT_MODE_STBY
0186     BC       (c3)
0187   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0188     C.
0189     C. ************ XRT END ***********
0190     C.
0191   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0192     C. (¾å°Ì¥Ó¥Ã¥Ë¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0193   . S. DC-BC dcbc-402:DCBC
```

```
0194     (MDP_known_event)
0195     C.
0196     C.
0197   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0198   . S. DC-BC dcbc-153:DCBC
0199     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200     C.
0201     C.
0202   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0203     C.
0204   . C. ********** LOS **********
0205     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-952 2015-02-17 12:17:13 178 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 4782497.4 +- 1.0 (s) [ ]
0136     C.
0137   . C.
0138   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_CHG_ENA
0140     BC       (20)
0141   . C. Verify EIS_MODE_CHG_FLG is ENA
0142   +. DC 07-FC EIS_MODE_MANU
0143     BC       (21 02)
0144   . C. Verify EIS in MANUAL mode
0145   . C. Estimated OBSTBL upload time is 44s
0146     C. ********************************
0147     C. EIS START OBSTBL LOAD
0148     C. ********************************
0149   . S. RAM   ram-820:EIS_OBSTBL
0150     ()
0151   +. DC 07-FC EIS_DUMP_OBSTBL
0152     BC       (07 07 07 00 00 70 00)
0153     C.
0154     C. Execute, after the success of OBSTBL upload.
0155     C. Set EIS TI-commands
0156   +. TI 2015-02-17 09:41:50.0
0157     DC 07-FC EIS_MODE_CHG_ENA
0158     BC       (20)
0159   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0160     C. ********************************
0161     C. EIS END OBSTBL LOAD
0162     C. ********************************
0163     C.
0164   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0165     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0166   . S. DC-BC dcbc-402:DCBC
0167     (MDP_known_event)
0168     C.
0169     C.
0170   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0171   . S. DC-BC dcbc-153:DCBC
0172     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173     C.
0174     C.
0175   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0176     C.
0177   . C. ********** LOS **********
0178     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-953 2015-02-17 12:17:13 173 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017    . C. < Stop FG table >
0018   +. DC 07-F0 MDP_FG_CTRL_MANU
0019     BC       (51)
0020    . C. ------------------------------------
0021     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024    . C. <Upload FG Observation Table>
0025    . S. RAM   ram-261:MDP_OBS_F
0026     ()
0027     C.
0028    . C. < Dump RAMID=MDP_OBS_F >
0029   +. DC 07-F0 MDP_DUMP_FGTBL
0030     BC       (82 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_F verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035    . C. < Upload DPL table >
0036     C.
0037     C. ¥¢¥Ã¥×¥í¡¼¥É¤ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0038     C.
0039    . S. RAM   ram-271:MDP_DPL
0040     ()
0041     C.
0042    . C. < Dump RAMID=MDP_DPL >
0043   +. DC 07-F0 MDP_DUMP_FGTBL
0044     BC       (82 07 00 38 b8 00 40)
0045     C. ------------------------------------
0046     C.  MDP_DPL verify          = OK    [ ]
0047     C. ------------------------------------
0048     C.
0049     C. STS_CHK¤òON¤Ë¤¹¤ë
0050     C.
0051    . C. < Update MDP DSC PAR1 >
0052   +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053     BC       (4c)
0054     C.  MDP_CMD_CODE          = F04C0700[ ]
0055     C.  MDP_CMD_CNT        (count-up 1) [ ]
0056     C. ------------------------------------
0057     C.
0058    . C.
0059    . C. < Resume FG table (auto mode) >
0060   +. DC 07-F0 MDP_FG_CTRL_AUTO
0061     BC       (50)
0062    . C. ------------------------------------
0063     C.  MDP_FG_CTRL_MODE      = AUTO    [ ]
0064     C. ------------------------------------
0065     C.
0066     C. ********************************
0067     C.  SOT TI command set
0068     C. ********************************
0069     C. Execute, after the success of TBL upload.
0070   +. TI 2015-02-17 09:41:18.0
0071     DC 07-F0 MDP_SOT_MODE_OBSV
0072     BC       (40)
0073    . C. ------------------------------------
0074     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0075     C. ------------------------------------
0076     C.
0077     C. Only when FG_CTRL_AUTO is used in RT.
0078   +. TI 2015-02-17 09:41:20.0
0079     DC 07-F0 MDP_FG_CTRL_AUTO
0080     BC       (50)
0081    . C. ------------------------------------
0082     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0083     C. ------------------------------------
0084     C. ************ SOT END ************
0085     C.
0086     C. ************ XRT START ************
0087     C.
0088   +. DC 07-F0 MDP_XRT_CTRL_MANU
0089     BC       (c1)
0090   +  DC 07-F0 MDP_XRT_MODE_STBY
0091     BC       (c3)
0092    . C. ------------ Success Verify ?              OK / NG____
0093     C.
0094     C. XRT Obs. Table Upload
0095    . S. RAM   ram-291:MDP_OBS_X
```

```
0096     ()
0097     C.
0098  +. DC 07-F0 MDP_DUMP_XRTTBL
0099     BC       (84 07 00 00 00 3a d4)
0100   . C. ----------- Comparison Check ?                      OK / ERR ____
0101     C.
0102     C.
0103  +. DC 07-F0 MDP_XRT_ROI_SET
0104     BC       (cd 01 b1 b1 04 04)
0105  +  DC 07-F0 MDP_XRT_ROI_SET
0106     BC       (cd 02 b1 b1 08 08)
0107  +  DC 07-F0 MDP_XRT_ROI_SET
0108     BC       (cd 03 b1 b1 08 08)
0109  +  DC 07-F0 MDP_XRT_ROI_SET
0110     BC       (cd 04 b1 b1 06 06)
0111  +  DC 07-F0 MDP_XRT_ROI_SET
0112     BC       (cd 05 85 83 06 06)
0113  +  DC 07-F0 MDP_XRT_ROI_SET
0114     BC       (cd 06 c0 c0 10 10)
0115  +  DC 07-F0 MDP_XRT_ROI_SET
0116     BC       (cd 07 80 80 20 20)
0117  +  DC 07-F0 MDP_XRT_ROI_SET
0118     BC       (cd 08 40 c0 10 10)
0119  +  DC 07-F0 MDP_XRT_ROI_SET
0120     BC       (cd 09 40 40 10 10)
0121  +  DC 07-F0 MDP_XRT_ROI_SET
0122     BC       (cd 0a c0 40 10 10)
0123  +  DC 07-F0 MDP_XRT_ROI_SET
0124     BC       (cd 0b 85 83 06 06)
0125  +  DC 07-F0 MDP_XRT_ROI_SET
0126     BC       (cd 0c 85 83 08 08)
0127  +  DC 07-F0 MDP_XRT_ROI_SET
0128     BC       (cd 0d 80 80 20 08)
0129  +  DC 07-F0 MDP_XRT_ROI_SET
0130     BC       (cd 0e 80 80 08 20)
0131  +  DC 07-F0 MDP_XRT_ROI_SET
0132     BC       (cd 0f 80 80 06 06)
0133  +  DC 07-F0 MDP_XRT_ROI_SET
0134     BC       (cd 10 80 80 08 08)
0135  +  DC 07-F0 MDP_XRT_FLD_ENA
0136     BC       (d8)
0137  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0138     BC       (c8)
0139  +  DC 07-F0 MDP_XRT_AEC_RESET
0140     BC       (d0)
0141  +  DC 07-F0 MDP_XRT_ARS_DIS
0142     BC       (d5)
0143  +  DC 07-F0 MDP_XRT_FLD_RESET
0144     BC       (da)
0145   . C. ---------- Success Verify ?            OK / NG ____
0146     C.
0147     C.
0148   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0149     C.
0150  +. DC 07-F0 MDP_XRT_MODE_OBSV
0151     BC       (c2)
0152  +. TI 2015-02-17 09:41:02.0
0153     DC 07-F0 MDP_XRT_MODE_OBSV
0154     BC       (c2)
0155   . C. ---------- Success Verify ?            OK / NG ____
0156     C.
0157     C. ************ XRT END ***********
0158     C.
0159   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0160     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0161   . S. DC-BC dcbc-402:DCBC
0162     (MDP_known_event)
0163     C.
0164     C.
0165   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0166   . S. DC-BC dcbc-153:DCBC
0167     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0168     C.
0169     C.
0170   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0171     C.
0172   . C. *********** LOS ***********
0173     C.
```

```
*** OP Sequence for XRT ***

2015/02/17  09:52:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 15 58 53 4b
2015/02/17  10:35:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 11 99 44 72
2015/02/17  10:50:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 0f be 34 9b
2015/02/17  11:05:30.5  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 0e 97 24 31
2015/02/17  11:20:30.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 0d f3 13 64
2015/02/17  11:35:30.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 0d ca 02 0c
2015/02/17  11:50:30.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 0d f3 f0 e6
2015/02/17  12:05:30.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 0e 97 e0 19
2015/02/17  12:20:30.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 0f be cf a6
2015/02/17  12:35:30.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 11 99 bf cf
2015/02/17  12:50:30.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 15 58 b0 fe
2015/02/17  13:05:30.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM              5 02-76 00 01 58 52 97
2015/02/17  13:20:30.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM              5 02-76 00 01 58 40 e5
2015/02/17  13:35:30.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM              5 02-76 00 01 58 2f 33
2015/02/17  13:50:30.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM              5 02-76 00 01 58 1d 81
2015/02/17  14:05:30.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 00 01 58 0b ce
2015/02/17  14:20:30.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM              5 02-76 00 01 58 fa 0d
2015/02/17  14:55:00.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM              5 02-76 00 01 58 e8 5b
2015/02/17  15:10:00.0  AOCS_ORe-point_Start_19_OG [0x0a9]
                                AOCU_NM              5 02-76 00 01 58 d6 a8
2015/02/17  15:25:00.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                AOCU_NM              5 02-76 00 01 58 c4 fe
2015/02/17  15:40:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                AOCU_NM              5 02-76 00 01 58 b3 4c
2015/02/17  15:55:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 02 00 00 00 00
2015/02/17  20:00:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                AOCU_NM              5 02-76 01 00 00 00 00
2015/02/18  02:00:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 02 00 00 00 00
2015/02/18  06:00:00.0  XRT_TCIB_XRT_S_HTR_A_DIS_447_OG [0x1bf]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2015/02/18  06:03:00.0  AOCS_ORe-point_Start_24_OG [0x0ae]
                                AOCU_NM              5 02-76 00 00 00 00 00
2015/02/18  06:13:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 02 00 00 00 00
2015/02/18  11:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  11:59:56.0  XRT_CTRL_MANU_449_OG [0x1c1]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  12:00:00.0  AOCS_ORe-point_Start_25_OG [0x0af]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2015/02/18  12:02:32.0  XRT_FOCUS_POSITION_432_OG [0x1b0]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/02/18  12:02:52.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2015/02/18  12:02:54.0  XRT_FLD_DIS_437_OG [0x1b5]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/02/18  12:02:56.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/02/18  12:02:58.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/02/18  12:03:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  12:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  12:09:56.0  XRT_CTRL_MANU_449_OG [0x1c1]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  12:10:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                AOCU_NM              5 02-76 00 2e f9 d1 07
2015/02/18  12:12:32.0  XRT_FOCUS_POSITION_432_OG [0x1b0]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/02/18  12:12:52.0  XRT_QT_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2015/02/18  12:12:54.0  XRT_FLD_DIS_437_OG [0x1b5]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/02/18  12:12:56.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/02/18  12:12:58.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/02/18  12:13:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  12:19:54.0  XRT_CTRL_MANU_402_OG [0x192]
```

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                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  12:19:56.0  XRT_CTRL_MANU_449_OG [0x1c1]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  12:20:00.0  AOCS_ORe-point_Start_27_OG [0x0b1]
                                    AOCU_NM              5 02-76 00 d1 07 d1 07
2015/02/18  12:22:32.0  XRT_FOCUS_POSITION_432_OG [0x1b0]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/02/18  12:22:52.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2015/02/18  12:22:54.0  XRT_FLD_DIS_437_OG [0x1b5]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2015/02/18  12:22:56.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/02/18  12:22:58.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2015/02/18  12:23:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  12:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  12:29:56.0  XRT_CTRL_MANU_449_OG [0x1c1]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  12:30:00.0  AOCS_ORe-point_Start_28_OG [0x0b2]
                                    AOCU_NM              5 02-76 00 d1 07 2e f9
2015/02/18  12:32:32.0  XRT_FOCUS_POSITION_432_OG [0x1b0]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/02/18  12:32:52.0  XRT_QT_PROG_SET_448_OG [0x1c0]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2015/02/18  12:32:54.0  XRT_FLD_DIS_437_OG [0x1b5]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2015/02/18  12:32:56.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/02/18  12:32:58.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2015/02/18  12:33:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  12:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  12:39:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  12:39:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/02/18  12:40:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                    AOCU_NM              5 02-76 02 00 00 00 00
2015/02/18  12:40:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2015/02/18  12:40:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2015/02/18  12:40:22.0  XRT_AEC_RESET_413_OG [0x19d]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2015/02/18  12:40:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2015/02/18  12:40:26.0  XRT_FLD_RESET_407_OG [0x197]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/18  12:42:56.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2015/02/18  12:42:58.0  XRT_FL_PROG_SET_401_OG [0x191]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2015/02/18  12:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  15:17:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  15:17:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  15:17:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/18  15:17:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/18  15:20:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/18  15:40:00.0  XRT_Custom_430_OG [0x1ae]
2015/02/18  15:41:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  16:53:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  16:53:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  16:53:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/18  16:53:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/18  16:56:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/18  17:16:30.0  XRT_Custom_430_OG [0x1ae]
2015/02/18  17:17:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  17:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  17:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  17:59:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/02/18  18:00:00.0  AOCS_ORe-point_Start_24_OG [0x0ae]
                                    AOCU_NM              5 02-76 00 00 00 00 00
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```
2015/02/18  18:00:18.0  XRT_FLD_DIS_422_OG [0x1a6]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/02/18  18:02:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/02/18  18:02:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/02/18  18:02:58.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2015/02/18  18:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  18:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  18:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  18:09:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/02/18  18:10:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 02 00 00 00 00
2015/02/18  18:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2015/02/18  18:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2015/02/18  18:10:22.0  XRT_AEC_RESET_413_OG [0x19d]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2015/02/18  18:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/02/18  18:10:26.0  XRT_FLD_RESET_407_OG [0x197]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/18  18:12:56.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2015/02/18  18:12:58.0  XRT_FL_PROG_SET_401_OG [0x191]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2015/02/18  18:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  18:30:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  18:30:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  18:30:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/18  18:30:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/18  18:33:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/18  18:53:30.0  XRT_Custom_430_OG [0x1ae]
2015/02/18  18:54:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  20:07:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  20:07:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  20:07:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/18  20:07:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/18  20:10:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/18  20:30:30.0  XRT_Custom_430_OG [0x1ae]
2015/02/18  20:31:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  21:44:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  21:44:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  21:44:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/18  21:44:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/18  21:47:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/18  22:06:30.0  XRT_Custom_430_OG [0x1ae]
2015/02/18  22:07:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/18  23:22:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  23:22:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/18  23:22:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/18  23:22:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/18  23:25:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/18  23:35:00.0  XRT_Custom_430_OG [0x1ae]
2015/02/18  23:36:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/19  00:50:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  00:50:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  00:50:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/19  00:50:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
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                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/19  00:53:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/19  01:05:00.0  XRT_Custom_430_OG [0x1ae]
2015/02/19  01:06:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/19  02:20:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  02:20:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  02:20:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/19  02:20:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/19  02:23:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/19  02:41:30.0  XRT_Custom_430_OG [0x1ae]
2015/02/19  02:42:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/19  03:49:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  03:49:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  03:49:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/19  03:49:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/19  03:52:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/19  04:18:30.0  XRT_Custom_430_OG [0x1ae]
2015/02/19  04:19:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/19  05:29:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  05:29:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  05:29:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/19  05:29:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/19  05:32:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/19  06:05:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  06:05:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  06:05:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/02/19  06:06:00.5  AOCS_ORe-point_Start_24_OG [0x0ae]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2015/02/19  06:06:18.0  XRT_FLD_DIS_422_OG [0x1a6]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2015/02/19  06:08:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/02/19  06:08:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2015/02/19  06:08:58.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2015/02/19  06:09:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/19  06:15:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  06:15:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  06:15:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/02/19  06:16:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                        AOCU_NM              5 02-76 02 00 00 00 00
2015/02/19  06:16:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2015/02/19  06:16:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2015/02/19  06:16:22.0  XRT_AEC_RESET_413_OG [0x19d]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2015/02/19  06:16:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2015/02/19  06:16:26.0  XRT_FLD_RESET_407_OG [0x197]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/19  06:18:56.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2015/02/19  06:18:58.0  XRT_FL_PROG_SET_401_OG [0x191]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2015/02/19  06:19:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/19  07:09:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  07:09:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  07:09:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/19  07:09:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/19  07:12:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
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                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/19  07:33:00.0  XRT_Custom_430_OG [0x1ae]
2015/02/19  07:34:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/19  08:49:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  08:49:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  08:49:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2015/02/19  08:49:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/02/19  08:52:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/02/19  09:09:30.0  XRT_Custom_430_OG [0x1ae]
2015/02/19  09:10:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/02/19  09:35:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/02/19  11:03:00.0  AOCS_ORe-point_Start_24_OG [0x0ae]
                                    AOCU_NM              5 02-76 00 00 00 00 00
```