

XRT Timeline to be uploaded on 2015/02/19

Period: 2015/02/19 10:53:00 - 2015/02/24 10:36:00

* * * * *

Normal mode

* * * * *

XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
02/19 11:06:00 - 02/19 17:28:30	Track (889.2, 149.9) ^{@ 02/19 11:03:00}	# OP start + 10min, AR 12282 monitoring.
02/19 18:06:30 - 02/19 19:59:54	Track (912.5, 142.5) ^{@ 02/19 18:03:30}	# AR cont.
PROG= 20 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 8 2-time(s) 2.0sec		
Open/G-band Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Subr= 2 2-time(s) 2.0sec		
Seqn= 24 1-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
Open/G-band Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
Seqn= 42 4-time(s) 2.0sec		
Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048)	Q=95 3 0 2.0sec
Open/thick-Al Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Seqn= 62 8-time(s) 270.0sec		
thin-Be/Open med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 1 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 1 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 2 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 2 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thin-Be

Term	Pointing (x, y)	Comment
02/19 17:56:30 - 02/19 18:03:24	Fixed (0.0, 0.0)	synoptic, shifted -6.5 min
02/20 05:41:30 - 02/20 05:48:24	Fixed (0.0, 0.0)	synoptic, shifted -21.5 min
02/20 17:43:30 - 02/20 17:50:24	Fixed (0.0, 0.0)	synoptic, shifted -19.5 min
02/21 06:01:30 - 02/21 06:08:24	Fixed (0.0, 0.0)	synoptic, shifted -1.5 min
PROG= 03 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
Seqn= 33 1-time(s) 4.0sec		
Open/Al-mesh Open/Al-mesh close	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024)	DPCM 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024)	DPCM 0 0 2.0sec
Seqn= 40 1-time(s) 4.0sec		
Open/Ti-poly Open/Ti-poly close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Ti-poly Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Ti-poly Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 77 1-time(s) 2.0sec		
thin-Be/Open thin-Be/Open close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Open/G-band Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

XOB #1A02: AR standard-B (thin-Be) with PFB (384FOV), 512x512 at 1064 1048, thick Al and Al/Poly context, With G-band (33ms/45ms leak), 5min cad

Term	Pointing (x, y)	Comment
02/19 20:03:00 - 02/20 05:38:24	Fixed (918.0, 140.0)	# AR fixed pointing.
02/20 05:51:30 - 02/20 17:40:24	Fixed (918.0, 140.0)	# AR cont.
02/20 21:42:00 - 02/21 05:58:24	Track (-747.7, -65.7) ^{@ 02/20 21:30:00}	# AR 12289 at East limb.
02/21 06:11:30 - 02/21 09:52:00	Track (-694.6, -58.2) ^{@ 02/21 06:08:30}	# AR 12289 at East limb.
PROG= 12 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 20 2-time(s) 2.0sec		
Open/G-band Open/G-band close	Safe Norm 44ms Obs 1x1 512x512 (1064, 1048)	DPCM 0 0 2.0sec
Seqn= 75 1-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048)	Q=98 0 0 2.0sec
Open/G-band Open/G-band open	Safe Norm 32ms Obs 1x1 512x512 (1064, 1048)	Q=98 0 0 2.0sec
Subr= 2 2-time(s) 2.0sec		
Seqn= 66 2-time(s) 2.0sec		
Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048)	Q=95 3 0 2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 85 7-time(s) 300.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	17.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A33: CME watch - 4x4 - AEC 2 - Al-poly - G-band (2x2,8ms) - Leak (33ms) - 5min cad

Term	Pointing (x, y)	Comment										
02/20 17:53:30 - 02/20 21:18:30	Track (0.0, -0.0) @ 02/20 17:50:30	# EIS sensitivity monitoring.										
PROG= 11 Inf-time(s)												
Subr= 1 20-time(s) 300.0sec												
Seqn= 78 1-time(s) 4.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Flare mode

* * * * *

XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment										
02/19 11:06:00 - 02/19 17:28:30	Track (889.2, 149.9) @ 02/19 11:03:00	# OP start + 10min, AR 12282 monitoring.										
02/19 18:06:30 - 02/19 19:59:54	Track (912.5, 142.5) @ 02/19 18:03:30	# AR cont.										
02/19 20:03:00 - 02/20 05:38:24	Fixed (918.0, 140.0)	# AR fixed pointing.										
02/20 05:51:30 - 02/20 17:40:24	Fixed (918.0, 140.0)	# AR cont.										
02/20 17:53:30 - 02/20 21:18:30	Track (0.0, -0.0) @ 02/20 17:50:30	# EIS sensitivity monitoring.										
02/20 21:42:00 - 02/21 05:58:24	Track (-747.7, -65.7) @ 02/20 21:30:00	# AR 12289 at East limb.										
02/21 06:11:30 - 02/21 09:52:00	Track (-694.6, -58.2) @ 02/21 06:08:30	# AR 12289 at East limb.										
PROG= 19 30-time(s)												
Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

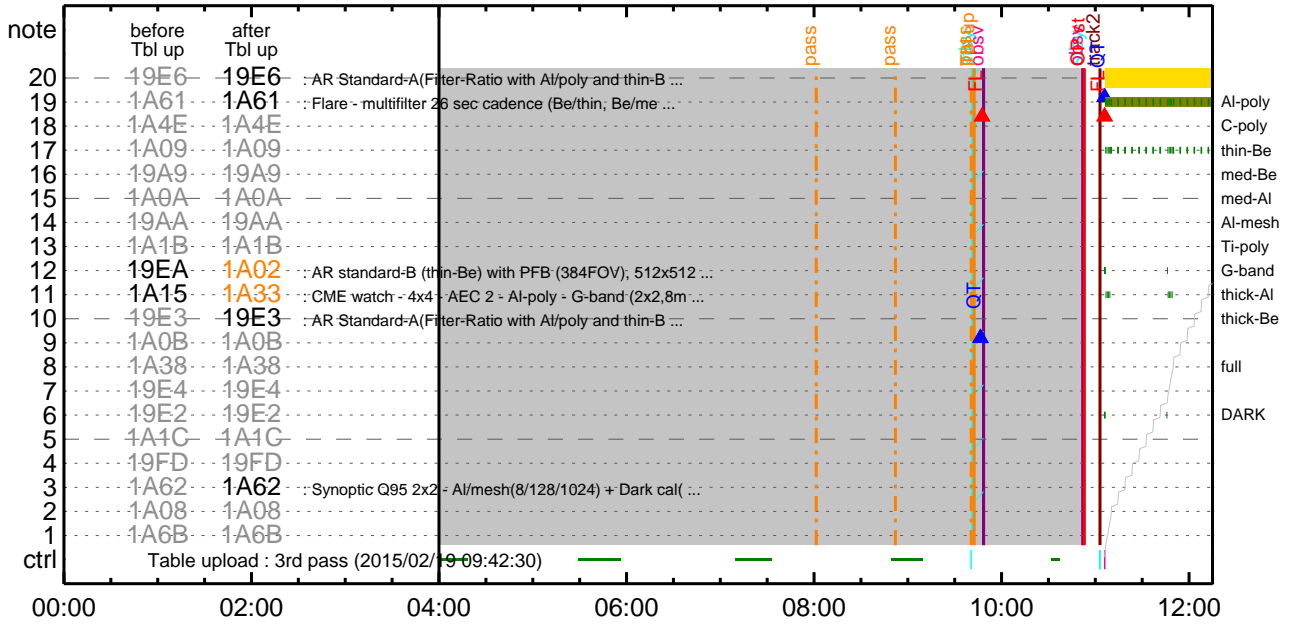
Flare Detection

* * * * *

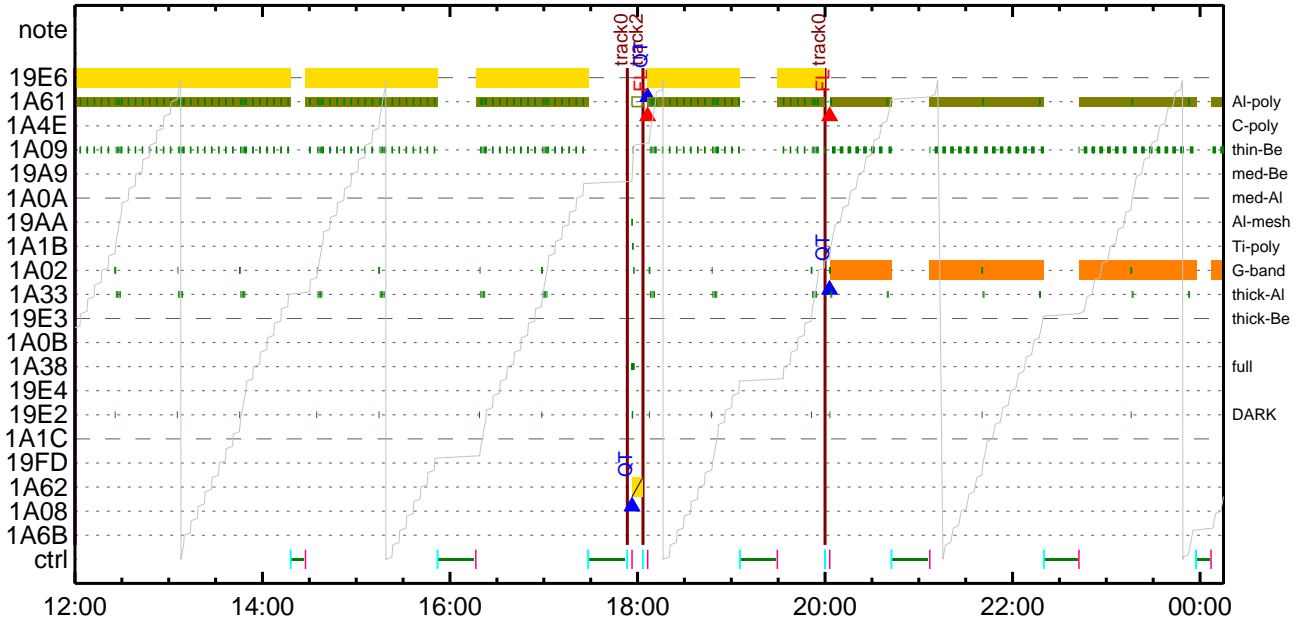
FLD Patrol

Term	Pointing (x, y)	Comment									
02/19 18:03:48 - 02/20 05:38:48	Track (912.5, 142.5) @ 02/19 18:03:30	# AR cont.									
02/20 05:48:48 - 02/20 17:40:48	Fixed (918.0, 140.0)	# AR cont.									
02/20 17:50:48 - 02/21 05:58:48	Track (0.0, -0.0) @ 02/20 17:50:30	# EIS sensitivity monitoring.									
02/21 06:08:48 - 02/24 10:36:00	Track (-694.6, -58.2) @ 02/21 06:08:30	# AR 12289 at East limb.									
Open/TI-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

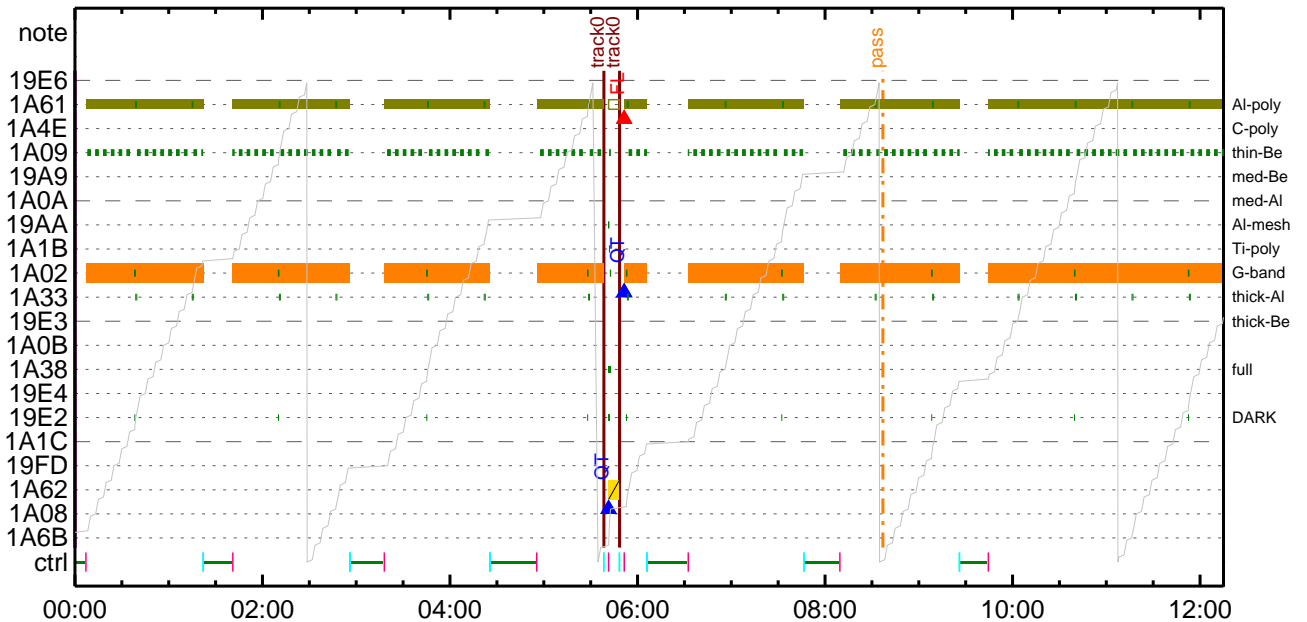
CMDI #0936 2015/02/19



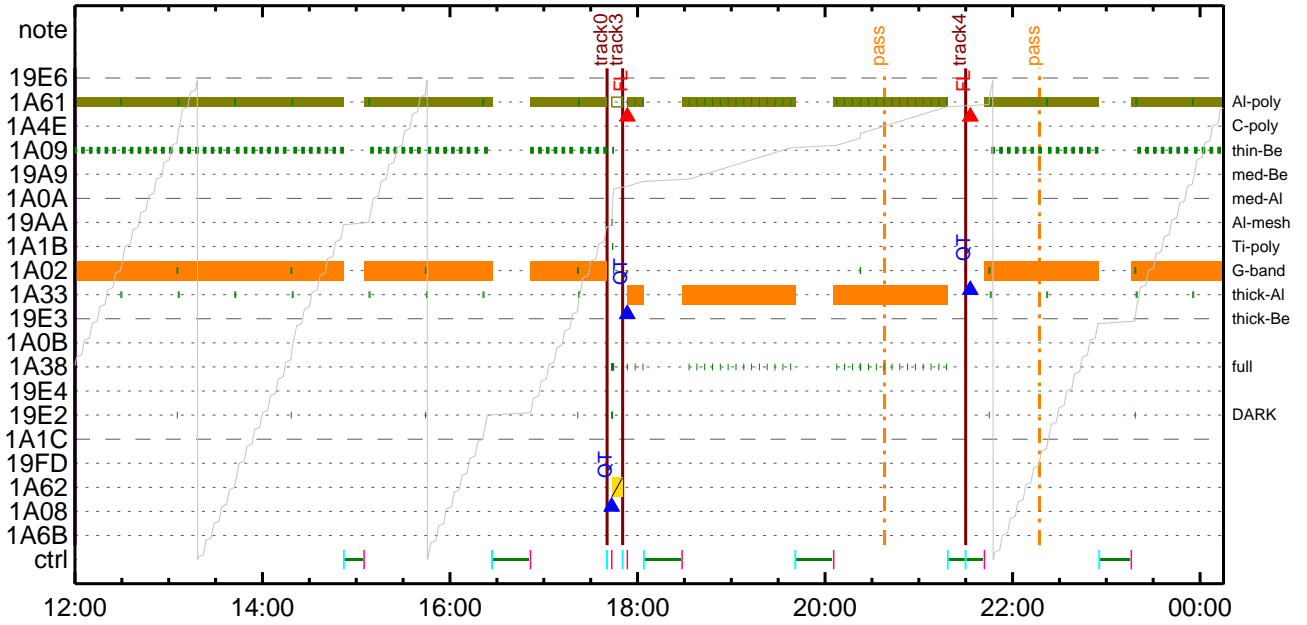
CMDI #0936 2015/02/19



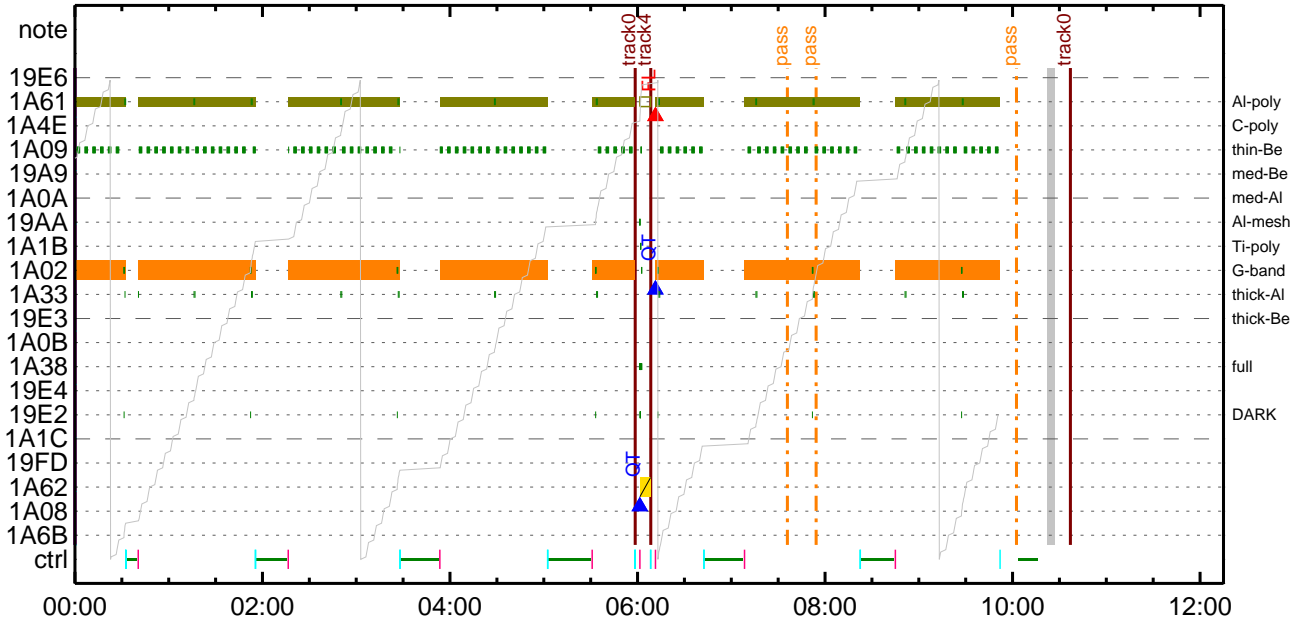
CMDI #0936 2015/02/20



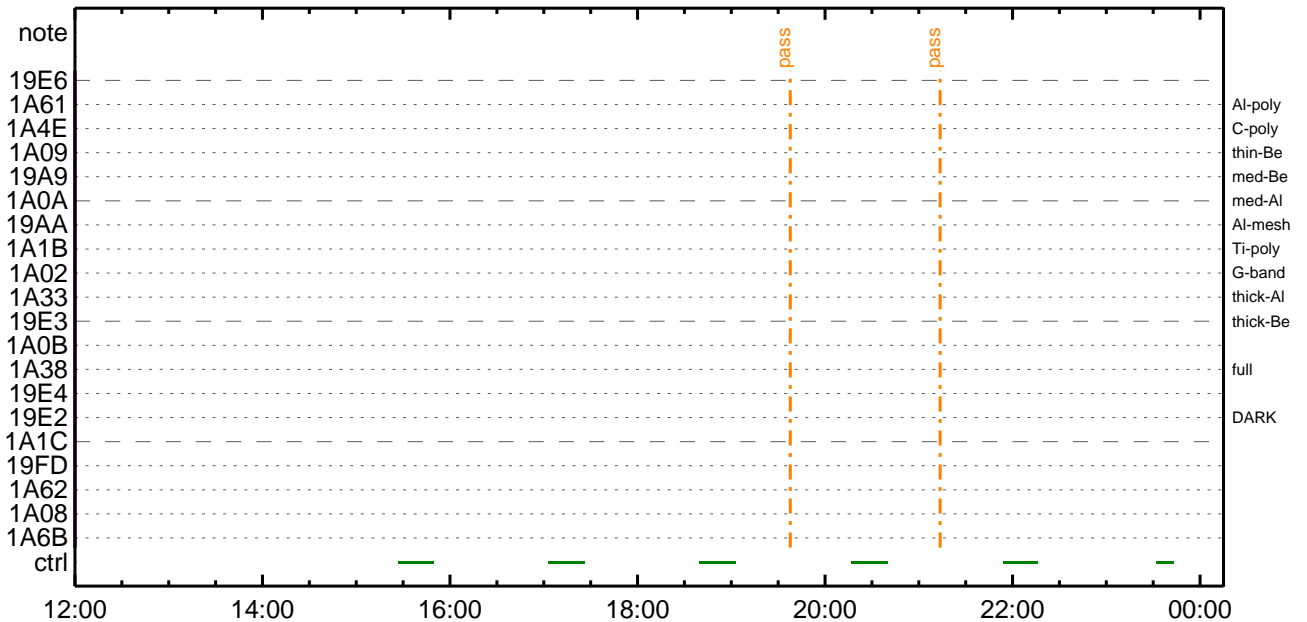
CMDI #0936 2015/02/20



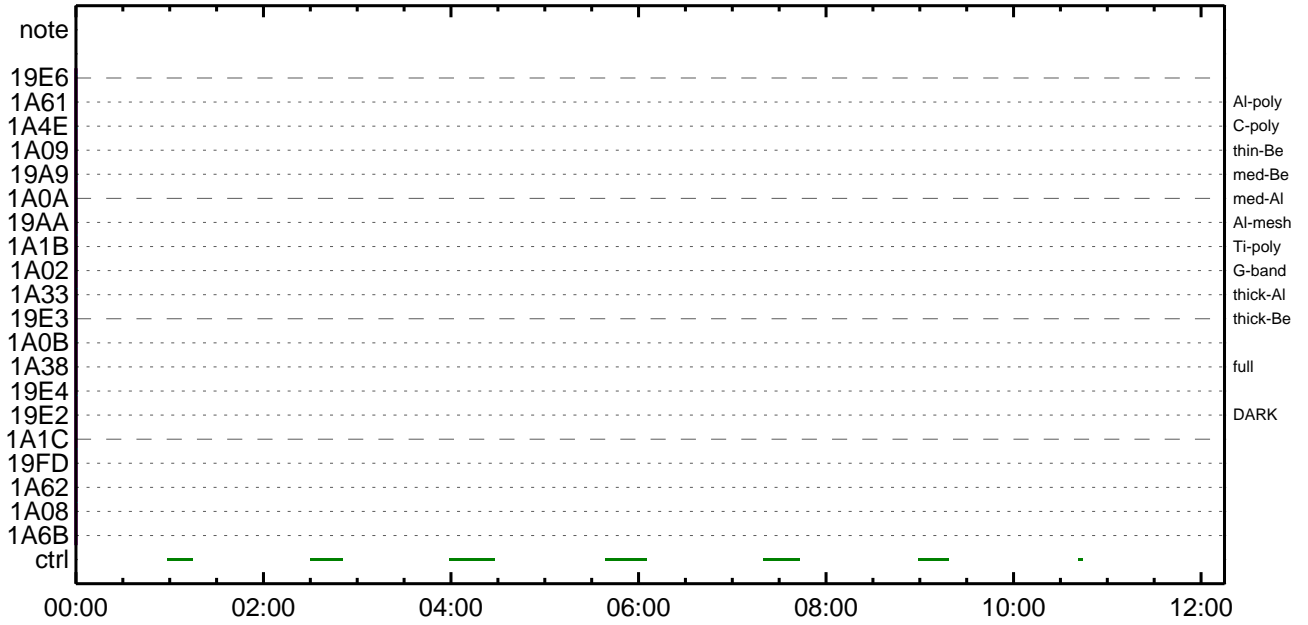
CMDI #0936 2015/02/21



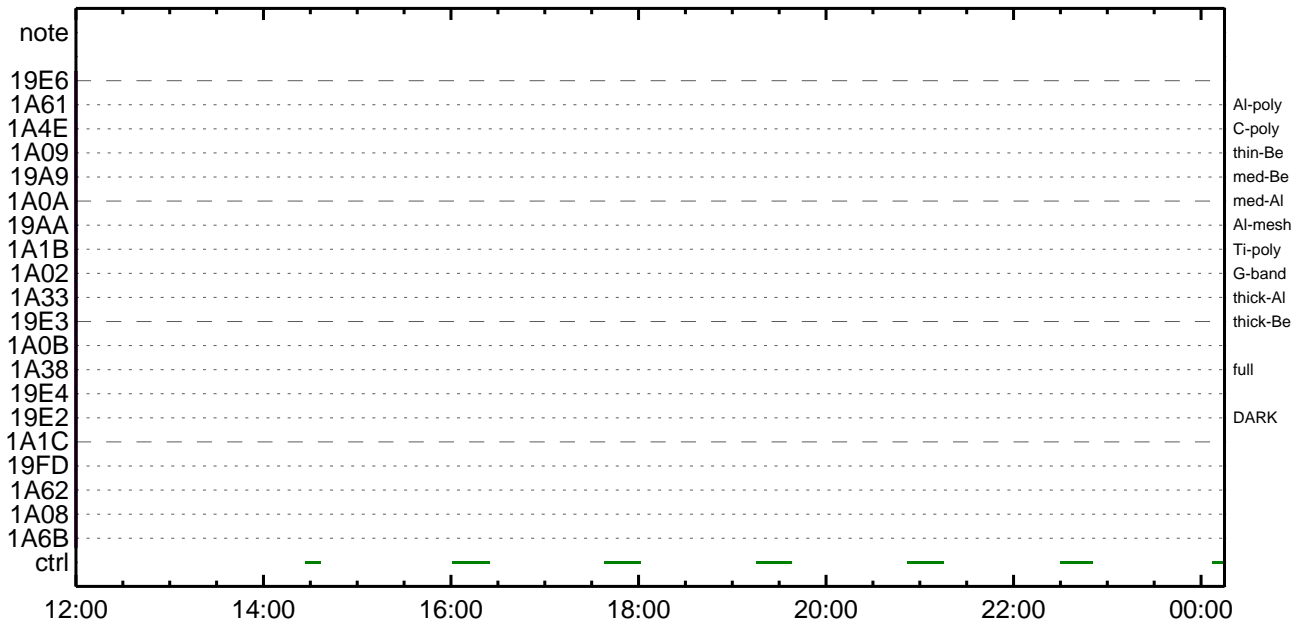
CMDI #0936 2015/02/21



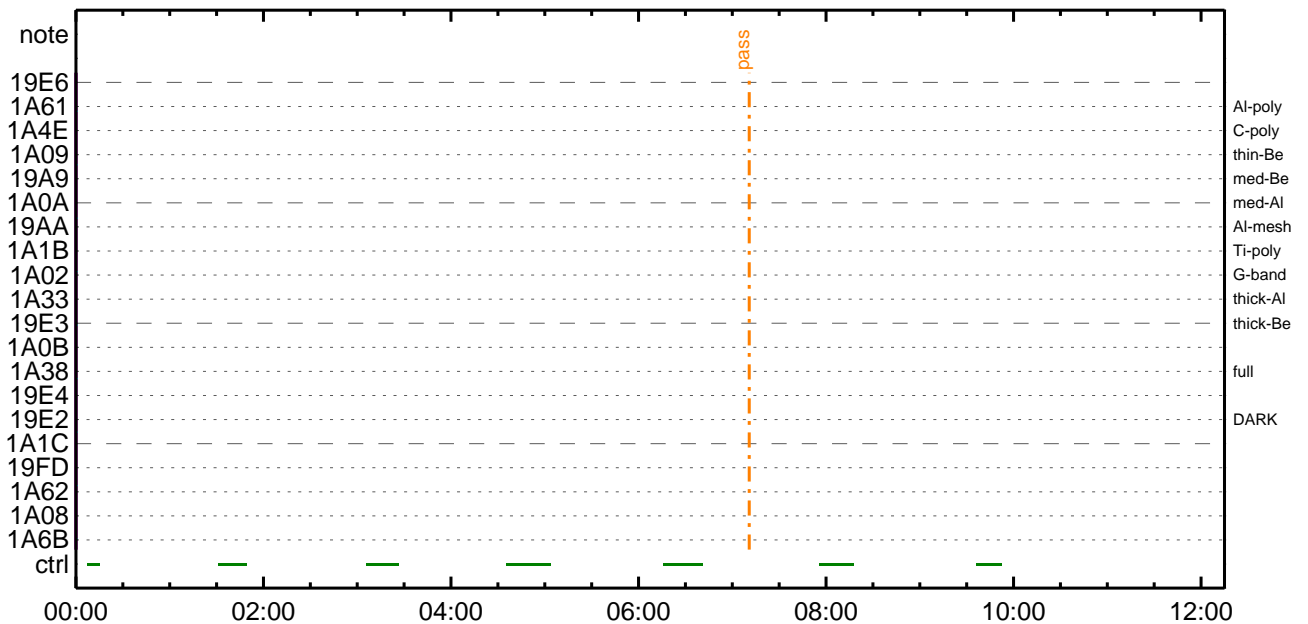
CMDI #0936 2015/02/22



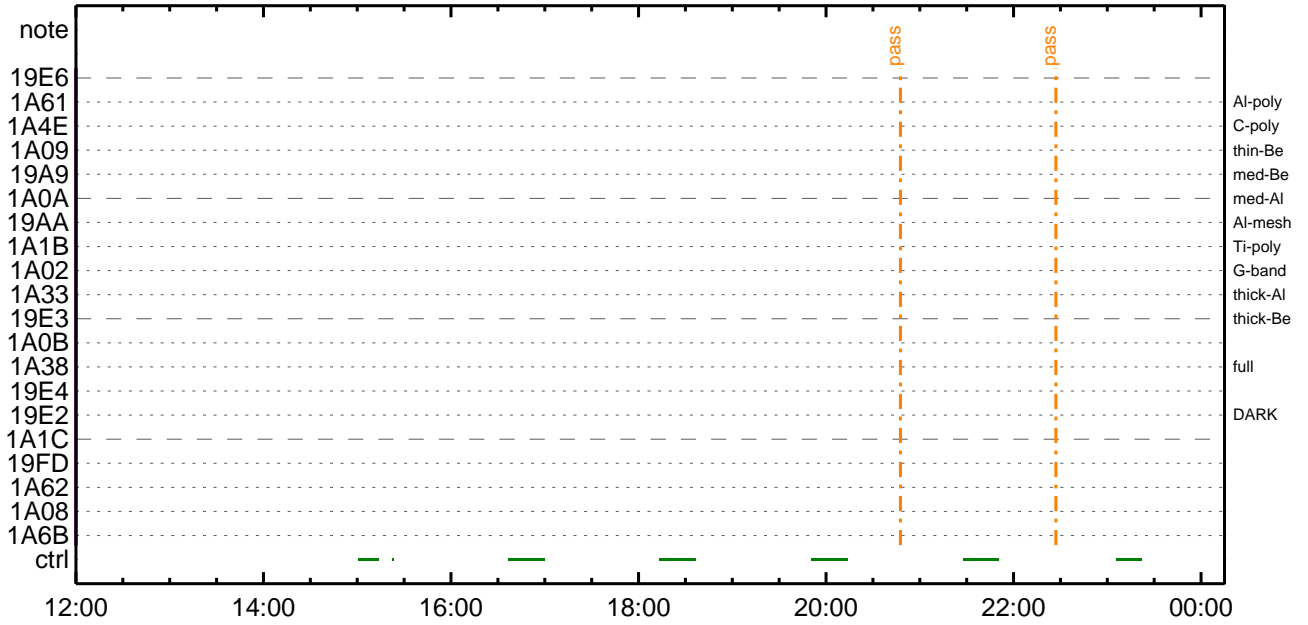
CMDI #0936 2015/02/22



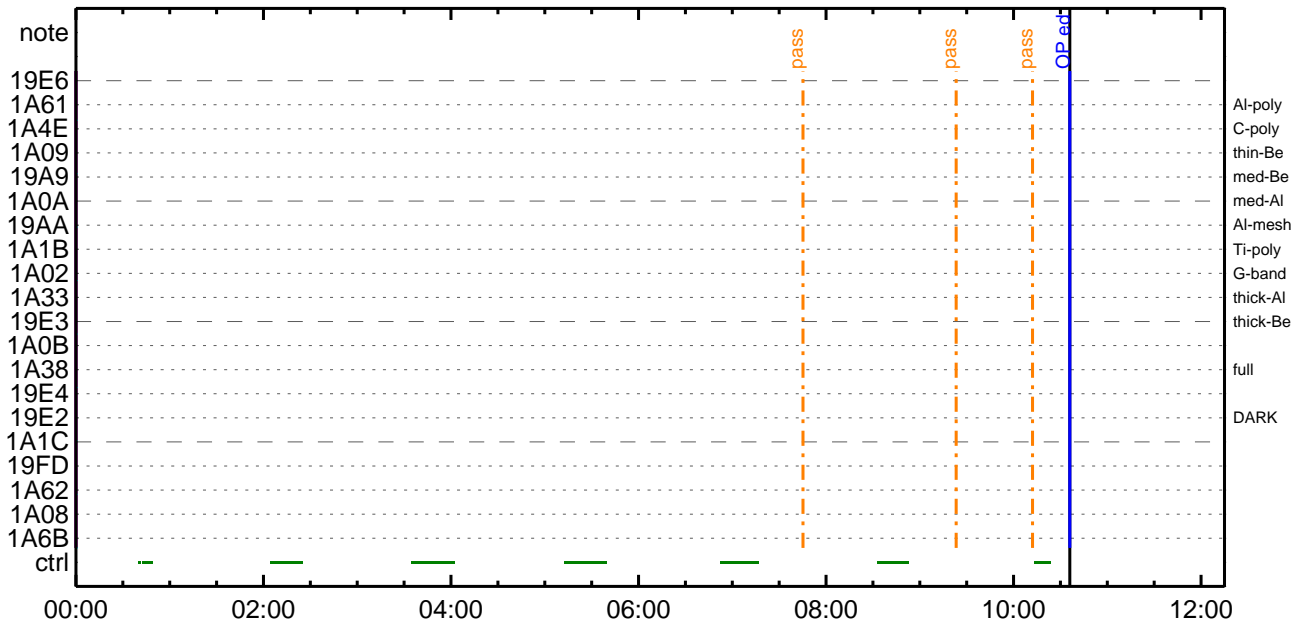
CMDI #0936 2015/02/23



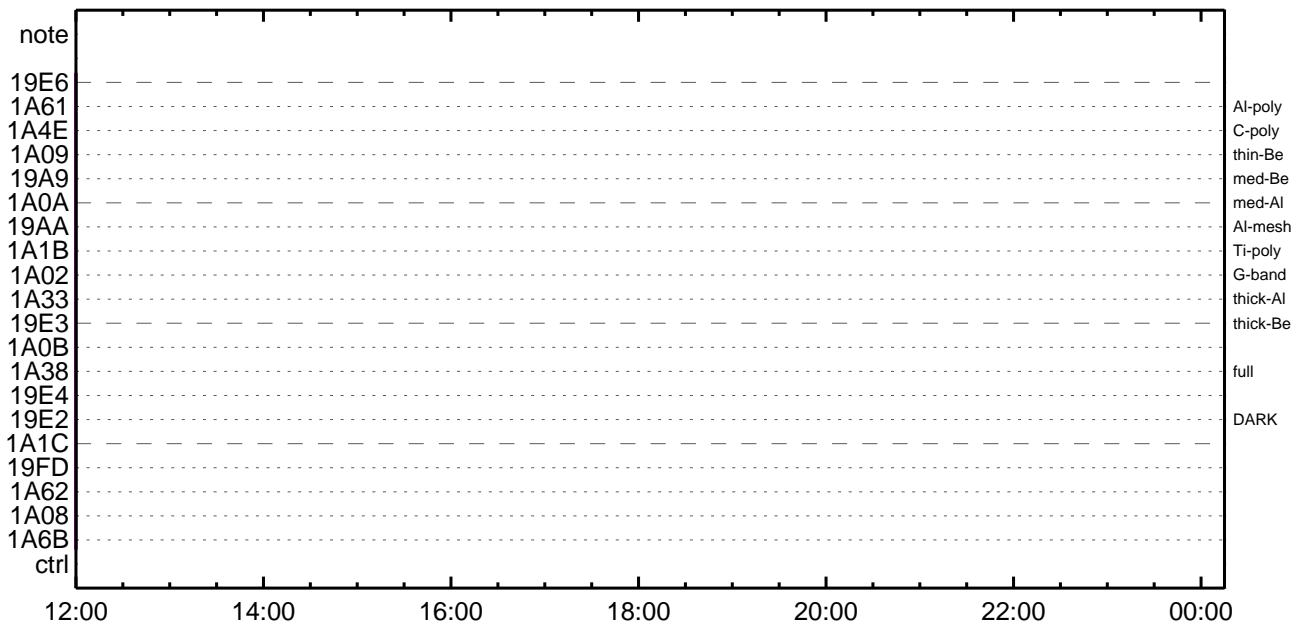
CMDI #0936 2015/02/23



CMDI #0936 2015/02/24



CMDI #0936 2015/02/24




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-956:OP
0104 ( )
0105 S. OG og-956:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½î¹çòE¼î¹çòÇòâ)
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³YpYóYEòðÁDî¿(UT)
0183 +. TI 2015-02-19 10:48:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-02-19 10:48:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-02-19 10:48:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 0a)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 13)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2015-02-19 10:52:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 . C. *****
0168 C. SOT table upload
0169 C. *****
0170 . C. < Stop FG table >
0171 +. DC 07-F0 MDP_FG_CTRL_MANU
0172 BC (51)
0173 . C. -----
0174 C. MDP_FG_CTRL_MODE = MANU [ ]
0175 C. -----
0176 C.
0177 . C. <Upload FG Observation Table>
0178 . S. RAM ram-262:MDP_OBS_F
0179 ( )
0180 C.
0181 . C. < Dump RAMID=MDP_OBS_F >
0182 +. DC 07-F0 MDP_DUMP_FGTBL
0183 BC (82 07 00 00 00 38 b8)
0184 C. -----
0185 C. MDP_OBS_F verify = OK/NG [ ]
0186 C. -----
0187 C.
0188 . C. < Resume FG table (auto mode) >
0189 +. DC 07-F0 MDP_FG_CTRL_AUTO
0190 BC (50)
0191 . C. -----
0192 C. MDP_FG_CTRL_MODE = AUTO [ ]
0193 C. -----

```

```
0194 C.
0195 C. *****
0196 C. SOT TI command set
0197 C. *****
0198 C. Execute, after the success of TBL upload.
0199 +. TI 2015-02-19 10:52:18.0
0200 DC 07-F0 MDP_SOT_MODE_OBSV
0201 BC (40)
0202 . C. -----
0203 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0204 C. -----
0205 C.
0206 C. Only when FG_CTRL_AUTO is used in RT.
0207 +. TI 2015-02-19 10:52:20.0
0208 DC 07-F0 MDP_FG_CTRL_AUTO
0209 BC (50)
0210 . C. -----
0211 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0212 C. -----
0213 C. ***** SOT END *****
0214 C.
0215 . C. ***** MDP `úÃîñî»ò¼ÝñÊÂð¹ñèDCBC•x²è *****
0216 C. (¼Á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ¼ñ¼Á»Û¹ñè)
0217 . S. DC-BC dcbc-402:DCBC
0218 (MDP_known_event)
0219 C.
0220 C.
0221 . C. ***** ¥DÝ!•Ï Daily±¿ÍÑñÊ´Ø¹ñèDCBC•x²è *****
0222 . S. DC-BC dcbc-153:DCBC
0223 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0224 C.
0225 C.
0226 . C. ¡ãLOS¥Á¥§¥Ã¥¬¼Á»Û¡ä
0227 C.
0228 . C. ***** LOS *****
0229 C.
```

Feb 19, 15 12:04

XRT_OGLIST_0936.chk

Page 1/7

*** OP Sequence for XRT ***

2015/02/19	11:02:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	11:02:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	11:02:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/02/19	11:03:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2015/02/19	11:03:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/02/19	11:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/02/19	11:03:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/02/19	11:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/02/19	11:03:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/19	11:05:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2015/02/19	11:05:58.0	XRT_FL_PROG_SET_401_OG [0x191]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/02/19	11:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/19	14:18:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	14:18:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	14:18:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/19	14:18:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/19	14:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/19	14:26:30.0	XRT_Custom_430_OG [0x1ae]					
2015/02/19	14:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/19	15:52:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	15:52:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	15:52:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/19	15:52:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/19	15:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/19	16:15:30.0	XRT_Custom_430_OG [0x1ae]					
2015/02/19	16:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/19	17:28:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	17:28:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	17:28:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/19	17:28:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/19	17:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/19	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	17:53:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	17:53:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/02/19	17:53:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2015/02/19	17:53:48.0	XRT_FLD_DIS_422_OG [0x1a6]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/02/19	17:56:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/02/19	17:56:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/02/19	17:56:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2015/02/19	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/19	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/19	18:03:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/02/19	18:03:30.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2015/02/19	18:03:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/02/19	18:03:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/02/19	18:03:52.0	XRT_AEC_RESET_413_OG [0x19d]					

Feb 19, 15 12:04

XRT_OGLIST_0936.chk

Page 2/7

2015/02/19	18:03:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/02/19	18:03:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/19	18:06:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2015/02/19	18:06:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/02/19	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/19	19:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	19:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	19:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/19	19:05:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/02/19	19:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/02/19	19:28:30.0	XRT_Custom_430_OG [0x1ae]								
2015/02/19	19:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/19	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	19:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/02/19	20:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	f3	8e	ae	67
2015/02/19	20:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/02/19	20:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/02/19	20:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/02/19	20:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/02/19	20:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/19	20:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2015/02/19	20:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/02/19	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/19	20:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	20:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	20:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/19	20:42:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/02/19	20:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/02/19	21:06:00.0	XRT_Custom_430_OG [0x1ae]								
2015/02/19	21:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/19	22:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	22:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	22:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/19	22:20:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/02/19	22:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/02/19	22:41:30.0	XRT_Custom_430_OG [0x1ae]								
2015/02/19	22:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/19	23:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	23:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/19	23:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/19	23:57:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/02/20	00:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/02/20	00:06:00.0	XRT_Custom_430_OG [0x1ae]								
2015/02/20	00:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/20	01:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	01:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	01:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				

Feb 19, 15 12:04

XRT_OGLIST_0936.chk

Page 3/7

2015/02/20	01:22:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/02/20	01:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/02/20	01:40:00.0	XRT_Custom_430_OG [0x1ae]							
2015/02/20	01:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/20	02:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	02:56:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	02:56:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/20	02:56:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/02/20	02:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/02/20	03:17:00.0	XRT_Custom_430_OG [0x1ae]							
2015/02/20	03:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/20	04:25:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	04:25:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	04:25:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/20	04:25:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/02/20	04:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/02/20	04:54:30.0	XRT_Custom_430_OG [0x1ae]							
2015/02/20	04:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/20	05:38:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	05:38:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	05:38:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/02/20	05:38:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/02/20	05:38:48.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/02/20	05:41:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/02/20	05:41:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/02/20	05:41:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/02/20	05:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/20	05:48:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	05:48:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	05:48:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/02/20	05:48:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 f3 8e ae 67				
2015/02/20	05:48:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/02/20	05:48:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/02/20	05:48:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/02/20	05:48:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/02/20	05:48:56.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/20	05:51:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2015/02/20	05:51:28.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/02/20	05:51:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/20	06:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	06:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	06:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/02/20	06:06:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/02/20	06:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/02/20	06:31:30.0	XRT_Custom_430_OG [0x1ae]							
2015/02/20	06:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/02/20	07:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	07:46:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/02/20	07:46:34.0	XRT_FLD_RESET_415_OG [0x19f]							

2015/02/20	07:46:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/20	07:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/20	08:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/20	08:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/20	09:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	09:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	09:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/20	09:26:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/20	09:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/20	09:43:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/20	09:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	14:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	14:52:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	14:52:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/20	14:52:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/20	14:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/20	15:04:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/20	15:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	16:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	16:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	16:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/20	16:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/20	16:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/20	16:50:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/20	16:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	17:40:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	17:40:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	17:40:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	17:40:30.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/02/20	17:40:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCS_Ore-point_Start_2_OG [0x098]	5	02-76	00 00 00 00 00		
2015/02/20	17:40:48.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/02/20	17:43:24.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/02/20	17:43:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/02/20	17:43:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/02/20	17:43:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/02/20	17:43:30.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2015/02/20	17:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2015/02/20	17:50:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/20	17:50:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	17:50:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	17:50:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	17:50:30.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/02/20	17:50:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCS_Ore-point_Start_4_OG [0x09a]	5	02-76	03 00 00 00 00		
2015/02/20	17:50:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00		
2015/02/20	17:50:50.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/02/20	17:50:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/02/20	17:50:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/02/20	17:50:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/02/20	17:50:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/02/20	17:50:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/02/20	17:53:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/20	17:53:26.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/20	17:53:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2015/02/20	17:53:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2015/02/20	17:53:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/02/20	18:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/20	18:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/20	18:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

2015/02/21	02:15:30.0	XRT_Custom_430_OG [0x1ae]					
2015/02/21	02:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/21	03:28:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	03:28:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	03:28:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/21	03:28:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/21	03:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/21	03:52:30.0	XRT_Custom_430_OG [0x1ae]					
2015/02/21	03:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/21	05:02:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	05:02:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	05:02:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/21	05:02:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/21	05:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/21	05:30:00.0	XRT_Custom_430_OG [0x1ae]					
2015/02/21	05:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/21	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	05:58:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	05:58:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/02/21	05:58:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2015/02/21	05:58:48.0	XRT_FLD_DIS_422_OG [0x1a6]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/02/21	06:01:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/02/21	06:01:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/02/21	06:01:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2015/02/21	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/21	06:08:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	06:08:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	06:08:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/02/21	06:08:30.0	AOCS_Ore-point_Start_5_OG [0x09b]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2015/02/21	06:08:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/02/21	06:08:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/02/21	06:08:52.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/02/21	06:08:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/02/21	06:08:56.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/21	06:11:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2015/02/21	06:11:28.0	XRT_FL_PROG_SET_401_OG [0x191]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/02/21	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/21	06:42:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	06:42:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	06:42:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/21	06:42:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/21	06:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/02/21	07:07:30.0	XRT_Custom_430_OG [0x1ae]					
2015/02/21	07:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/02/21	08:22:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	08:22:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/02/21	08:22:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/02/21	08:22:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/02/21	08:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					

