XRT Timeline to be uploaded on 2015/03/07

Period: 2015/03/07 10:39:00 - 2015/03/12 10:22:00

* * * * * **Normal mode** * * * * *

XOB #1A38: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m

Term	Pointing (x, y)	Comment
03/07 10:52:00 - 03/07 11:56:54	Fixed (-930.0, -300.0)	# OP start + 10min AR obs at E-limb
03/07 13:32:30 - 03/07 14:59:54	Fixed (-930.0, -300.0)	AR obs E-limb
03/07 18:08:30 - 03/07 23:43:30	Fixed (-930.0, -300.0)	# AR obs at E-limb
03/07 23:54:30 - 03/08 06:27:24	Track (-866.6, -270.3) @ 03/07 23:50:00	AR obs cont.

PROG= 16 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 8 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 2-time(s) 2.0sec

Seqn= 24 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 42 4-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 62 60-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

XOB #1A6D: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,8ms) - Leak (33ms) - 30s cad

Term	Pointing (x, y)	Comment
03/07 12:00:00 - 03/07 13:29:24	Track (-323.0, -76.9) @ 03/07 11:57:00	Plage dynamics for SOT
03/07 15:03:00 - 03/07 17:55:24	Track (-296.5, -75.7) @ 03/07 15:00:00	HOP 277 with DST

PROG= 17 Inf.-time(s)

Subr= 1 120-time(s) 30.0sec

Seqn= 29 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi

Term	Pointing (x, y)	Comment
03/07 17:58:30 - 03/07 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min

PROG= 03 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 33 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 40 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 77 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1A6B: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45

Term	Pointing (x, y)	Comment

03/08 06:30:30 - 03/08 06:37:24 Fixed (0.0, 0.0) synoptic, shifted 27.5 min

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 01 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 33 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 40 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 12.0sec												
Seqn= 46 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25 1-time(s) 4.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 21 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 37 1-time(s) 2.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25 1-time(s) 4.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 21 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 37 1-time(s) 2.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 46 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

* * * * * **Flare mode** * * * * *

XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
03/07 10:52:00 - 03/07 11:56:54	Fixed (-930.0, -300.0)	# OP start + 10min AR obs at E-limb
03/07 12:00:00 - 03/07 13:29:24	Track (-323.0, -76.9) @ 03/07 11:57:00	Plage dynamics for SOT
03/07 13:32:30 - 03/07 14:59:54	Fixed (-930.0, -300.0)	AR obs E-limb
03/07 15:03:00 - 03/07 17:55:24	Track (-296.5, -75.7) @ 03/07 15:00:00	HOP 277 with DST
03/07 18:08:30 - 03/07 23:43:30	Fixed (-930.0, -300.0)	# AR obs at E-limb
03/07 23:54:30 - 03/08 06:27:24	Track (-866.6, -270.3) @ 03/07 23:50:00	AR obs cont.

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 19 30-time(s)												
Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

✲ ✲ ✲ ✲ ✲ Active Region Search ✲ ✲ ✲ ✲ ✲

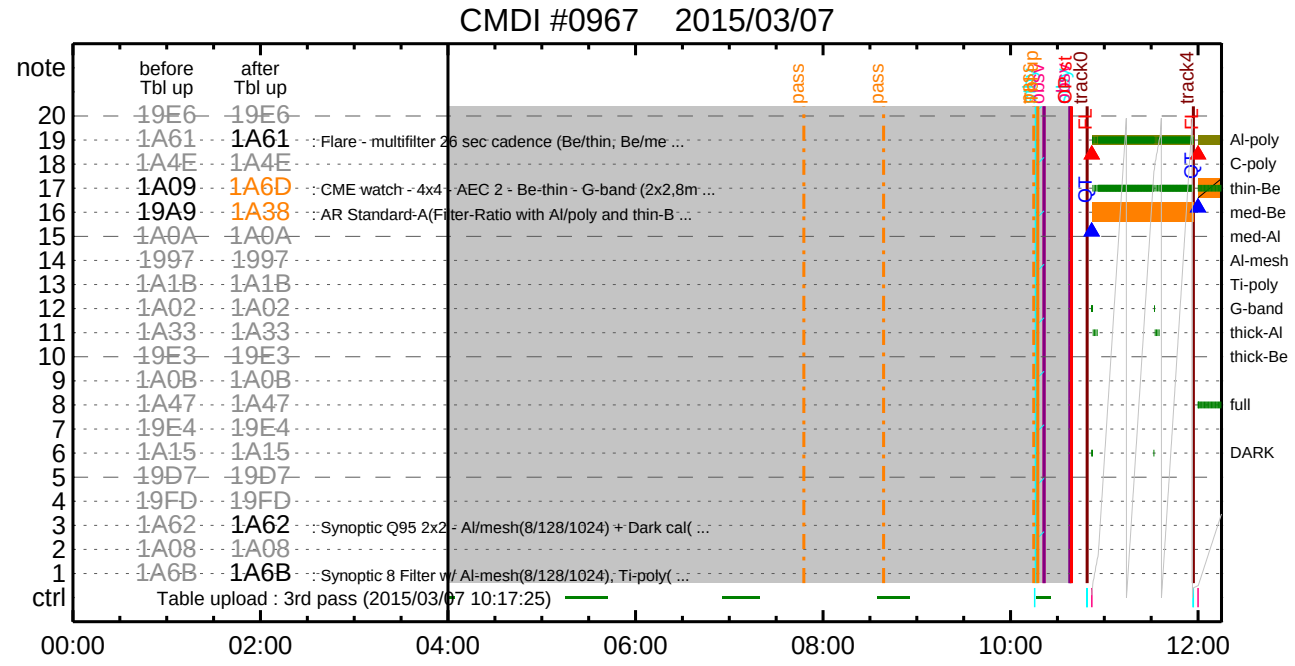
NOT USED

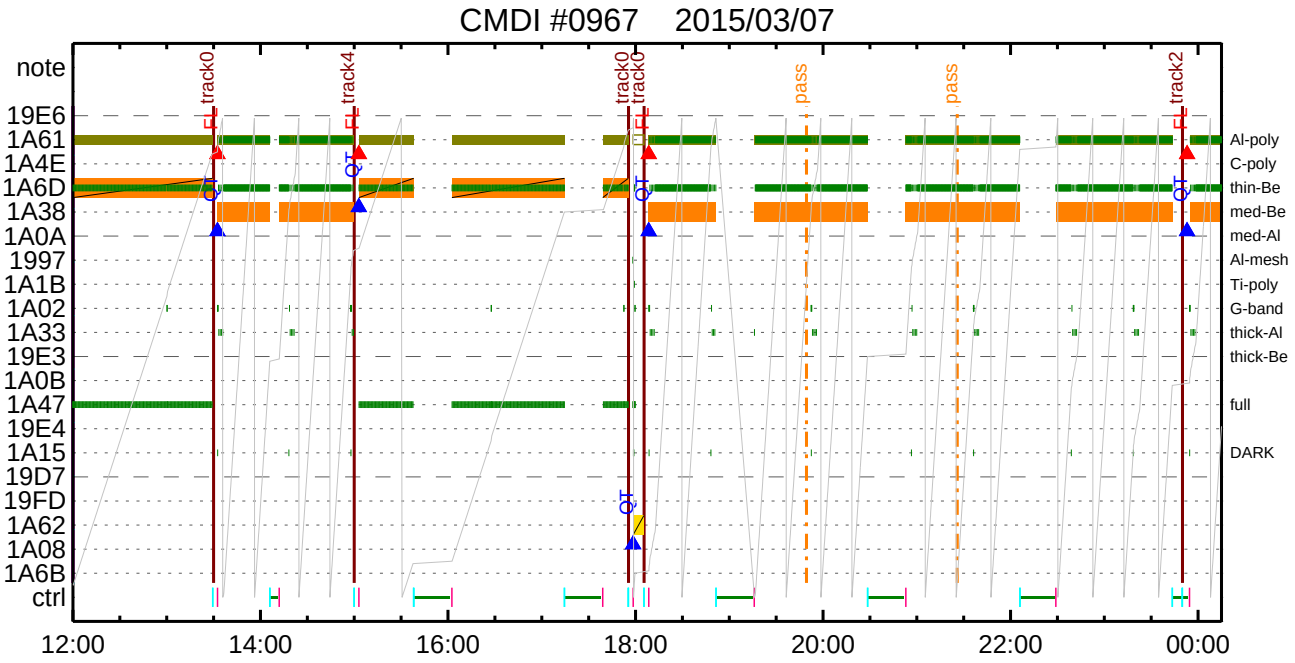
✲ ✲ ✲ ✲ ✲ Flare Detection ✲ ✲ ✲ ✲ ✲

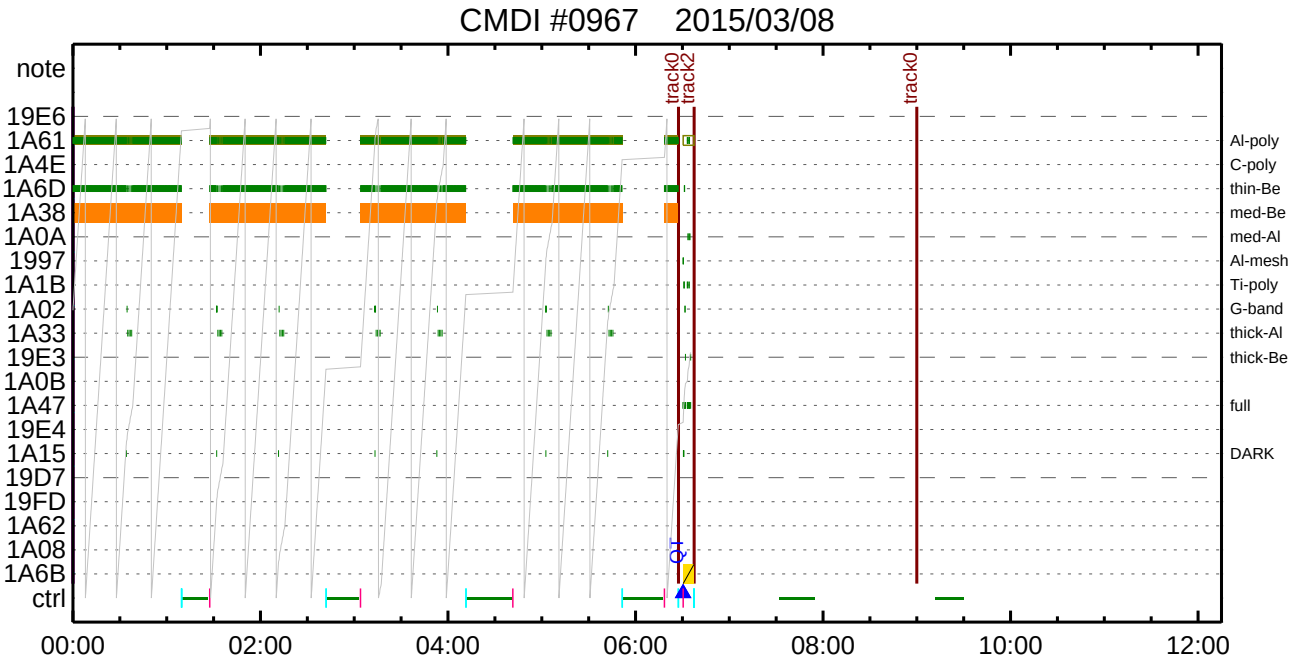
FLD Patrol

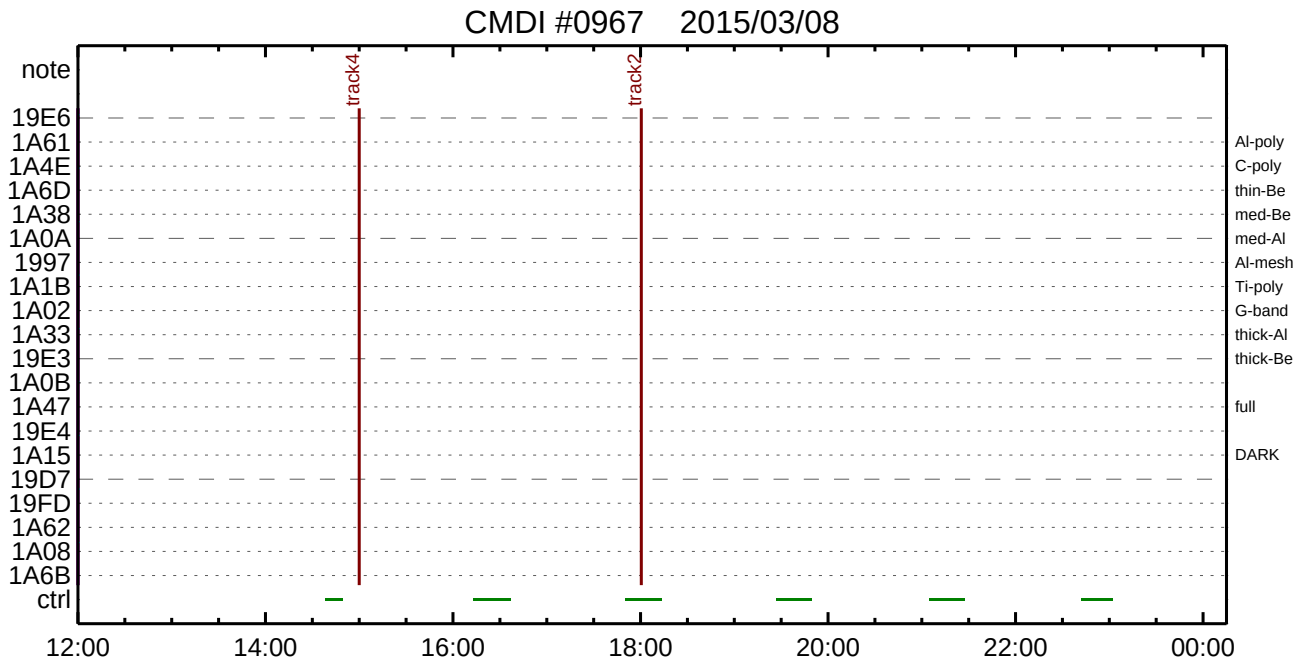
Term	Pointing (x, y)	Comment
03/07 18:05:48 - 03/08 06:27:48	Fixed (-930.0, -300.0)	# AR obs at E-limb

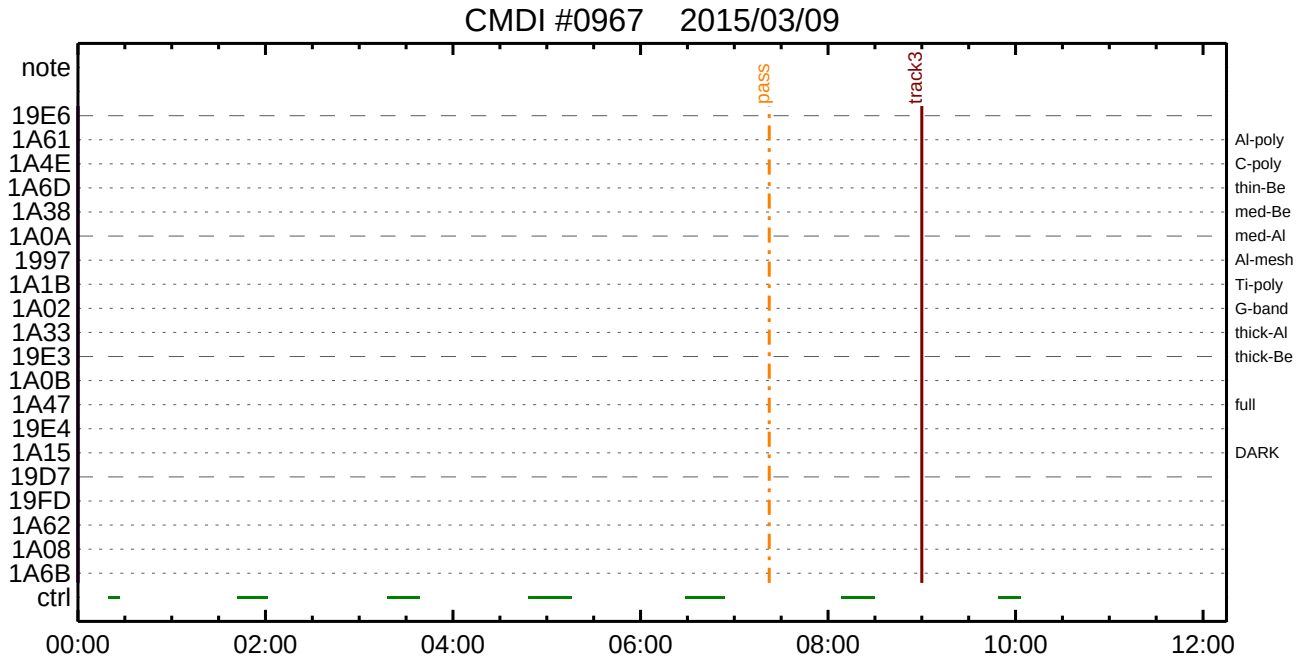
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec

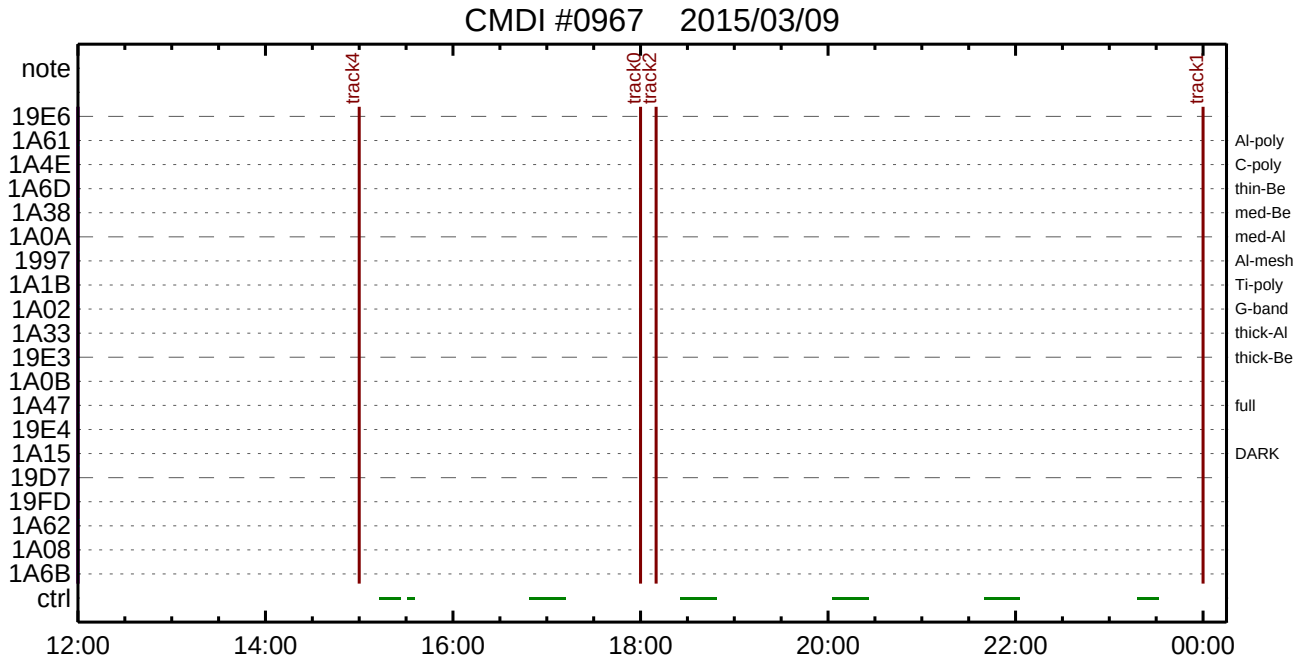

CMDI #0967 2015/03/07
note
before Tbl up
after Tbl up
: Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,8m ...
: AR Standard-A(Filter-Ratio with Al/poly and thin-B ...
: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(...
: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(...
Table upload : 3rd pass (2015/03/07 10:17:25)
pass
track0
track4
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

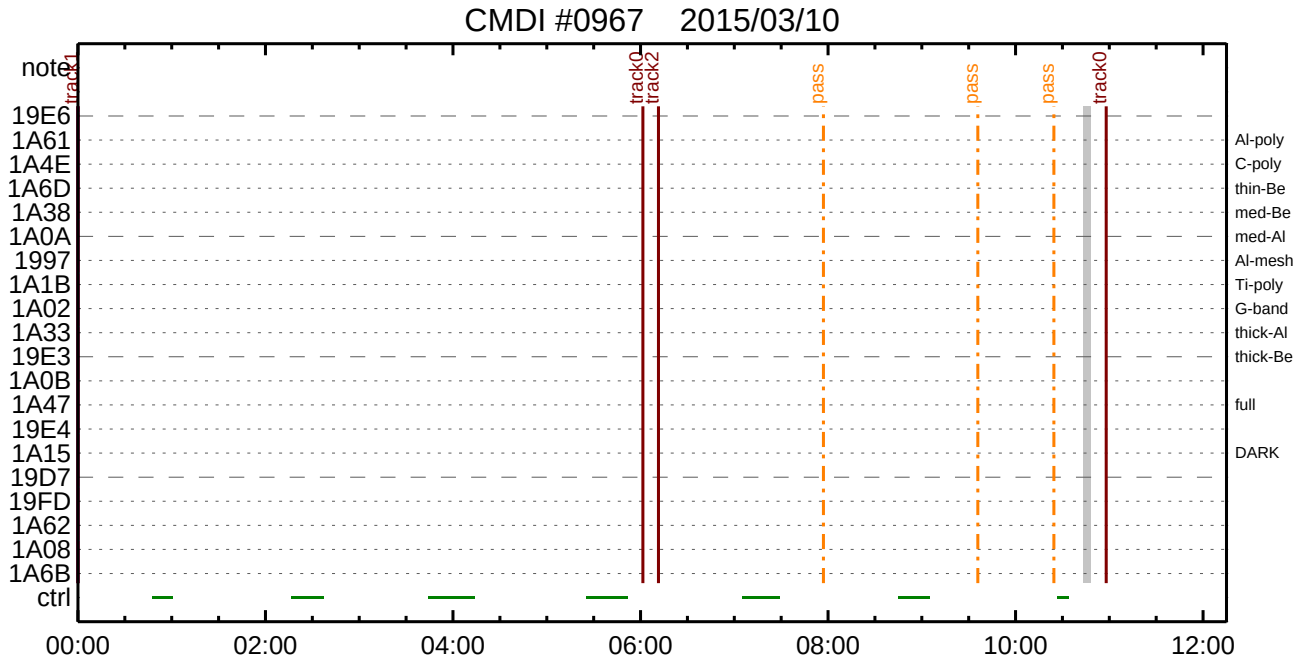

CMDI #0967 2015/03/07
note
track0
track4
track2
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

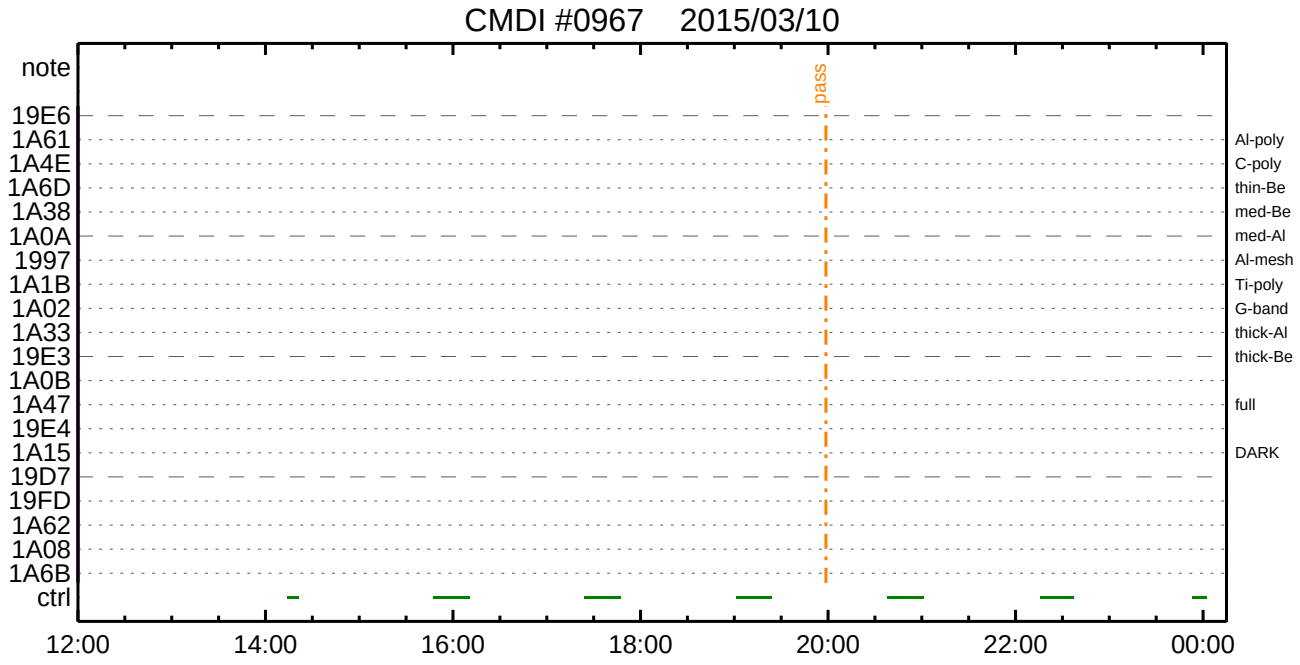

CMDI #0967 2015/03/08
note
track0
track2
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

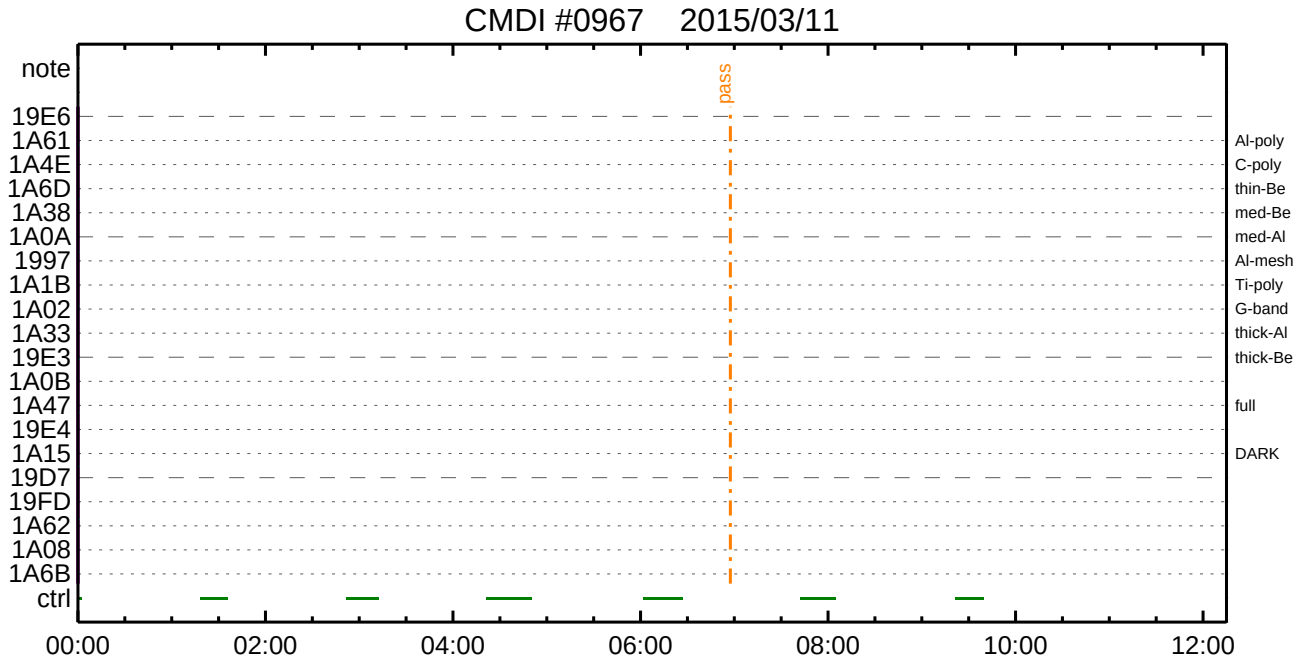

CMDI #0967 2015/03/08
note
19E6
1A61
1A4E
1A6D
1A38
1A0A
1997
1A1B
1A02
1A33
19E3
1A0B
1A47
19E4
1A15
19D7
19FD
1A62
1A08
1A6B
ctrl
track4
track2
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

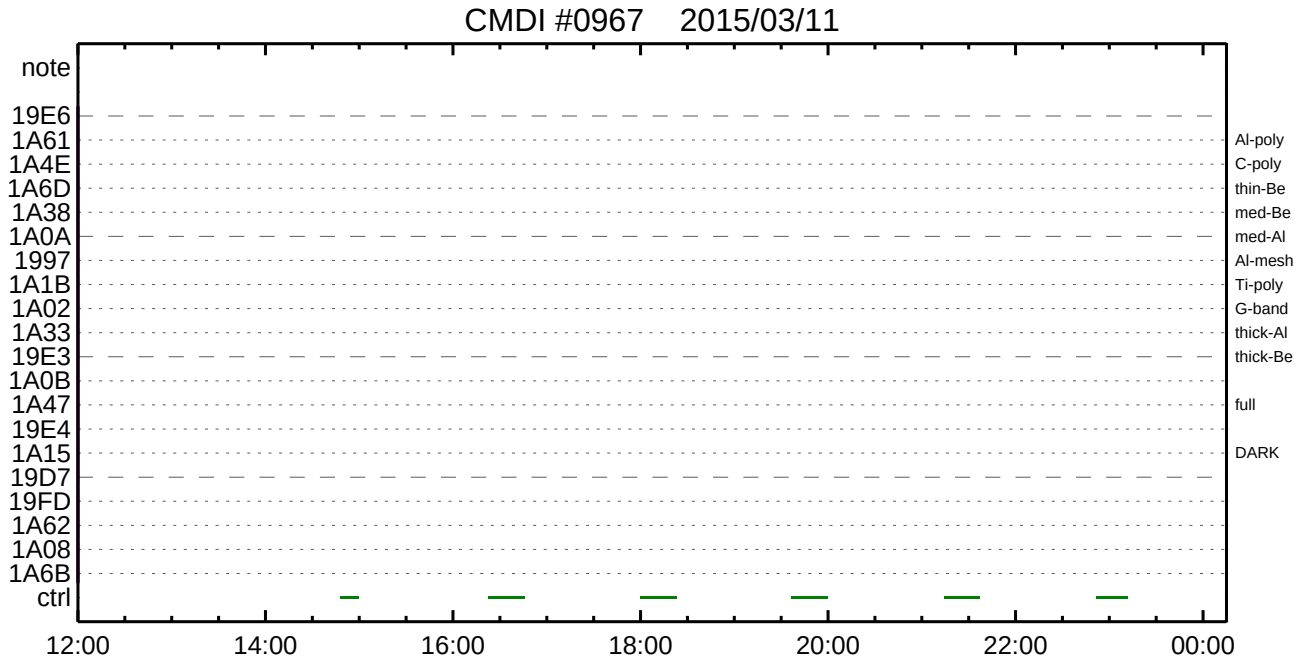

CMDI #0967 2015/03/09
note
19E6
1A61
1A4E
1A6D
1A38
1A0A
1997
1A1B
1A02
1A33
19E3
1A0B
1A47
19E4
1A15
19D7
19FD
1A62
1A08
1A6B
ctrl
pass
track3
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

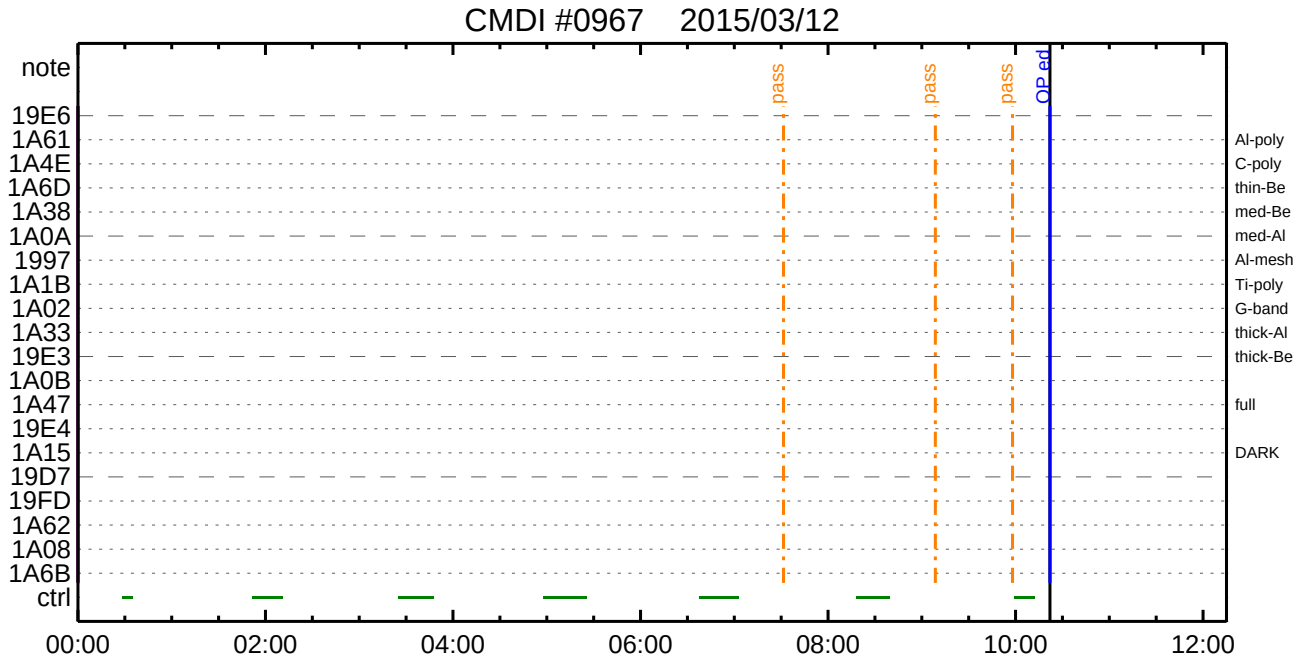

CMDI #0967 2015/03/09
note
19E6
1A61
1A4E
1A6D
1A38
1A0A
1997
1A1B
1A02
1A33
19E3
1A0B
1A47
19E4
1A15
19D7
19FD
1A62
1A08
1A6B
ctrl
track4
track0
track2
track1
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

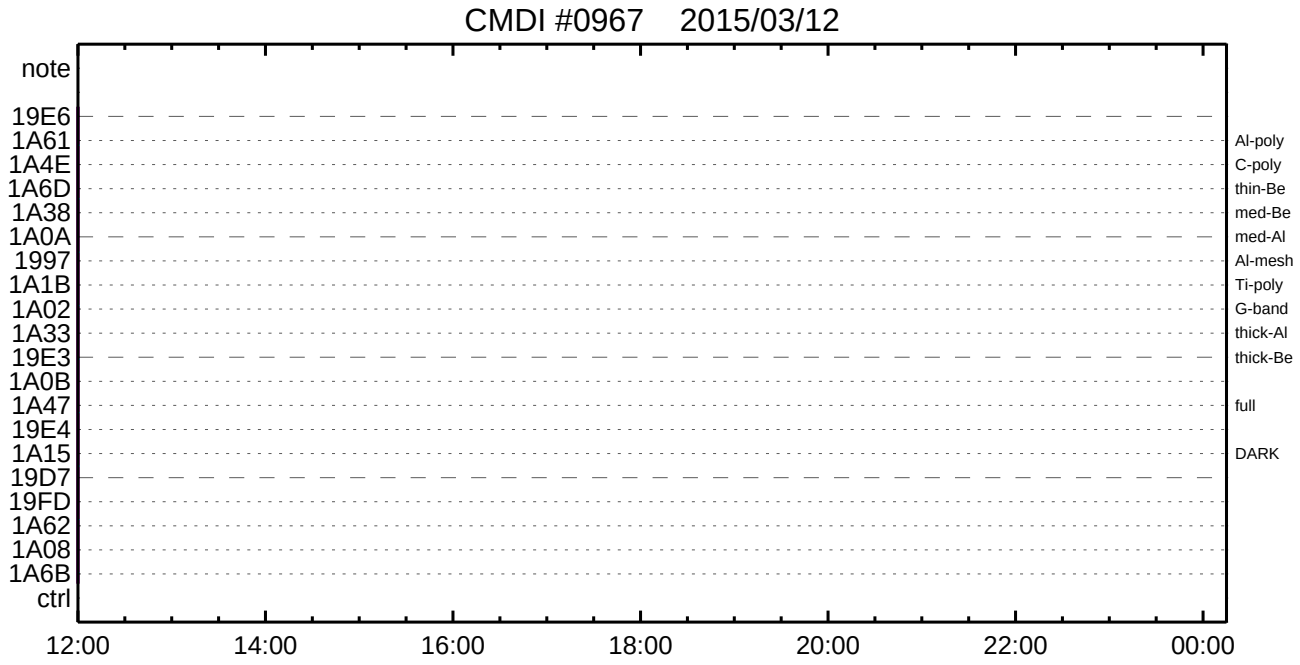

CMDI #0967 2015/03/10
note
track1
track0
track2
pass
pass
pass
track0
19E6
1A61
1A4E
1A6D
1A38
1A0A
1997
1A1B
1A02
1A33
19E3
1A0B
1A47
19E4
1A15
19D7
19FD
1A62
1A08
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0967 2015/03/10
note
pass
19E6
1A61
1A4E
1A6D
1A38
1A0A
1997
1A1B
1A02
1A33
19E3
1A0B
1A47
19E4
1A15
19D7
19FD
1A62
1A08
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0967 2015/03/11
note
pass
19E6
1A61
1A4E
1A6D
1A38
1A0A
1997
1A1B
1A02
1A33
19E3
1A0B
1A47
19E4
1A15
19D7
19FD
1A62
1A08
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0967 2015/03/11
note
19E6
1A61
1A4E
1A6D
1A38
1A0A
1997
1A1B
1A02
1A33
19E3
1A0B
1A47
19E4
1A15
19D7
19FD
1A62
1A08
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0967 2015/03/12
note
pass
pass
pass
OP ed
19E6
1A61
1A4E
1A6D
1A38
1A0A
1997
1A1B
1A02
1A33
19E3
1A0B
1A47
19E4
1A15
19D7
19FD
1A62
1A08
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0967 2015/03/12
note
19E6
1A61
1A4E
1A6D
1A38
1A0A
1997
1A1B
1A02
1A33
19E3
1A0B
1A47
19E4
1A15
19D7
19FD
1A62
1A08
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-987 2015-03-07 12:56:21 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÇ¤Î»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
   C.
   C.
 . C. ****************************
   C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
   C. ****************************
   C.
 . C. ¡ãOP/OG¥í¡¼¥É¡ä
 . S. OP    op-987:OP
   ()
 . S. OG    og-987:OG
   ()
   C.
 . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
   C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
+. DC 01-23 DHU_DMA_DMP_PRM_SET
   BC       (20 00 7f 01 02)
   C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
   C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
   C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
   C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
   C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
+. DC 01-22 DHU_MODE_CHNG
   BC       (07 0b f8)
   C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
   C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
   C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
   C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
 . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
 . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
   C.
   C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
+. DC 01-23 DHU_DMA_DMP_PRM_SET
   BC       (20 80 7f 01 02)
   C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
   C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
   C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
   C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
   C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
+. DC 01-22 DHU_MODE_CHNG
   BC       (07 0b f8)
   C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
   C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
   C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
   C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
 . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
 . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
   C.
   C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
+. DC 01-23 DHU_DMA_DMP_PRM_SET
   BC       (21 00 41 01 02)
   C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
   C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
   C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
   C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
   C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
+. DC 01-22 DHU_MODE_CHNG
   BC       (07 0b f8)
   C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
   C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
   C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
   C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
 . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
   C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
 . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
   C.
 . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
   C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
+. DC 01-22 DHU_MODE_CHNG
   BC       (02 0a f8)
   C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     2
   C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
   C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
   C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
   C.
 . C. *********************************
   C. TI-CMD SET (OPOG STOP/COPY/START)
   C. *********************************
   C.
 . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
   C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
   C.
 . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
+. TI 2015-03-07 10:34:00.0
   DC 01-B3 DHU_OP_STOP
   C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
   C.
+. TI 2015-03-07 10:34:01.0
   DC 01-B4 DHU_OP_COPY
   C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
   C.
+. TI 2015-03-07 10:34:01.0
   DC 01-B5 DHU_OPOG_COPY
   C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195  +. TI 2015-03-07 10:38:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205   . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217  +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230   . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231  +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242  +. TI 2015-03-07 10:38:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245   . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249   . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255  +. TI 2015-03-07 10:38:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258  +. TI 2015-03-07 10:38:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268  +. TI 2015-03-07 10:38:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277   . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282   . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288   . C. ********** LOS **********
0289     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-988 2015-03-07 12:56:21 85 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046   +. DC 07-FC EIS_MODE_CHG_ENA
0047     BC       (20)
0048   . C. Verify EIS_MODE_CHG_FLG is ENA
0049   +. DC 07-FC EIS_MODE_MANU
0050     BC       (21 02)
0051   . C. Verify EIS in MANUAL mode
0052   . C. Estimated OBSTBL upload time is 41s
0053     C. *********************************
0054     C. EIS START OBSTBL LOAD
0055     C. *********************************
0056   . S. RAM   ram-820:EIS_OBSTBL
0057     ()
0058   +. DC 07-FC EIS_DUMP_OBSTBL
0059     BC       (07 07 07 00 00 70 00)
0060     C.
0061     C. Execute, after the success of OBSTBL upload.
0062     C. Set EIS TI-commands
0063   +. TI 2015-03-07 10:38:50.0
0064     DC 07-FC EIS_MODE_CHG_ENA
0065     BC       (20)
0066   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0067     C. *********************************
0068     C. EIS END OBSTBL LOAD
0069     C. *********************************
0070     C.
0071   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0072     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0073   . S. DC-BC dcbc-402:DCBC
0074     (MDP_known_event)
0075     C.
0076     C.
0077   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0078   . S. DC-BC dcbc-153:DCBC
0079     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080     C.
0081     C.
0082   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0083     C.
0084   . C. *********** LOS ***********
0085     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-989 2015-03-07 12:56:21 126 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Stop FG table >
0018   +. DC 07-F0 MDP_FG_CTRL_MANU
0019     BC       (51)
0020   . C. ------------------------------------
0021     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024   . C. <Upload FG Observation Table>
0025   . S. RAM   ram-268:MDP_OBS_F
0026     ()
0027     C.
0028   . C. < Dump RAMID=MDP_OBS_F >
0029   +. DC 07-F0 MDP_DUMP_FGTBL
0030     BC       (82 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_F verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035     C. ********************************
0036     C.  SOT TI command set
0037     C. ********************************
0038     C. Execute, after the success of TBL upload.
0039   +. TI 2015-03-07 10:38:18.0
0040     DC 07-F0 MDP_SOT_MODE_OBSV
0041     BC       (40)
0042   . C. ------------------------------------
0043     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0044     C. ------------------------------------
0045     C.
0046     C.
0047     C. ************ XRT START ************
0048     C.
0049   +. DC 07-F0 MDP_XRT_CTRL_MANU
0050     BC       (c1)
0051   +  DC 07-F0 MDP_XRT_MODE_STBY
0052     BC       (c3)
0053   . C. ----------- Success Verify ?             OK / NG____
0054     C.
0055     C. XRT Obs. Table Upload
0056   . S. RAM   ram-291:MDP_OBS_X
0057     ()
0058     C.
0059   +. DC 07-F0 MDP_DUMP_XRTTBL
0060     BC       (84 07 00 00 00 3a d4)
0061   . C. ----------- Comparison Check ?                      OK / ERR ____
0062     C.
0063     C.
0064   +. DC 07-F0 MDP_XRT_ROI_SET
0065     BC       (cd 01 b1 b1 04 04)
0066   +  DC 07-F0 MDP_XRT_ROI_SET
0067     BC       (cd 02 b1 b1 08 08)
0068   +  DC 07-F0 MDP_XRT_ROI_SET
0069     BC       (cd 03 b1 b1 08 08)
0070   +  DC 07-F0 MDP_XRT_ROI_SET
0071     BC       (cd 04 b1 b1 06 06)
0072   +  DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 05 85 83 06 06)
0074   +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 06 85 83 06 06)
0076   +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 07 85 83 08 08)
0078   +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 08 80 80 20 20)
0080   +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 09 80 80 20 08)
0082   +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 0a 80 80 08 20)
0084   +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 0f 80 80 06 06)
0086   +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 10 80 80 08 08)
0088   +  DC 07-F0 MDP_XRT_FLD_ENA
0089     BC       (d8)
0090   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091     BC       (c8)
0092   +  DC 07-F0 MDP_XRT_AEC_RESET
0093     BC       (d0)
0094   +  DC 07-F0 MDP_XRT_ARS_DIS
0095     BC       (d5)
```

```
0096  +  DC 07-F0 MDP_XRT_FLD_RESET
0097     BC       (da)
0098   . C. ---------- Success Verify ?            OK / NG ____
0099     C.
0100     C.
0101   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0102     C.
0103  +. DC 07-F0 MDP_XRT_MODE_OBSV
0104     BC       (c2)
0105  +. TI 2015-03-07 10:38:02.0
0106     DC 07-F0 MDP_XRT_MODE_OBSV
0107     BC       (c2)
0108   . C. ---------- Success Verify ?            OK / NG ____
0109     C.
0110     C. ************ XRT END ***********
0111     C.
0112   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0113     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0114   . S. DC-BC dcbc-402:DCBC
0115     (MDP_known_event)
0116     C.
0117     C.
0118   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0119   . S. DC-BC dcbc-153:DCBC
0120     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0121     C.
0122     C.
0123   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0124     C.
0125   . C. *********** LOS ***********
0126     C.
```

```
*** OP Sequence for XRT ***

2015/03/07  10:48:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  10:48:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  10:48:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/03/07  10:49:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 1a a7 52 a7
2015/03/07  10:49:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2015/03/07  10:49:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2015/03/07  10:49:22.0  XRT_AEC_RESET_413_OG [0x19d]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2015/03/07  10:49:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/07  10:49:26.0  XRT_FLD_RESET_407_OG [0x197]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  10:51:56.0  XRT_QT_PROG_SET_406_OG [0x196]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2015/03/07  10:51:58.0  XRT_FL_PROG_SET_401_OG [0x191]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2015/03/07  10:52:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  11:56:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  11:56:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  11:56:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/03/07  11:57:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 04 00 00 00 00
2015/03/07  11:57:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2015/03/07  11:57:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2015/03/07  11:57:22.0  XRT_AEC_RESET_413_OG [0x19d]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2015/03/07  11:57:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/07  11:57:26.0  XRT_FLD_RESET_407_OG [0x197]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  11:59:56.0  XRT_QT_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2015/03/07  11:59:58.0  XRT_FL_PROG_SET_401_OG [0x191]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2015/03/07  12:00:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  13:29:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  13:29:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  13:29:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/03/07  13:29:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2015/03/07  13:29:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2015/03/07  13:29:52.0  XRT_AEC_RESET_413_OG [0x19d]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2015/03/07  13:29:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/07  13:29:56.0  XRT_FLD_RESET_407_OG [0x197]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  13:30:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 1a a7 52 a7
2015/03/07  13:32:26.0  XRT_QT_PROG_SET_406_OG [0x196]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2015/03/07  13:32:28.0  XRT_FL_PROG_SET_401_OG [0x191]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2015/03/07  13:32:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  14:06:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  14:06:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  14:06:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  14:06:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/07  14:09:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/07  14:11:00.0  XRT_Custom_430_OG [0x1ae]
2015/03/07  14:12:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  14:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  14:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  14:59:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
```

```
2015/03/07  15:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 04 00 00 00 00
2015/03/07  15:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2015/03/07  15:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2015/03/07  15:00:22.0  XRT_AEC_RESET_413_OG [0x19d]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2015/03/07  15:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/07  15:00:26.0  XRT_FLD_RESET_407_OG [0x197]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  15:02:56.0  XRT_QT_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2015/03/07  15:02:58.0  XRT_FL_PROG_SET_401_OG [0x191]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2015/03/07  15:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  15:38:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  15:38:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  15:38:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  15:38:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/07  15:41:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/07  16:01:30.0  XRT_Custom_430_OG [0x1ae]
2015/03/07  16:02:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  17:14:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  17:14:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  17:14:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  17:14:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/07  17:17:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/07  17:38:00.0  XRT_Custom_430_OG [0x1ae]
2015/03/07  17:39:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  17:55:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  17:55:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  17:55:28.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/03/07  17:55:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2015/03/07  17:55:48.0  XRT_FLD_DIS_422_OG [0x1a6]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/03/07  17:58:24.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/03/07  17:58:26.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/07  17:58:28.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2015/03/07  17:58:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  18:05:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  18:05:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  18:05:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/03/07  18:05:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 1a a7 52 a7
2015/03/07  18:05:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2015/03/07  18:05:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2015/03/07  18:05:52.0  XRT_AEC_RESET_413_OG [0x19d]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2015/03/07  18:05:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/07  18:05:56.0  XRT_FLD_RESET_407_OG [0x197]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  18:08:26.0  XRT_QT_PROG_SET_406_OG [0x196]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2015/03/07  18:08:28.0  XRT_FL_PROG_SET_401_OG [0x191]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2015/03/07  18:08:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  18:51:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  18:51:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  18:51:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  18:51:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
```

```
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/07  18:54:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/07  19:15:00.0  XRT_Custom_430_OG [0x1ae]
2015/03/07  19:16:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  20:28:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  20:28:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  20:28:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  20:28:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/07  20:31:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/07  20:52:00.0  XRT_Custom_430_OG [0x1ae]
2015/03/07  20:53:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  22:06:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  22:06:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  22:06:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  22:06:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/07  22:09:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/07  22:28:00.0  XRT_Custom_430_OG [0x1ae]
2015/03/07  22:29:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/07  23:43:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  23:43:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  23:43:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  23:43:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/07  23:46:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/07  23:49:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  23:49:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/07  23:49:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/03/07  23:50:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 02 01 ca 01 99
2015/03/07  23:50:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2015/03/07  23:50:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2015/03/07  23:50:22.0  XRT_AEC_RESET_413_OG [0x19d]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2015/03/07  23:50:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/07  23:50:26.0  XRT_FLD_RESET_407_OG [0x197]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/07  23:52:56.0  XRT_QT_PROG_SET_406_OG [0x196]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2015/03/07  23:52:58.0  XRT_FL_PROG_SET_401_OG [0x191]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2015/03/07  23:53:30.0  XRT_Custom_430_OG [0x1ae]
2015/03/07  23:54:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/08  01:09:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  01:09:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  01:09:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/08  01:09:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/08  01:12:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/08  01:26:30.0  XRT_Custom_430_OG [0x1ae]
2015/03/08  01:27:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/08  02:42:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  02:42:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  02:42:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/08  02:42:06.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/08  02:45:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/08  03:03:00.0  XRT_Custom_430_OG [0x1ae]
2015/03/08  03:04:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
```

```
2015/03/08  04:11:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  04:11:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  04:11:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/08  04:11:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/08  04:14:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/08  04:40:30.0  XRT_Custom_430_OG [0x1ae]
2015/03/08  04:41:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/08  05:51:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  05:51:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  05:51:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2015/03/08  05:51:36.0  XRT_PREFLR_STRT_439_OG [0x1b7]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2015/03/08  05:54:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2015/03/08  06:17:30.0  XRT_Custom_430_OG [0x1ae]
2015/03/08  06:18:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/08  06:27:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  06:27:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  06:27:28.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/03/08  06:27:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2015/03/08  06:27:48.0  XRT_FLD_DIS_422_OG [0x1a6]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/03/08  06:30:24.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/03/08  06:30:26.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/08  06:30:28.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2015/03/08  06:30:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/08  06:37:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  06:37:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/08  06:37:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 02 01 ca 01 99
2015/03/08  06:38:00.0  XRT_TCIB_XRT_S_HTR_A_ENA_426_OG [0x1aa]
                                TCIB_XRT_S_HTR_A_ENA 0 04-BC
2015/03/08  09:00:00.5  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 53 8d 01 99
2015/03/08  15:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 04 00 00 00 00
2015/03/08  18:00:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 02 01 ca 01 99
2015/03/09  09:00:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 03 00 00 00 00
2015/03/09  15:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 04 00 00 00 00
2015/03/09  18:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2015/03/09  18:10:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 02 01 ca 01 99
2015/03/10  00:00:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 01 00 00 00 00
2015/03/10  06:01:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2015/03/10  06:11:30.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 02 01 ca 01 99
2015/03/10  10:58:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
```