

# XRT Timeline to be uploaded on 2015/03/10

Period: 2015/03/10 10:48:00 - 2015/03/14 10:28:00

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Normal mode

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## XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
03/11 15:03:00 - 03/11 15:09:54	Fixed ( -528.4, -528.4)	XRT quadrant pointing 1/4
<b>PROG= 02 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 88 1-time(s) 12.0sec		
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
03/11 15:13:00 - 03/11 15:19:54	Fixed ( 528.4, -528.4)	XRT quadrant pointing 2/4
<b>PROG= 04 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 31 1-time(s) 12.0sec		
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
03/11 15:23:00 - 03/11 15:29:54	Fixed ( 528.4, 528.4)	XRT quadrant pointing 3/4
<b>PROG= 15 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 81 1-time(s) 12.0sec		
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
03/11 15:33:00 - 03/11 15:39:54	Fixed ( -528.4, 528.4)	XRT quadrant pointing 4/4
<b>PROG= 09 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 28 1-time(s) 12.0sec		
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec

	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec	
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>												
	<b>Seqn= 23</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
<b>Subr= 3</b>	<b>2-time(s)</b>	<b>2.0sec</b>												
	<b>Seqn= 12</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	<b>Seqn= 14</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

**XOB #19EF: AR Standard-B(Morphology), Be/thin 24s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33m)**

Term	Pointing (x, y)	Comment												
03/11 15:47:00 - 03/11 17:59:30	Track ( -337.9, -182.6) @ 03/11 15:40:00	flare watch AR12297												
<b>PROG= 12 Inf.-time(s)</b>														
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>												
	<b>Seqn= 8</b>	<b>1-time(s)</b>												
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
	<b>Seqn= 24</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
<b>Subr= 2</b>	<b>2-time(s)</b>	<b>2.0sec</b>												
	<b>Seqn= 16</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	<b>Seqn= 52</b>	<b>100-time(s)</b>	<b>2.0sec</b>											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec	
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

**XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi**

Term	Pointing (x, y)	Comment												
03/11 18:26:00 - 03/11 18:32:54	Fixed ( 0.0, 0.0)	synoptic, shifted 23.0 min												
03/12 05:57:00 - 03/12 06:03:54	Fixed ( 0.0, 0.0)	synoptic, shifted -6.0 min												
<b>PROG= 03 1-time(s)</b>														
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>12.0sec</b>												
	<b>Seqn= 33</b>	<b>1-time(s)</b>												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	<b>Seqn= 40</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	<b>Seqn= 77</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	<b>Seqn= 6</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

**XOB #19EE: AR Standard-B(Morphology), Be/thin 48s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33m)**

Term	Pointing (x, y)	Comment												
03/11 18:36:00 - 03/12 05:53:54	Track ( -313.7, -181.5) @ 03/11 18:33:00	# flare watch AR12297, HOP245												
03/12 06:07:00 - 03/12 07:25:00	Track ( -215.0, -178.2) @ 03/12 06:04:00	# flare watch AR12297												
<b>PROG= 06 Inf.-time(s)</b>														
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>												
	<b>Seqn= 8</b>	<b>1-time(s)</b>												
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
	<b>Seqn= 24</b>	<b>1-time(s)</b>	<b>2.0sec</b>											

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 16</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 58</b>		<b>50-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
03/11 15:47:00 - 03/11 17:59:30	Track ( -337.9, -182.6) <sup>Ⓜ 03/11 15:40:00</sup>	flare watch AR12297
03/11 18:36:00 - 03/12 05:53:54	Track ( -313.7, -181.5) <sup>Ⓜ 03/11 18:33:00</sup>	# flare watch AR12297, HOP245
03/12 06:07:00 - 03/12 07:25:00	Track ( -215.0, -178.2) <sup>Ⓜ 03/12 06:04:00</sup>	# flare watch AR12297

#### PROG= 19 30-time(s)

<b>Subr= 1</b>		<b>20-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 11</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn=100</b>		<b>1-time(s)</b>	<b>10.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 10</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 15</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

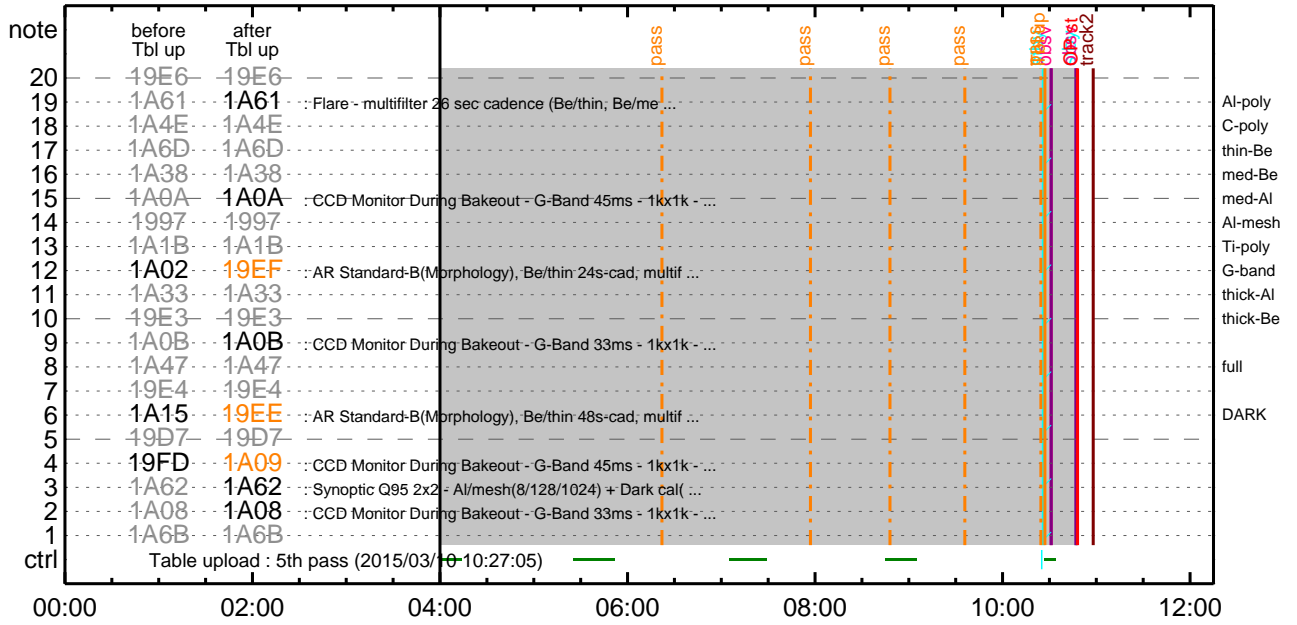
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### Flare Detection

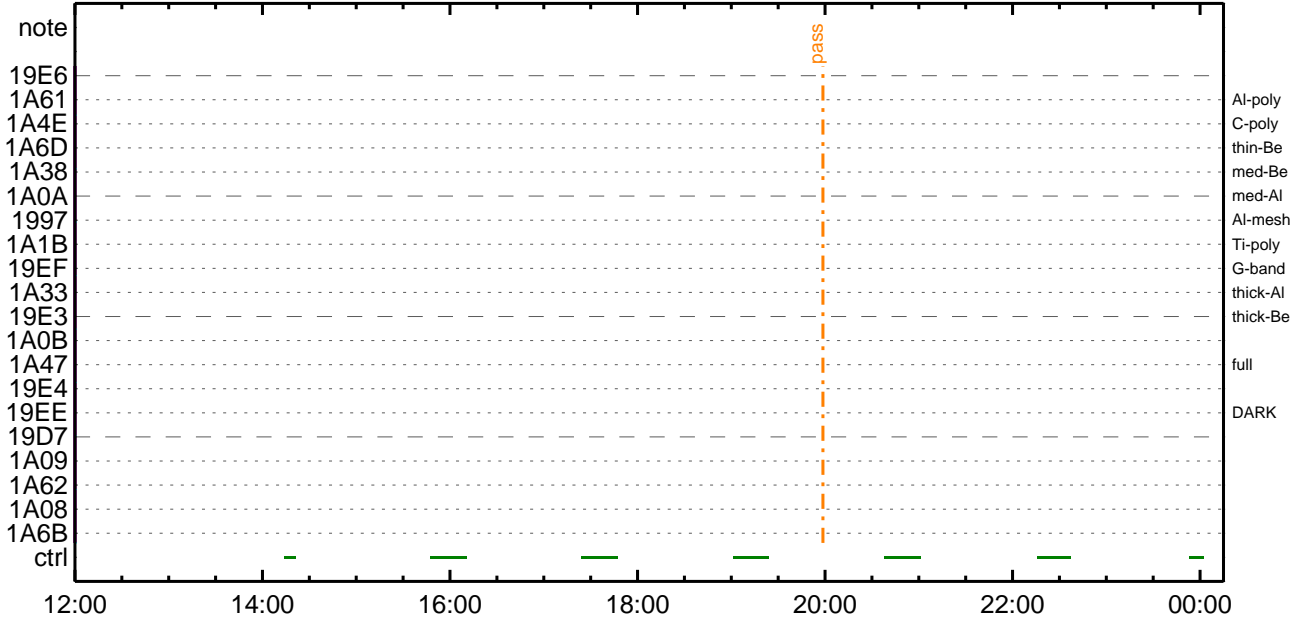
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FLD Patrol												
Term	Pointing (x, y)	Comment										
03/11 15:44:18 - 03/11 18:23:18	Track ( -337.9, -182.6) <sup>Ⓜ 03/11 15:40:00</sup>	flare watch AR12297										
03/11 18:33:18 - 03/12 05:54:18	Track ( -313.7, -181.5) <sup>Ⓜ 03/11 18:33:00</sup>	# flare watch AR12297, HOP245										
03/12 06:04:18 - 03/14 10:28:00	Track ( -215.0, -178.2) <sup>Ⓜ 03/12 06:04:00</sup>	# flare watch AR12297										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

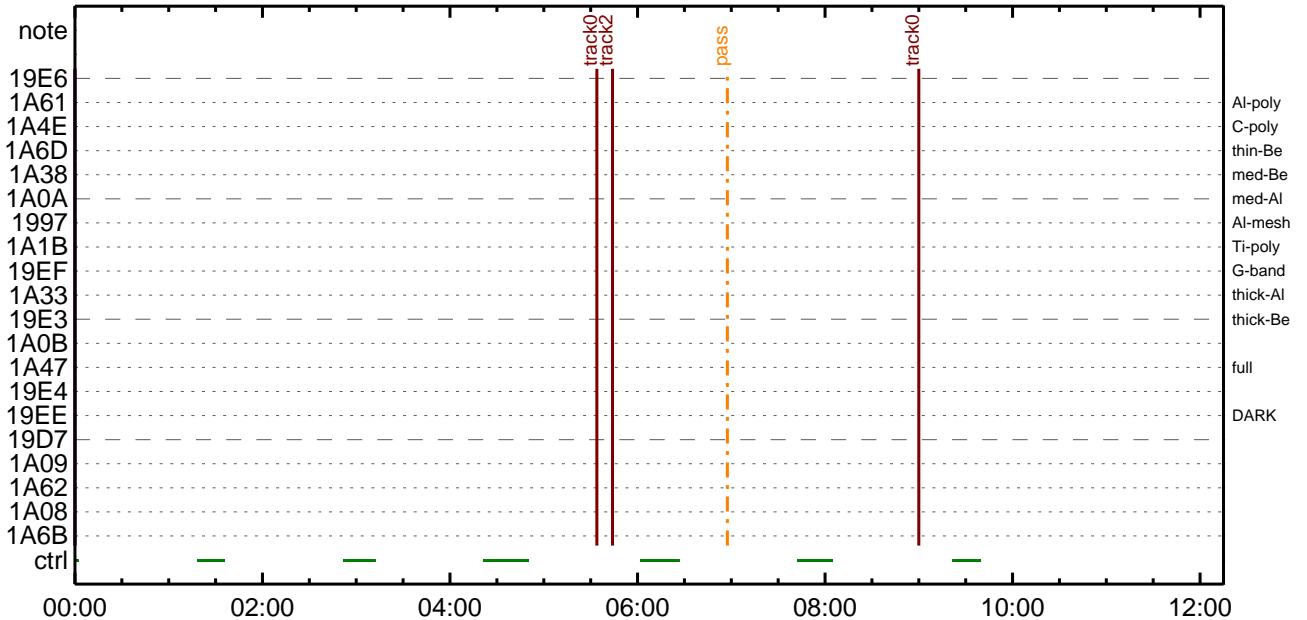
### CMDI #0977 2015/03/10



### CMDI #0977 2015/03/10

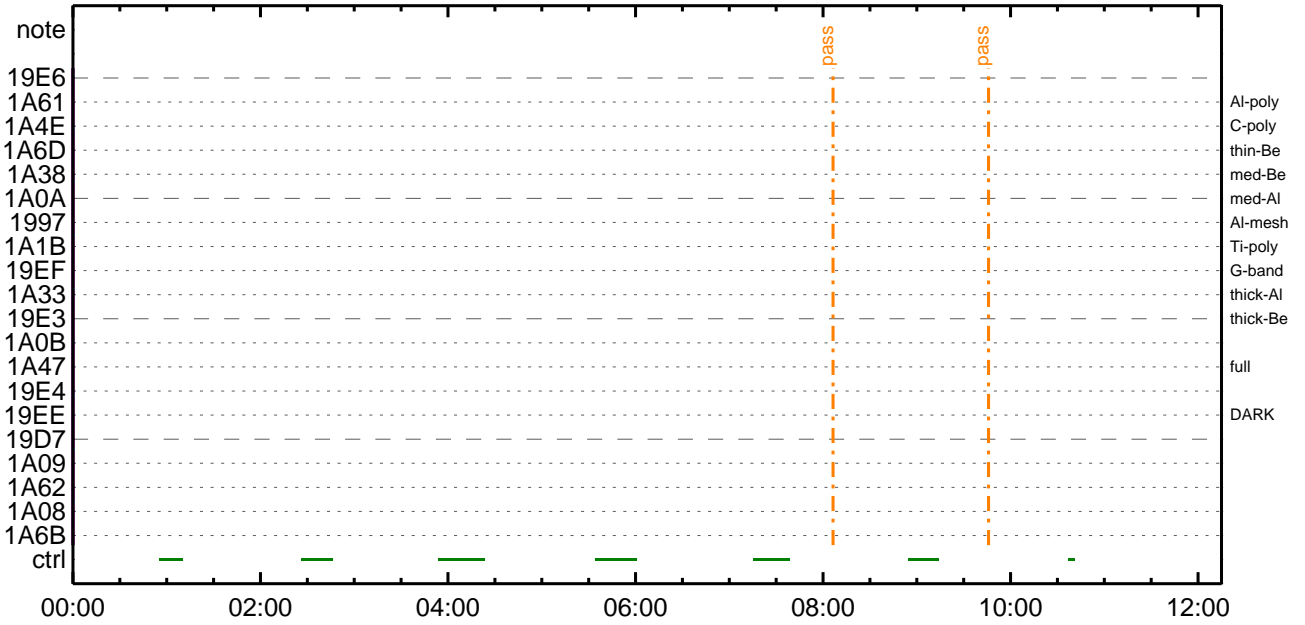


### CMDI #0977 2015/03/11

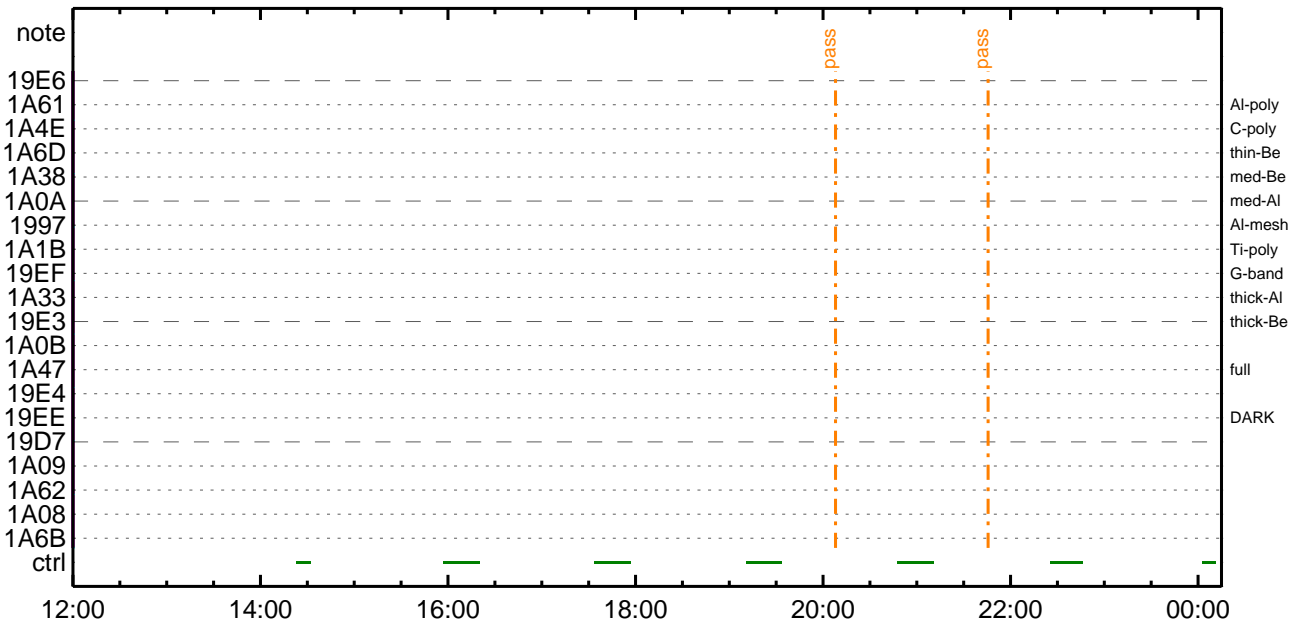




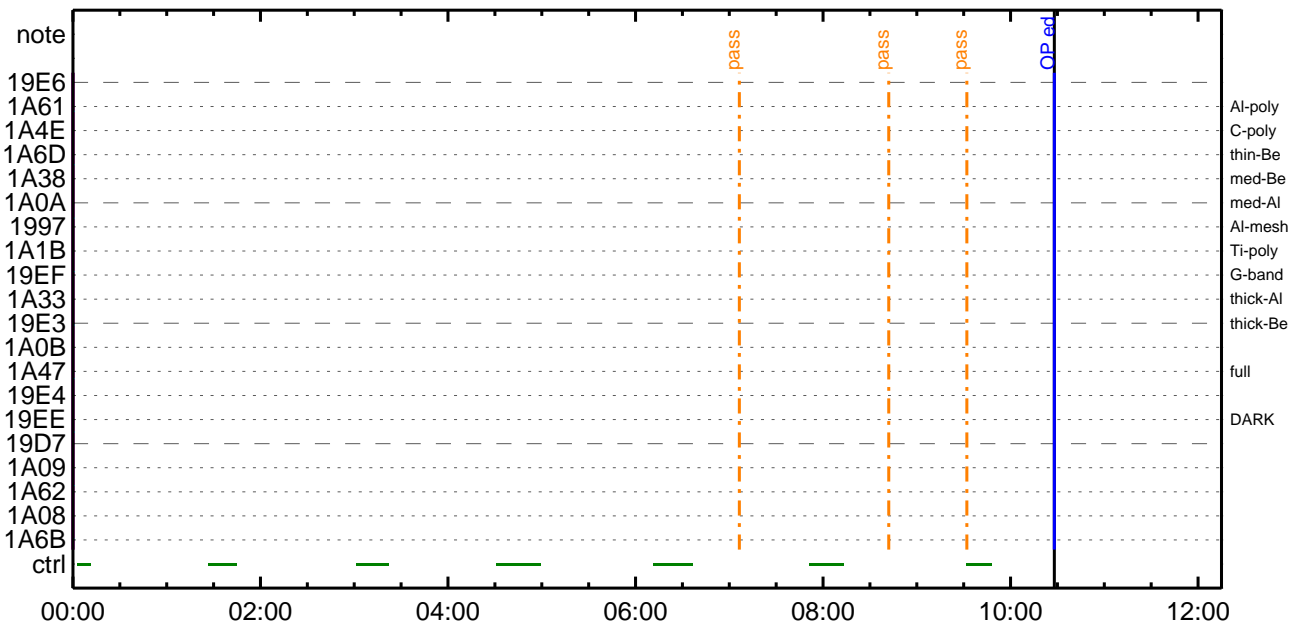
CMDI #0977 2015/03/13

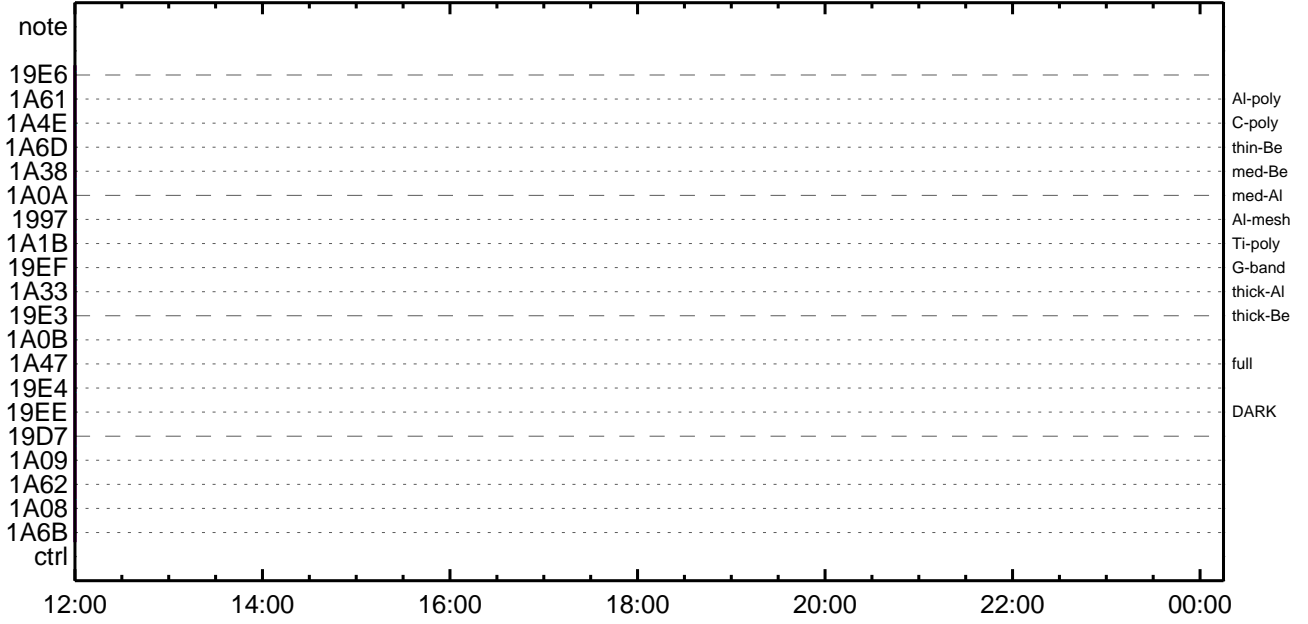


CMDI #0977 2015/03/13



CMDI #0977 2015/03/14





(a) Spacecraft Operation Procedure (real-commands)

```
main-992 2015-03-10 14:47:35 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µ°Æ»Í×ÁÇúÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÍÉ;ÈÈÈ¼°ÇÔα•α¿¼í¹çαÍ;çÀ®, ùα¹αèαβαÇÁ+¿®α•αÊααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP 'úÁÍσÍ»ò¼YαÊÊα¹αèDCBC•x²è *****
0016 C. (¼á°íYÓYÁYÈYβYÈYáYçYèαÈ¼αα¼Á»Ûα¹αè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YDÿ¹•İ Daily+¿ÍÑαÈ'Øα¹αèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁYŞYÁY-¼Á»Û;ã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```





0096 C.  
0097 C.  
0098 C. \*\*\*\*\*  
0099 C. SOT TI command set  
0100 C. \*\*\*\*\*  
0101 C. Execute, after the success of OP upload.  
0102 +. TI 2015-03-10 10:47:16.0  
0103 DC 07-F0 MDP\_SOT\_MODE\_STBY  
0104 BC (41)  
0105 . C. -----  
0106 C. HK1\_TI\_CMD\_NUM = 1 CNTUP [ ]  
0107 C. -----  
0108 C. \*\*\*\*\* SOT END \*\*\*\*\*  
0109 C.  
0110 C. \*\*\*\*\* XRT START \*\*\*\*\*  
0111 C. Execute, after the success of OP upload.  
0112 +. TI 2015-03-10 10:47:00.0  
0113 DC 07-F0 MDP\_XRT\_MODE\_STBY  
0114 BC (c3)  
0115 . C. [ ] [HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0116 C.  
0117 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0118 C.  
0119 . C. \*\*\*\*\* MDP 'úÃîâî»ö¼ÝðĚÂð¹ñēDCBC•x²è \*\*\*\*\*  
0120 C. (¼ãºîŸÓŸĀŸĒŸŦŸĚŸáŸŸŸēñ¼ñ¼Ā»Ūñ¹ñē)  
0121 . S. DC-BC dcbc-402:DCBC  
0122 (MDP\_known\_event)  
0123 C.  
0124 C.  
0125 . C. \*\*\*\*\* ŸDŸ¹•Ĭ Daily±ġİÑñĚ'Øñ¹ñēDCBC•x²è \*\*\*\*\*  
0126 . S. DC-BC dcbc-153:DCBC  
0127 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0128 C.  
0129 C.  
0130 . C. ;ãLOŠŸĀŸŸŸĀŸŸŸŸĀ»Ū;ã  
0131 C.  
0132 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0133 C.



```
0096 C.      0p00z;çSET0EDUMP0İÆ±°iYÑY¹0ç¹Ô0|0³0E;f
0097 C.
0098 . C.   TIY³YFYYÖYÉ00dÅDİ¿(UT)
0099 +. TI  2015-03-10 10:43:00.0
0100 DC  01-B3 DHU_OP_STOP
0101 C.      çç[HK1_TI_CMD_NUM] EQ      1COUNTUP
0102 C.
0103 +. TI  2015-03-10 10:43:01.0
0104 DC  01-B4 DHU_OP_COPY
0105 C.      çç[HK1_TI_CMD_NUM] EQ      1COUNTUP
0106 C.
0107 +. TI  2015-03-10 10:43:01.0
0108 DC  01-B5 DHU_OPOG_COPY
0109 C.      çç[HK1_TI_CMD_NUM] EQ      1COUNTUP
0110 C.
0111 +. TI  2015-03-10 10:47:59.5
0112 DC  01-B2 DHU_OP_START
0113 C.      çç[HK1_TI_CMD_NUM] EQ      1COUNTUP
0114 C.
0115 C.   °Ê²¼0İÄê%îİÑ0İYÁYŞYÄY-¹àİÜ
0116 C.      çç[HK1_TI_CMD_ENA/DIS] EQ      ENA
0117 C.      çç[HK1_TI_CMD_NUM] EQ      4
0118 C.      çç[HK1_NEXT_EXEC_PIM] EQ      DHU
0119 C.      çç[HK1_NEXT_EXEC_DC] EQ      0xB3
0120 C.
0121 . C.   *****
0122 C.   TIİİî°èYÄYÖYx
0123 C.   *****
0124 C.
0125 C.   TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC  01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.      çç[HK1_DMP_TOP_ADRS_1] EQ      07
0129 C.      çç[HK1_DMP_TOP_ADRS_0] EQ      2B
0130 C.      çç[HK1_DMP_BLOCK_NUM] EQ      3
0131 C.      çç[HK1_DMP_REPEAT_NUM] EQ      0
0132 C.      çç[HK1_DMA_DMP_PIM] EQ      DHU
0133 +. DC  01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.      çç[HK1_PKT_FORM_NO] EQ      7
0136 C.      çç[HK1_PKT_GEN_TIME] EQ      0.25 s
0137 C.      çç[HK1_S_TLM_BIT_RATE] EQ      32k
0138 C.      çç[HK1_X_TLM_BIT_RATE] EQ      4M
0139 C.      çç[HK1_DMP_CHK_FLG] EQ      EXEC
0140 C.
0141 . C.   YÄYÖYx½ªİ»0d³İÇŞ
0142 C.      çç[HK1_DMP_CHK_FLG] EQ      NON
0143 C.
0144 . C.   RAM ID=TI_TBL0İÈ¹ç•è²İOK0d³İÇŞ
0145 C.
0146 . C.   DHUYâ;¼YÉ;È¼Y¼, Yì;¼YÈ;È0dİá0¹
0147 +. DC  01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.      çç[HK1_PKT_FORM_NO] EQ      2
0150 C.      çç[HK1_PKT_GEN_TIME] EQ      0.5S
0151 C.      çç[HK1_S_TLM_BIT_RATE] EQ      32K
0152 C.      çç[HK1_X_TLM_BIT_RATE] EQ      4M
0153 C.
0154 . C.   Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C.   ***** Start EIS operation (TI set) *****
0158 C.   Execute, after the success of OP upload.
0159 C.   Set EIS TI-commands
0160 +. TI  2015-03-10 10:47:30.0
0161 DC  07-FC EIS_MODE_MANU
0162 BC      (21 02)
0163 +. TI  2015-03-10 10:47:40.0
0164 DC  07-FC EIS_MODE_CHG_DIS
0165 BC      (22)
0166 . C.      [ ] [HK1_TI_CMD_NUM] EQ      2 COUNTUP
0167 C.   ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 . C.   ***** MDP `ûÄİ0İ»ö¼Y0ÈÄ00¹0èDCBC•x²è *****
0172 C.   (¼á°İYÖYÄYÈYFYYÄYçYè0È¼00¼Ä»Ü0¹0è)
0173 . S. DC-BC dcbc-402:DCBC
0174 (MDP_known_event)
0175 C.
0176 C.
0177 . C.   ***** YDY¹•İ Daily±¿İÑ0È´00¹0èDCBC•x²è *****
0178 . S. DC-BC dcbc-153:DCBC
0179 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0180 C.
0181 C.
0182 . C.   ;ãLOS¥ÁYŞYÄY-¼Ä»Ü;ã
0183 C.
0184 . C.   ***** LOS *****
0185 C.
```





(a) Spacecraft Operation Procedure (real-commands)

```
main-996 2015-03-10 14:47:35 156 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÄã•µ°È»Í×ÁÇãÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÈ;ÈãÈ¼°ÇÔã•ã¿¼í¹çãÍ;çÀ®, ùã¹ãèãÈãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-269:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉãîÁ°ãÈSTS_CHKãðOFFãÈã¹ãè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 . C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2015-03-10 10:47:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG_____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR _____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 c0 c0 10 10)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 80 80 20 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 40 c0 10 10)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 40 40 10 10)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a c0 40 10 10)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0b 85 83 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0c 80 80 20 08)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 0d 80 80 08 20)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 0f 80 80 06 06)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 10 80 80 08 08)
0118 + DC 07-F0 MDP_XRT_FLD_ENA
0119 BC (d8)
0120 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0121 BC (c8)
0122 + DC 07-F0 MDP_XRT_AEC_RESET
0123 BC (d0)
0124 + DC 07-F0 MDP_XRT_ARS_DIS
0125 BC (d5)
0126 + DC 07-F0 MDP_XRT_FLD_RESET
0127 BC (da)
0128 . C. ----- Success Verify ? OK / NG ____
0129 C.
0130 C.
0131 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0132 C.
0133 +. DC 07-F0 MDP_XRT_MODE_OBSV
0134 BC (c2)
0135 +. TI 2015-03-10 10:47:02.0
0136 DC 07-F0 MDP_XRT_MODE_OBSV
0137 BC (c2)
0138 . C. ----- Success Verify ? OK / NG ____
0139 C.
0140 C. ***** XRT END *****
0141 C.
0142 . C. ***** MDP `uAÎaÎ»ô%ÿaÊAa¹aèDCBC•x²è *****
0143 C. (%â°îÿÓÿAÿEÿPÿEÿÿâÿçÿèaE%¼aa¼A»Ûa¹aè)
0144 . S. DC-BC dcbc-402:DCBC
0145 (MDP_known_event)
0146 C.
0147 C.
0148 . C. ***** ÿDÿ¹.Ï Daily±;îÑaÊ´Øa¹aèDCBC•x²è *****
0149 . S. DC-BC dcbc-153:DCBC
0150 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0151 C.
0152 C.
0153 . C. ;ãLOSÿÁÿSÿÿÿÿ¼A»Û;ã
0154 C.
0155 . C. ***** LOS *****
0156 C.
```



\*\*\* OP Sequence for XRT \*\*\*

```

2015/03/10 10:58:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 02 01 ca 01 99
2015/03/11 05:34:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2015/03/11 05:44:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 02 01 ca 01 99
2015/03/11 06:38:00.0 XRT_TCIB_XRT_S_HTR_A_DIS_447_OG [0x1bf]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2015/03/11 09:00:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 00 53 8d 01 99
2015/03/11 14:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 14:59:56.0 XRT_CTRL_MANU_449_OG [0x1c1]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 15:00:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2015/03/11 15:02:32.0 XRT_FOCUS_POSITION_432_OG [0x1b0]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/03/11 15:02:52.0 XRT_QT_PROG_SET_431_OG [0x1af]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 02
2015/03/11 15:02:54.0 XRT_FLD_DIS_437_OG [0x1b5]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2015/03/11 15:02:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2015/03/11 15:02:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2015/03/11 15:03:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2015/03/11 15:09:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 15:09:56.0 XRT_CTRL_MANU_449_OG [0x1c1]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 15:10:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2015/03/11 15:12:32.0 XRT_FOCUS_POSITION_432_OG [0x1b0]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/03/11 15:12:52.0 XRT_QT_PROG_SET_433_OG [0x1b1]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 04
2015/03/11 15:12:54.0 XRT_FLD_DIS_437_OG [0x1b5]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2015/03/11 15:12:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2015/03/11 15:12:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2015/03/11 15:13:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2015/03/11 15:19:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 15:19:56.0 XRT_CTRL_MANU_449_OG [0x1c1]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 15:20:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2015/03/11 15:22:32.0 XRT_FOCUS_POSITION_432_OG [0x1b0]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/03/11 15:22:52.0 XRT_QT_PROG_SET_416_OG [0x1a0]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 0f
2015/03/11 15:22:54.0 XRT_FLD_DIS_437_OG [0x1b5]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2015/03/11 15:22:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2015/03/11 15:22:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2015/03/11 15:23:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2015/03/11 15:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 15:29:56.0 XRT_CTRL_MANU_449_OG [0x1c1]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 15:30:00.0 AOCs_OrE-point_Start_7_OG [0x09d]
                        AOCU_NM                    5 02-76 00 d1 07 2e f9
2015/03/11 15:32:32.0 XRT_FOCUS_POSITION_432_OG [0x1b0]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/03/11 15:32:52.0 XRT_QT_PROG_SET_448_OG [0x1c0]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 09
2015/03/11 15:32:54.0 XRT_FLD_DIS_437_OG [0x1b5]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2015/03/11 15:32:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2015/03/11 15:32:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2015/03/11 15:33:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2015/03/11 15:39:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 15:39:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/03/11 15:39:58.0 XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                        XRT_FOCUS_RECAL          2 07-F8 78 00
2015/03/11 15:40:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 02 01 ca 01 99
2015/03/11 15:43:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
    
```

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2015/03/11	15:44:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/03/11	15:44:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/03/11	15:44:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/03/11	15:44:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/03/11	15:44:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/11	15:46:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2015/03/11	15:46:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2015/03/11	15:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/03/11	16:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	16:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	16:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/11	16:22:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/03/11	16:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/03/11	16:46:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/11	16:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/03/11	17:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	17:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	17:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/11	17:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/03/11	18:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/03/11	18:22:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	18:22:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	18:22:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2015/03/11	18:23:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00
2015/03/11	18:23:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/03/11	18:25:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/03/11	18:25:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/03/11	18:25:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2015/03/11	18:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/03/11	18:32:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	18:32:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	18:32:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/03/11	18:33:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	01	ca	01
2015/03/11	18:33:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/03/11	18:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/03/11	18:33:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/03/11	18:33:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/03/11	18:33:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/11	18:35:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2015/03/11	18:35:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2015/03/11	18:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/03/11	19:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	19:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/11	19:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/11	19:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/03/11	19:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/03/11	20:00:00.0	XRT_Custom_430_OG [0x1ae]							

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2015/03/11	20:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/11	21:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/11	21:14:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/11	21:14:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/11	21:14:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/11	21:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/11	21:37:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/11	21:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/11	22:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/11	22:51:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/11	22:51:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/11	22:51:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/11	22:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/11	23:11:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/11	23:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/12	00:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	00:28:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	00:28:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/12	00:28:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/12	00:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/12	00:35:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/12	00:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/12	01:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	01:51:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	01:51:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/12	01:51:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/12	01:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/12	02:11:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/12	02:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/12	03:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	03:25:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	03:25:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/12	03:25:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/12	03:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/12	03:48:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/12	03:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/12	04:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	04:58:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	04:58:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/12	04:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/12	05:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/12	05:25:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/12	05:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/12	05:53:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	05:53:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/12	05:53:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/03/12	05:54:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/03/12	05:54:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/03/12	05:56:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/03/12	05:56:56.0	XRT_ARS_DIS_423_OG [0x1a7]							

2015/03/12	05:56:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/03/12	05:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2015/03/12	06:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/12	06:03:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/12	06:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/12	06:04:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/03/12	06:04:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02	01 ca 01 99
2015/03/12	06:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/03/12	06:04:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/03/12	06:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/03/12	06:04:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/03/12	06:06:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/12	06:06:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2015/03/12	06:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2015/03/12	06:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/12	06:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/12	06:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/12	06:38:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/12	06:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/03/12	07:03:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/03/12	07:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/03/12	07:25:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/12	07:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/12	10:32:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			AOCU_NM	5	02-76	00	00 00 00 00