

XRT Timeline to be uploaded on 2015/03/26

Period: 2015/03/26 10:00:00 - 2015/03/31 10:17:00

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Normal mode

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XOB #19EE: AR Standard-B(Morphology), Be/thin 48s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33ms)

Term	Pointing (x, y)	Comment
03/26 10:26:06 - 03/26 13:29:54	Fixed (-962.0, 10.0)	# OP start + 10min, HOP262 with IRIS at E-Limb
03/27 23:03:00 - 03/28 03:13:00	Fixed (-949.0, -180.0)	HOP262 with IRIS at East Limb
PROG= 06 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 8 1-time(s) 2.0sec		
Open/G-band Open/G-band close	Safe Norm 44ms	Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Seqn= 24 1-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band Open/G-band open	Safe Norm 32ms	Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 2-time(s) 2.0sec		
Seqn= 16 2-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Norm 250ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al Open/thick-Al close	Safe Norm 16.0s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 177ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open C-poly/thick-Al close	Safe Norm 250ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open Open/thick-Al close	Safe Norm 2.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 58 50-time(s) 2.0sec		
thin-Be/Open med-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 177ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec
med-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 177ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms	Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec
med-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19F6: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,8ms) - Leak (33ms) - 100s cad

Term	Pointing (x, y)	Comment
03/26 13:33:00 - 03/26 14:02:00	Fixed (-932.0, 212.0)	HOP79 (1/21)
03/26 14:09:00 - 03/26 14:20:54	Fixed (-739.0, 298.0)	(2/21)
03/26 14:24:00 - 03/26 14:35:54	Fixed (-554.0, 319.0)	(3/21)
03/26 14:39:00 - 03/26 14:50:54	Fixed (-366.0, 331.0)	(4/21)
03/26 14:54:00 - 03/26 15:05:54	Fixed (-175.0, 338.0)	(5/21)
03/26 15:09:00 - 03/26 15:33:30	Fixed (21.0, 339.0)	(6/21)
03/26 16:00:30 - 03/26 16:12:24	Fixed (139.0, 338.0)	(7/21)
03/26 16:15:30 - 03/26 16:27:24	Fixed (330.0, 331.0)	(8/21)
03/26 16:30:30 - 03/26 16:42:24	Fixed (518.0, 319.0)	(9/21)
03/26 16:45:30 - 03/26 17:09:30	Fixed (703.0, 298.0)	(10/21)
03/26 17:37:00 - 03/26 18:04:54	Fixed (896.0, 212.0)	(11/21)
03/26 18:18:00 - 03/26 18:46:30	Fixed (-913.0, -20.0)	HOP79 (12/21)
03/26 19:14:00 - 03/26 19:25:54	Fixed (-714.0, -20.0)	(13/21)
03/26 19:29:00 - 03/26 19:40:54	Fixed (-515.0, -20.0)	(14/21)
03/26 19:44:00 - 03/26 19:55:54	Fixed (-316.0, -20.0)	(15/21)
03/26 19:59:00 - 03/26 20:24:00	Fixed (-117.0, -20.0)	(16/21)
03/26 20:51:00 - 03/26 21:02:54	Fixed (82.0, -20.0)	(17/21)
03/26 21:06:00 - 03/26 21:17:54	Fixed (281.0, -20.0)	(18/21)
03/26 21:21:00 - 03/26 21:32:54	Fixed (480.0, -20.0)	(19/21)
03/26 21:36:00 - 03/26 22:01:00	Fixed (679.0, -20.0)	(20/21)
03/26 22:27:00 - 03/26 22:59:54	Fixed (878.0, -20.0)	(21/21)
03/26 23:03:00 - 03/27 06:22:24	Track (353.1, 430.0) @ 03/26 23:00:00	HOP243 with IRIS on QS
03/27 11:03:00 - 03/27 13:29:54	Track (222.0, 620.1) @ 03/27 11:00:00	HOP243 with IRIS on QS
PROG= 16 Inf.-time(s)		
Subr= 1 60-time(s) 100.0sec		
Seqn= 29 1-time(s) 4.0sec		
thin-Be/Open med-Be/Open close	Safe Norm 1.41s	Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 6 1-time(s) 2.0sec		
Open/G-band Open/G-band open	Safe Norm 8ms	Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 32ms	Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi

Term	Pointing (x, y)	Comment
03/26 18:08:00 - 03/26 18:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min
03/27 06:25:30 - 03/27 06:32:24	Fixed (0.0, 0.0)	synoptic, shifted 22.5 min
03/27 18:11:30 - 03/27 18:18:24	Fixed (0.0, 0.0)	synoptic, shifted 8.5 min
03/28 05:58:30 - 03/28 06:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min

PROG= 03 1-time(s)													
└ Subr= 1 1-time(s) 12.0sec													
└ Seqn= 33 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└ Seqn= 40 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 77 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19ED: AR Standard-B(Morphology), Be/thin 104s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33

Term	Pointing (x, y)	Comment
03/27 06:35:30 - 03/27 10:49:30	Track (-10.7, -64.2) @ 03/27 06:32:30	AR12305 obs.
03/27 13:33:00 - 03/27 17:45:00	Track (53.0, -64.6) @ 03/27 13:30:00	AR12305 obs.
03/27 18:21:30 - 03/27 22:37:00	Track (96.9, -65.1) @ 03/27 18:18:30	AR12305 obs.
03/28 03:37:06 - 03/28 05:55:24	Track (180.1, -66.9) @ 03/28 03:30:00	AR12305 obs.
03/28 06:08:30 - 03/28 09:28:00	Track (203.3, -67.5) @ 03/28 06:05:30	AR12305 obs.

PROG= 11 Inf.-time(s)													
└ Subr= 1 1-time(s) 2.0sec													
└ Seqn= 8 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└ Subr= 2 2-time(s) 2.0sec													
└ Seqn= 16 2-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└ Seqn= 19 20-time(s) 2.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	13.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	13.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	13.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	13.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	13.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	13.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	13.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	13.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
03/26 10:26:06 - 03/26 13:29:54	Fixed (-962.0, 10.0)	# OP start + 10min, HOP262 with IRIS at E-Limb
03/26 13:33:00 - 03/26 14:02:00	Fixed (-932.0, 212.0)	HOP79 (1/21)
03/26 14:09:00 - 03/26 14:20:54	Fixed (-739.0, 298.0)	(2/21)
03/26 14:24:00 - 03/26 14:35:54	Fixed (-554.0, 319.0)	(3/21)
03/26 14:39:00 - 03/26 14:50:54	Fixed (-366.0, 331.0)	(4/21)
03/26 14:54:00 - 03/26 15:05:54	Fixed (-175.0, 338.0)	(5/21)
03/26 15:09:00 - 03/26 15:33:30	Fixed (21.0, 339.0)	(6/21)
03/26 16:00:30 - 03/26 16:12:24	Fixed (139.0, 338.0)	(7/21)
03/26 16:15:30 - 03/26 16:27:24	Fixed (330.0, 331.0)	(8/21)
03/26 16:30:30 - 03/26 16:42:24	Fixed (518.0, 319.0)	(9/21)
03/26 16:45:30 - 03/26 17:09:30	Fixed (703.0, 298.0)	(10/21)
03/26 17:37:00 - 03/26 18:04:54	Fixed (896.0, 212.0)	(11/21)
03/26 18:18:00 - 03/26 18:46:30	Fixed (-913.0, -20.0)	HOP79 (12/21)
03/26 19:14:00 - 03/26 19:25:54	Fixed (-714.0, -20.0)	(13/21)
03/26 19:29:00 - 03/26 19:40:54	Fixed (-515.0, -20.0)	(14/21)

03/26 19:44:00 - 03/26 19:55:54	Fixed (-316.0, -20.0)	(15/21)
03/26 19:59:00 - 03/26 20:24:00	Fixed (-117.0, -20.0)	(16/21)
03/26 20:51:00 - 03/26 21:02:54	Fixed (82.0, -20.0)	(17/21)
03/26 21:06:00 - 03/26 21:17:54	Fixed (281.0, -20.0)	(18/21)
03/26 21:21:00 - 03/26 21:32:54	Fixed (480.0, -20.0)	(19/21)
03/26 21:36:00 - 03/26 22:01:00	Fixed (679.0, -20.0)	(20/21)
03/26 22:27:00 - 03/26 22:59:54	Fixed (878.0, -20.0)	(21/21)
03/26 23:03:00 - 03/27 06:22:24	Track (353.1, 430.0) @ 03/26 23:00:00	HOP243 with IRIS on QS
03/27 06:35:30 - 03/27 10:49:30	Track (-10.7, -64.2) @ 03/27 06:32:30	AR12305 obs.
03/27 11:03:00 - 03/27 13:29:54	Track (222.0, 620.1) @ 03/27 11:00:00	HOP243 with IRIS on QS
03/27 13:33:00 - 03/27 17:45:00	Track (53.0, -64.6) @ 03/27 13:30:00	AR12305 obs.
03/27 18:21:30 - 03/27 22:37:00	Track (96.9, -65.1) @ 03/27 18:18:30	AR12305 obs.
03/27 23:03:00 - 03/28 03:13:00	Fixed (-949.0, -180.0)	HOP262 with IRIS at East Limb
03/28 03:37:06 - 03/28 05:55:24	Track (180.1, -66.9) @ 03/28 03:30:00	AR12305 obs.
03/28 06:08:30 - 03/28 09:28:00	Track (203.3, -67.5) @ 03/28 06:05:30	AR12305 obs.

PROG= 19 30-time(s)

Subr= 1 20-time(s) 2.0sec	
Seqn= 11 1-time(s) 2.0sec	
Al-poly/Open	Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec	
Seqn= 10 1-time(s) 2.0sec	
med-Al/Open	med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec	
Al-poly/Open	Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 15 1-time(s) 2.0sec	
Open/G-band	Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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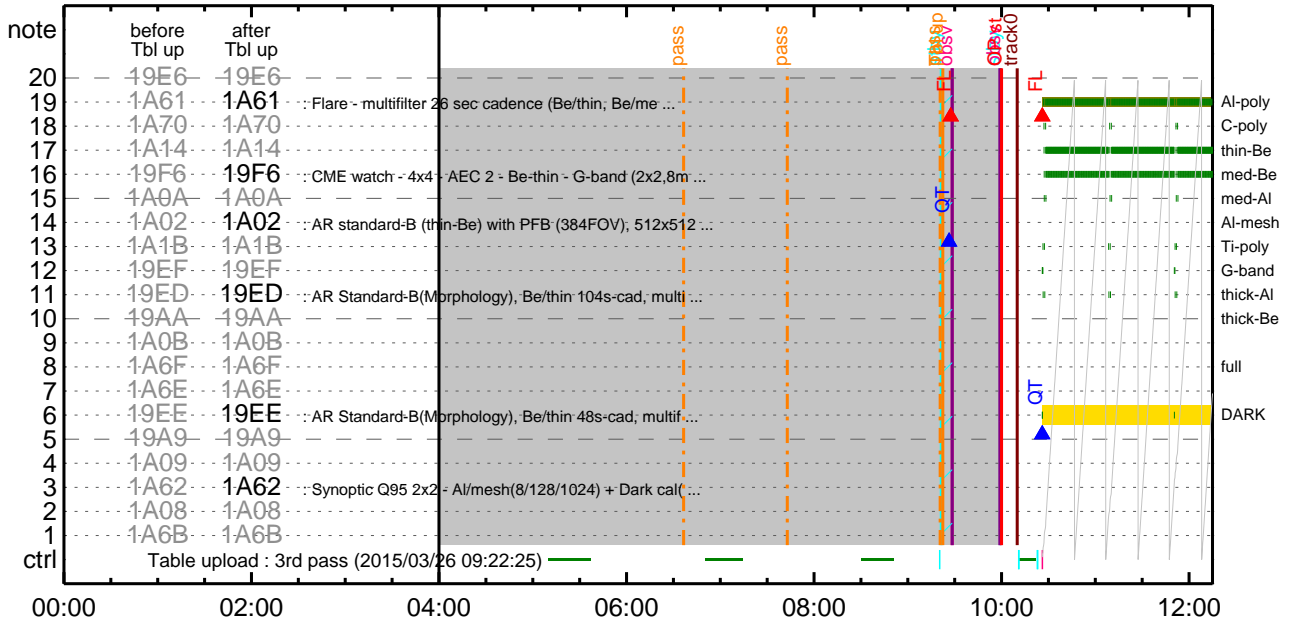
Flare Detection

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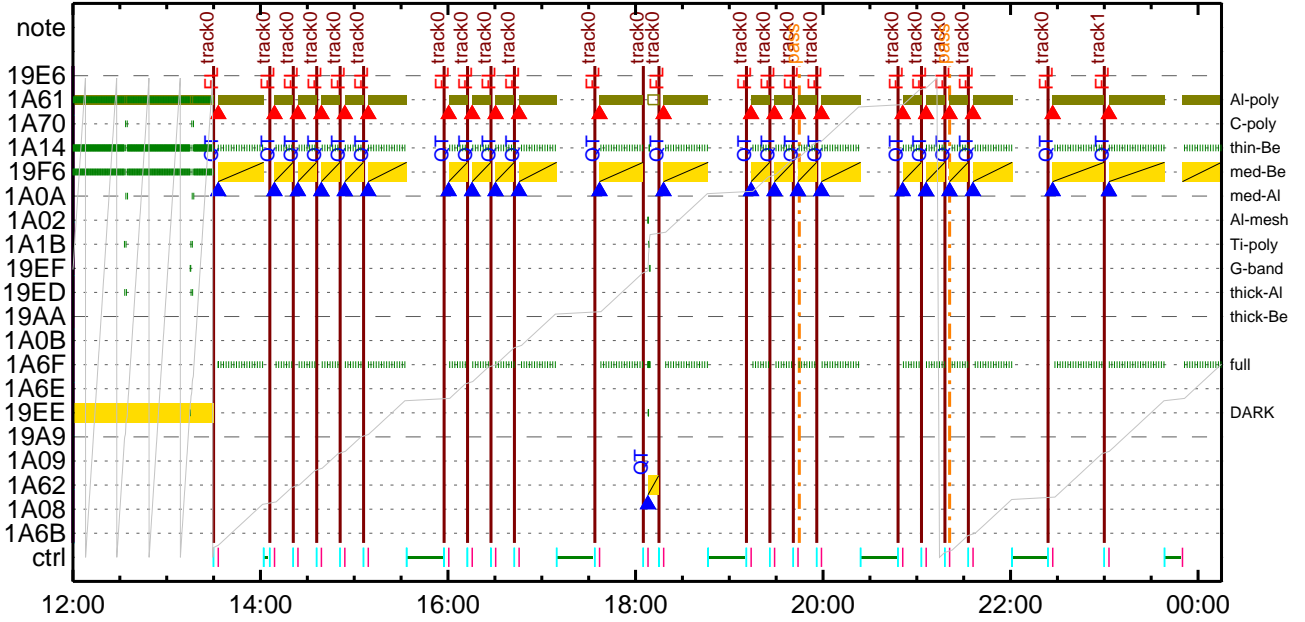
FLD Patrol

Term	Pointing (x, y)	Comment
03/26 18:15:18 - 03/27 06:22:48	Fixed (-913.0, -20.0)	HOP79 (12/21)
03/27 06:32:48 - 03/27 18:08:48	Track (-10.7, -64.2) @ 03/27 06:32:30	AR12305 obs.
03/27 18:18:48 - 03/28 05:55:48	Track (96.9, -65.1) @ 03/27 18:18:30	AR12305 obs.
03/28 06:05:48 - 03/31 10:17:00	Track (203.3, -67.5) @ 03/28 06:05:30	AR12305 obs.
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 30sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

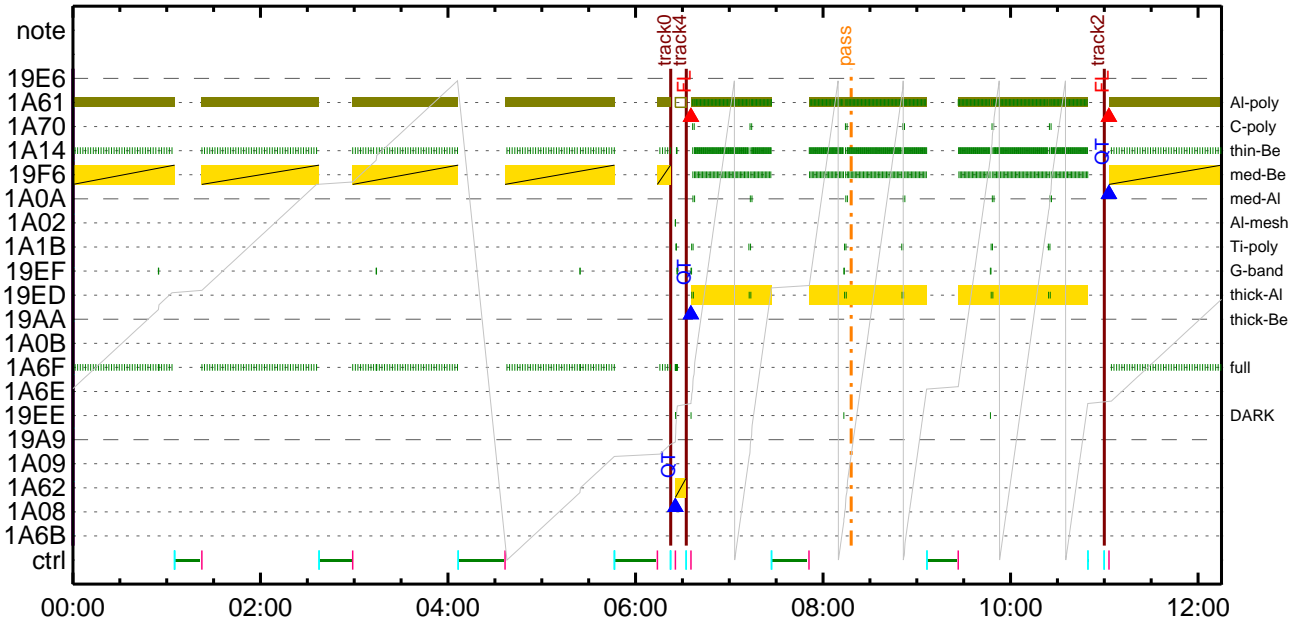
CMDI #0018 2015/03/26



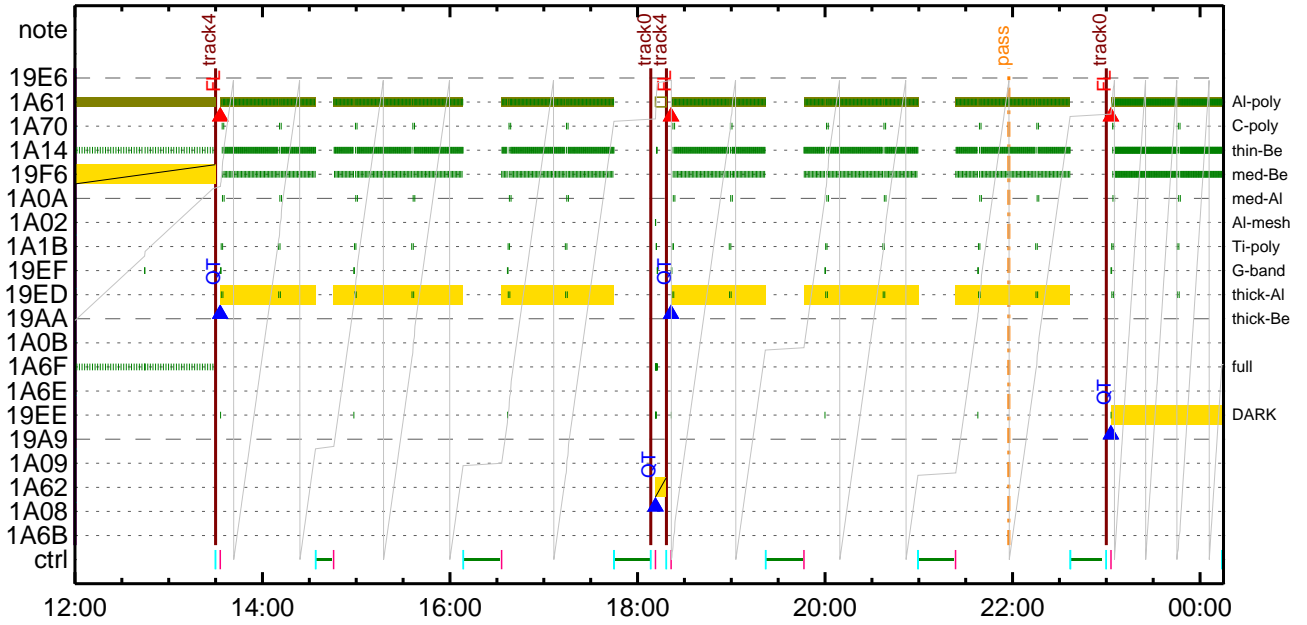
CMDI #0018 2015/03/26



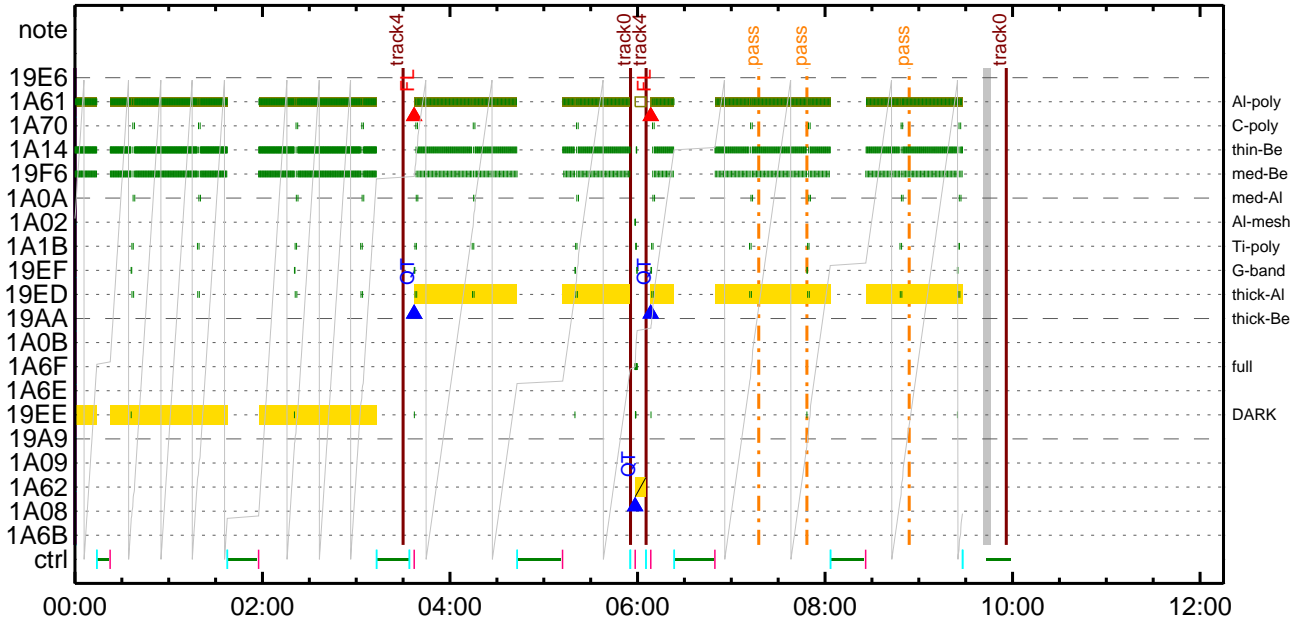
CMDI #0018 2015/03/27



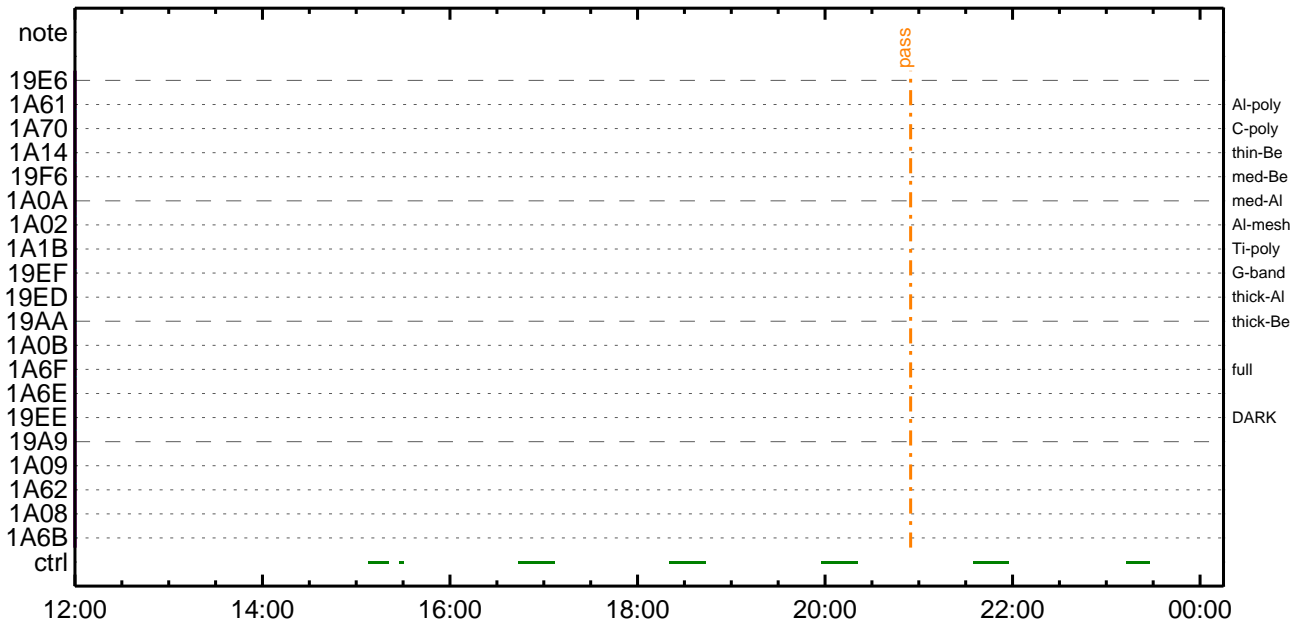
CMDI #0018 2015/03/27



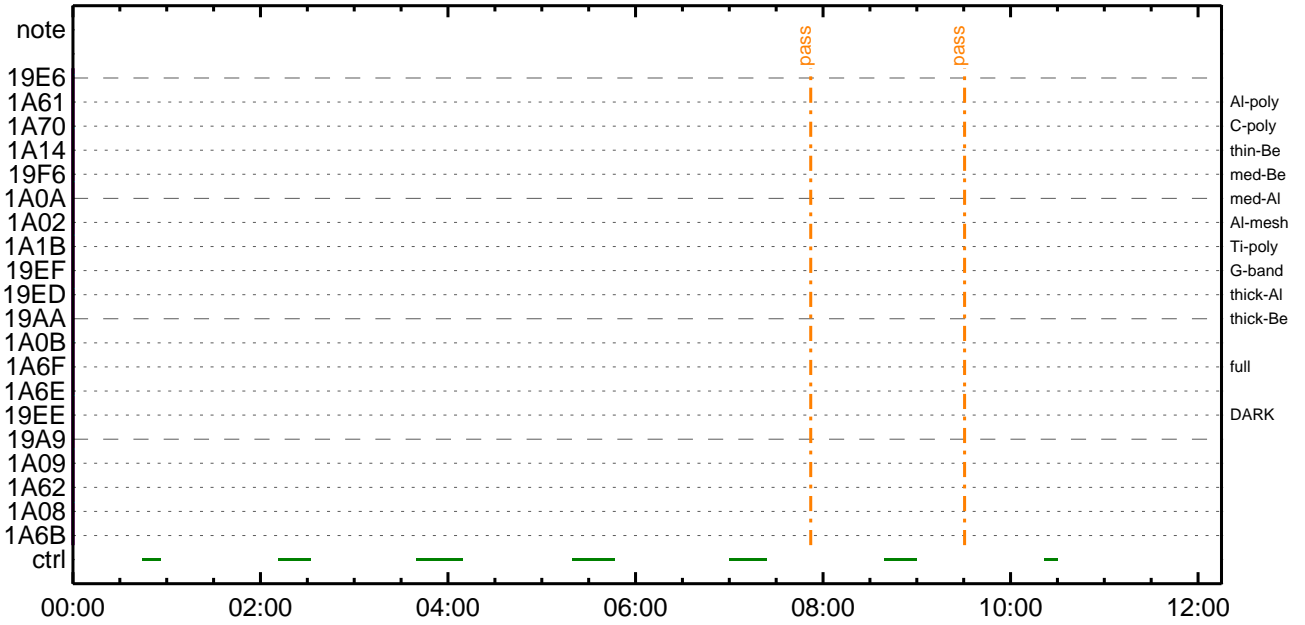
CMDI #0018 2015/03/28



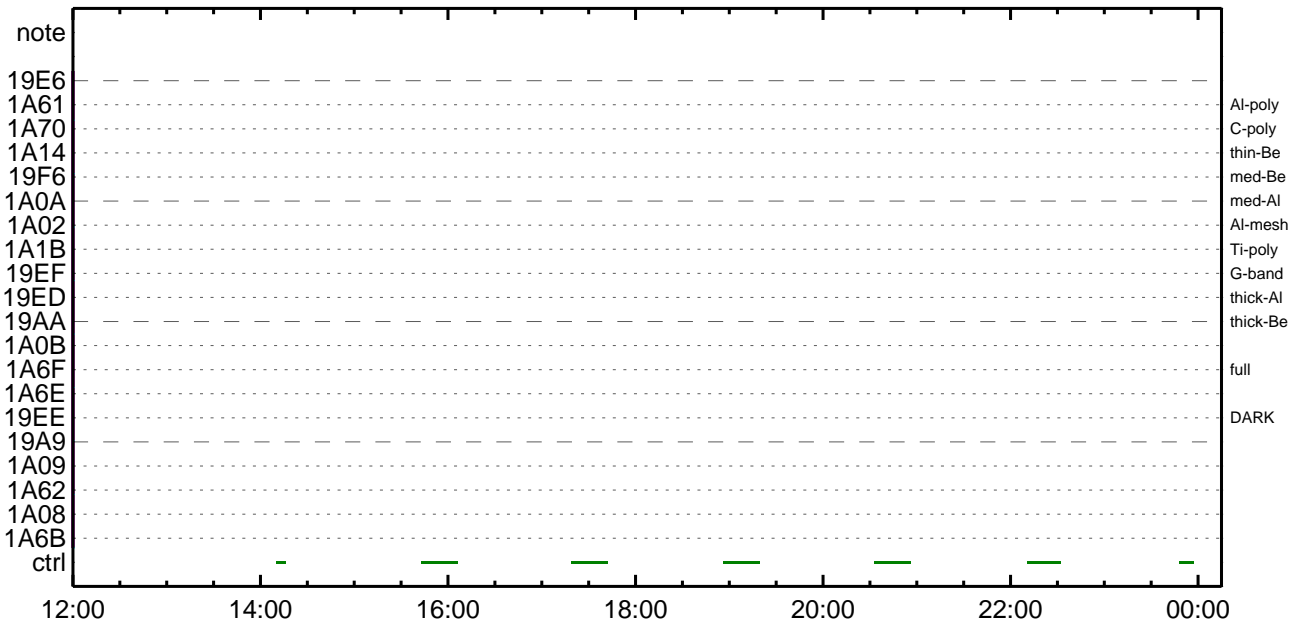
CMDI #0018 2015/03/28



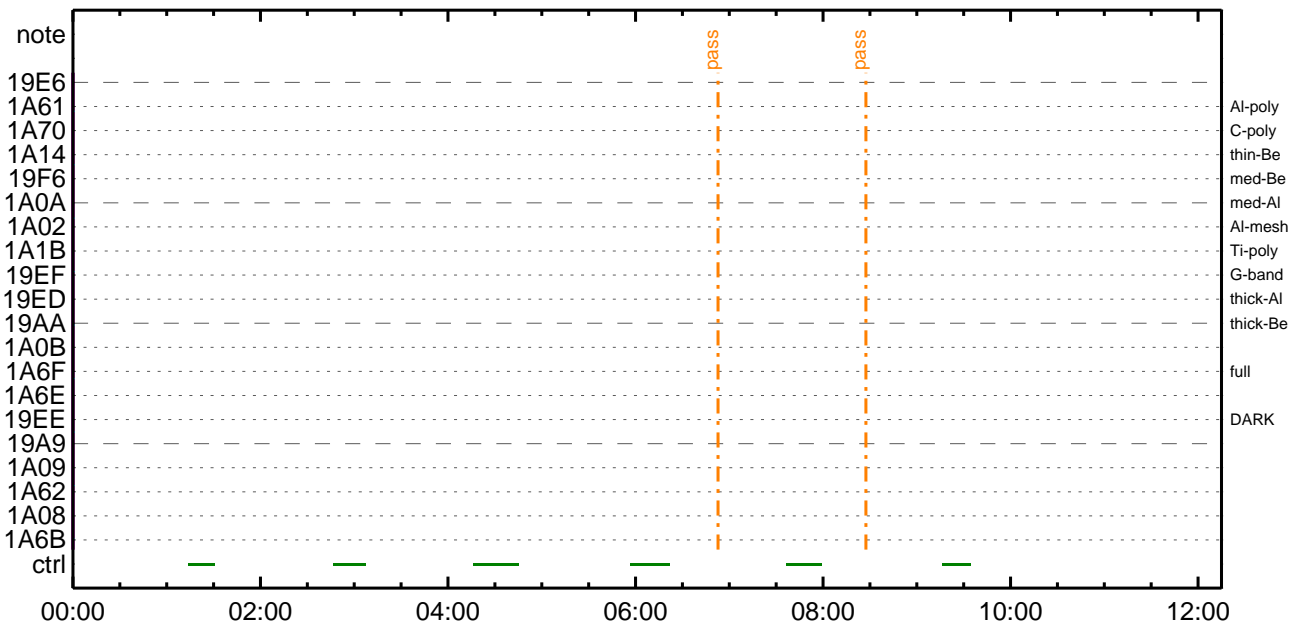
CMDI #0018 2015/03/29



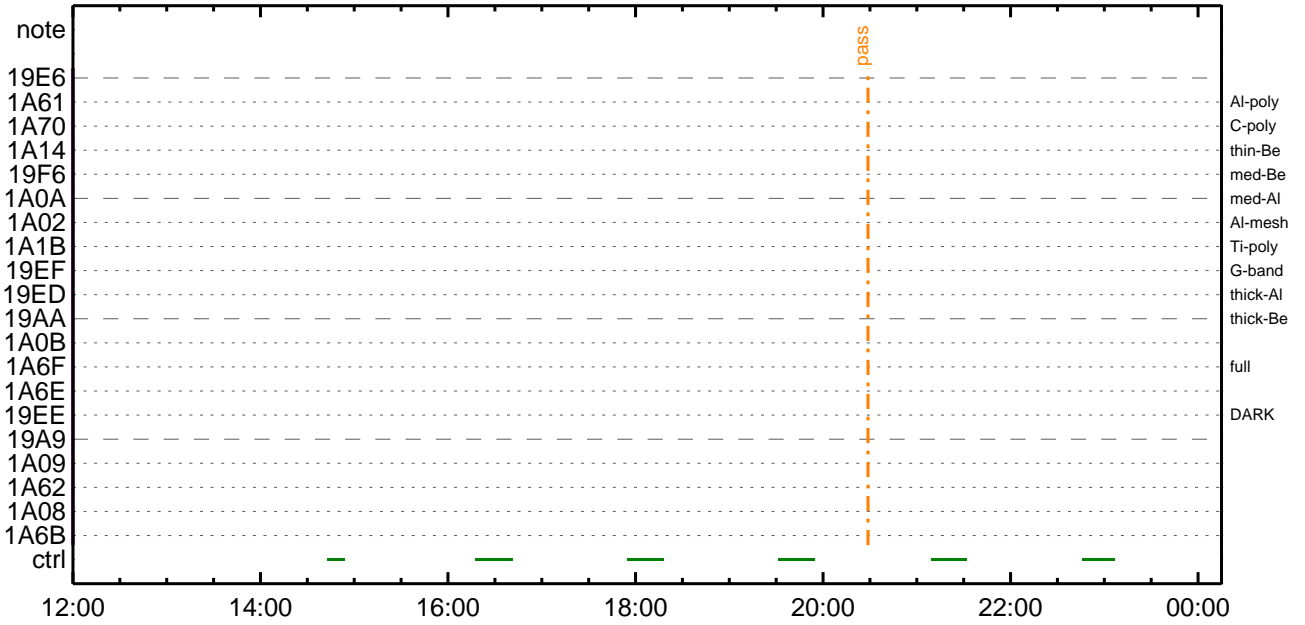
CMDI #0018 2015/03/29



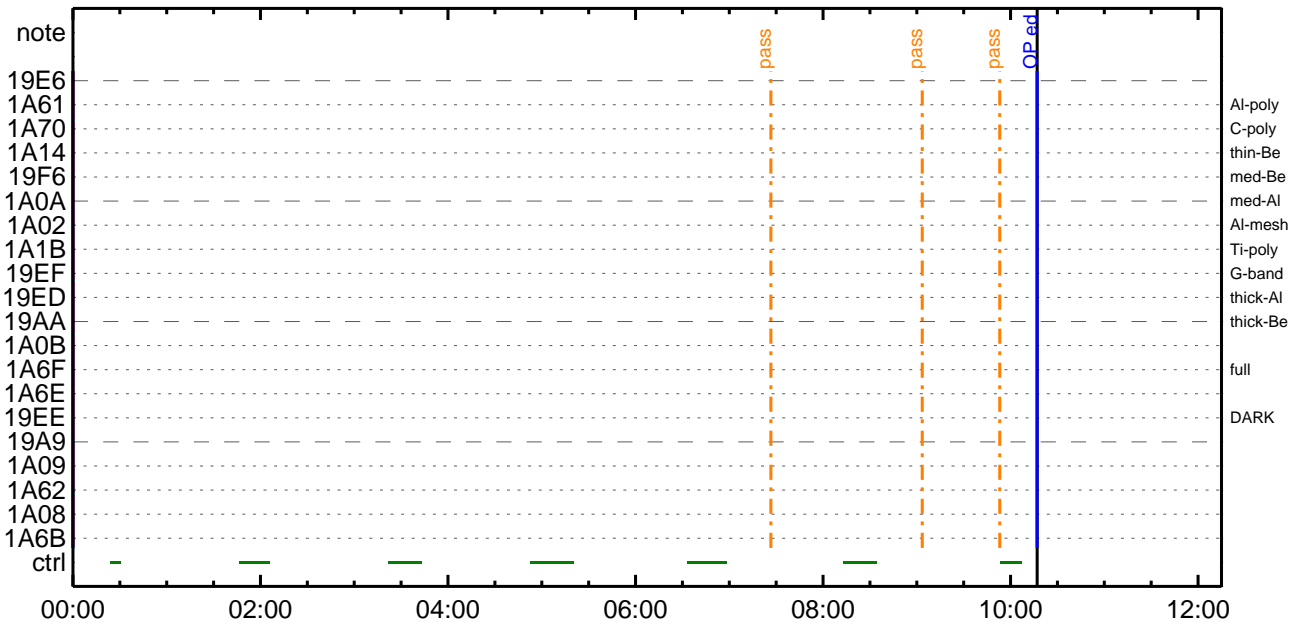
CMDI #0018 2015/03/30



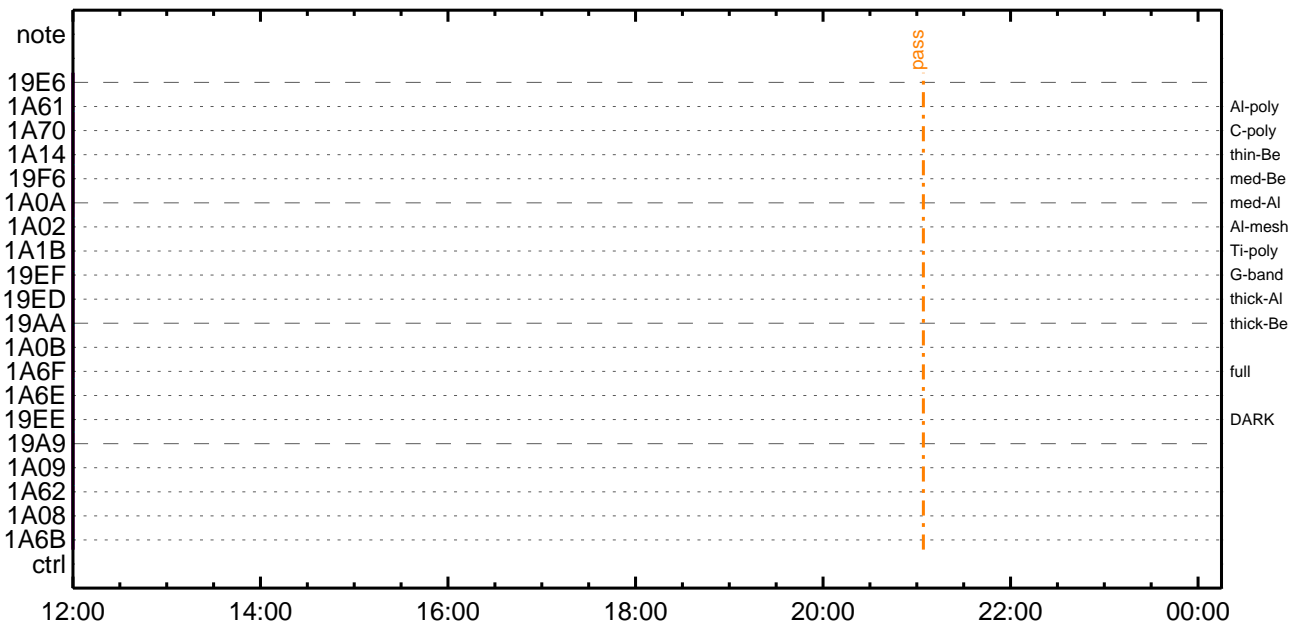
CMDI #0018 2015/03/30



CMDI #0018 2015/03/31



CMDI #0018 2015/03/31



(a) Spacecraft Operation Procedure (real-commands)

```
main-041 2015-03-26 12:49:41 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÁYŞYÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYÊ;¼Y³YÊYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ê¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;äOP/OGYí;¼YÉ;ä
0019 . S. OP op-041:OP
0020 ()
0021 . S. OG og-041:OG
0022 ()
0023 C.
0024 . C. ;änMOG&OPî°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C.        ¿¿[HK1_DMP_TOP_ADRS_1]           EQ      40
0029 C.        ¿¿[HK1_DMP_TOP_ADRS_0]           EQ      0
0030 C.        ¿¿[HK1_DMP_BLOCK_NUM]            EQ     127
0031 C.        ¿¿[HK1_DMP_REPEAT_NUM]           EQ      0
0032 C.        ¿¿[HK1_DMA_DMP_PIM]              EQ     DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C.        ¿¿[HK1_PKT_FORM_NO]               EQ      7
0036 C.        ¿¿[HK1_PKT_GEN_TIME]              EQ     0.25 s
0037 C.        ¿¿[HK1_S_TLM_BIT_RATE]           EQ     32k
0038 C.        ¿¿[HK1_X_TLM_BIT_RATE]           EQ      4M
0039 C.        ¿¿[HK1_DMP_CHK_FLG]              EQ     EXEC
0040 . C. YÀYóY×½ªî»¿¿¿¿¿¿¿¿
0041 C.        ¿¿[HK1_DMP_CHK_FLG]              EQ     NON
0042 . C. RAM ID=NMOG¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C.        ¿¿[HK1_DMP_TOP_ADRS_1]           EQ     41
0048 C.        ¿¿[HK1_DMP_TOP_ADRS_0]           EQ      0
0049 C.        ¿¿[HK1_DMP_BLOCK_NUM]            EQ     127
0050 C.        ¿¿[HK1_DMP_REPEAT_NUM]           EQ      0
0051 C.        ¿¿[HK1_DMA_DMP_PIM]              EQ     DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C.        ¿¿[HK1_PKT_FORM_NO]               EQ      7
0055 C.        ¿¿[HK1_PKT_GEN_TIME]              EQ     0.25 s
0056 C.        ¿¿[HK1_S_TLM_BIT_RATE]           EQ     32k
0057 C.        ¿¿[HK1_X_TLM_BIT_RATE]           EQ      4M
0058 C.        ¿¿[HK1_DMP_CHK_FLG]              EQ     EXEC
0059 . C. YÀYóY×½ªî»¿¿¿¿¿¿¿¿
0060 C.        ¿¿[HK1_DMP_CHK_FLG]              EQ     NON
0061 . C. RAM ID=NMOG¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C.        ¿¿[HK1_DMP_TOP_ADRS_1]           EQ     42
0067 C.        ¿¿[HK1_DMP_TOP_ADRS_0]           EQ      0
0068 C.        ¿¿[HK1_DMP_BLOCK_NUM]            EQ     65
0069 C.        ¿¿[HK1_DMP_REPEAT_NUM]           EQ      0
0070 C.        ¿¿[HK1_DMA_DMP_PIM]              EQ     DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C.        ¿¿[HK1_PKT_FORM_NO]               EQ      7
0074 C.        ¿¿[HK1_PKT_GEN_TIME]              EQ     0.25 s
0075 C.        ¿¿[HK1_S_TLM_BIT_RATE]           EQ     32k
0076 C.        ¿¿[HK1_X_TLM_BIT_RATE]           EQ      4M
0077 C.        ¿¿[HK1_DMP_CHK_FLG]              EQ     EXEC
0078 . C. YÀYóY×½ªî»¿¿¿¿¿¿¿¿
0079 C.        ¿¿[HK1_DMP_CHK_FLG]              EQ     NON
0080 . C. RAM ID=NMOG,RAM ID=OP¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿
0081 C.
0082 . C. ***** °Ê¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿ *****
0083 C. DHU¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C.        ¿¿[HK1_PKT_FORM_NO]               EQ      2
0087 C.        ¿¿[HK1_PKT_GEN_TIME]              EQ     0.5S
0088 C.        ¿¿[HK1_S_TLM_BIT_RATE]           EQ     32K
0089 C.        ¿¿[HK1_X_TLM_BIT_RATE]           EQ      4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOAD¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿¿
```



```
0096 C.                0300;çSET0EDUMP01A±0iYÑY¹aÇ¹0a|a³0E;E
0097 C.
0098 . C.  TIY³YFÿ0ÿEÐ0ðÁDÍ¸(UT)
0099 +. TI 2015-03-26 09:55:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                    EQ     1COUNTUP
0102 C.
0103 +. TI 2015-03-26 09:55:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                    EQ     1COUNTUP
0106 C.
0107 +. TI 2015-03-26 09:55:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                    EQ     1COUNTUP
0110 C.
0111 +. TI 2015-03-26 09:59:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                    EQ     1COUNTUP
0114 C.
0115 C.  °E²¼aİÄè%íÍÑ0İYÁY§YÁY¹àìÛ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]                EQ     ENA
0117 C.                çç[HK1_TI_CMD_NUM]                   EQ     4
0118 C.                çç[HK1_NEXT_EXEC_PIM]                 EQ     DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]                  EQ     0xB3
0120 C.
0121 . C.  *****
0122 C.  TIİİ°èYÀYÓY×
0123 C.  *****
0124 C.
0125 C.  TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]                EQ     07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]                EQ     2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]                 EQ     3
0131 C.                çç[HK1_DMP_REPEAT_NUM]                EQ     0
0132 C.                çç[HK1_DMA_DMP_PIM]                   EQ     DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                   EQ     7
0136 C.                çç[HK1_PKT_GEN_TIME]                   EQ     0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]                 EQ     32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]                 EQ     4M
0139 C.                çç[HK1_DMP_CHK_FLG]                    EQ     EXEC
0140 C.
0141 . C.  YÀYÓY×¼¹İ»0ð³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]                    EQ     NON
0143 C.
0144 . C.  RAM ID=TI_TBL0İE¹Ç•è²İOK0ð³İÇ§
0145 C.
0146 . C.  DHUÿâ;¼YÉ;È¼Y½, Yì;¼YÈ;È0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                   EQ     2
0150 C.                çç[HK1_PKT_GEN_TIME]                   EQ     0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]                 EQ     32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]                 EQ     4M
0153 C.
0154 C.  *****
0155 C.  SOT TI command set
0156 C.  *****
0157 C.  Execute, after the success of OP upload.
0158 +. TI 2015-03-26 09:59:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 C.  -----
0162 C.  HK1_TI_CMD_NUM           = 1 CNTUP [ ]
0163 C.  -----
0164 C.  ***** SOT END *****
0165 . C.  Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C.  ***** Start EIS operation (TI set) *****
0169 C.  Execute, after the success of OP upload.
0170 C.  Set EIS TI-commands
0171 +. TI 2015-03-26 09:59:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2015-03-26 09:59:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 . C.                [ ] [HK1_TI_CMD_NUM]                EQ     2 COUNTUP
0178 C.  ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C.  ***** XRT START *****
0183 C.  Execute, after the success of OP upload.
0184 +. TI 2015-03-26 09:59:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]                EQ     1COUNTUP
0188 C.
0189 C.  ***** XRT END *****
0190 C.
0191 C.  ***** MDP ´úĂİ0İ»0¼Y0ÈÄ00¹0EDCBC•×²è *****
0192 C.  (¼0İYÓYÀYÈY0YÙYÀYçYè0È¼00¼Ä»Ü0¹0è)
0193 . S. DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCS_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS_DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCS_ORB_UPD
0128 . C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCS_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCS_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 7810119.4 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 58s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2015-03-26 09:59:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP `úÄîî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0165 C. (¼á°îÝÓÝÄÝËÝÞÝËÝ¸Ý¸ÝèñË¼ñ¼Ä»Û¹ñè)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝÐÝ¹•İ Daily±;İÑñË´Ø¹ñèDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÄÝSÝÄÝ¹¼Ä»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-264:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-286:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¥ç¥Ã¥×¥í;¼¥É°âÊSTS_CHKðOFFðËð¹ðë
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHKðONðËð¹ðë
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700[ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2015-03-26 09:59:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 . C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** XRT START *****
0174 C.
0175 +. DC 07-F0 MDP_XRT_CTRL_MANU
0176 BC (c1)
0177 + DC 07-F0 MDP_XRT_MODE_STBY
0178 BC (c3)
0179 . C. ----- Success Verify ? OK / NG____
0180 C.
0181 C. XRT Obs. Table Upload
0182 . S. RAM ram-291:MDP_OBS_X
0183 ( )
0184 C.
0185 +. DC 07-F0 MDP_DUMP_XRTTBL
0186 BC (84 07 00 00 00 3a d4)
0187 . C. ----- Comparison Check ? OK / ERR ____
0188 C.
0189 C.
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 01 b1 b1 04 04)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 02 b1 b1 08 08)

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```
0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 03 b1 b1 08 08)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 04 b1 b1 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 05 85 83 06 06)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 06 85 83 08 08)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 07 85 83 06 06)
0204 + DC 07-F0 MDP_XRT_ROI_SET
0205 BC (cd 08 80 80 20 20)
0206 + DC 07-F0 MDP_XRT_ROI_SET
0207 BC (cd 09 80 80 20 08)
0208 + DC 07-F0 MDP_XRT_ROI_SET
0209 BC (cd 0a 80 80 08 20)
0210 + DC 07-F0 MDP_XRT_ROI_SET
0211 BC (cd 0f 80 80 06 06)
0212 + DC 07-F0 MDP_XRT_ROI_SET
0213 BC (cd 10 80 80 08 08)
0214 + DC 07-F0 MDP_XRT_FLD_ENA
0215 BC (d8)
0216 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0217 BC (c8)
0218 + DC 07-F0 MDP_XRT_AEC_RESET
0219 BC (d0)
0220 + DC 07-F0 MDP_XRT_ARS_DIS
0221 BC (d5)
0222 + DC 07-F0 MDP_XRT_FLD_RESET
0223 BC (da)
0224 + DC 07-F0 MDP_XRT_QT_PROG_SET
0225 BC (c4 0e)
0226 + DC 07-F0 MDP_XRT_FL_PROG_SET
0227 BC (c5 13)
0228 . C. ----- Success Verify ? OK / NG ____
0229 C.
0230 C.
0231 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0232 C.
0233 + DC 07-F0 MDP_XRT_MODE_OBSV
0234 BC (c2)
0235 + TI 2015-03-26 09:59:02.0
0236 DC 07-F0 MDP_XRT_MODE_OBSV
0237 BC (c2)
0238 . C. ----- Success Verify ? OK / NG ____
0239 C.
0240 C. ***** XRT END *****
0241 C.
0242 . C. ***** MDP `úÃîñî»ö%ÝñÊÃðñ¹ñèDCBC•x²è *****
0243 C. (%ã°îÿÓÿÃÿÈÿPÿËÿáÿçÿèñÈ%¼ññ¼Ã»Ûñ¹ñè)
0244 . S. DC-BC dcbc-402:DCBC
0245 (MDP_known_event)
0246 C.
0247 C.
0248 . C. ***** ÿDÿ¹•Ï Daily±¿ÎññÈ´Øñ¹ñèDCBC•x²è *****
0249 . S. DC-BC dcbc-153:DCBC
0250 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0251 C.
0252 C.
0253 . C. ¡ãLOSÿÃÿ§ÿËÿÿ¼Ã»Ûñ¹ñè
0254 C.
0255 . C. ***** LOS *****
0256 C.
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Mar 26, 15 12:49

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*** OP Sequence for XRT ***

2015/03/26	10:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00	ff 1b 55 81	
2015/03/26	10:11:00.5	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	10:11:02.5	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	10:11:04.5	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	10:11:06.5	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/26	10:14:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/26	10:23:00.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	10:23:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	10:23:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/03/26	10:23:24.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	10:23:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/26	10:23:28.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	10:23:30.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	10:23:32.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	10:26:02.0	XRT_QT_PROG_SET_442_OG [0x1ba]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2015/03/26	10:26:04.0	XRT_FL_PROG_SET_401_OG [0x191]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/03/26	10:26:06.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/26	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	13:29:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	13:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/03/26	13:30:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 ed 27 52 d9		
2015/03/26	13:30:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	13:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/26	13:30:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	13:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	13:30:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	13:32:56.0	XRT_QT_PROG_SET_406_OG [0x196]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2015/03/26	13:32:58.0	XRT_FL_PROG_SET_401_OG [0x191]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/03/26	13:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/26	14:02:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	14:02:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	14:02:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	14:02:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/26	14:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/26	14:05:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	14:05:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	14:05:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/03/26	14:06:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 e5 82 41 b2		
2015/03/26	14:06:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	14:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/26	14:06:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	14:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	14:06:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	14:08:56.0	XRT_QT_PROG_SET_406_OG [0x196]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2015/03/26	14:08:58.0	XRT_FL_PROG_SET_401_OG [0x191]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/03/26	14:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]					

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2015/03/26	14:20:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/26	14:20:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/26	14:20:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/03/26	14:21:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	00 e3 a6 31 3f
2015/03/26	14:21:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/26	14:21:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/26	14:21:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/26	14:21:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/26	14:21:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/26	14:23:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2015/03/26	14:23:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/03/26	14:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/26	14:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/26	14:35:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/26	14:35:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/03/26	14:36:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			AOCU_NM	5	02-76	00 e2 98 20 8b
2015/03/26	14:36:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/26	14:36:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/26	14:36:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/26	14:36:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/26	14:36:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/26	14:38:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2015/03/26	14:38:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/03/26	14:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/26	14:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/26	14:50:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/26	14:50:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/03/26	14:51:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			AOCU_NM	5	02-76	00 e1 f4 0f 8d
2015/03/26	14:51:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/26	14:51:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/26	14:51:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/26	14:51:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/26	14:51:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/26	14:53:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2015/03/26	14:53:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/03/26	14:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/26	15:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/26	15:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/26	15:05:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/03/26	15:06:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			AOCU_NM	5	02-76	00 e1 dc fe 25
2015/03/26	15:06:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
			MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/26	15:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/26	15:06:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/26	15:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/26	15:06:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/26	15:08:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10

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2015/03/26	15:08:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/26	15:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	15:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	15:33:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	15:33:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	15:33:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/26	15:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/26	15:57:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	15:57:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	15:57:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/03/26	15:57:30.0	AOCS_Ore-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	e1	f4	f3	a6
2015/03/26	15:57:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/26	15:57:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/26	15:57:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/26	15:57:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	15:57:56.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	16:00:26.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2015/03/26	16:00:28.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/26	16:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	16:12:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	16:12:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	16:12:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/03/26	16:12:30.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	e2	98	e2	a8
2015/03/26	16:12:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/26	16:12:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/26	16:12:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/26	16:12:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	16:12:56.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	16:15:26.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2015/03/26	16:15:28.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/26	16:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	16:27:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	16:27:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	16:27:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/03/26	16:27:30.0	AOCS_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	e3	a6	d1	f4
2015/03/26	16:27:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/26	16:27:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/26	16:27:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/26	16:27:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	16:27:56.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	16:30:26.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2015/03/26	16:30:28.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/26	16:30:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	16:42:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	16:42:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	16:42:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/03/26	16:42:30.0	AOCS_Ore-point_Start_11_OG [0x0a1]							

2015/03/26	16:42:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	e5	82	c1	82
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/26	16:42:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/26	16:42:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/26	16:42:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	16:42:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	16:45:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2015/03/26	16:45:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/26	16:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	17:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	17:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	17:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	17:09:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/26	17:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/26	17:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	17:33:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	17:33:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/03/26	17:34:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	ed	27	b0	5b
2015/03/26	17:34:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/26	17:34:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/26	17:34:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/26	17:34:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	17:34:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	17:36:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2015/03/26	17:36:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/26	17:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	18:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/03/26	18:05:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	00	00	00	00
2015/03/26	18:05:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/03/26	18:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/03/26	18:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	18:07:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2015/03/26	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	18:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	18:14:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/03/26	18:15:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	01	ca	51	26
2015/03/26	18:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/26	18:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/26	18:15:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/26	18:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	18:15:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	18:17:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2015/03/26	18:17:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/26	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2015/03/26	18:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	18:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	18:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	18:46:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/26	18:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/26	19:10:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	19:10:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	19:10:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/03/26	19:11:00.5	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 01 ca 3f 74		
2015/03/26	19:11:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	19:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/26	19:11:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	19:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	19:11:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	19:13:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2015/03/26	19:13:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/03/26	19:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/26	19:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	19:25:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	19:25:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/03/26	19:26:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 01 ca 2d ca		
2015/03/26	19:26:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	19:26:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/26	19:26:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	19:26:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	19:26:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	19:28:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2015/03/26	19:28:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/03/26	19:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/26	19:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	19:40:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	19:40:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/03/26	19:41:00.0	AOCS_OrE-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 01 ca 1c 18		
2015/03/26	19:41:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	19:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/26	19:41:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	19:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	19:41:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	19:43:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2015/03/26	19:43:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/03/26	19:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/26	19:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	19:55:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	19:55:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/03/26	19:56:00.5	AOCS_OrE-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 01 ca 0a 66		
2015/03/26	19:56:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	19:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	d8		

2015/03/26	19:56:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	19:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	19:56:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	19:58:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10	
2015/03/26	19:58:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13	
2015/03/26	19:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/26	20:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	20:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	20:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	20:24:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/26	20:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/26	20:47:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	20:47:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	20:47:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2015/03/26	20:48:00.0	AOCS_Ore-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	01	ca f8 b5
2015/03/26	20:48:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	20:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/26	20:48:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	20:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	20:48:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	20:50:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10	
2015/03/26	20:50:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13	
2015/03/26	20:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/26	21:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	21:02:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	21:02:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2015/03/26	21:03:00.0	AOCS_Ore-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00	01	ca e7 03
2015/03/26	21:03:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	21:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/26	21:03:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	21:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	21:03:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	21:05:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10	
2015/03/26	21:05:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13	
2015/03/26	21:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/26	21:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	21:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/26	21:17:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2015/03/26	21:18:00.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00	01	ca d5 59
2015/03/26	21:18:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/26	21:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/26	21:18:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/26	21:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/26	21:18:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/26	21:20:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10	
2015/03/26	21:20:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13	

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2015/03/26	21:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	21:32:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	21:32:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	21:32:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/03/26	21:33:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00 01 ca c3 a6				
2015/03/26	21:33:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/26	21:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/26	21:33:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/26	21:33:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	21:33:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	21:35:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2015/03/26	21:35:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/26	21:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	22:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	22:01:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	22:01:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	22:01:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/26	22:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/26	22:23:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	22:23:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	22:23:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/03/26	22:24:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 01 ca b1 f4				
2015/03/26	22:24:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/26	22:24:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/26	22:24:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/26	22:24:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	22:24:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	22:26:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2015/03/26	22:26:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/26	22:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	22:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/03/26	23:00:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/03/26	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/26	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/26	23:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/26	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/26	23:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	23:02:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2015/03/26	23:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/26	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/26	23:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	23:38:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/26	23:38:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/26	23:38:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/26	23:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2015/03/26	23:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/26	23:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/03/27	01:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	01:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	01:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	01:05:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/27	01:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/27	01:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/27	01:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/03/27	02:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	02:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	02:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	02:37:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/27	02:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/27	02:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/27	02:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/03/27	04:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	04:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	04:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	04:06:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/27	04:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/27	04:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/27	04:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/03/27	05:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	05:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	05:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	05:46:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/27	05:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/27	06:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/27	06:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/03/27	06:22:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	06:22:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	06:22:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	06:22:30.0	AOCS_Ore-point_Start_13_OG [0x0a3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/03/27	06:22:48.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCS_Ore-point_Start_13_OG [0x0a3]	5	02-76	00 00 00 00 00		
2015/03/27	06:25:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	AOCU_NM	1	07-F0	d9		
2015/03/27	06:25:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/03/27	06:25:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/03/27	06:25:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/27	06:32:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2015/03/27	06:32:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	06:32:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	06:32:30.0	AOCS_Ore-point_Start_25_OG [0x0af]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	06:32:48.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/03/27	06:32:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCS_Ore-point_Start_25_OG [0x0af]	5	02-76	04 00 00 00 00		
2015/03/27	06:32:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/27	06:32:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/27	06:32:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/27	06:35:26.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_FLD_RESET	1	07-F0	da		

2015/03/27	06:35:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	13	
2015/03/27	06:35:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	07:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	07:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	07:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/27	07:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/27	07:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/27	07:50:00.0	XRT_Custom_430_OG [0x1ae]						
2015/03/27	07:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	09:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	09:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	09:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/27	09:06:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/27	09:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/27	09:25:30.0	XRT_Custom_430_OG [0x1ae]						
2015/03/27	09:26:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	10:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	10:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	10:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/27	10:49:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/03/27	10:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/03/27	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	10:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2015/03/27	11:00:00.0	AOCS_Ore-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	02	00 00 00 00	
2015/03/27	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/27	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/27	11:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/27	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/27	11:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/27	11:02:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10	
2015/03/27	11:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13	
2015/03/27	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	13:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	13:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2015/03/27	13:30:00.0	AOCS_Ore-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	04	00 00 00 00	
2015/03/27	13:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/03/27	13:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/03/27	13:30:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/03/27	13:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/03/27	13:30:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/03/27	13:32:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2015/03/27	13:32:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13	
2015/03/27	13:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/03/27	14:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/03/27	14:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2015/03/27	14:34:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/03/27	14:34:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/03/27	14:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/03/27	14:44:30.0	XRT_Custom_430_OG [0x1ae]								
2015/03/27	14:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/03/27	16:08:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	16:08:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	16:08:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/03/27	16:08:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/03/27	16:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/03/27	16:32:00.0	XRT_Custom_430_OG [0x1ae]								
2015/03/27	16:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/03/27	17:45:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	17:45:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	17:45:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/03/27	17:45:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/03/27	17:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/03/27	18:08:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	18:08:26.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	18:08:28.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2015/03/27	18:08:30.5	AOCS_Orе-point_Start_13_OG [0x0a3]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2015/03/27	18:08:48.0	XRT_FLD_DIS_422_OG [0x1a6]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2015/03/27	18:11:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2015/03/27	18:11:26.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/03/27	18:11:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03					
2015/03/27	18:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/03/27	18:18:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	18:18:26.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	18:18:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2015/03/27	18:18:30.0	AOCS_Orе-point_Start_25_OG [0x0af]								
		AOCU_NM	5	02-76	04 00 00 00 00					
2015/03/27	18:18:48.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2015/03/27	18:18:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2015/03/27	18:18:52.0	XRT_AEC_RESET_413_OG [0x19d]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2015/03/27	18:18:54.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/03/27	18:18:56.0	XRT_FLD_RESET_407_OG [0x197]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/03/27	18:21:26.0	XRT_QT_PROG_SET_418_OG [0x1a2]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b					
2015/03/27	18:21:28.0	XRT_FL_PROG_SET_401_OG [0x191]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13					
2015/03/27	18:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/03/27	19:22:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	19:22:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	19:22:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/03/27	19:22:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/03/27	19:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/03/27	19:45:30.0	XRT_Custom_430_OG [0x1ae]								
2015/03/27	19:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/03/27	20:59:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	20:59:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/03/27	20:59:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					

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2015/03/27	20:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/03/27	21:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/03/27	21:22:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/27	21:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/03/27	22:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/27	22:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/27	22:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/27	22:37:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/03/27	22:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/03/27	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/27	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/27	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/03/27	23:00:00.0	AOCS_Ore-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00 10 00 54 5a			
2015/03/27	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/03/27	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/03/27	23:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/03/27	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/03/27	23:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/27	23:02:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06			
2015/03/27	23:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13			
2015/03/27	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/03/28	00:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/28	00:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/28	00:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/28	00:14:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/03/28	00:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/03/28	00:21:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/28	00:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/03/28	01:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/28	01:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/28	01:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/28	01:37:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/03/28	01:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/03/28	01:56:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/28	01:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/03/28	03:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/28	03:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/28	03:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/03/28	03:13:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/03/28	03:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/03/28	03:30:00.0	AOCS_Ore-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	04 00 00 00 00			
2015/03/28	03:34:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/28	03:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/03/28	03:34:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/03/28	03:34:24.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/03/28	03:34:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/03/28	03:34:28.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/03/28	03:34:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			

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2015/03/28	03:34:32.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/28	03:37:02.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2015/03/28	03:37:04.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/28	03:37:06.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/28	04:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	04:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	04:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/28	04:43:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/28	04:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/28	05:11:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/28	05:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/28	05:55:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	05:55:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	05:55:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/03/28	05:55:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/03/28	05:55:48.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/03/28	05:58:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/03/28	05:58:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/28	05:58:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/03/28	05:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/28	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	06:05:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	06:05:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/28	06:05:30.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/03/28	06:05:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/28	06:05:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/28	06:05:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/28	06:05:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/28	06:05:56.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/28	06:08:26.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2015/03/28	06:08:28.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/28	06:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/28	06:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	06:23:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	06:23:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/28	06:23:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/28	06:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/28	06:48:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/28	06:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/28	08:03:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	08:03:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	08:03:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/28	08:03:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/28	08:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/28	08:25:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/28	08:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/28	09:28:00.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/28	09:28:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2015/03/28 09:56:00.0 AOCs_OrE-point_Start_13_OG [0x0a3]
AOCU_NM 5 02-76 00 00 00 00 00