XRT Timeline to be uploaded on 2015/03/28

Period: 2015/03/28 09:46:00 - 2015/04/02 09:18:00

* * * * * Normal mode * * * * *

XOB #1A72: HOP 177 - Al/Poly, Ti/Poly with FOV 512x512 at 1064 1048 with AEC 2, With G-band (33ms/45ms leak) and Dark (16sec), 3min cad

Term	Pointing (x, y)	Comment
03/28 10:03:00 - 03/29 05:59:54	Track (-45.3, -680.0) @ 03/28 09:56:00	# OP start + 10min HOP 177 Coronal Hole
03/29 06:17:00 - 03/30 05:28:54	Track (54.6, -680.4) @ 03/29 06:10:00	# HOP 177 Coronal Hole
03/30 05:46:00 - 03/31 06:03:24	Track (168.4, -683.2) @ 03/30 05:39:00	# HOP 177 Coronal Hole
03/31 06:20:30 - 03/31 10:26:54	Track (280.5, -688.7) @ 03/31 06:13:30	# HOP 177 Coronal Hole

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
PROG= 02 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 20 2-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0 2.0sec
Seqn= 75 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0 2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 98 20-time(s) 180.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0 2.0sec

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi

Term	Pointing (x, y)	Comment
03/29 06:03:00 - 03/29 06:09:54	Fixed (0.0, 0.0)	synoptic
03/30 05:32:00 - 03/30 05:38:54	Fixed (0.0, 0.0)	synoptic, shifted -31.0 min
03/31 06:06:30 - 03/31 06:13:24	Fixed (0.0, 0.0)	synoptic, shifted 3.5 min

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
PROG= 03 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 33 1-time(s) 4.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Seqn= 5 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0 2.0sec
Seqn= 40 1-time(s) 4.0sec												
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Seqn= 77 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Seqn= 6 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec

* * * * * Flare mode * * * * *

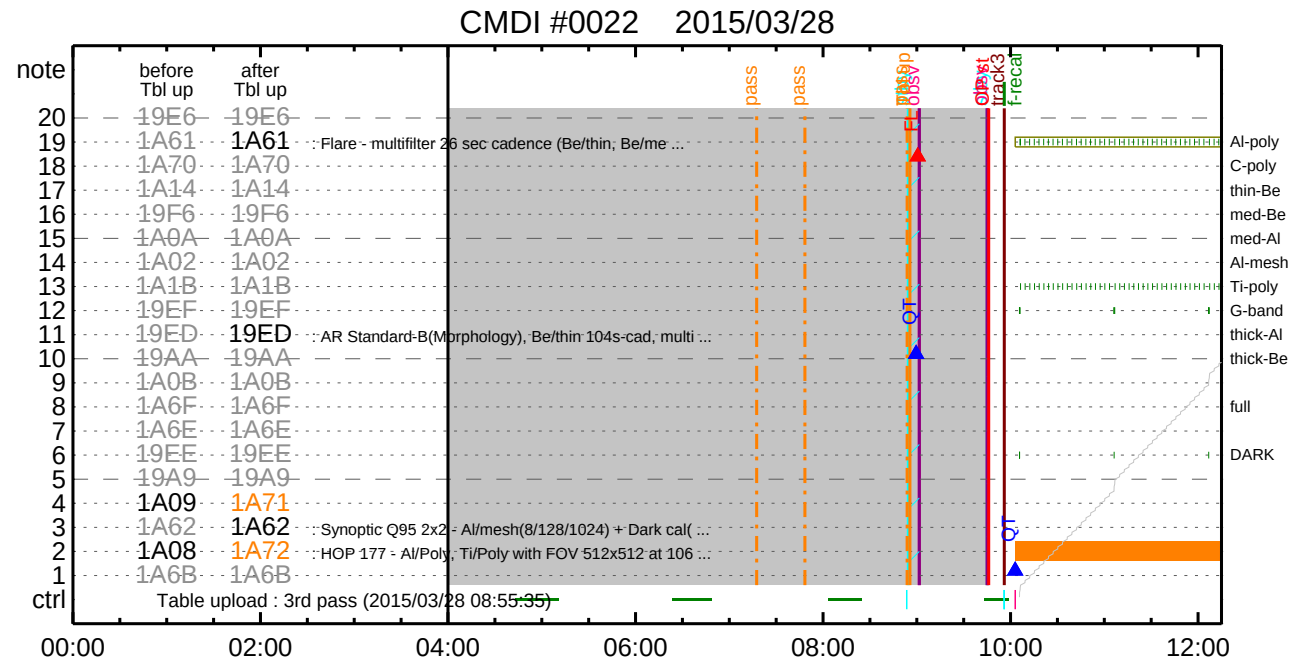
NOT USED

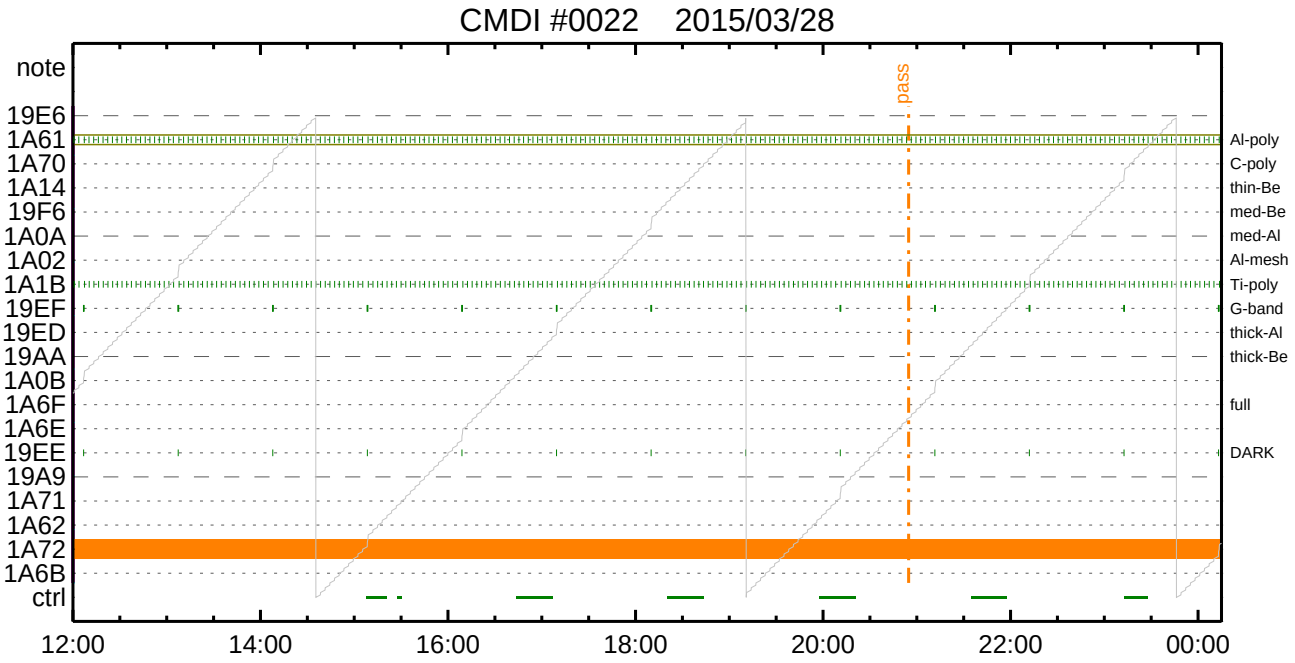
* * * * * Active Region Search * * * * *

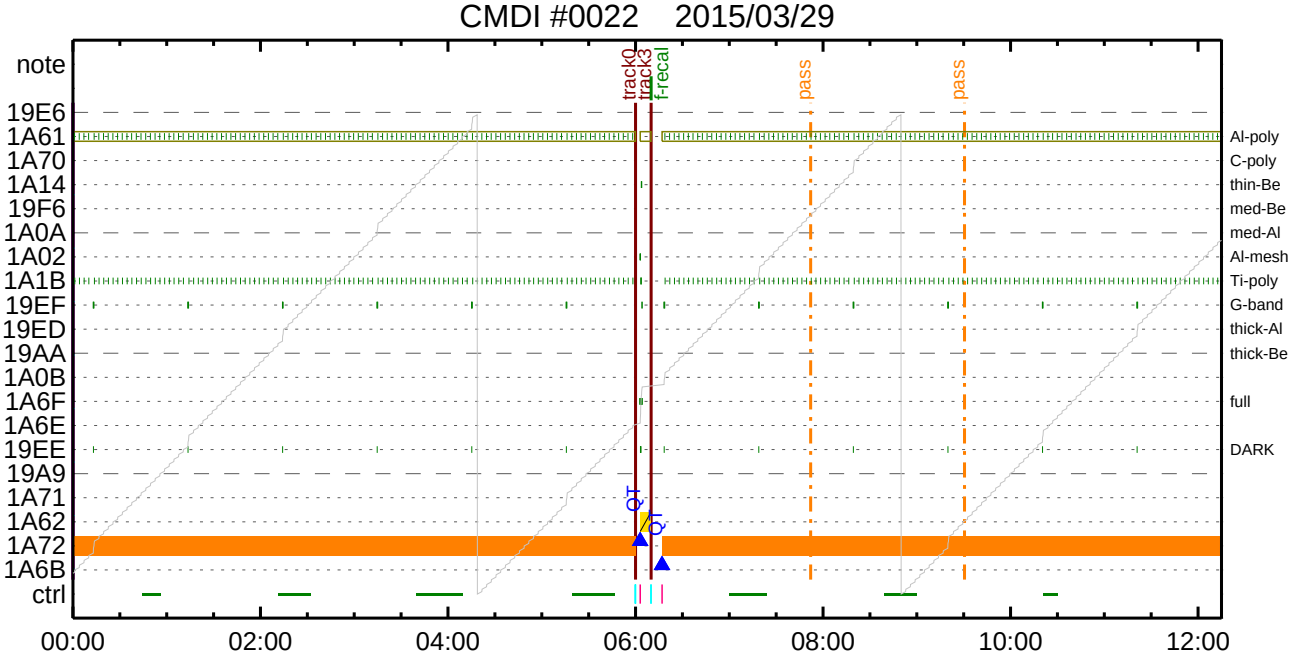
NOT USED

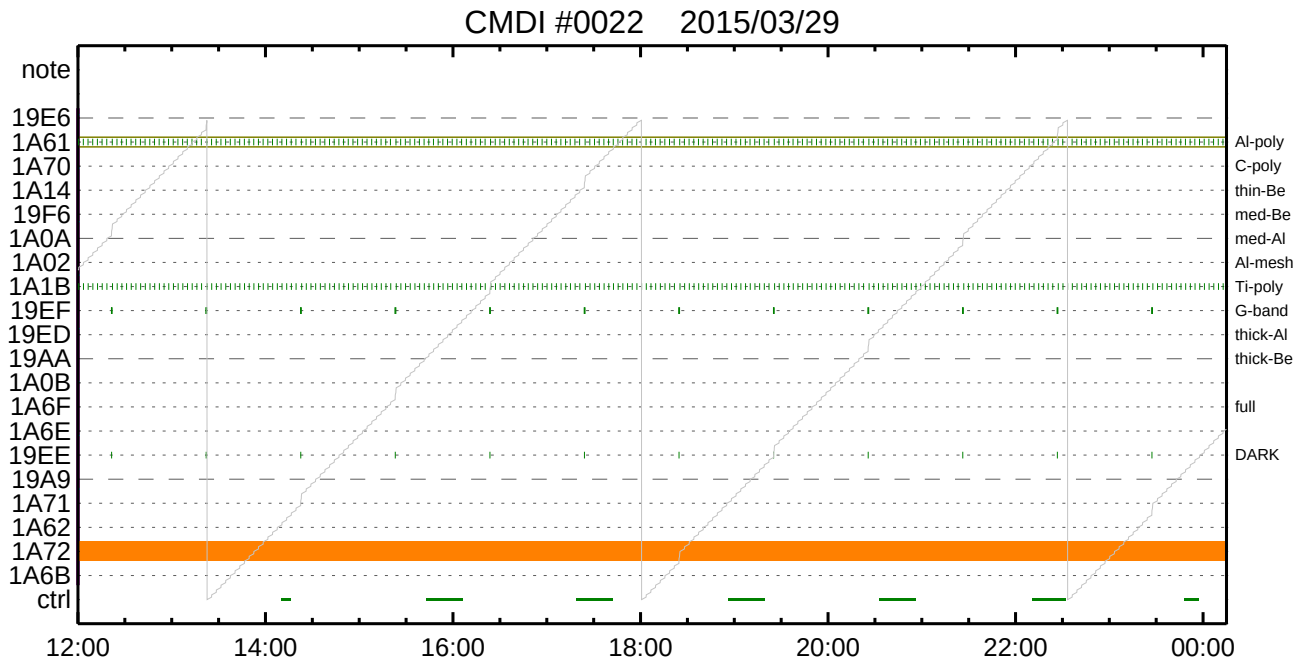
* * * * * Flare Detection * * * * *

NOT USED


CMDI #0022 2015/03/28
note
before Tbl up
after Tbl up
20 19E6 19E6
19 1A61 1A61 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
18 1A70 1A70
17 1A14 1A14
16 19F6 19F6
15 1A0A 1A0A
14 1A02 1A02
13 1A1B 1A1B
12 19EF 19EF
11 19ED 19ED : AR Standard-B(Morphology), Be/thin 104s-cad, multi ...
10 19AA 19AA
9 1A0B 1A0B
8 1A6F 1A6F
7 1A6E 1A6E
6 19EE 19EE
5 19A9 19A9
4 1A09 1A71
3 1A62 1A62 : Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(...
2 1A08 1A72 : HOP 177 - Al/Poly, Ti/Poly with FOV 512x512 at 106 ...
1 1A6B 1A6B
ctrl Table upload : 3rd pass (2015/03/28 08:55:35)
pass
pass
QT
QT
track3
f-recal
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0022 2015/03/28
note
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
19ED
19AA
1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0022 2015/03/29
note
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
19ED
19AA
1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
track0
track3
f-recal
pass
pass
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0022 2015/03/29
note
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
19ED
19AA
1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
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20:00
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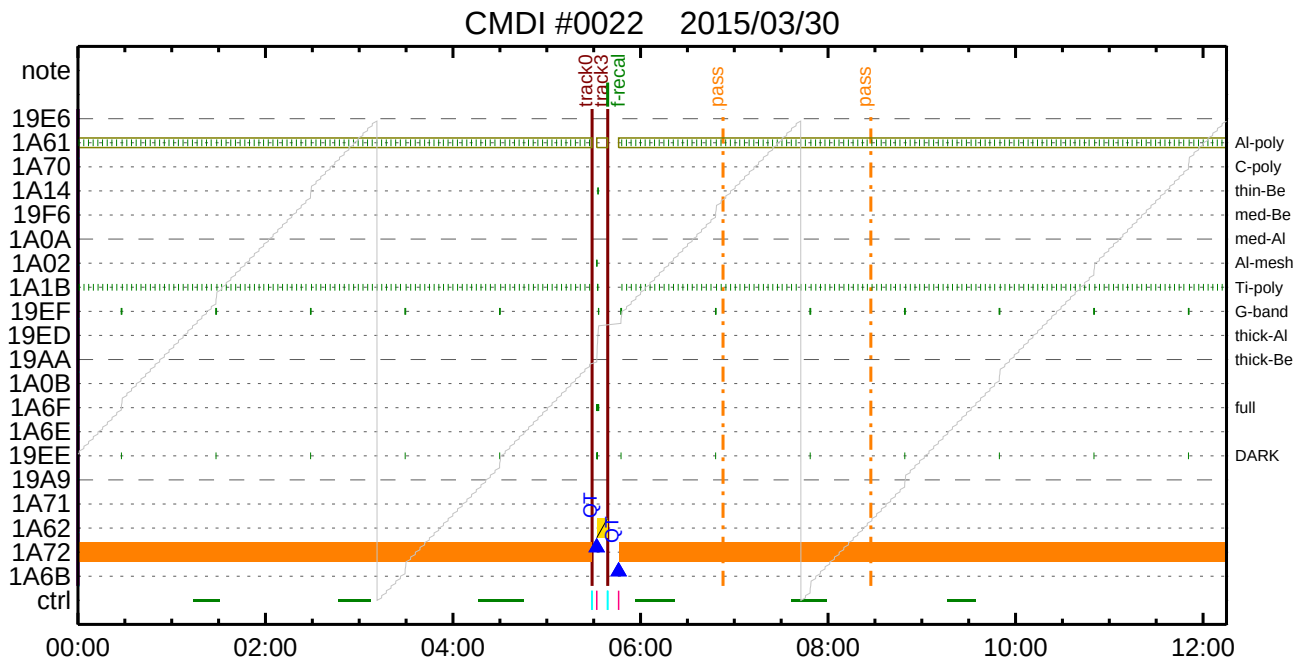
CMDI #0022 2015/03/30
note
track0
track3
f-recal
pass
pass
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
19ED
19AA
1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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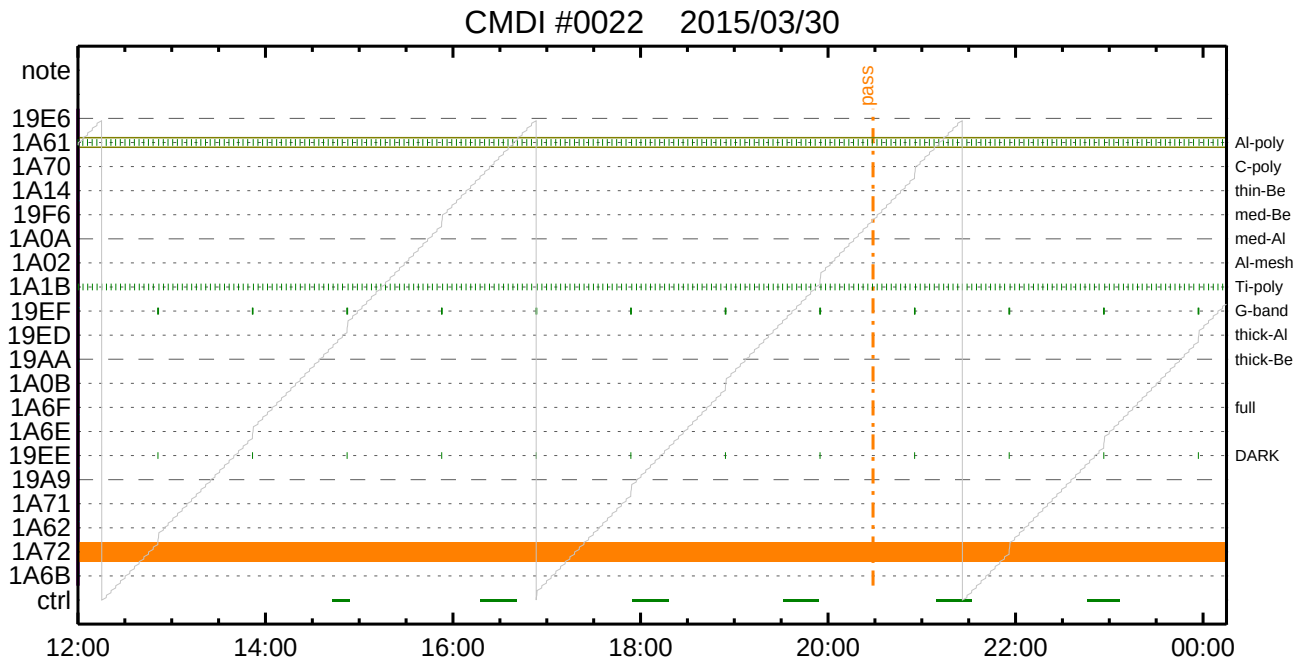
CMDI #0022 2015/03/30
note
pass
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
19ED
19AA
1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
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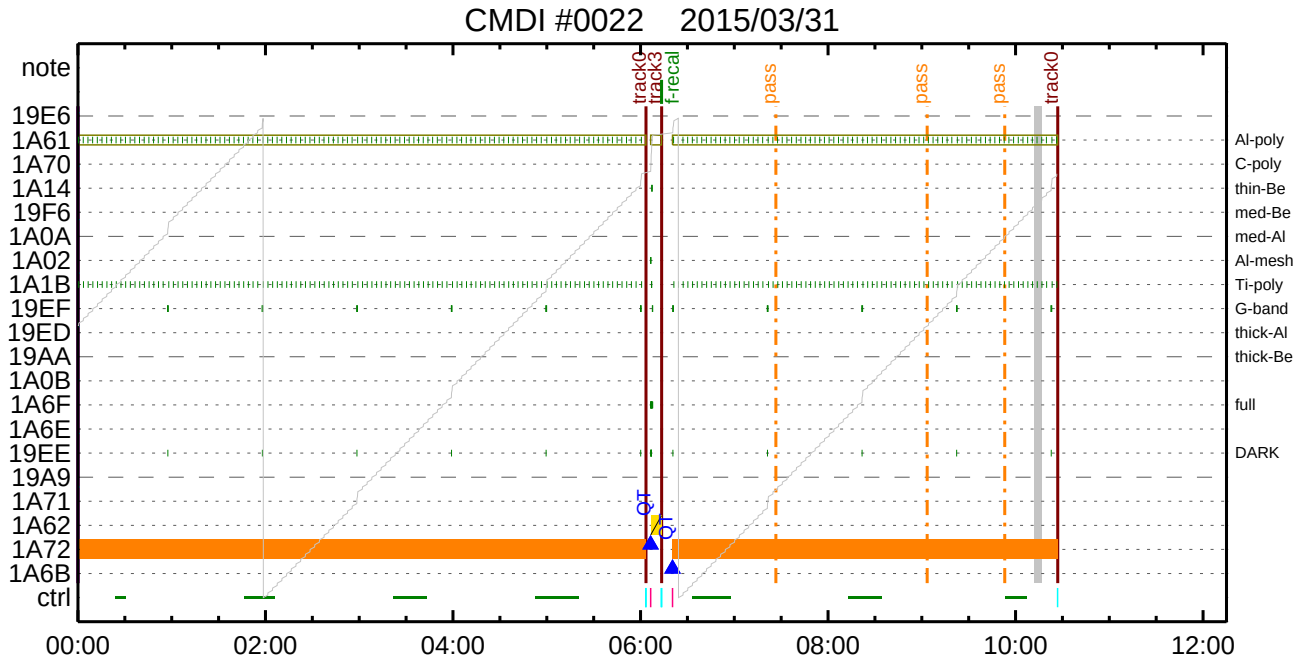
CMDI #0022 2015/03/31
note
19E6
1A61
1A70
1A14
19F6
1A0A
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1A1B
19EF
19ED
19AA
1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
track0
track3
f-recal
pass
pass
pass
track0
OT
OT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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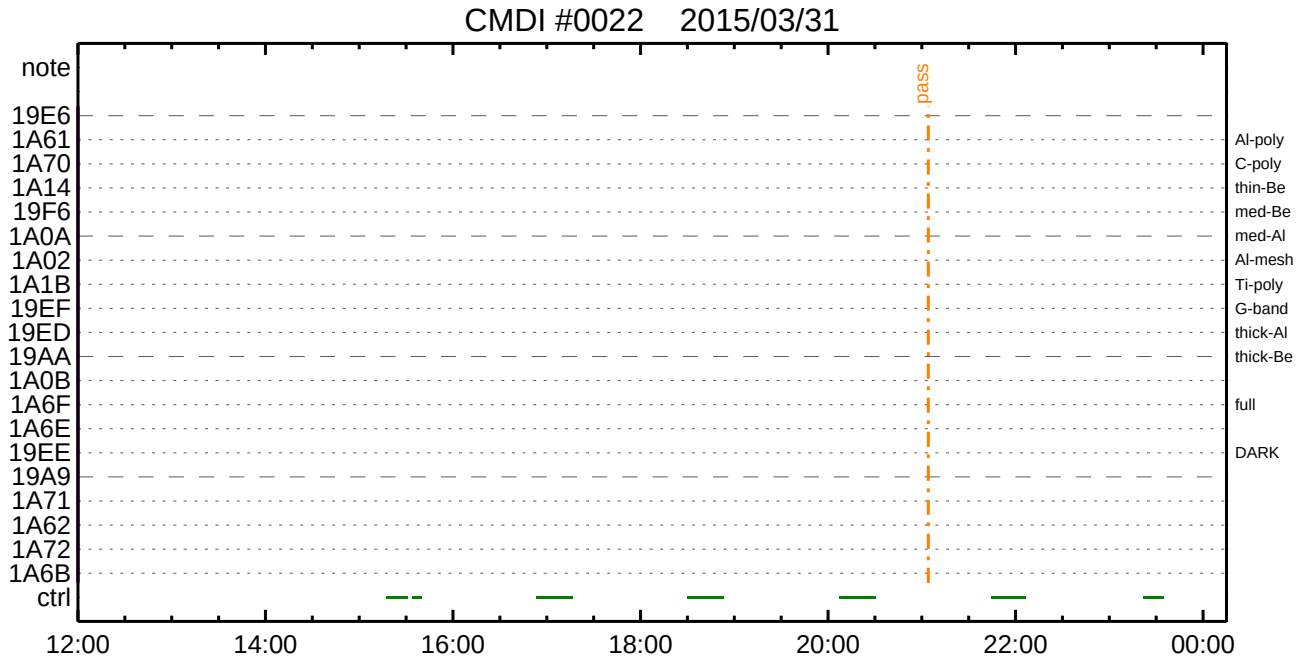
06:00
08:00
10:00
12:00


CMDI #0022 2015/03/31
note
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
19ED
19AA
1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
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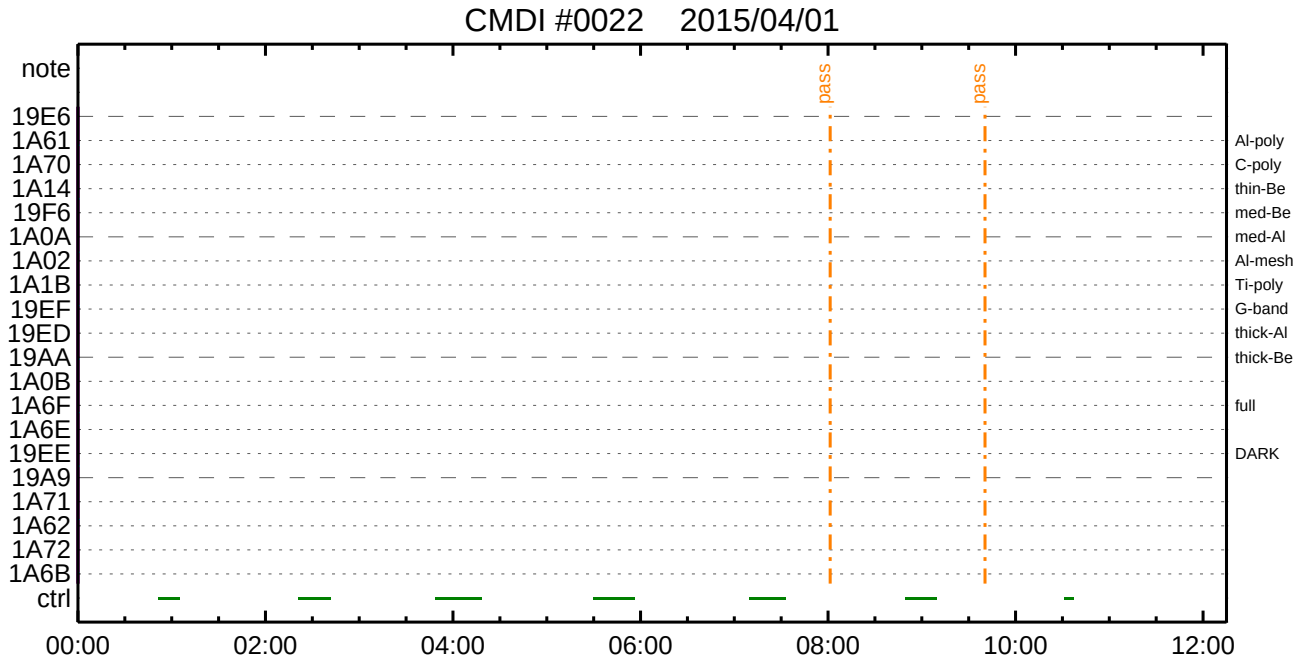
CMDI #0022 2015/04/01
note
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
19ED
19AA
1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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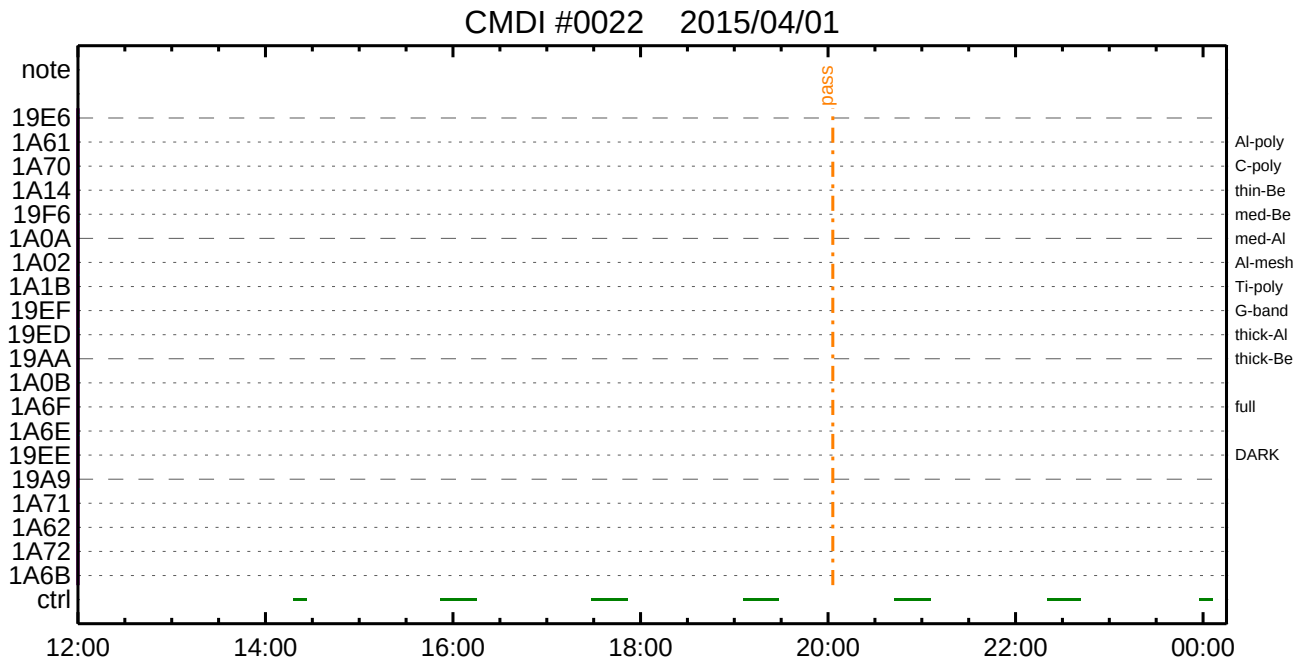
CMDI #0022 2015/04/01
note
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
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1A6E
19EE
19A9
1A71
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1A72
1A6B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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22:00
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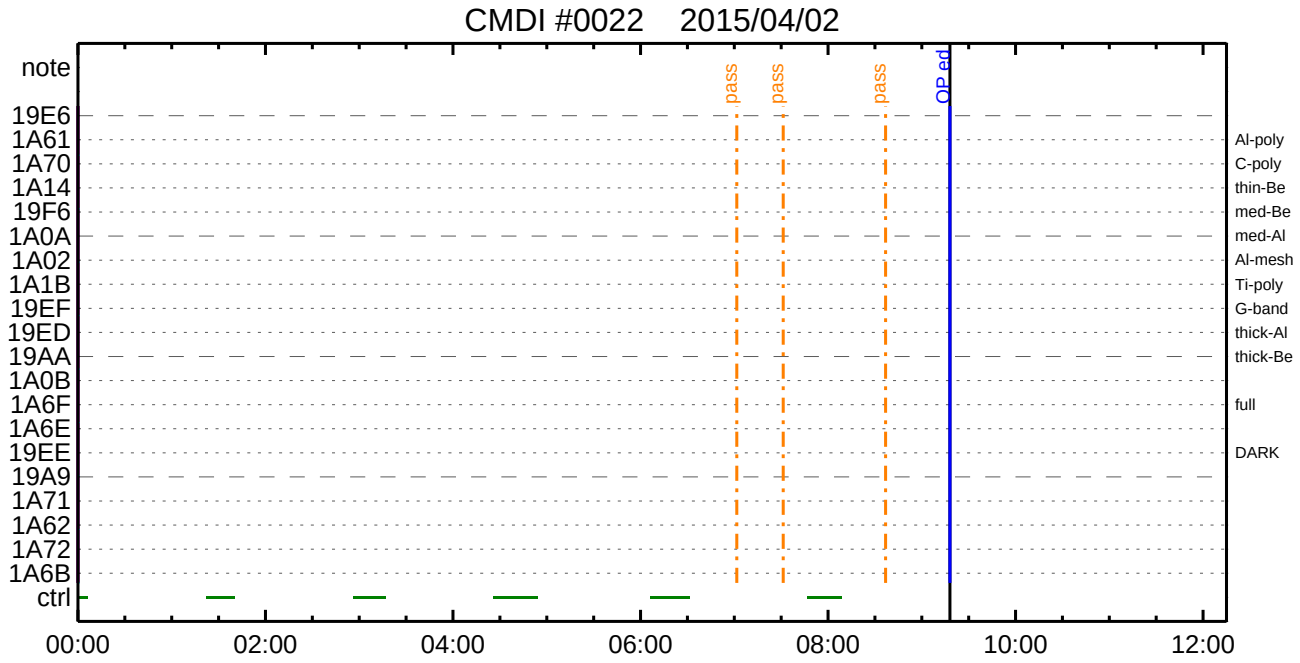
CMDI #0022 2015/04/02
note
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
19ED
19AA
1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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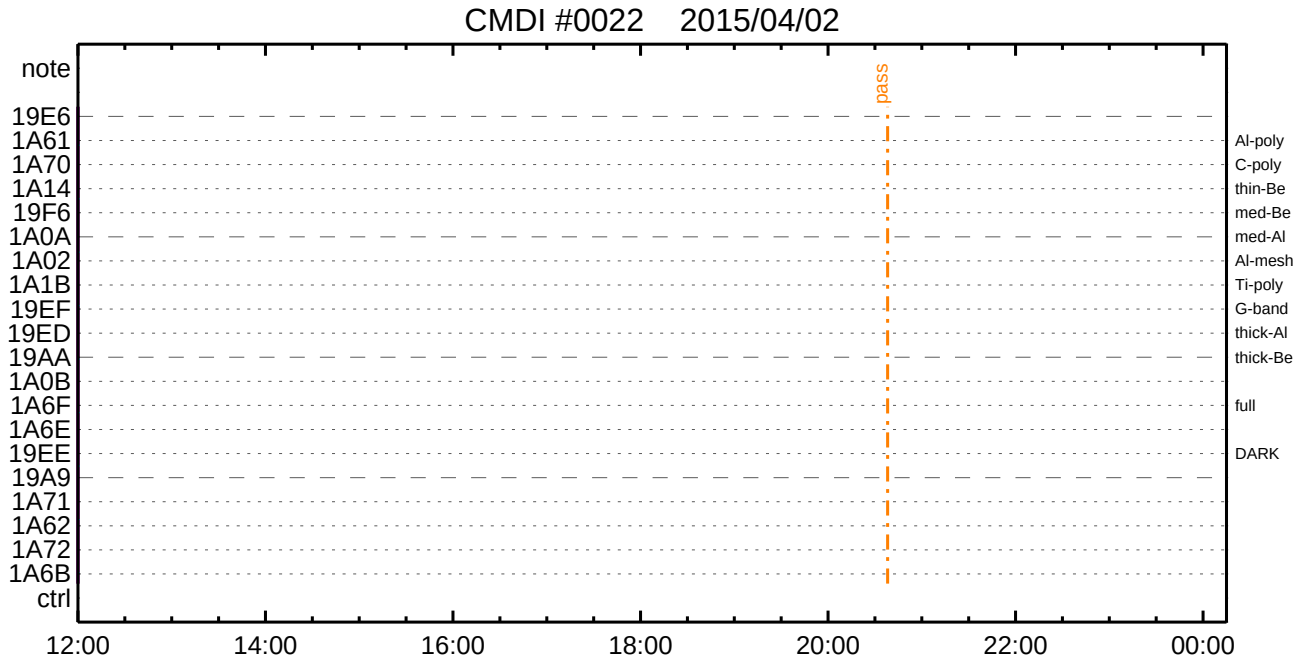
CMDI #0022 2015/04/02
note
19E6
1A61
1A70
1A14
19F6
1A0A
1A02
1A1B
19EF
19ED
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1A0B
1A6F
1A6E
19EE
19A9
1A71
1A62
1A72
1A6B
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
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20:00
22:00
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```
(a) Spacecraft Operation Procedure (real-commands)

main-045 2015-03-28 13:20:01 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-045:OP
0104     ()
0105   . S. OG    og-045:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2015-03-28 09:41:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2015-03-28 09:41:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2015-03-28 09:41:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

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0194     C.
0195   +. TI 2015-03-28 09:45:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2015-03-28 09:45:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2015-03-28 09:45:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2015-03-28 09:45:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2015-03-28 09:45:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-046 2015-03-28 13:20:02 85 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022  +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029  +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038  +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043  +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046  +. DC 07-FC EIS_MODE_CHG_ENA
0047     BC       (20)
0048   . C. Verify EIS_MODE_CHG_FLG is ENA
0049  +. DC 07-FC EIS_MODE_MANU
0050     BC       (21 02)
0051   . C. Verify EIS in MANUAL mode
0052   . C. Estimated OBSTBL upload time is 13s
0053     C. ********************************
0054     C. EIS START OBSTBL LOAD
0055     C. ********************************
0056   . S. RAM   ram-820:EIS_OBSTBL
0057     ()
0058  +. DC 07-FC EIS_DUMP_OBSTBL
0059     BC       (07 07 07 00 00 70 00)
0060     C.
0061     C. Execute, after the success of OBSTBL upload.
0062     C. Set EIS TI-commands
0063  +. TI 2015-03-28 09:45:50.0
0064     DC 07-FC EIS_MODE_CHG_ENA
0065     BC       (20)
0066   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0067     C. ********************************
0068     C. EIS END OBSTBL LOAD
0069     C. ********************************
0070     C.
0071   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0072     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0073   . S. DC-BC dcbc-402:DCBC
0074     (MDP_known_event)
0075     C.
0076     C.
0077   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0078   . S. DC-BC dcbc-153:DCBC
0079     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080     C.
0081     C.
0082   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0083     C.
0084   . C. *********** LOS ***********
0085     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-047 2015-03-28 13:20:02 214 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ********************************
0099     C.  SOT table upload
0100     C. ********************************
0101   . C. < Stop FG table >
0102  +. DC 07-F0 MDP_FG_CTRL_MANU
0103     BC       (51)
0104   . C. ------------------------------------
0105     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0106     C. ------------------------------------
0107     C.
0108   . C. <Upload FG Observation Table>
0109   . S. RAM   ram-265:MDP_OBS_F
0110     ()
0111     C.
0112   . C. < Dump RAMID=MDP_OBS_F >
0113  +. DC 07-F0 MDP_DUMP_FGTBL
0114     BC       (82 07 00 00 00 38 b8)
0115     C. ------------------------------------
0116     C.  MDP_OBS_F verify = OK/NG        [ ]
0117     C. ------------------------------------
0118     C.
0119     C. ********************************
0120     C.  SOT TI command set
0121     C. ********************************
0122     C. Execute, after the success of TBL upload.
0123  +. TI 2015-03-28 09:45:18.0
0124     DC 07-F0 MDP_SOT_MODE_OBSV
0125     BC       (40)
0126    . C. ------------------------------------
0127     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0128     C. ------------------------------------
0129     C.
0130     C.
0131     C. ************ XRT START ************
0132     C.
0133  +. DC 07-F0 MDP_XRT_CTRL_MANU
0134     BC       (c1)
0135  +  DC 07-F0 MDP_XRT_MODE_STBY
0136     BC       (c3)
0137   . C. ----------- Success Verify ?              OK / NG____
0138     C.
0139     C. XRT Obs. Table Upload
0140   . S. RAM   ram-291:MDP_OBS_X
0141     ()
0142     C.
0143  +. DC 07-F0 MDP_DUMP_XRTTBL
0144     BC       (84 07 00 00 00 3a d4)
0145    . C. ----------- Comparison Check ?                        OK / ERR ____
0146     C.
0147     C.
0148  +. DC 07-F0 MDP_XRT_ROI_SET
0149     BC       (cd 01 b1 b1 04 04)
0150  +  DC 07-F0 MDP_XRT_ROI_SET
0151     BC       (cd 02 b1 b1 08 08)
0152  +  DC 07-F0 MDP_XRT_ROI_SET
0153     BC       (cd 03 b1 b1 08 08)
0154  +  DC 07-F0 MDP_XRT_ROI_SET
0155     BC       (cd 04 b1 b1 06 06)
0156  +  DC 07-F0 MDP_XRT_ROI_SET
0157     BC       (cd 05 85 83 06 06)
0158  +  DC 07-F0 MDP_XRT_ROI_SET
0159     BC       (cd 06 85 83 06 06)
0160  +  DC 07-F0 MDP_XRT_ROI_SET
0161     BC       (cd 07 85 83 08 08)
0162  +  DC 07-F0 MDP_XRT_ROI_SET
0163     BC       (cd 08 80 80 20 20)
0164  +  DC 07-F0 MDP_XRT_ROI_SET
0165     BC       (cd 09 80 80 20 08)
0166  +  DC 07-F0 MDP_XRT_ROI_SET
0167     BC       (cd 0a 80 80 08 20)
0168  +  DC 07-F0 MDP_XRT_ROI_SET
0169     BC       (cd 0f 80 80 06 06)
0170  +  DC 07-F0 MDP_XRT_ROI_SET
0171     BC       (cd 10 80 80 08 08)
0172  +  DC 07-F0 MDP_XRT_FLD_ENA
0173     BC       (d8)
0174  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175     BC       (c8)
0176  +  DC 07-F0 MDP_XRT_AEC_RESET
0177     BC       (d0)
0178  +  DC 07-F0 MDP_XRT_ARS_DIS
0179     BC       (d5)
0180  +  DC 07-F0 MDP_XRT_FLD_RESET
0181     BC       (da)
0182  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0183     BC       (c4 0b)
0184  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0185     BC       (c5 13)
0186    . C. ---------- Success Verify ?           OK / NG ____
0187     C.
0188     C.
0189    . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0190     C.
0191  +. DC 07-F0 MDP_XRT_MODE_OBSV
0192     BC       (c2)
0193  +. TI 2015-03-28 09:45:02.0
```

```
0194     DC 07-F0 MDP_XRT_MODE_OBSV
0195     BC       (c2)
0196   . C. ---------- Success Verify ?            OK / NG ____
0197     C.
0198     C. ************ XRT END ***********
0199     C.
0200   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0201     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0202   . S. DC-BC dcbc-402:DCBC
0203     (MDP_known_event)
0204     C.
0205     C.
0206   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0207   . S. DC-BC dcbc-153:DCBC
0208     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209     C.
0210     C.
0211   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0212     C.
0213   . C. *********** LOS ***********
0214     C.
```

```
*** OP Sequence for XRT ***

2015/03/28  09:55:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/28  09:55:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/28  09:55:58.0  XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                                XRT_FOCUS_RECAL      2 07-F8 78 00
2015/03/28  09:56:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2015/03/28  09:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/03/28  10:00:18.0  XRT_FLD_DIS_422_OG [0x1a6]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/03/28  10:02:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/03/28  10:02:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/28  10:02:58.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2015/03/28  10:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/29  05:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/29  05:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/29  05:59:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/03/29  06:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2015/03/29  06:00:18.0  XRT_FLD_DIS_422_OG [0x1a6]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/03/29  06:02:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/03/29  06:02:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/29  06:02:58.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2015/03/29  06:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/29  06:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/29  06:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/29  06:09:58.0  XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                                XRT_FOCUS_RECAL      2 07-F8 78 00
2015/03/29  06:10:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2015/03/29  06:13:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/03/29  06:14:18.0  XRT_FLD_DIS_422_OG [0x1a6]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/03/29  06:16:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/03/29  06:16:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/29  06:16:58.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2015/03/29  06:17:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/30  05:28:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/30  05:28:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/30  05:28:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/03/30  05:29:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2015/03/30  05:29:18.0  XRT_FLD_DIS_422_OG [0x1a6]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/03/30  05:31:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/03/30  05:31:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/30  05:31:58.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2015/03/30  05:32:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/30  05:38:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/30  05:38:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/30  05:38:58.0  XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                                XRT_FOCUS_RECAL      2 07-F8 78 00
2015/03/30  05:39:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2015/03/30  05:42:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/03/30  05:43:18.0  XRT_FLD_DIS_422_OG [0x1a6]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2015/03/30  05:45:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/03/30  05:45:56.0  XRT_ARS_DIS_423_OG [0x1a7]
```

```
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/30  05:45:58.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2015/03/30  05:46:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/31  06:03:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/31  06:03:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/31  06:03:28.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2015/03/31  06:03:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2015/03/31  06:03:48.0  XRT_FLD_DIS_422_OG [0x1a6]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2015/03/31  06:06:24.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/03/31  06:06:26.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/31  06:06:28.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2015/03/31  06:06:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/31  06:13:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/31  06:13:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/31  06:13:28.0  XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                                    XRT_FOCUS_RECAL      2 07-F8 78 00
2015/03/31  06:13:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 03 00 00 00 00
2015/03/31  06:17:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2015/03/31  06:17:48.0  XRT_FLD_DIS_422_OG [0x1a6]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2015/03/31  06:20:24.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2015/03/31  06:20:26.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2015/03/31  06:20:28.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2015/03/31  06:20:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2015/03/31  10:26:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2015/03/31  10:27:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
```