

# XRT Timeline to be uploaded on 2015/04/16

Period: 2015/04/16 10:02:00 - 2015/04/21 10:17:00

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**Normal mode**

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<b>XOB #1A76: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,8ms) - Leak (33ms) - 120s cad</b>													
Term	Pointing (x, y)				Comment								
04/16 10:15:00 - 04/16 17:49:24 Track ( 635.5, -429.8) <sup>04/16 10:12:00</sup> # OP start + 10min, CH boundary observations for EIS & IRIS.													
<b>PROG= 14 Inf.-time(s)</b>													
└─ Subr= 1 30-time(s) 120.0sec													
└─ Seqn= 29 1-time(s) 4.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 6 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

<b>XOB #1A73: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1443) + Thi</b>													
Term	Pointing (x, y)				Comment								
04/16 17:52:30 - 04/16 17:59:24 Fixed ( 0.0, 0.0) synoptic, shifted -10.5 min													
04/17 06:03:00 - 04/17 06:09:54 Fixed ( 0.0, 0.0) synoptic													
04/17 17:57:30 - 04/17 18:04:24 Fixed ( 0.0, 0.0) synoptic, shifted -5.5 min													
04/18 06:29:30 - 04/18 06:36:24 Fixed ( 0.0, 0.0) synoptic, shifted 26.5 min													

<b>PROG= 07 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 33 1-time(s) 4.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec													
└─ Seqn= 34 1-time(s) 4.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 77 1-time(s) 2.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 6 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

<b>XOB #19EE: AR Standard-B(Morphology), Be/thin 48s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33m</b>													
Term	Pointing (x, y)				Comment								
04/16 18:39:30 - 04/17 05:59:54 Track ( -140.8, 245.6) <sup>04/16 17:59:30</sup> # AR 12321 observations. IHOP 245.													

<b>PROG= 06 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec													
└─ Seqn= 24 1-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec													
└─ Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 16 2-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ med-Al/Open Open/thick-Al close Safe Norm 2.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
└─ Seqn= 58 50-time(s) 2.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec													
└─ Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec													
└─ med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec													
└─ Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec													
└─ med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec													

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
04/17 06:13:00 - 04/17 10:15:00	Track ( -30.0, 245.9) @ 04/17 06:10:00	# AR cont.
04/17 10:39:00 - 04/17 12:59:19	Track ( -170.0, 262.0) @ 04/17 10:21:30	SP mosaic #1
04/17 13:02:25 - 04/17 15:18:00	Track ( 109.6, 262.2) @ 04/17 12:59:25	SP mosaic #2
04/17 16:01:30 - 04/17 17:54:24	Track ( 57.6, 245.2) @ 04/17 15:45:00	AR cont.
04/17 18:07:30 - 04/17 22:59:54	Track ( 78.8, 245.0) @ 04/17 18:04:30	# AR cont.
04/18 03:03:30 - 04/18 06:26:24	Track ( 159.7, 243.5) @ 04/18 03:00:00	# Return to AR.
04/18 06:39:30 - 04/18 09:10:30	Track ( 192.1, 242.7) @ 04/18 06:36:30	# AR cont.

PROG= 08 Inf.-time(s)

Subr=	1-time(s)	2.0sec											
Subr= 1	1-time(s)	2.0sec											
Seqn= 8	2-time(s)	2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Subr= 2	2-time(s)	2.0sec											
Seqn= 24	1-time(s)	2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Seqn= 42	4-time(s)	2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Seqn= 62	8-time(s)	270.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A6B: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45ms)

Term	Pointing (x, y)	Comment
04/17 23:03:00 - 04/17 23:30:00	Track ( -18.0, -20.1) @ 04/17 23:00:00	# SOT & IRIS QS granulation program and EIS sensitivity monitoring.

PROG= 01 1-time(s)

Subr=	1-time(s)	12.0sec											
Subr= 1	1-time(s)	12.0sec											
Seqn= 33	1-time(s)	4.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 5	1-time(s)	2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 40	1-time(s)	4.0sec											
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 77	1-time(s)	2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 6	1-time(s)	2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2	1-time(s)	12.0sec											
Seqn= 46	1-time(s)	2.0sec											
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 25	1-time(s)	4.0sec											
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 21	1-time(s)	4.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 37	1-time(s)	2.0sec											
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 25	1-time(s)	4.0sec											
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 21	1-time(s)	4.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 37	1-time(s)	2.0sec											
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	

med-AI/Open	med-AI/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 46 1-time(s) 2.0sec</b>												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-AI,thick-Be -384x384 + AI-poly 512

Term	Pointing (x, y)	Comment
04/16 10:15:00 - 04/16 17:49:24	Track ( 635.5, -429.8) <sup>Ⓢ 04/16 10:12:00</sup>	# OP start + 10min, CH boundary observations for EIS & IRIS.
04/16 18:39:30 - 04/17 05:59:54	Track ( -140.8, 245.6) <sup>Ⓢ 04/16 17:59:30</sup>	# AR 12321 observations. IHOP 245.
04/17 06:13:00 - 04/17 10:15:00	Track ( -30.0, 245.9) <sup>Ⓢ 04/17 06:10:00</sup>	# AR cont.
04/17 10:39:00 - 04/17 12:59:19	Track ( -170.0, 262.0) <sup>Ⓢ 04/17 10:21:30</sup>	SP mosaic #1
04/17 13:02:25 - 04/17 15:18:00	Track ( 109.6, 262.2) <sup>Ⓢ 04/17 12:59:25</sup>	SP mosaic #2
04/17 16:01:30 - 04/17 17:54:24	Track ( 57.6, 245.2) <sup>Ⓢ 04/17 15:45:00</sup>	AR cont.
04/17 18:07:30 - 04/17 22:59:54	Track ( 78.8, 245.0) <sup>Ⓢ 04/17 18:04:30</sup>	# AR cont.
04/18 03:03:30 - 04/18 06:26:24	Track ( 159.7, 243.5) <sup>Ⓢ 04/18 03:00:00</sup>	# Return to AR.
04/18 06:39:30 - 04/18 09:10:30	Track ( 192.1, 242.7) <sup>Ⓢ 04/18 06:36:30</sup>	# AR cont.

#### PROG= 19 30-time(s)

<b>Subr= 1 20-time(s) 2.0sec</b>												
<b>Seqn= 11 1-time(s) 2.0sec</b>												
AI-poly/Open	AI-poly/thick-AI	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn=100 1-time(s) 10.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-AI	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-AI	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 10 1-time(s) 2.0sec</b>												
med-AI/Open	med-AI/thick-AI	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>												
AI-poly/Open	AI-poly/thick-AI	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 15 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

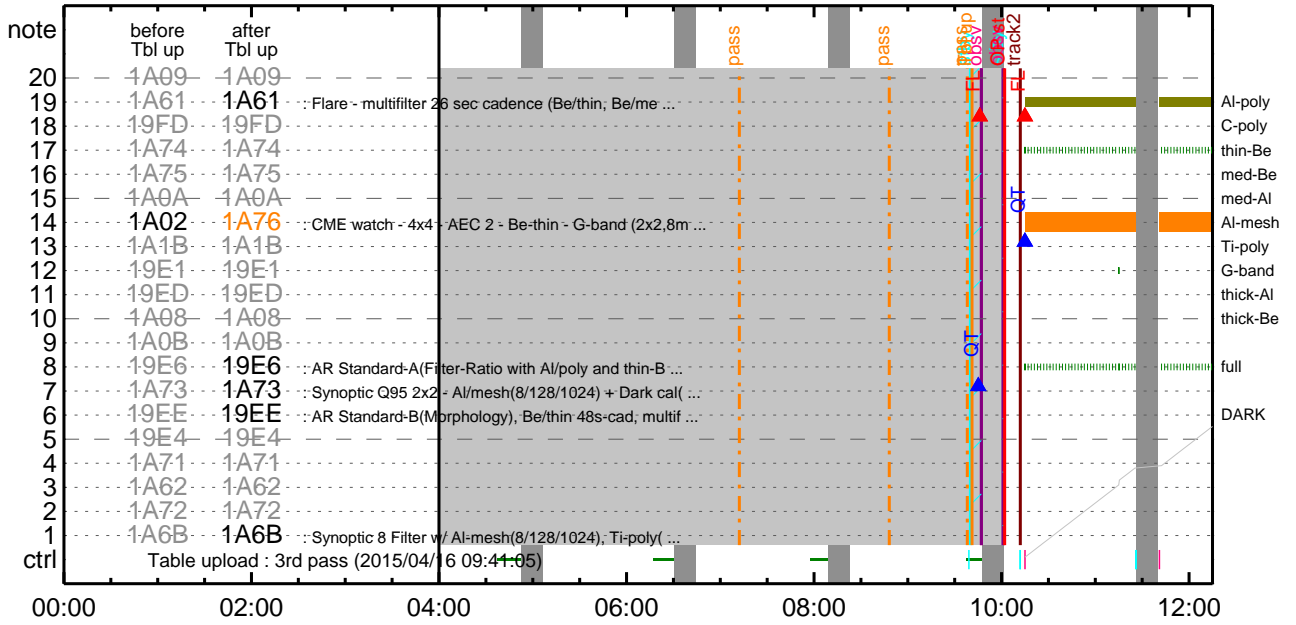
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### Flare Detection

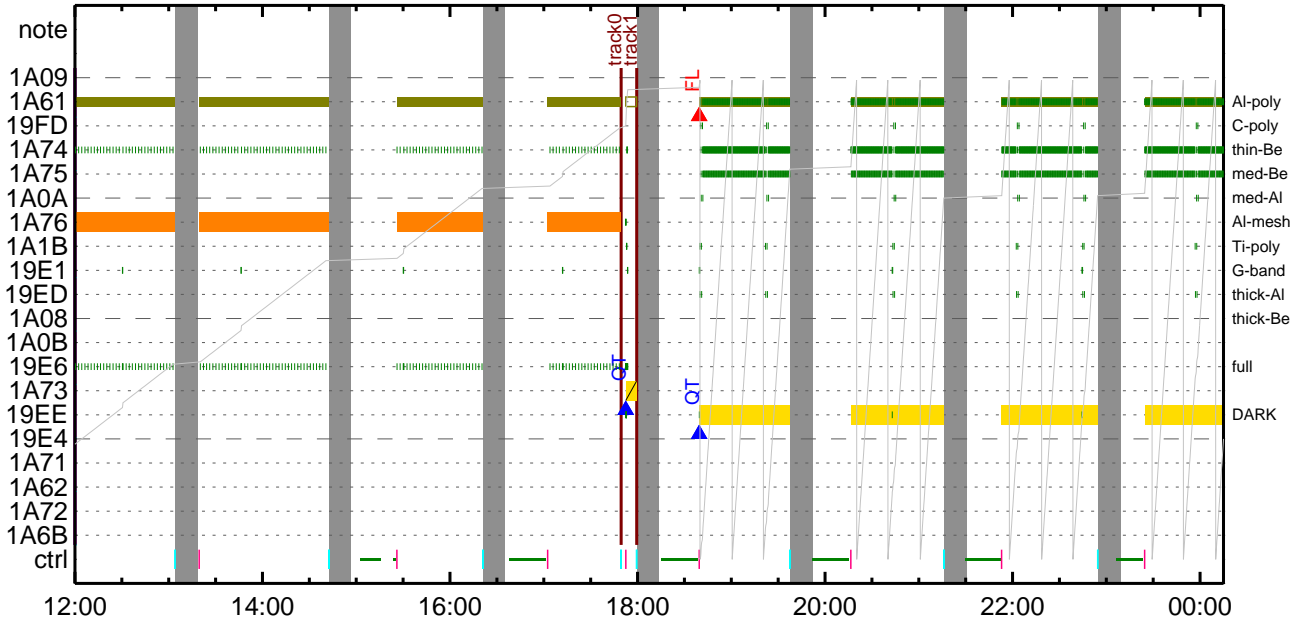
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FLD Patrol												
Term	Pointing (x, y)	Comment										
04/16 17:59:48 - 04/17 06:00:18	Track ( -140.8, 245.6) <sup>Ⓢ 04/16 17:59:30</sup>	# AR 12321 observations. IHOP 245.										
04/17 06:10:18 - 04/17 17:54:48	Track ( -30.0, 245.9) <sup>Ⓢ 04/17 06:10:00</sup>	# AR cont.										
04/17 18:04:48 - 04/17 23:00:18	Track ( 78.8, 245.0) <sup>Ⓢ 04/17 18:04:30</sup>	# AR cont.										
04/18 03:00:18 - 04/18 06:26:48	Track ( 159.7, 243.5) <sup>Ⓢ 04/18 03:00:00</sup>	# Return to AR.										
04/18 06:36:48 - 04/21 10:17:00	Track ( 192.1, 242.7) <sup>Ⓢ 04/18 06:36:30</sup>	# AR cont.										
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

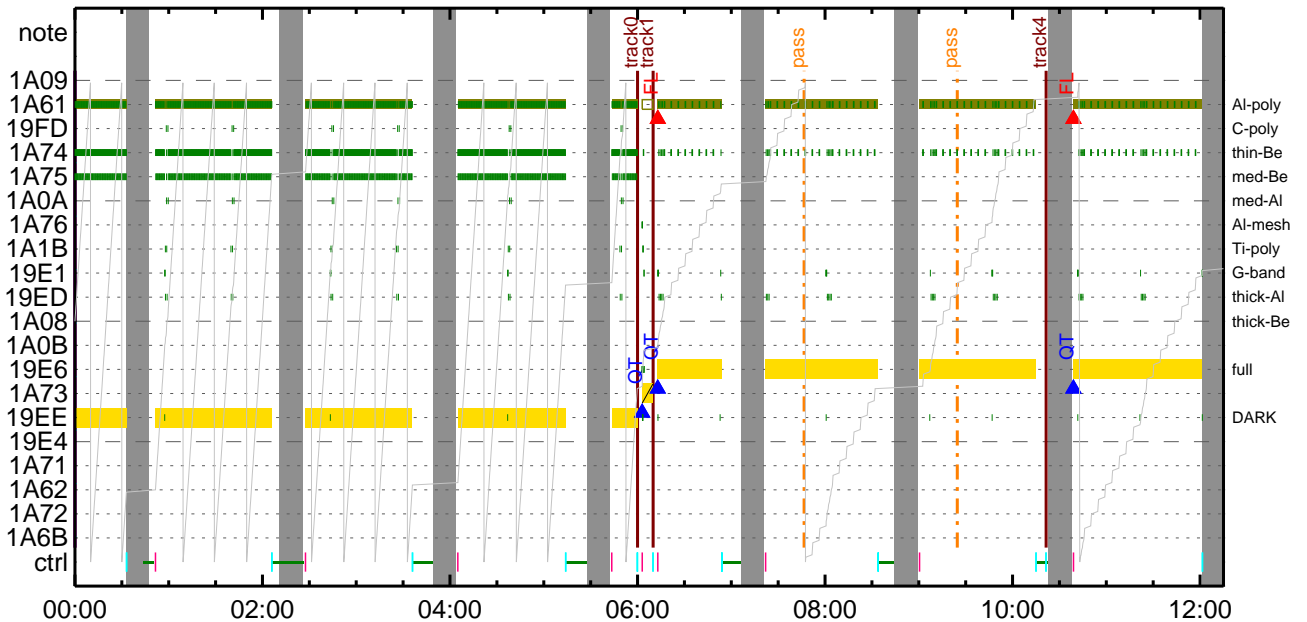
### CMDI #0063 2015/04/16



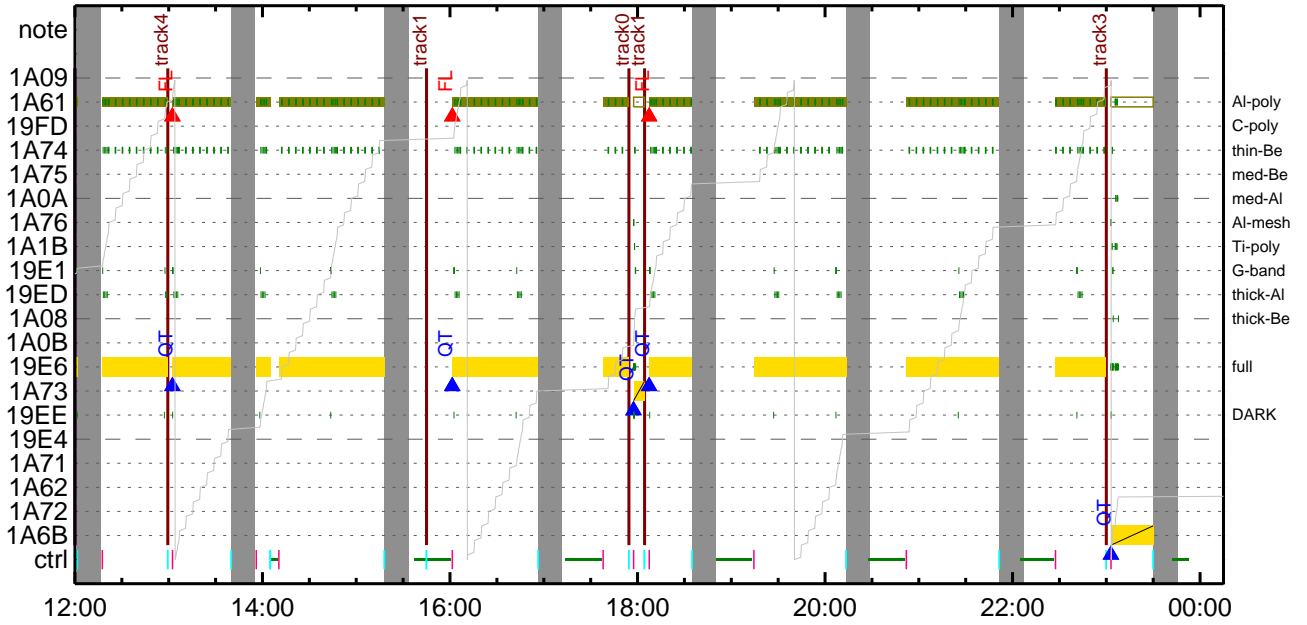
### CMDI #0063 2015/04/16



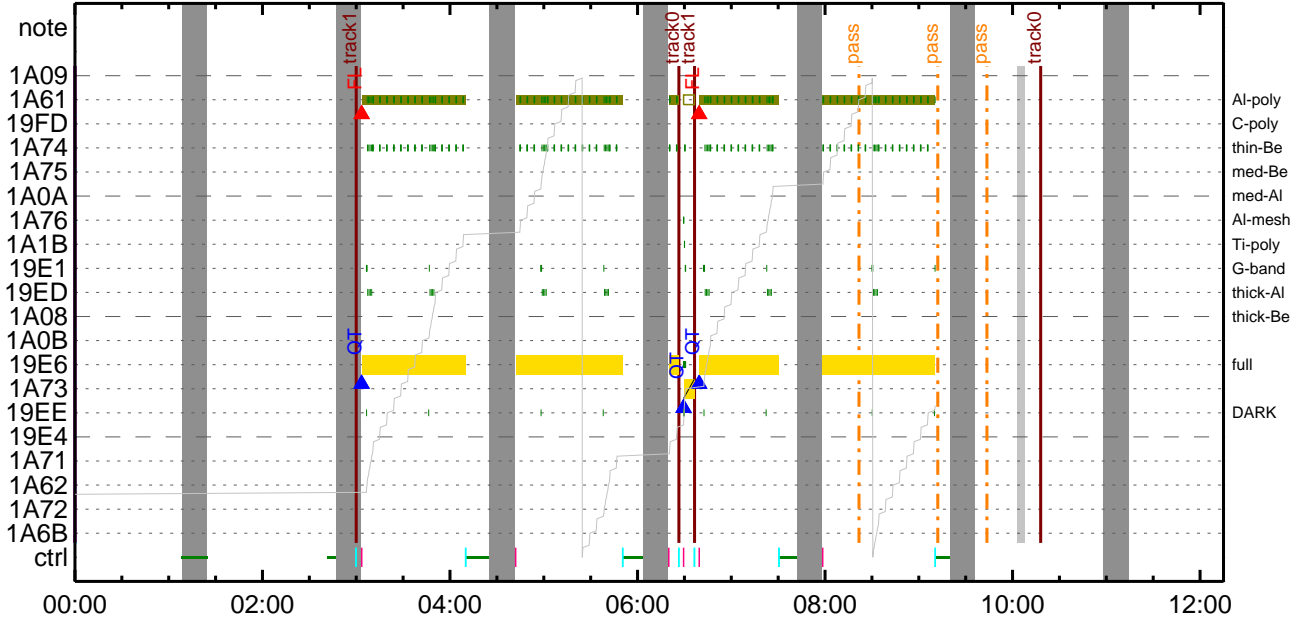
### CMDI #0063 2015/04/17



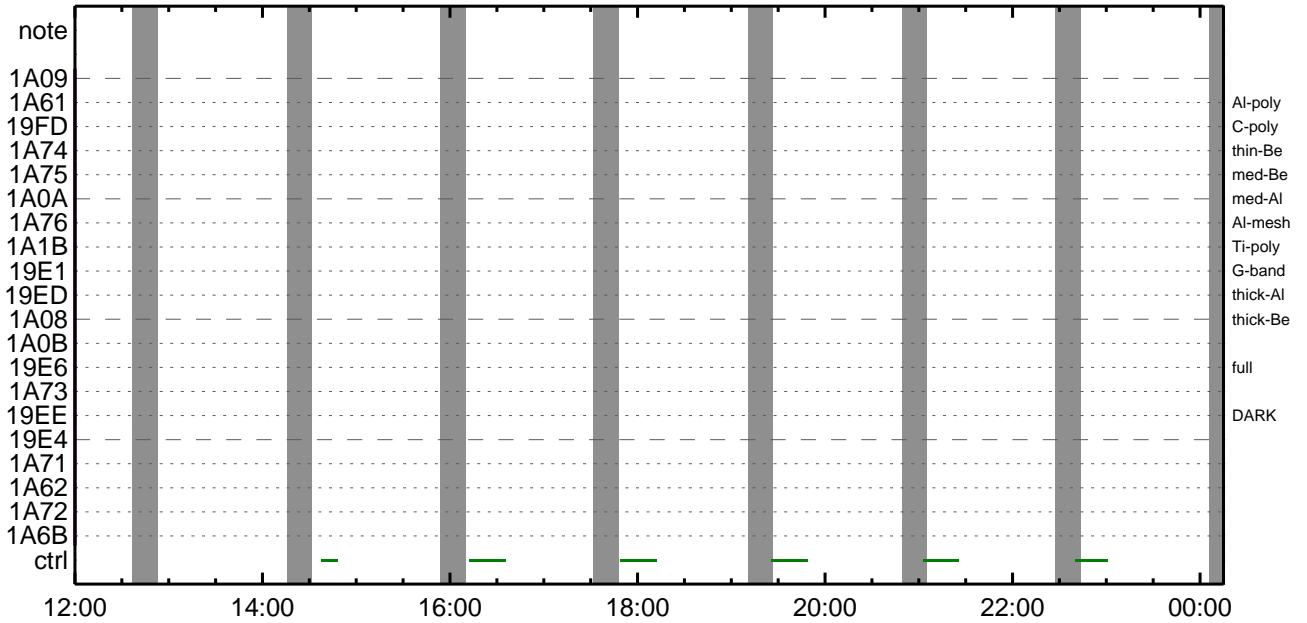
CMDI #0063 2015/04/17



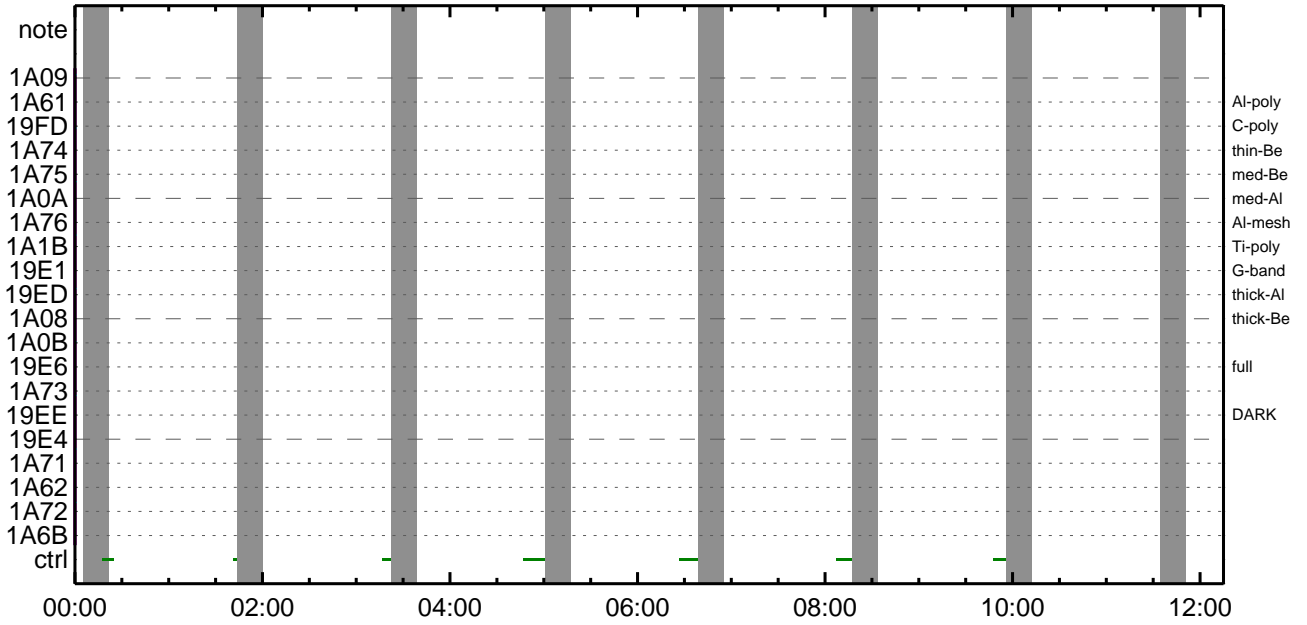
CMDI #0063 2015/04/18



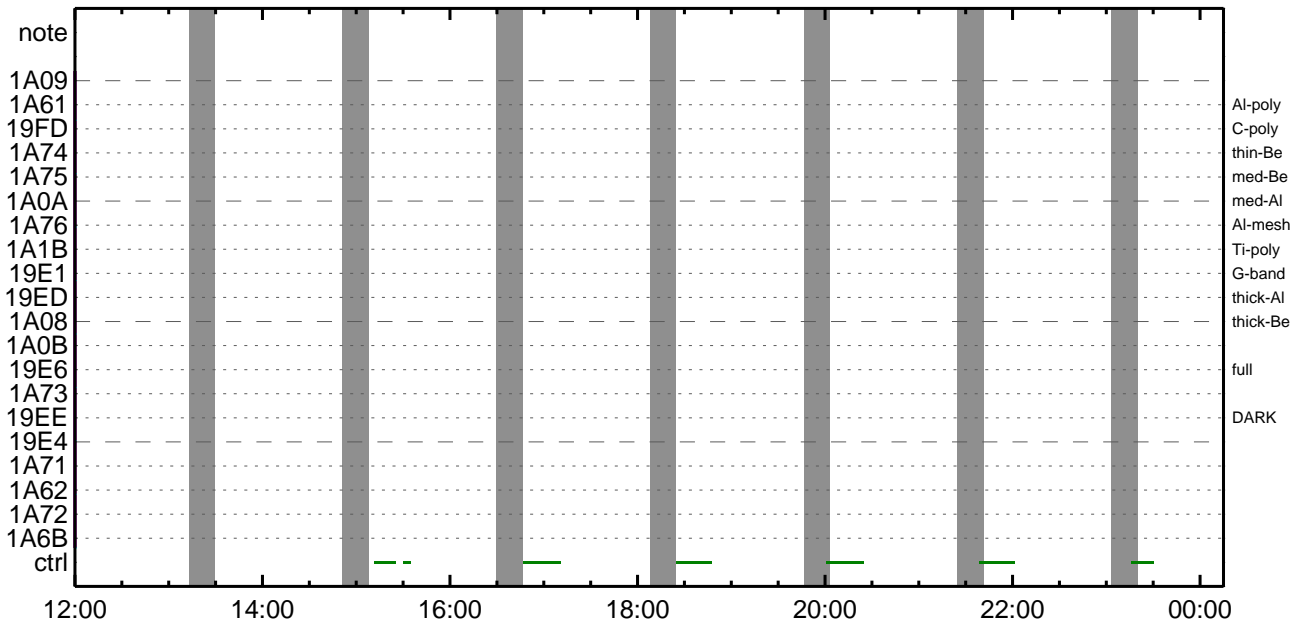
CMDI #0063 2015/04/18



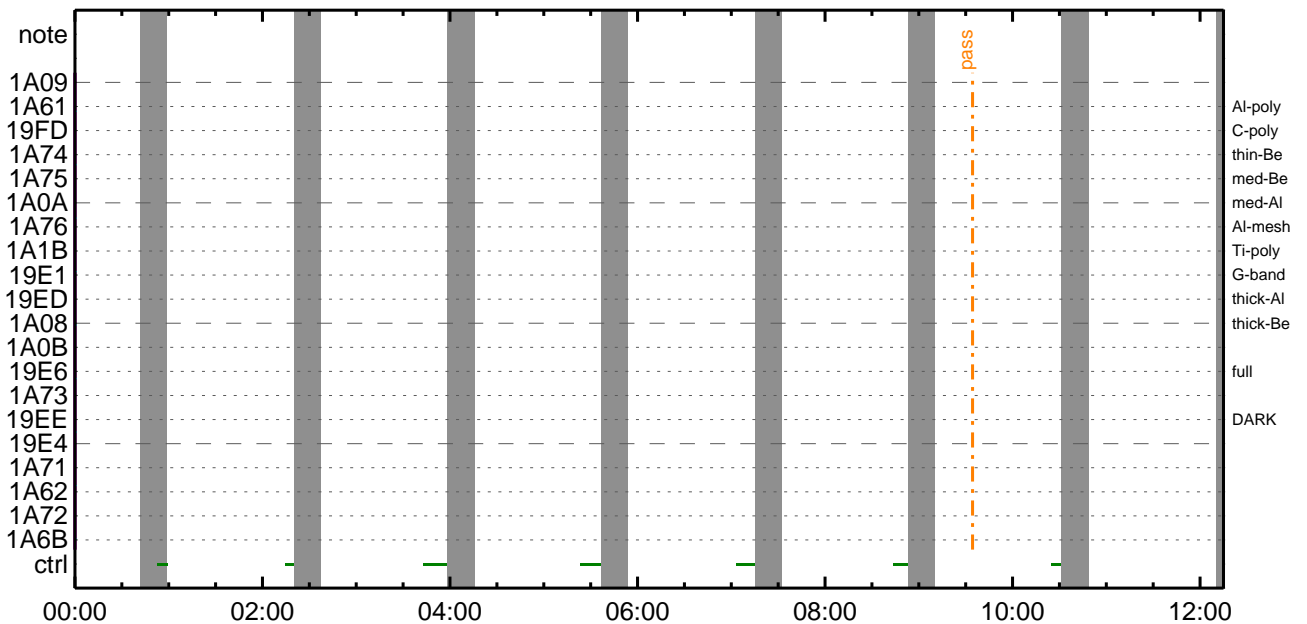
CMDI #0063 2015/04/19



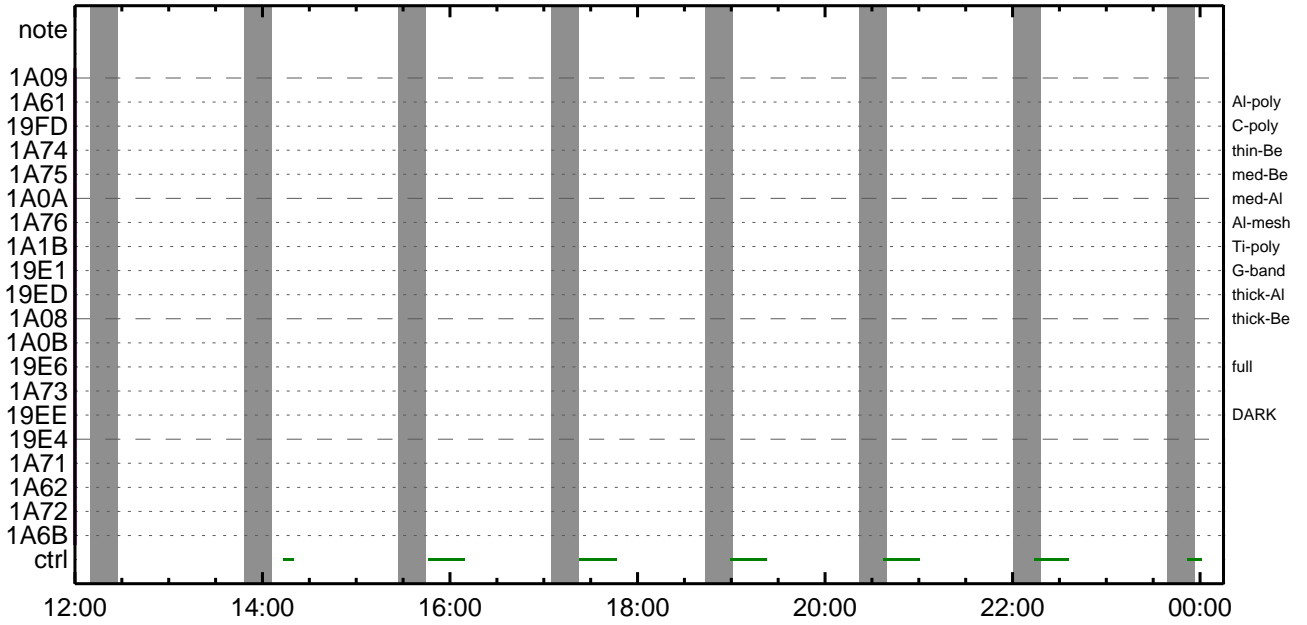
CMDI #0063 2015/04/19



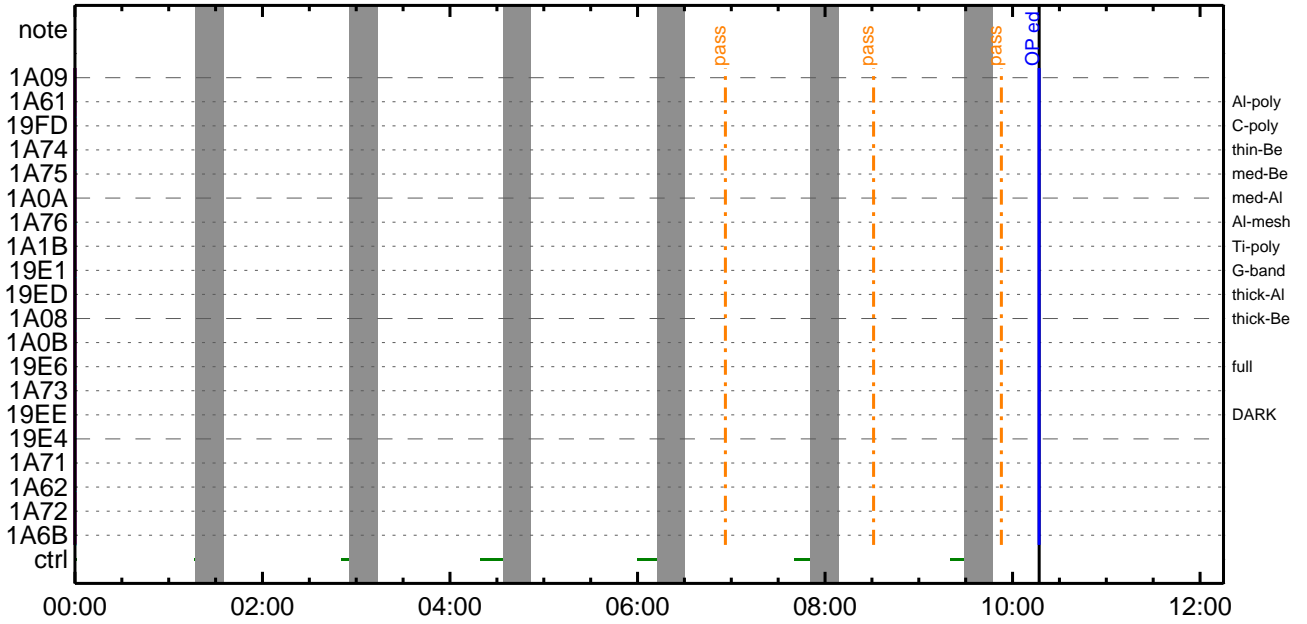
CMDI #0063 2015/04/20



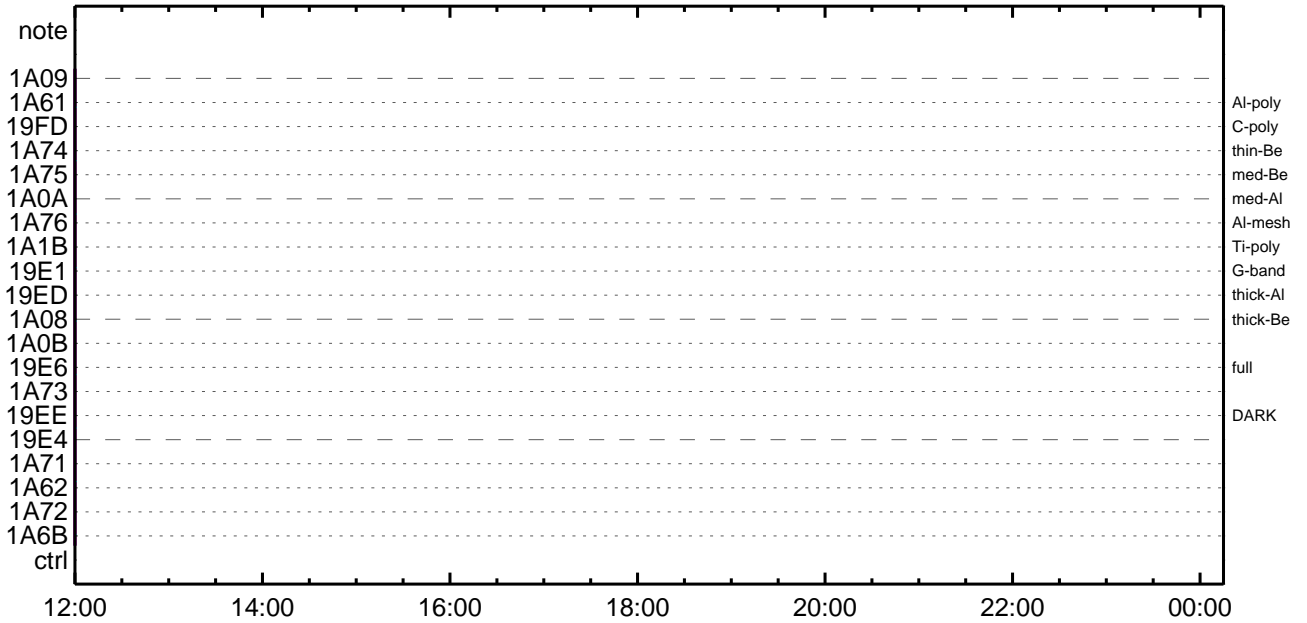
CMDI #0063 2015/04/20



CMDI #0063 2015/04/21



CMDI #0063 2015/04/21







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;YÈ;YAY6YX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;YÈ;ã
0103 S. OP op-081:OP
0104 ( )
0105 S. OG og-081:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPÏÏ°èYAY6YX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.         ¢¢[HK1_DMP_TOP_ADRS_1]           EQ         40
0113 C.         ¢¢[HK1_DMP_TOP_ADRS_0]           EQ          0
0114 C.         ¢¢[HK1_DMP_BLOCK_NUM]           EQ         127
0115 C.         ¢¢[HK1_DMP_REPEAT_NUM]          EQ          0
0116 C.         ¢¢[HK1_DMA_DMP_PIM]             EQ         DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.         ¢¢[HK1_PKT_FORM_NO]             EQ          7
0120 C.         ¢¢[HK1_PKT_GEN_TIME]            EQ         0.25 s
0121 C.         ¢¢[HK1_S_TLM_BIT_RATE]          EQ         32k
0122 C.         ¢¢[HK1_X_TLM_BIT_RATE]          EQ          4M
0123 C.         ¢¢[HK1_DMP_CHK_FLG]            EQ         EXEC
0124 C. YAY6YX½^aÎ»ð³ÎÇ§
0125 C.         ¢¢[HK1_DMP_CHK_FLG]            EQ         NON
0126 C. RAM ID=NMOGqÏÏ¹Ç.è²ÏOKqð³ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.         ¢¢[HK1_DMP_TOP_ADRS_1]           EQ         41
0132 C.         ¢¢[HK1_DMP_TOP_ADRS_0]           EQ          0
0133 C.         ¢¢[HK1_DMP_BLOCK_NUM]           EQ         127
0134 C.         ¢¢[HK1_DMP_REPEAT_NUM]          EQ          0
0135 C.         ¢¢[HK1_DMA_DMP_PIM]             EQ         DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.         ¢¢[HK1_PKT_FORM_NO]             EQ          7
0139 C.         ¢¢[HK1_PKT_GEN_TIME]            EQ         0.25 s
0140 C.         ¢¢[HK1_S_TLM_BIT_RATE]          EQ         32k
0141 C.         ¢¢[HK1_X_TLM_BIT_RATE]          EQ          4M
0142 C.         ¢¢[HK1_DMP_CHK_FLG]            EQ         EXEC
0143 C. YAY6YX½^aÎ»ð³ÎÇ§
0144 C.         ¢¢[HK1_DMP_CHK_FLG]            EQ         NON
0145 C. RAM ID=NMOGqÏÏ¹Ç.è²ÏOKqð³ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.         ¢¢[HK1_DMP_TOP_ADRS_1]           EQ         42
0151 C.         ¢¢[HK1_DMP_TOP_ADRS_0]           EQ          0
0152 C.         ¢¢[HK1_DMP_BLOCK_NUM]           EQ         65
0153 C.         ¢¢[HK1_DMP_REPEAT_NUM]          EQ          0
0154 C.         ¢¢[HK1_DMA_DMP_PIM]             EQ         DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.         ¢¢[HK1_PKT_FORM_NO]             EQ          7
0158 C.         ¢¢[HK1_PKT_GEN_TIME]            EQ         0.25 s
0159 C.         ¢¢[HK1_S_TLM_BIT_RATE]          EQ         32k
0160 C.         ¢¢[HK1_X_TLM_BIT_RATE]          EQ          4M
0161 C.         ¢¢[HK1_DMP_CHK_FLG]            EQ         EXEC
0162 C. YAY6YX½^aÎ»ð³ÎÇ§
0163 C.         ¢¢[HK1_DMP_CHK_FLG]            EQ         NON
0164 C. RAM ID=NMOG, RAM ID=OPqÏÏ¹Ç.è²ÏOKqð³ÎÇ§
0165 C.
0166 C. ***** ²¼qÏÏ¹Ç.¶A°EÈ¬qA÷¿@ (¼âµ-YAY6YX½^e½ÇqðAÓAæqÇ¼^a¬qè¼i¹ÇqÇqâ) *****
0167 C. DHUYâ;YÈ;ÈY½;Y;½YÈ;ÈðÏã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.         ¢¢[HK1_PKT_FORM_NO]             EQ          2
0171 C.         ¢¢[HK1_PKT_GEN_TIME]            EQ         0.5S
0172 C.         ¢¢[HK1_S_TLM_BIT_RATE]          EQ         32K
0173 C.         ¢¢[HK1_X_TLM_BIT_RATE]          EQ          4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ; § OPOG UPLOADq¬Á÷¿@NGqÏÏ¹Ç;¿°È²¼qÏÏ¹Ç-TI-CMDÁ÷¿@qÏÏ¹Ç°qÈqðq³qÈ; §
0180 C.         qðq¿;¿SETqÈDUMPqÏÏ¹±qÏÏ¹q¹Ç¹Çqðq³qÈ; §
0181 C.
0182 C. TIY³Y³Y³Y³EððÀDÏ¿¿(UT)
0183 +. TI 2015-04-16 09:57:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.         ¢¢[HK1_TI_CMD_NUM]             EQ         1COUNTUP
0186 C.
0187 +. TI 2015-04-16 09:57:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.         ¢¢[HK1_TI_CMD_NUM]             EQ         1COUNTUP
0190 C.
0191 +. TI 2015-04-16 09:57:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.         ¢¢[HK1_TI_CMD_NUM]             EQ         1COUNTUP

```

```
0194 C.
0195 +. TI 2015-04-16 10:01:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.             çç[HK1_TI_CMD_NUM]                  EQ        1COUNTUP
0198 C.
0199 C.  °È²¼õİÄè%İİñİîŷÄŷ§ŷÄŷ¹âİŰ
0200 C.             çç[HK1_TI_CMD_ENA/DIS]              EQ        ENA
0201 C.             çç[HK1_TI_CMD_NUM]                  EQ        4
0202 C.             çç[HK1_NEXT_EXEC_PIM]               EQ        DHU
0203 C.             çç[HK1_NEXT_EXEC_DC]                EQ        0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC        (03 ab 03 01 02)
0212 C.             çç[HK1_DMP_TOP_ADRS_1]              EQ        07
0213 C.             çç[HK1_DMP_TOP_ADRS_0]              EQ        2B
0214 C.             çç[HK1_DMP_BLOCK_NUM]               EQ        3
0215 C.             çç[HK1_DMP_REPEAT_NUM]             EQ        0
0216 C.             çç[HK1_DMA_DMP_PIM]                EQ        DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC        (07 0b f8)
0219 C.             çç[HK1_PKT_FORM_NO]                 EQ        7
0220 C.             çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0221 C.             çç[HK1_S_TLM_BIT_RATE]              EQ        32k
0222 C.             çç[HK1_X_TLM_BIT_RATE]              EQ        4M
0223 C.             çç[HK1_DMP_CHK_FLG]                 EQ        EXEC
0224 C.
0225 C.  ŷÄŷÖŷ×½ªİ»ò³İÇ§
0226 C.             çç[HK1_DMP_CHK_FLG]                 EQ        NON
0227 C.
0228 C. RAM ID=TI_TBLİî¼È¹ç.è²İOKò³İÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷİ;¼ŷÈ;Èòðİãò¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC        (02 0a f8)
0233 C.             çç[HK1_PKT_FORM_NO]                 EQ        2
0234 C.             çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0235 C.             çç[HK1_S_TLM_BIT_RATE]              EQ        32K
0236 C.             çç[HK1_X_TLM_BIT_RATE]              EQ        4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2015-04-16 10:01:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC        (41)
0245 C. -----
0246 C.       HK1_TI_CMD_NUM           = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2015-04-16 10:01:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC        (21 02)
0258 +. TI 2015-04-16 10:01:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC        (22)
0261 C.             [   ] [HK1_TI_CMD_NUM]              EQ        2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2015-04-16 10:01:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC        (c3)
0271 C.             [   ] [HK1_TI_CMD_NUM]              EQ        1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃİñİ»ö¼ŷ¼ÈÄĐò¹èDCBC.×²è *****
0276 C. (¼á°İŷÖŷÄŷÈŷŷŷÈŷáŷçŷèèÈ¼ò¼Ä»Ûò¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹.İ Daily±çİñèÈ¹ò¹èDCBC.×²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ¹¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-082 2015-04-16 12:17:35 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÅYYSYÄY^-¼Ä»Û;ä
0005 C.
0006 C. YÅYß;¼Y³YFYYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÅÍ;Èò¿òÄò·μ°È»Í×ÁÇòíYÇYÄY×YÍ;¼YÉ;ÈÈÈ¼μ·ÍÉ;ÈòÈ¼°Çòò·ò¿¼í¹ÇòÍ;çÄ®, ùò¹òÈòÈòçÄ+¿®ò·òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿@μ;ON
0016 C. *****
0017 C. ç" °ÈÄ, Í×ÈYòãLOSòÈÇòí»P´Òòð¹íí, ò·;çÉÒÍ×òÈXÁÓONòÍ¹ÒòÈòíòÈòòò³òÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÄY^-¾ÒÄòò-òÁÄêò·ò¿òé;ç°È²¼òÍ°ÈÄ, ¼È¾Çòòð¼Ä¹Òò¹òé;f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÅÍ¼í°ÈÄ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;Èò·ò¿òò¼í¹ÇòÍ;ç°È²¼òÍ¼Ä¹Òò»ò°;çDCBC-150òØ¿Èòà;f
0035 C.
0036 . C. ;ãPT1°ÈÄ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ò, ;¼Ù)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò, ;¼Ù)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò, ;¼Ù)
0045 C.
0046 . C. ;ãYÇYÓYÈYÈÄÜÄØ;ÈÄ·Ä°²óÈò;È, ãòÍ°ÈÄ, °È³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ò, ;¼Ù)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò, ;¼Ù)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò, ;¼Ù)
0052 C.
0053 C.
0054 . C. PT1°ÈÄ, ò-¼«È°Äã»ßò·ò¿, ã;ç°È²¼òò¼Ä¹Òò¹òé;f
0055 C. YÇYÓYÈYÈÄÜÄØòäÄ·Ä°²óÈòò-¾ãòò¼í¹ÇòÍ°Í»ò¹òÈòÈòçÄÒòÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÅÍ¼í°ÈÄ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;Èò·ò¿òò¼í¹ÇòÍ;ç°È²¼òÍ¼Ä¹Òò»ò°;çDCBC-151òØ¿Èòà;f
0061 C.
0062 . C. ;ãPT2°ÈÄ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ò, ;¼Ù)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò, ;¼Ù)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò, ;¼Ù)
0071 C.
0072 . C. ;ãYÇYÓYÈYÈÄÜÄØ;ÈÄ·Ä°²óÈò;È, ãòÍ°ÈÄ, °È³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ò, ;¼Ù)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò, ;¼Ù)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò, ;¼Ù)
0078 C.
0079 . C. *****
0080 C. DR°ÈÄ, Äã»ß;çXÁ+¿@μ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÄ, Äã»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿@μ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 13s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2015-04-16 10:01:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP `ũãîüî»ö¼ÿûËÆð¹ùèDCBC•x²è *****
0156 C. (¼ã°ïÿÔÿÄÿËÿÏÿÏÿËÿãÿçÿËëù½¼û¼Ä»Û¹ùè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥D¥¹•î Daily±;îÑùÉ'Ø¹ùèDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Áÿÿ¥Äÿ-¼Ä»Û;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-083 2015-04-16 12:17:35 148 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÁø•µ°È»Í×ÁÇøíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøí;çÁ®, ùø¹øèøøøçÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 + DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 + DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 08)
0068 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 13)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2015-04-16 10:01:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 . C. *****
0084 C. SOT table upload
0085 C. *****
0086 . C. < Stop FG table >
0087 +. DC 07-F0 MDP_FG_CTRL_MANU
0088 BC (51)
0089 . C. -----
0090 C. MDP_FG_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 . C. <Upload FG Observation Table>
0094 . S. RAM ram-263:MDP_OBS_F
0095 ( )
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\*\*\* OP Sequence for XRT \*\*\*

2015/04/16	10:11:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	10:11:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	10:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/04/16	10:12:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2015/04/16	10:12:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/04/16	10:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/04/16	10:12:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/04/16	10:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/04/16	10:12:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/16	10:14:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2015/04/16	10:14:58.0	XRT_FL_PROG_SET_401_OG [0x191]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/04/16	10:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/16	11:26:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	11:26:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	11:26:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/16	11:26:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/16	11:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/16	11:40:00.0	XRT_Custom_430_OG [0x1ae]					
2015/04/16	11:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/16	13:04:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	13:04:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	13:04:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/16	13:04:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/16	13:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/16	13:18:30.0	XRT_Custom_430_OG [0x1ae]					
2015/04/16	13:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/16	14:42:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	14:42:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	14:42:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/16	14:42:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/16	14:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/16	15:25:00.0	XRT_Custom_430_OG [0x1ae]					
2015/04/16	15:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/16	16:21:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	16:21:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	16:21:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/16	16:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/16	16:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/16	17:01:30.0	XRT_Custom_430_OG [0x1ae]					
2015/04/16	17:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/16	17:49:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	17:49:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/16	17:49:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/04/16	17:49:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2015/04/16	17:49:48.0	XRT_FLD_DIS_422_OG [0x1a6]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/04/16	17:52:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/04/16	17:52:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/04/16	17:52:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]					

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2015/04/16	17:52:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/16	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/16	17:59:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/16	17:59:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2015/04/16	17:59:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00	00 00 00
2015/04/16	17:59:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/04/16	17:59:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/04/16	17:59:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/04/16	17:59:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/16	17:59:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/16	18:39:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2015/04/16	18:39:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2015/04/16	18:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/16	19:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/16	19:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/16	19:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/16	19:37:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/16	19:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/16	20:15:30.0	XRT_Custom_430_OG [0x1ae]					
2015/04/16	20:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/16	21:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/16	21:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/16	21:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/16	21:16:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/16	21:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/16	21:52:00.0	XRT_Custom_430_OG [0x1ae]					
2015/04/16	21:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/16	22:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/16	22:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/16	22:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/16	22:54:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/16	22:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/16	23:23:30.0	XRT_Custom_430_OG [0x1ae]					
2015/04/16	23:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/17	00:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/17	00:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/17	00:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/17	00:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/17	00:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/17	00:50:30.0	XRT_Custom_430_OG [0x1ae]					
2015/04/17	00:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/17	02:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/17	02:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/17	02:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/17	02:06:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/17	02:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/17	02:26:30.0	XRT_Custom_430_OG [0x1ae]					
2015/04/17	02:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/17	03:36:00.0	XRT_CTRL_MANU_400_OG [0x190]					



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2015/04/17	03:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	03:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	03:36:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/17	03:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/17	04:04:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/17	04:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2015/04/17	05:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/04/17	05:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/17	05:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	05:14:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	05:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/17	05:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/17	05:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/17	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]					
2015/04/17	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/04/17	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/17	06:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/04/17	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	AOCS_OrE-point_Start_2_OG [0x098]					
2015/04/17	06:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/04/17	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FLRCTRL_DIS	1	07-F0	d9		
2015/04/17	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_ARS_DIS	1	07-F0	c9		
2015/04/17	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/04/17	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2015/04/17	06:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_CTRL_AUTO_408_OG [0x198]					
2015/04/17	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/17	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	06:10:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/04/17	06:10:26.0	XRT_FLD_RESET_407_OG [0x197]	AOCS_OrE-point_Start_3_OG [0x099]					
2015/04/17	06:10:26.0	XRT_FLD_RESET_407_OG [0x197]	AOCU_NM	5	02-76	01 00 00 00 00		
2015/04/17	06:12:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/04/17	06:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/04/17	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/04/17	06:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/04/17	06:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/17	06:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2015/04/17	06:54:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/04/17	06:54:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_CTRL_AUTO_408_OG [0x198]					
2015/04/17	06:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/17	07:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	07:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	08:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_FLD_RESET_415_OG [0x19f]					
2015/04/17	08:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/17	08:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	08:34:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/17	08:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/17	08:59:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/04/17	09:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2015/04/17	10:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	10:15:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	10:15:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	10:15:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/17	10:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/17	10:21:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	10:21:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	10:21:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/04/17	10:21:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/04/17	10:21:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/17	10:21:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/17	10:21:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/17	10:21:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/17	10:21:56.0	XRT_FLD_RESET_418_OG [0x1a2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	10:38:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2015/04/17	10:38:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/04/17	10:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/17	12:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	12:01:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	12:01:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	12:01:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/17	12:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/17	12:16:30.0	XRT_Custom_430_OG [0x1ae]							
2015/04/17	12:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/17	12:59:19.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	12:59:21.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	12:59:23.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/04/17	12:59:25.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	04 00 00 e9 40				
2015/04/17	12:59:43.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/17	12:59:45.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/17	12:59:47.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/17	12:59:49.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/17	12:59:51.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	13:02:21.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2015/04/17	13:02:23.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/04/17	13:02:25.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/17	13:40:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	13:40:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	13:40:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	13:40:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/17	13:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/17	13:55:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/17	13:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/17	14:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	14:05:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	14:05:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	14:05:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/17	14:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2015/04/17	14:09:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/17	14:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/04/17	15:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/17	15:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	15:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	15:18:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/17	15:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/17	15:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/17	15:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	15:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	15:45:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/04/17	15:45:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00		
2015/04/17	15:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/04/17	15:45:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/04/17	15:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/04/17	15:45:26.0	XRT_FLD_RESET_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/04/17	16:01:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/17	16:01:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2015/04/17	16:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/04/17	16:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/17	16:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	16:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	16:56:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/17	16:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/17	17:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/17	17:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/04/17	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/17	17:54:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	17:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	17:54:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/04/17	17:54:48.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/04/17	17:57:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/04/17	17:57:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/04/17	17:57:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/04/17	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2015/04/17	18:04:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/17	18:04:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	18:04:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/17	18:04:30.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/04/17	18:04:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00		
2015/04/17	18:04:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/04/17	18:04:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/04/17	18:04:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/04/17	18:04:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/04/17	18:07:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/17	18:07:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2015/04/17	18:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/04/17			MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2015/04/17	18:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	18:35:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	18:35:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	18:35:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/17	18:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/17	19:13:30.0	XRT_Custom_430_OG [0x1ae]							
2015/04/17	19:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/17	20:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	20:13:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	20:13:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	20:13:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/17	20:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/17	20:51:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/17	20:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/17	21:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	21:51:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	21:51:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	21:51:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/17	21:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/17	22:26:30.0	XRT_Custom_430_OG [0x1ae]							
2015/04/17	22:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/17	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	22:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/04/17	23:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/04/17	23:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/04/17	23:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/04/17	23:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/17	23:02:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2015/04/17	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/17	23:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	23:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/17	23:30:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/17	23:30:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/17	23:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/18	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/18	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/18	02:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/04/18	03:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/04/18	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/18	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/18	03:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/18	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/18	03:00:26.0	XRT_FLD_RESET_409_OG [0x199]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/18	03:03:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2015/04/18	03:03:28.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/04/18	03:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/18	04:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2015/04/18	04:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	04:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/18	04:10:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/04/18	04:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/04/18	04:41:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/18	04:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/18	05:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	05:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	05:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/18	05:50:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/04/18	05:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/04/18	06:19:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/18	06:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/18	06:26:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	06:26:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	06:26:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2015/04/18	06:26:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2015/04/18	06:26:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/04/18	06:29:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/04/18	06:29:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/04/18	06:29:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07			
2015/04/18	06:29:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/18	06:36:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	06:36:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	06:36:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/04/18	06:36:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00			
2015/04/18	06:36:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/04/18	06:36:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/04/18	06:36:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/04/18	06:36:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/04/18	06:36:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/18	06:39:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2015/04/18	06:39:28.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13			
2015/04/18	06:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/18	07:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	07:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	07:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/18	07:30:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/04/18	07:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/04/18	07:57:30.0	XRT_Custom_430_OG [0x1ae]							
2015/04/18	07:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/18	09:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	09:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/18	09:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/18	09:10:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/04/18	09:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/04/18	10:18:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			