

XRT Timeline to be uploaded on 2015/04/23

Period: 2015/04/23 10:22:00 - 2015/04/28 10:39:00

* * * * *

Normal mode

* * * * *

XOB #19D7: PFB observation for possible flare -w/leak test, 60s cad												
Term	Pointing (x, y)		Comment									
04/23 11:03:30 - 04/23 11:15:24	Fixed (-929.0, -256.0)		HOP079 EW #1									
04/23 11:18:30 - 04/23 11:30:24	Fixed (-761.0, -219.0)		#2									
04/23 11:33:30 - 04/23 11:45:24	Fixed (-581.0, -203.0)		#3									
04/23 11:48:30 - 04/23 12:00:24	Fixed (-397.0, -194.0)		#4									
04/23 12:03:30 - 04/23 12:19:00	Fixed (-207.0, -188.0)		#5									
04/23 12:42:00 - 04/23 12:53:54	Fixed (-12.0, -187.0)		#6									
04/23 12:57:00 - 04/23 13:08:54	Fixed (171.0, -188.0)		#7									
04/23 13:12:00 - 04/23 13:23:54	Fixed (361.0, -194.0)		#8									
04/23 13:27:00 - 04/23 13:38:54	Fixed (545.0, -203.0)		#9									
04/23 13:42:00 - 04/23 14:21:30	Fixed (725.0, -219.0)		#10									
04/23 14:34:00 - 04/23 14:49:54	Fixed (893.0, -256.0)		#11									
04/23 14:53:00 - 04/23 17:57:24	Track (-83.8, 89.4) ^{Ⓜ 04/23 14:50:00}		Granulation obs.									
PROG= 18 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 97 2-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 3 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 0 3 2.0sec												
└─ Subr= 2 2-time(s) 2.0sec												
└─┬─ Seqn= 51 30-time(s) 60.0sec												
└─┬─┬─ thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 3 1 2.0sec												
└─┬─┬─ Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) DPCM 3 1 2.0sec												
└─┬─┬─ thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 3 2 2.0sec												
└─┬─┬─ Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) DPCM 3 2 2.0sec												
└─┬─┬─ thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 3 3 2.0sec												
└─┬─┬─ Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) DPCM 3 3 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi												
Term	Pointing (x, y)		Comment									
04/23 18:00:30 - 04/23 18:07:24	Fixed (0.0, 0.0)		synoptic, shifted -2.5 min									
04/24 05:45:00 - 04/24 05:51:54	Fixed (0.0, 0.0)		synoptic, shifted -18.0 min									
04/24 17:43:00 - 04/24 17:49:54	Fixed (0.0, 0.0)		synoptic, shifted -20.0 min									
04/25 06:03:00 - 04/25 06:09:54	Fixed (0.0, 0.0)		synoptic									
PROG= 03 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─┬─ Seqn= 33 1-time(s) 4.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 5 1-time(s) 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─ Seqn= 40 1-time(s) 4.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 77 1-time(s) 2.0sec												
└─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Seqn= 6 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m												
Term	Pointing (x, y)		Comment									
04/23 18:10:30 - 04/24 05:41:54	Track (-673.6, 174.6) ^{Ⓜ 04/23 18:07:30}		#AR obs.									
04/24 05:55:00 - 04/24 17:39:54	Track (-593.5, 180.2) ^{Ⓜ 04/24 05:52:00}		#AR obs. cont									
04/24 18:32:00 - 04/25 05:59:54	Track (-503.9, 185.0) ^{Ⓜ 04/24 17:50:00}		#AR Obs. cont.									
04/25 06:13:00 - 04/25 10:07:00	Track (-404.5, 188.9) ^{Ⓜ 04/25 06:10:00}		#AR Obs. cnt.									
PROG= 13 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 8 2-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Subr= 2 2-time(s) 2.0sec												
└─┬─ Seqn= 24 1-time(s) 2.0sec												

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62 15-time(s) 120.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Flare mode

* * * * *

XOB #1A77: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
04/23 11:03:30 - 04/23 11:15:24	Fixed (-929.0, -256.0)	HOP079 EW #1
04/23 11:18:30 - 04/23 11:30:24	Fixed (-761.0, -219.0)	#2
04/23 11:33:30 - 04/23 11:45:24	Fixed (-581.0, -203.0)	#3
04/23 11:48:30 - 04/23 12:00:24	Fixed (-397.0, -194.0)	#4
04/23 12:03:30 - 04/23 12:19:00	Fixed (-207.0, -188.0)	#5
04/23 12:42:00 - 04/23 12:53:54	Fixed (-12.0, -187.0)	#6
04/23 12:57:00 - 04/23 13:08:54	Fixed (171.0, -188.0)	#7
04/23 13:12:00 - 04/23 13:23:54	Fixed (361.0, -194.0)	#8
04/23 13:27:00 - 04/23 13:38:54	Fixed (545.0, -203.0)	#9
04/23 13:42:00 - 04/23 14:21:30	Fixed (725.0, -219.0)	#10
04/23 14:34:00 - 04/23 14:49:54	Fixed (893.0, -256.0)	#11
04/23 14:53:00 - 04/23 17:57:24	Track (-83.8, 89.4) @ 04/23 14:50:00	Granulation obs.
04/23 18:10:30 - 04/24 05:41:54	Track (-673.6, 174.6) @ 04/23 18:07:30	#AR obs.
04/24 05:55:00 - 04/24 17:39:54	Track (-593.5, 180.2) @ 04/24 05:52:00	#AR Obs. cont
04/24 18:32:00 - 04/25 05:59:54	Track (-503.9, 185.0) @ 04/24 17:50:00	#AR Obs. cont.
04/25 06:13:00 - 04/25 10:07:00	Track (-404.5, 188.9) @ 04/25 06:10:00	#AR Obs. cnt.

PROG= 02 30-time(s)

Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

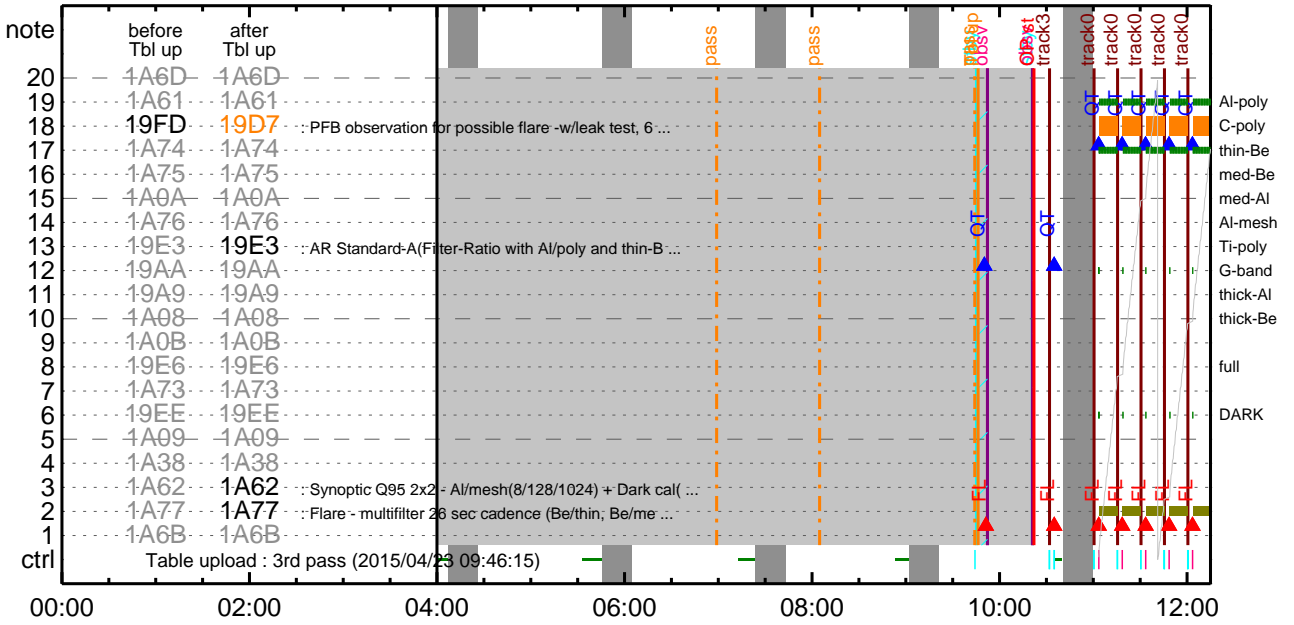
Flare Detection

* * * * *

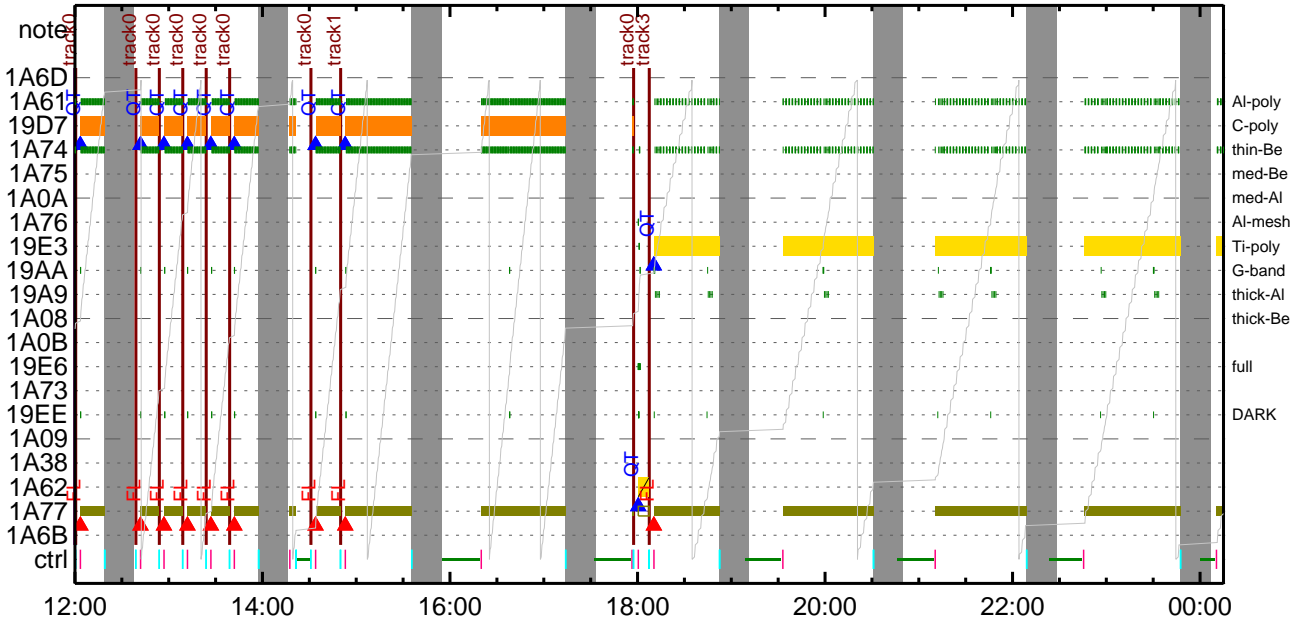
FLD Patrol

Term	Pointing (x, y)	Comment									
04/23 18:07:48 - 04/24 05:42:18	Track (-673.6, 174.6) @ 04/23 18:07:30	#AR obs.									
04/24 05:52:18 - 04/24 17:40:18	Track (-593.5, 180.2) @ 04/24 05:52:00	#AR Obs. cont									
04/24 17:50:18 - 04/25 06:00:18	Track (-503.9, 185.0) @ 04/24 17:50:00	#AR Obs. cont.									
04/25 06:10:18 - 04/28 10:39:00	Track (-404.5, 188.9) @ 04/25 06:10:00	#AR Obs. cnt.									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

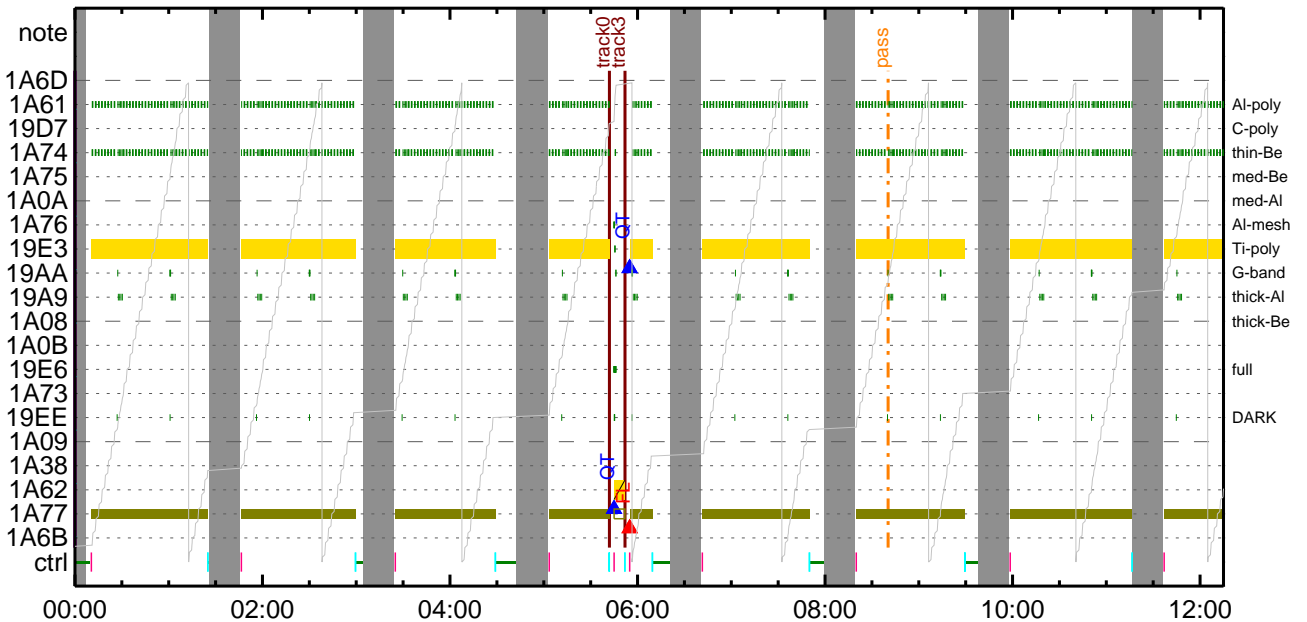
CMDI #0077 2015/04/23



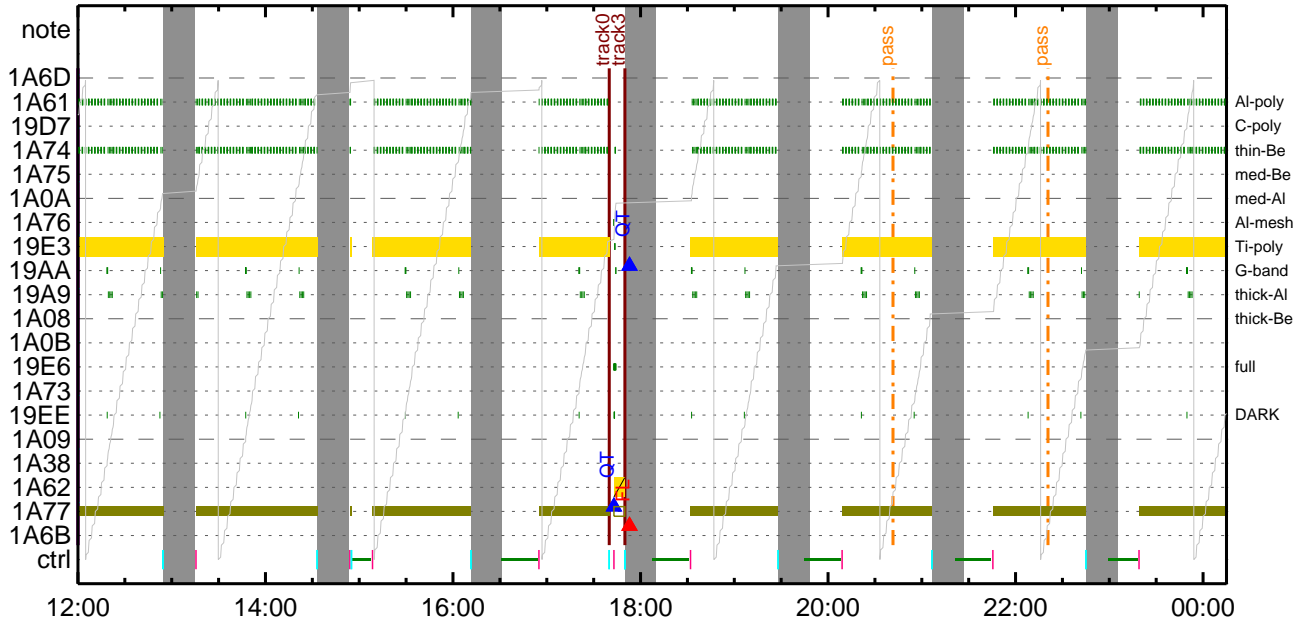
CMDI #0077 2015/04/23



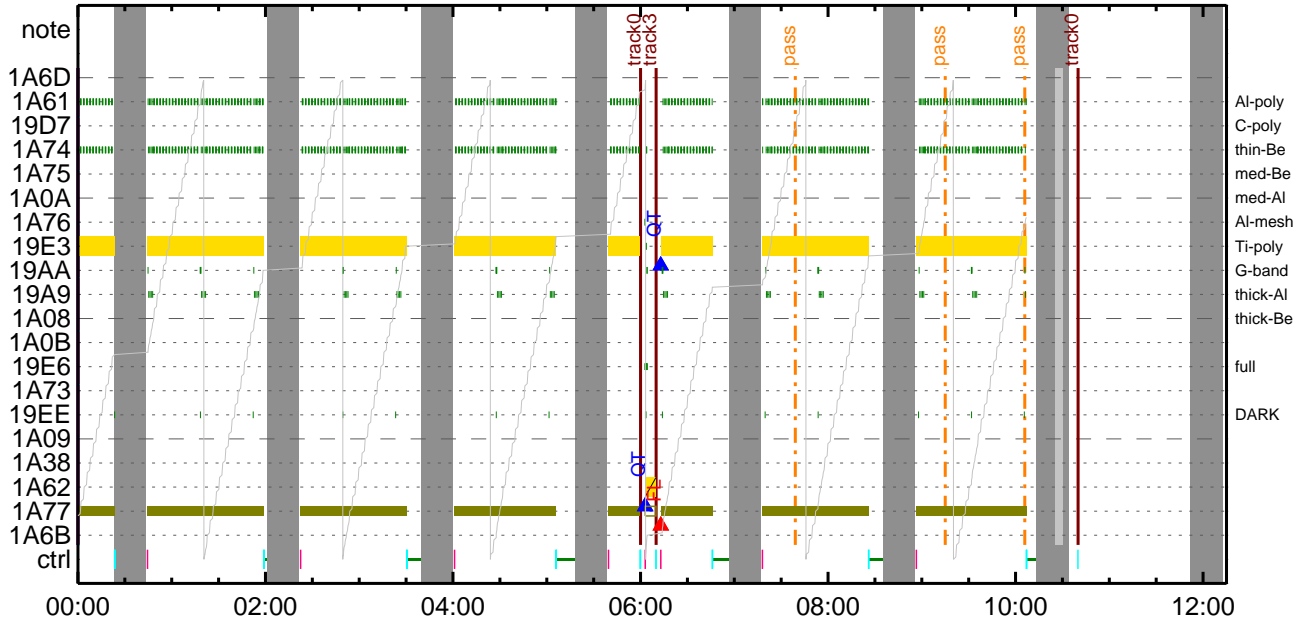
CMDI #0077 2015/04/24



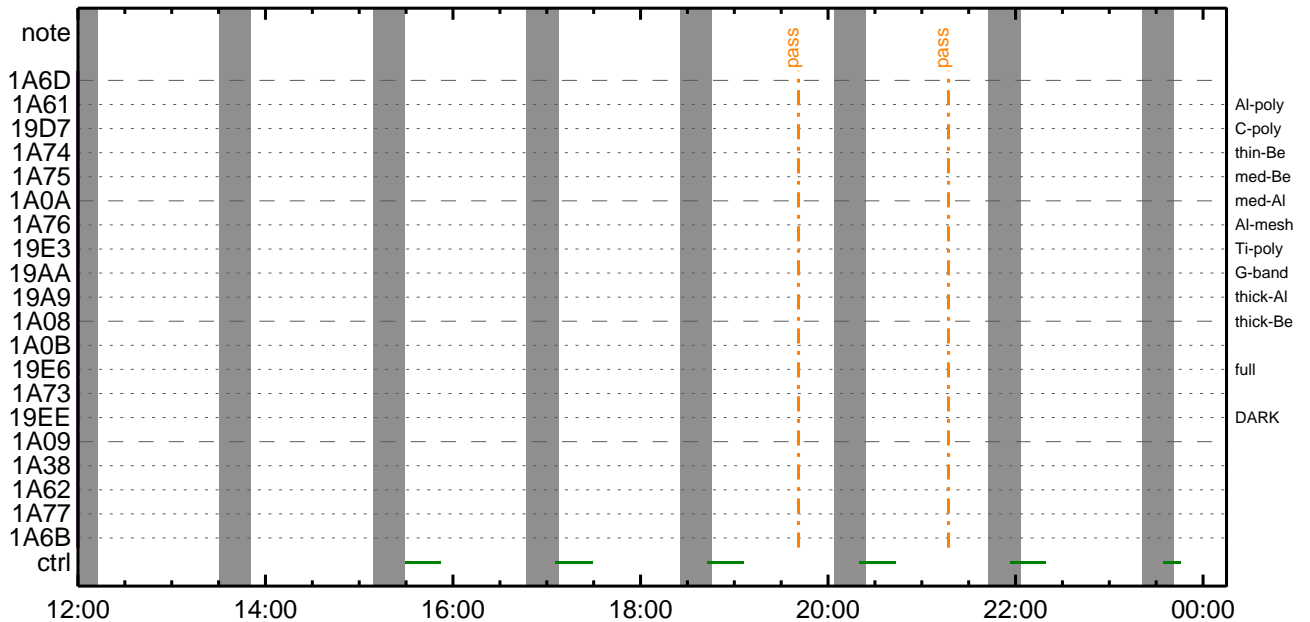
CMDI #0077 2015/04/24



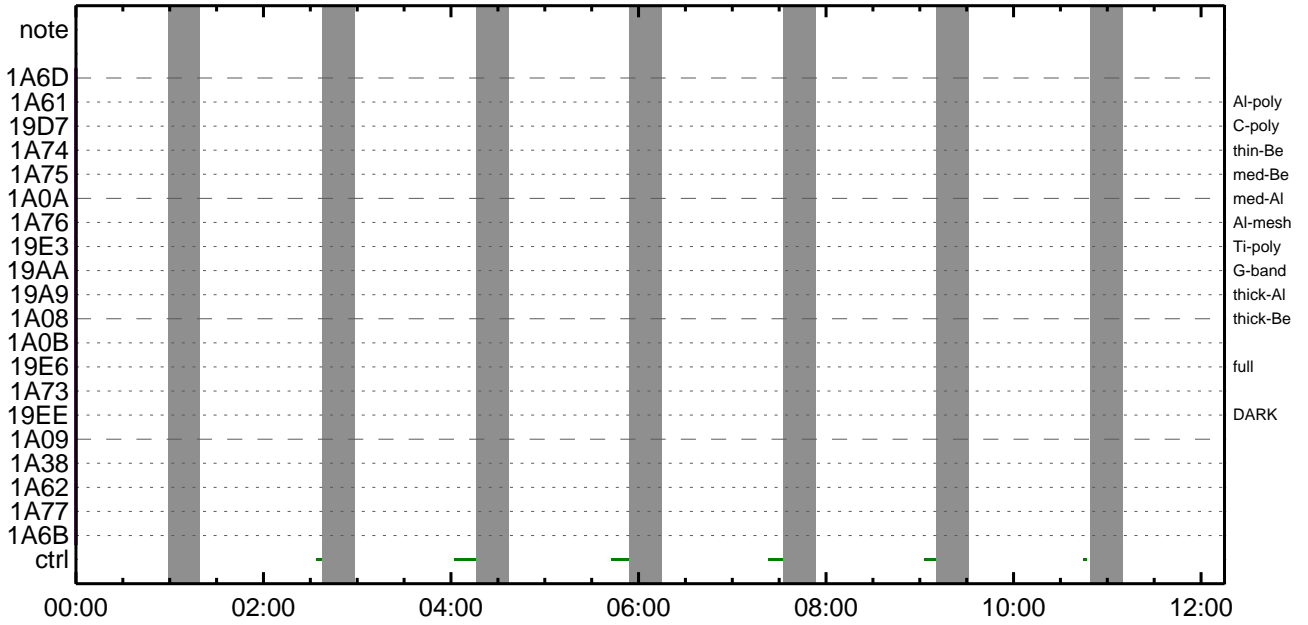
CMDI #0077 2015/04/25



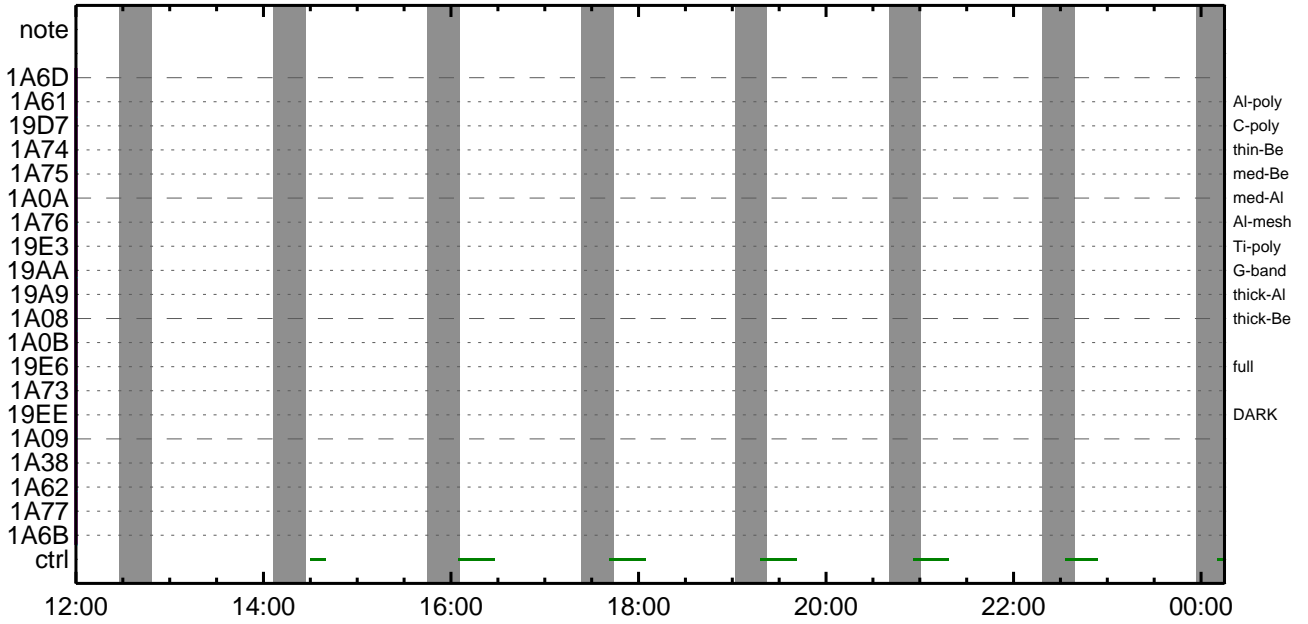
CMDI #0077 2015/04/25



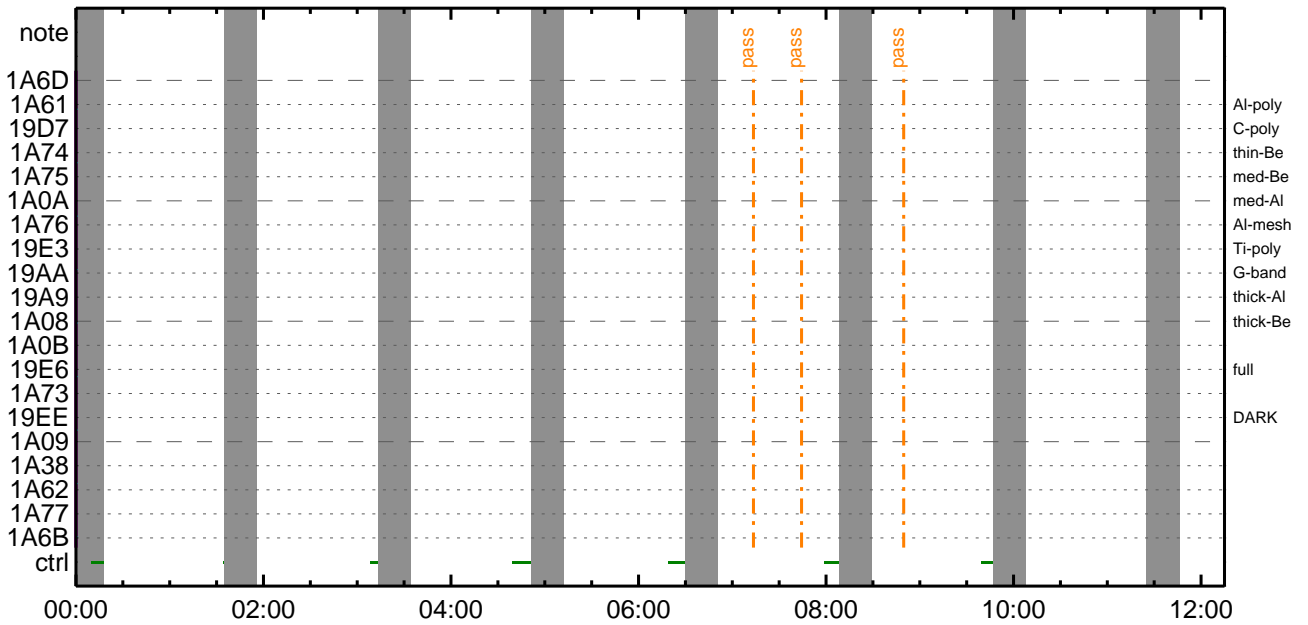
CMDI #0077 2015/04/26



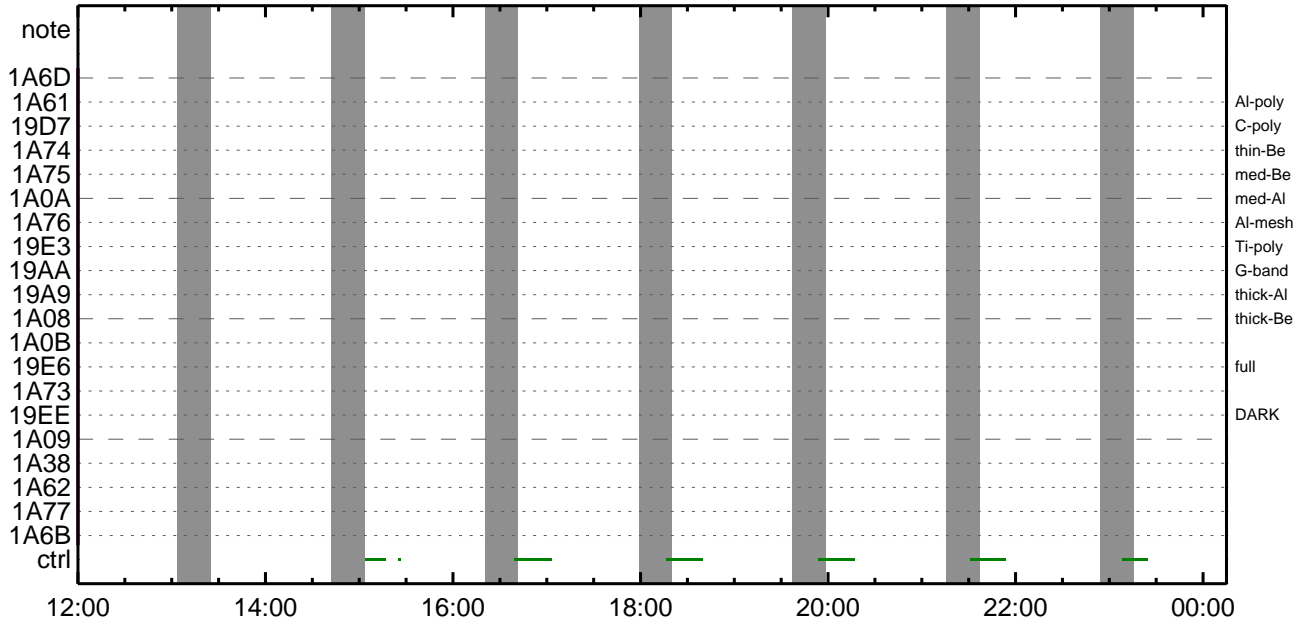
CMDI #0077 2015/04/26



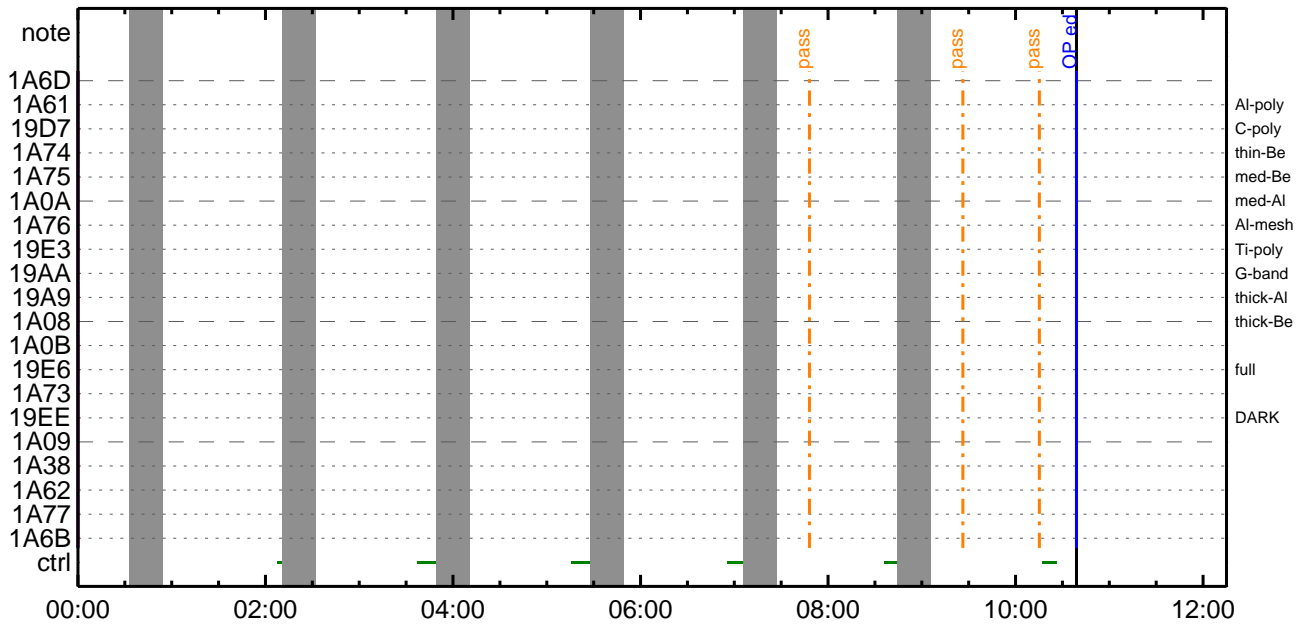
CMDI #0077 2015/04/27



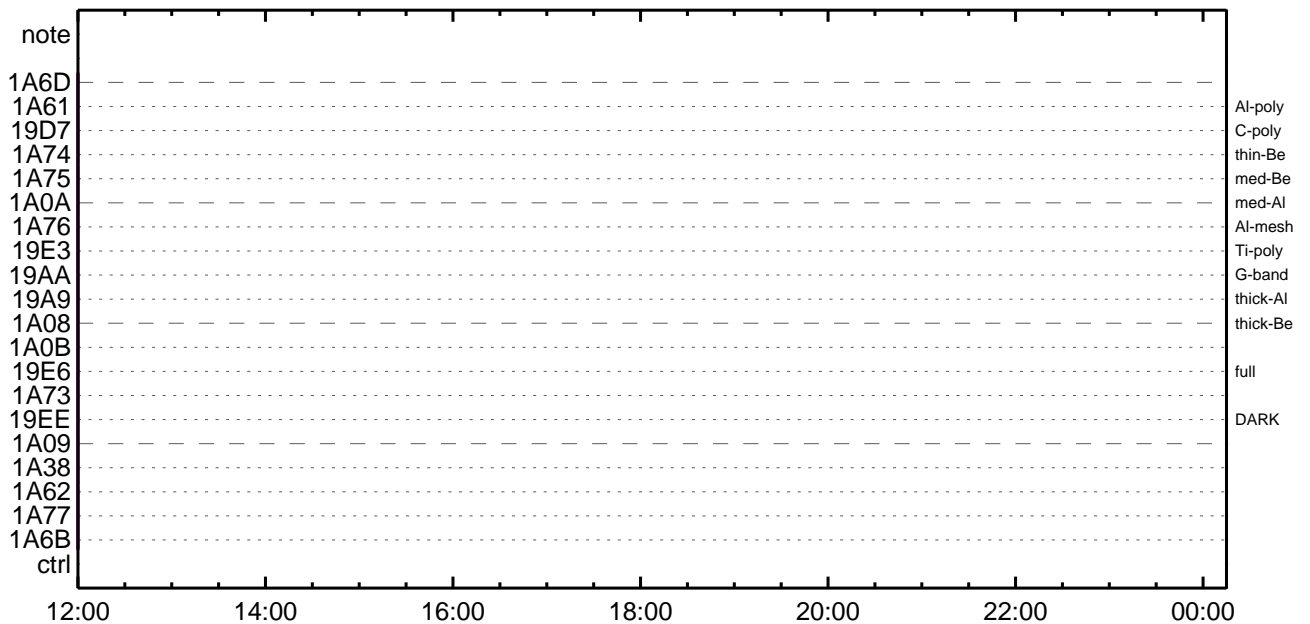
CMDI #0077 2015/04/27



CMDI #0077 2015/04/28



CMDI #0077 2015/04/28




```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-096 2015-04-23 13:43:35 234 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÄá•µ°Æ»Í×ÁÇóÍYçYÁY×YÍ;¼YÉ;ÈÈè%µ•íÉ;ÈóÈ¼°ÇÓá•ç¼l¹çáí;çÀ®, ùá¹áèáèáèçÁ+ç®•áèááááè;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®µ;ON
0016 C. *****
0017 C. ç“ °ÆÀ, í×ÈYááLOsáèáçáí»p´òáð¹íí, á•; çÈÓÍ×áÈXÁÓONáí¹òáèáíáèááááè;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYíYÁY-¾ÔÁòá•çç; ç°È²¼áí°ÆÀ, ¼è%çáðð¼Á¹òá¹è;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç“ RESTART;ÈPT1;Èá•çç¼á¹çáí; ç°È²¼áí¼Á¹òá»á°; çDCBC-150áðçÈáè;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹ò, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹ò, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹ò, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÁÚÁØ;ÈÁ•Á°²óÈð;È, ááí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹ò, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹ò, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, á-¼«Æ°Áá»áá•çç; á; ç°È²¼áðð¼Á¹òá¹è;ç
0055 C. YçYÓYÉYÈÁÚÁØáÁ•Á°²óÈðá-¾ááá¼l¹çáí´°í»á¹áèáèáèçÁòáè;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç“ RESTART;ÈPT2;Èá•çç¼á¹çáí; ç°È²¼áí¼Á¹òá»á°; çDCBC-151áðçÈáè;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹ò, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹ò, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹ò, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÁÚÁØ;ÈÁ•Á°²óÈð;È, ááí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹ò, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹ò, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»á;çXÁ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»á;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. *****
0130 C. SOT table upload
0131 C. *****
0132 . C. < Stop FG table >
0133 +. DC 07-F0 MDP_FG_CTRL_MANU
0134 BC (51)
0135 . C. -----
0136 C. MDP_FG_CTRL_MODE = MANU [ ]
0137 C. -----
0138 C.
0139 . C. <Upload FG Observation Table>
0140 . S. RAM ram-266:MDP_OBS_F
0141 ( )
0142 C.
0143 . C. < Dump RAMID=MDP_OBS_F >
0144 +. DC 07-F0 MDP_DUMP_FGTBL
0145 BC (82 07 00 00 00 38 b8)
0146 C. -----
0147 C. MDP_OBS_F verify = OK/NG [ ]
0148 C. -----
0149 C.
0150 . C. < Stop SP table >
0151 +. DC 07-F0 MDP_SP_CTRL_MANU
0152 BC (61)
0153 C. -----
0154 C. MDP_SP_CTRL_MODE = MANU [ ]
0155 C. -----
0156 C.
0157 . C. <Upload SP Observation Table>
0158 . S. RAM ram-284:MDP_OBS_S
0159 ( )
0160 C.
0161 . C. < Dump RAMID=MDP_OBS_S >
0162 +. DC 07-F0 MDP_DUMP_SPTBL
0163 BC (83 07 00 00 00 38 b8)
0164 C. -----
0165 C. MDP_OBS_S verify = OK/NG [ ]
0166 C. -----
0167 C.
0168 . C. < Resume FG table (auto mode) >
0169 +. DC 07-F0 MDP_FG_CTRL_AUTO
0170 BC (50)
0171 . C. -----
0172 C. MDP_FG_CTRL_MODE = AUTO [ ]
0173 C. -----
0174 C.
0175 C. *****
0176 C. SOT TI command set
0177 C. *****
0178 C. Execute, after the success of TBL upload.
0179 +. TI 2015-04-23 10:21:18.0
0180 DC 07-F0 MDP_SOT_MODE_OBSV
0181 BC (40)
0182 . C. -----
0183 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0184 C. -----
0185 C.
0186 C. Only when FG_CTRL_AUTO is used in RT.
0187 +. TI 2015-04-23 10:21:20.0
0188 DC 07-F0 MDP_FG_CTRL_AUTO
0189 BC (50)
0190 . C. -----
0191 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0192 C. -----
0193 C. ***** SOT END *****

```

```
0194 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0195 +. DC 07-FC EIS_MODE_CHG_ENA
0196 BC (20)
0197 . C. Verify EIS_MODE_CHG_FLG is ENA
0198 +. DC 07-FC EIS_MODE_MANU
0199 BC (21 02)
0200 . C. Verify EIS in MANUAL mode
0201 . C. Estimated OBSTBL upload time is 34s
0202 C. *****
0203 C. EIS START OBSTBL LOAD
0204 C. *****
0205 . S. RAM ram-820:EIS_OBSTBL
0206 ( )
0207 +. DC 07-FC EIS_DUMP_OBSTBL
0208 BC (07 07 07 00 00 70 00)
0209 C.
0210 C. Execute, after the success of OBSTBL upload.
0211 C. Set EIS TI-commands
0212 +. TI 2015-04-23 10:21:50.0
0213 DC 07-FC EIS_MODE_CHG_ENA
0214 BC (20)
0215 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0216 C. *****
0217 C. EIS END OBSTBL LOAD
0218 C. *****
0219 C.
0220 . C. ***** MDP 'úÃîñî»ö¼ÝðÊÂðñ¹ñèDCBC•x²è *****
0221 C. (¼ã°îÿÓÿÄÿËÿÌÿÍÿÎÿÏÿÐÿÑÿÒÿÓÿÔÿÕÿÖÿ×ÿØÿ¹ÿºÿ»ÿ¼ÿ½ÿ¾ÿ¿ÿÀÿÁÿÂÿÃÿÄÿÅÿ
0222 . S. DC-BC dcbc-402:DCBC
0223 (MDP_known_event)
0224 C.
0225 C.
0226 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎññË´Øñ¹ñèDCBC•x²è *****
0227 . S. DC-BC dcbc-153:DCBC
0228 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0229 C.
0230 C.
0231 . C. ;ãLOSÿÄÿËÿÏÿÏÿ¼Ä»Û;ä
0232 C.
0233 . C. ***** LOS *****
0234 C.
```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 0d)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 02)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2015-04-23 10:21:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `úÃîï»ö%ÝðËÄð¹ñèDCBC•x²è *****
0169 C. (%á°îÿÓYÁYÈYÏYÉYáYçYèèE%¼¼¼»Û¹ñè)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥D¥¹•İ Daily±çİÑñÈ´Øñ¹ñèDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Å»Û;ã
0180 C.
0181 . C. ***** LOS *****
0182 C.

```

*** OP Sequence for XRT ***

```

2015/04/23 10:31:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 10:31:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 10:31:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2015/04/23 10:32:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 03 00 00 00 00
2015/04/23 10:32:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/04/23 10:32:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2015/04/23 10:32:22.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2015/04/23 10:32:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2015/04/23 10:32:26.0 XRT_FLD_RESET_407_OG [0x197]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/04/23 10:34:56.0 XRT_QT_PROG_SET_417_OG [0x1a1]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0d
2015/04/23 10:34:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 02
2015/04/23 10:35:00.5 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 10:35:03.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 10:35:06.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/04/23 10:35:08.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/04/23 10:38:16.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/04/23 11:00:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 11:00:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 11:00:28.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2015/04/23 11:00:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 16 c0 52 97
2015/04/23 11:00:48.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/04/23 11:00:50.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2015/04/23 11:00:52.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2015/04/23 11:00:54.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2015/04/23 11:00:56.0 XRT_FLD_RESET_407_OG [0x197]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/04/23 11:03:26.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 12
2015/04/23 11:03:28.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 02
2015/04/23 11:03:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/04/23 11:15:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 11:15:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 11:15:28.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2015/04/23 11:15:30.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 13 74 43 a5
2015/04/23 11:15:48.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/04/23 11:15:50.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2015/04/23 11:15:52.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2015/04/23 11:15:54.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2015/04/23 11:15:56.0 XRT_FLD_RESET_407_OG [0x197]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/04/23 11:18:26.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 12
2015/04/23 11:18:28.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 02
2015/04/23 11:18:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/04/23 11:30:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 11:30:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/23 11:30:28.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2015/04/23 11:30:30.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM 5 02-76 00 12 0c 33 a5
2015/04/23 11:30:48.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/04/23 11:30:50.0 XRT_FLRCTRL_ENA_412_OG [0x19c]

```

Apr 23, 15 13:43

XRT_OGLIST_0077.chk

Page 2/10

2015/04/23	11:30:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/04/23	11:30:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/23	11:30:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	11:33:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2015/04/23	11:33:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2015/04/23	11:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/23	11:45:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	11:45:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	11:45:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/04/23	11:45:30.0	AOCS_Or-e-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	11 3f 23 4b
2015/04/23	11:45:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/04/23	11:45:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/04/23	11:45:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/04/23	11:45:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/23	11:45:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	11:48:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2015/04/23	11:48:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2015/04/23	11:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/23	12:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	12:00:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	12:00:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/04/23	12:00:30.0	AOCS_Or-e-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	10 b4 12 66
2015/04/23	12:00:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/04/23	12:00:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/04/23	12:00:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/04/23	12:00:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/23	12:00:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	12:03:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2015/04/23	12:03:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2015/04/23	12:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/23	12:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	12:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	12:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	12:19:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/23	12:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/23	12:38:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	12:38:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	12:38:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/04/23	12:39:00.0	AOCS_Or-e-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	10 9b 01 0e
2015/04/23	12:39:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/04/23	12:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/04/23	12:39:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/04/23	12:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/23	12:39:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	12:41:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2015/04/23	12:41:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02

Apr 23, 15 13:43

XRT_OGLIST_0077.chk

Page 3/10

2015/04/23	12:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/23	12:53:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/23	12:53:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/23	12:53:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/04/23	12:54:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 10 b4 f0 cd				
2015/04/23	12:54:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/23	12:54:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/23	12:54:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/23	12:54:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/23	12:54:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/23	12:56:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2015/04/23	12:56:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/04/23	12:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/23	13:08:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/23	13:08:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/23	13:08:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/04/23	13:09:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 11 3f df e8				
2015/04/23	13:09:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/23	13:09:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/23	13:09:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/23	13:09:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/23	13:09:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/23	13:11:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2015/04/23	13:11:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/04/23	13:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/23	13:23:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/23	13:23:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/23	13:23:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/04/23	13:24:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 12 0c cf 8e				
2015/04/23	13:24:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/23	13:24:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/23	13:24:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/23	13:24:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/23	13:24:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/23	13:26:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2015/04/23	13:26:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/04/23	13:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/23	13:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/23	13:38:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/23	13:38:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/04/23	13:39:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 13 74 bf 8e				
2015/04/23	13:39:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/23	13:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/23	13:39:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/23	13:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/23	13:39:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/23	13:41:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							

2015/04/23	13:41:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2015/04/23	13:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/23	13:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	13:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	13:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	13:57:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/23	14:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/23	14:16:30.0	XRT_Custom_430_OG [0x1ae]					
2015/04/23	14:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/23	14:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	14:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	14:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	14:21:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/23	14:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/23	14:30:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	14:30:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	14:30:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/04/23	14:31:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 16 c0 b0 9c	
2015/04/23	14:31:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/04/23	14:31:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/04/23	14:31:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/04/23	14:31:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/23	14:31:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	14:33:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2015/04/23	14:33:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/04/23	14:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/23	14:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	14:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	14:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/04/23	14:50:00.0	AOCS_OrE-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/04/23	14:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/04/23	14:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/04/23	14:50:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/04/23	14:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/23	14:50:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	14:52:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2015/04/23	14:52:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/04/23	14:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/23	15:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	15:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	15:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/23	15:35:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/23	15:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/23	16:19:00.0	XRT_Custom_430_OG [0x1ae]					
2015/04/23	16:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/23	17:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/23	17:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

Apr 23, 15 13:43

XRT_OGLIST_0077.chk

Page 5/10

2015/04/23	17:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/23	17:14:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/23	17:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/23	17:55:30.0	XRT_Custom_430_OG [0x1ae]				
2015/04/23	17:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/23	17:57:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	17:57:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	17:57:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/04/23	17:57:30.0	AOCS_Or-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 00 00 00 00
2015/04/23	17:57:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/04/23	18:00:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/04/23	18:00:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/23	18:00:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/04/23	18:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/23	18:07:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	18:07:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	18:07:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/04/23	18:07:30.0	AOCS_Or-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2015/04/23	18:07:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/04/23	18:07:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/04/23	18:07:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/04/23	18:07:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/23	18:07:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/23	18:10:26.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2015/04/23	18:10:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/04/23	18:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/23	18:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	18:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	18:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/23	18:52:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/23	18:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/23	19:32:00.0	XRT_Custom_430_OG [0x1ae]				
2015/04/23	19:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/23	20:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	20:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	20:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/23	20:31:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/23	20:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/23	21:09:30.0	XRT_Custom_430_OG [0x1ae]				
2015/04/23	21:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/23	22:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	22:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	22:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/23	22:09:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/23	22:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/23	22:44:30.0	XRT_Custom_430_OG [0x1ae]				
2015/04/23	22:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/23	23:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/23	23:47:32.0	XRT_CTRL_MANU_402_OG [0x192]				

2015/04/23	23:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/23	23:47:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/23	23:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/24	00:09:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/24	00:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/24	01:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	01:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	01:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	01:25:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/24	01:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/24	01:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/24	01:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/24	02:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	02:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	02:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	02:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/24	03:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/24	03:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/24	03:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/24	04:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	04:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	04:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	04:29:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/24	04:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/04/24	05:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/04/24	05:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/24	05:41:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	05:41:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	05:41:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	05:42:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/04/24	05:42:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/04/24	05:44:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/04/24	05:44:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/04/24	05:44:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/04/24	05:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2015/04/24	05:51:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/04/24	05:51:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	05:51:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/04/24	05:52:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/04/24	05:52:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00		
2015/04/24	05:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/04/24	05:52:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/04/24	05:52:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/04/24	05:52:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/04/24	05:54:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/04/24	05:54:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2015/04/24	05:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2015/04/24	06:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		

Apr 23, 15 13:43

XRT_OGLIST_0077.chk

Page 7/10

2015/04/24	06:09:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	06:09:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/04/24	06:09:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/04/24	06:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/04/24	06:40:30.0	XRT_Custom_430_OG [0x1ae]		
2015/04/24	06:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/04/24	07:50:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	07:50:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	07:50:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/04/24	07:50:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/04/24	07:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/04/24	08:19:00.0	XRT_Custom_430_OG [0x1ae]		
2015/04/24	08:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/04/24	09:29:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	09:29:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	09:29:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/04/24	09:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/04/24	09:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/04/24	09:57:30.0	XRT_Custom_430_OG [0x1ae]		
2015/04/24	09:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/04/24	11:16:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	11:16:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	11:16:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/04/24	11:16:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/04/24	11:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/04/24	11:36:00.0	XRT_Custom_430_OG [0x1ae]		
2015/04/24	11:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/04/24	12:54:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	12:54:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	12:54:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/04/24	12:54:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/04/24	12:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/04/24	13:14:30.0	XRT_Custom_430_OG [0x1ae]		
2015/04/24	13:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/04/24	14:33:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	14:33:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	14:33:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/04/24	14:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/04/24	14:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/04/24	14:53:00.0	XRT_Custom_430_OG [0x1ae]		
2015/04/24	14:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/04/24	14:55:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	14:55:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	14:55:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/04/24	14:55:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/04/24	14:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/04/24	15:07:30.0	XRT_Custom_430_OG [0x1ae]		
2015/04/24	15:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/04/24	16:11:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/04/24	16:11:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1

2015/04/24	16:11:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/24	16:11:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/24	16:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/24	16:54:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/24	16:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/24	17:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	17:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	17:39:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/04/24	17:40:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/04/24	17:40:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/04/24	17:42:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/04/24	17:42:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/24	17:42:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/04/24	17:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/24	17:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	17:49:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	17:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/04/24	17:50:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/04/24	17:50:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	17:50:02.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	17:50:04.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/24	17:50:06.5	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/24	17:50:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/24	17:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/24	17:50:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/24	17:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/24	17:50:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/24	17:52:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2015/04/24	17:52:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/04/24	17:53:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/24	18:31:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/24	18:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/24	19:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	19:28:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	19:28:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/24	19:28:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/24	19:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/24	20:08:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/24	20:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/24	21:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	21:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	21:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/24	21:06:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/24	21:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/24	21:44:30.0	XRT_Custom_430_OG [0x1ae]							
2015/04/24	21:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/24	22:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	22:45:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/24	22:45:04.0	XRT_FLD_RESET_415_OG [0x19f]							

2015/04/24	22:45:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/24	22:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/24	23:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/24	23:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/25	00:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	00:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	00:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/25	00:23:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/25	00:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/25	00:43:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/25	00:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	01:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	01:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	01:59:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/25	01:59:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/25	02:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/25	02:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/25	02:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	03:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	03:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	03:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/25	03:30:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/25	03:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/25	04:00:01.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/25	04:01:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	05:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	05:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	05:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/25	05:06:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/25	05:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/25	05:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/25	05:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/04/25	06:00:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/04/25	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/04/25	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/04/25	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/25	06:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/04/25	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/25	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/25	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/04/25	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/04/25	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/04/25	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/04/25	06:10:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/04/25	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]					

Apr 23, 15 13:43

XRT_OGLIST_0077.chk

Page 10/10

2015/04/25	06:10:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/25	06:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/25	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2015/04/25	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/04/25	06:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/25	06:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/25	06:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/25	06:46:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/25	06:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/25	07:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/25	07:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/04/25	08:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/25	08:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/25	08:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/25	08:26:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/25	08:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/25	08:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/25	08:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/04/25	10:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/25	10:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/25	10:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/25	10:07:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/25	10:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/25	10:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/25	10:40:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00