

XRT Timeline to be uploaded on 2015/04/28

Period: 2015/04/28 10:39:00 - 2015/05/02 09:47:00

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Normal mode

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XOB #1A78: QS (Filter-Ratio with Al/poly and Al/mesh), 384x384 at 1064, 1048 with G-band (33ms leak) 60s cad - AEC4													
Term	Pointing (x, y)		Comment										
04/28 10:52:00 - 04/28 16:49:54	Fixed (-684.0, 642.0)		# OP start + 10min Big Filament Obs.										
PROG= 06 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 99 90-time(s) 60.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	4	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	4	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19E5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)													
Term	Pointing (x, y)		Comment										
04/28 16:53:00 - 04/28 17:55:54	Track (116.7, 382.4) ^{Ⓞ 04/28 16:50:00}		# track AR 12333										
04/29 18:27:00 - 04/30 00:59:54	Track (329.0, 376.5) ^{Ⓞ 04/29 18:24:00}		# track AR 12333										
PROG= 07 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 62 8-time(s) 240.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi													
Term	Pointing (x, y)		Comment										
04/28 17:59:00 - 04/28 18:05:54	Fixed (0.0, 0.0)		synoptic, shifted -4.0 min										
04/29 06:31:00 - 04/29 06:37:54	Fixed (0.0, 0.0)		synoptic, shifted 28.0 min										
04/29 10:33:00 - 04/29 10:39:54	Fixed (0.0, 0.0)		# Synoptic for HOP282										
04/29 13:33:00 - 04/29 13:39:54	Fixed (0.0, 0.0)		# Synoptic for HOP282										
04/29 18:17:00 - 04/29 18:23:54	Fixed (0.0, 0.0)		synoptic, shifted 14.0 min										
04/30 06:03:00 - 04/30 06:09:54	Fixed (0.0, 0.0)		synoptic										
PROG= 03 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 33 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 40 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 77 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19E4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
04/28 18:09:00 - 04/28 23:29:30	Track (127.5, 382.2) ^{® 04/28 18:06:00}	# track AR 12333
04/29 06:41:00 - 04/29 10:29:54	Track (233.0, 379.7) ^{® 04/29 06:38:00}	# track AR 12333
04/29 13:43:00 - 04/29 17:32:00	Track (290.9, 377.9) ^{® 04/29 13:40:00}	# track AR12333
PROG= 19 Inf.-time(s)		
Subr= 1	1-time(s) 2.0sec	
└─ Seqn= 8	2-time(s) 2.0sec	
└─ Open/G-band	Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2	2-time(s) 2.0sec	
└─ Seqn= 24	1-time(s) 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 42	4-time(s) 2.0sec	
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Seqn= 62	10-time(s) 180.0sec	
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A76: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,8ms) - Leak (33ms) - 120s cad

Term	Pointing (x, y)	Comment
04/28 23:58:00 - 04/29 01:08:00	Fixed (-684.0, 642.0)	# IHOP254 Prominence Obs. with IRIS
PROG= 14 Inf.-time(s)		
Subr= 1	30-time(s) 120.0sec	
└─ Seqn= 29	1-time(s) 4.0sec	
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Subr= 2	1-time(s) 2.0sec	
└─ Seqn= 6	1-time(s) 2.0sec	
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19D7: PFB observation for possible flare -w/leak test, 60s cad

Term	Pointing (x, y)	Comment
04/29 01:31:00 - 04/29 05:52:00	Track (188.7, 380.9) ^{® 04/29 01:20:00}	# track AR 12333
04/30 01:03:00 - 04/30 05:59:54	Track (-285.0, -112.5) ^{® 04/30 01:00:00}	# Granulation Obs. (with IRIS) & EIS Sensitivity Monitoring
04/30 06:13:00 - 04/30 09:40:00	Track (420.9, 372.6) ^{® 04/30 06:10:00}	# track AR 12333
PROG= 18 Inf.-time(s)		
Subr= 1	1-time(s) 2.0sec	
└─ Seqn= 97	2-time(s) 2.0sec	
└─ Open/G-band	Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 3 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 0 3 2.0sec
Subr= 2	2-time(s) 2.0sec	
└─ Seqn= 51	30-time(s) 60.0sec	
└─ thin-Be/Open	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 3 1 2.0sec
└─ Al-poly/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) DPCM 3 1 2.0sec
└─ thin-Be/Open	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 3 2 2.0sec
└─ Al-poly/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) DPCM 3 2 2.0sec
└─ thin-Be/Open	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 3 3 2.0sec
└─ Al-poly/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) DPCM 3 3 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A79: HOP282 multi-thick filters (thin/med/thick-Be, med/thick-Al) w/G-band (33ms/45ms leak) wo/PFB, 120s

Term	Pointing (x, y)	Comment
04/29 10:43:00 - 04/29 13:29:54	Track (266.3, 378.7) ^{® 04/29 10:40:00}	# HOP282 @AR12333
PROG= 12 Inf.-time(s)		
Subr= 1	1-time(s) 2.0sec	
└─ Seqn= 8	1-time(s) 2.0sec	
└─ Open/G-band	Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Seqn= 24	1-time(s) 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2	15-time(s) 120.0sec	
└─ Seqn= 35	1-time(s) 2.0sec	
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=98 3 0 2.0sec
└─ med-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=98 3 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 3 0 2.0sec
└─ med-Al/Open	med-Al/Open close	Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=98 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 3 0 2.0sec

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Flare mode

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XOB #1A77: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
04/28 10:52:00 - 04/28 16:49:54	Fixed (-684.0, 642.0)	# OP start + 10min Big Filament Obs.
04/28 16:53:00 - 04/28 17:55:54	Track (116.7, 382.4) ^{Ⓜ 04/28 16:50:00}	# track AR 12333
04/28 18:09:00 - 04/28 23:29:30	Track (127.5, 382.2) ^{Ⓜ 04/28 18:06:00}	# track AR 12333
04/28 23:58:00 - 04/29 01:08:00	Fixed (-684.0, 642.0)	# IHOP254 Prominence Obs. with IRIS
04/29 01:31:00 - 04/29 05:52:00	Track (188.7, 380.9) ^{Ⓜ 04/29 01:20:00}	# track AR 12333
04/29 06:41:00 - 04/29 10:29:54	Track (233.0, 379.7) ^{Ⓜ 04/29 06:38:00}	# track AR 12333
04/29 10:43:00 - 04/29 13:29:54	Track (266.3, 378.7) ^{Ⓜ 04/29 10:40:00}	# HOP282 @AR12333
04/29 13:43:00 - 04/29 17:32:00	Track (290.9, 377.9) ^{Ⓜ 04/29 13:40:00}	# track AR12333
04/29 18:27:00 - 04/30 00:59:54	Track (329.0, 376.5) ^{Ⓜ 04/29 18:24:00}	# track AR 12333
04/30 01:03:00 - 04/30 05:59:54	Track (-285.0, -112.5) ^{Ⓜ 04/30 01:00:00}	# Granulation Obs. (with IRIS) & EIS Sensitivity Monitoring
04/30 06:13:00 - 04/30 09:40:00	Track (420.9, 372.6) ^{Ⓜ 04/30 06:10:00}	# track AR 12333

PROG= 02 30-time(s)

Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38 1-time(s) 2.0sec	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

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Active Region Search

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NOT USED

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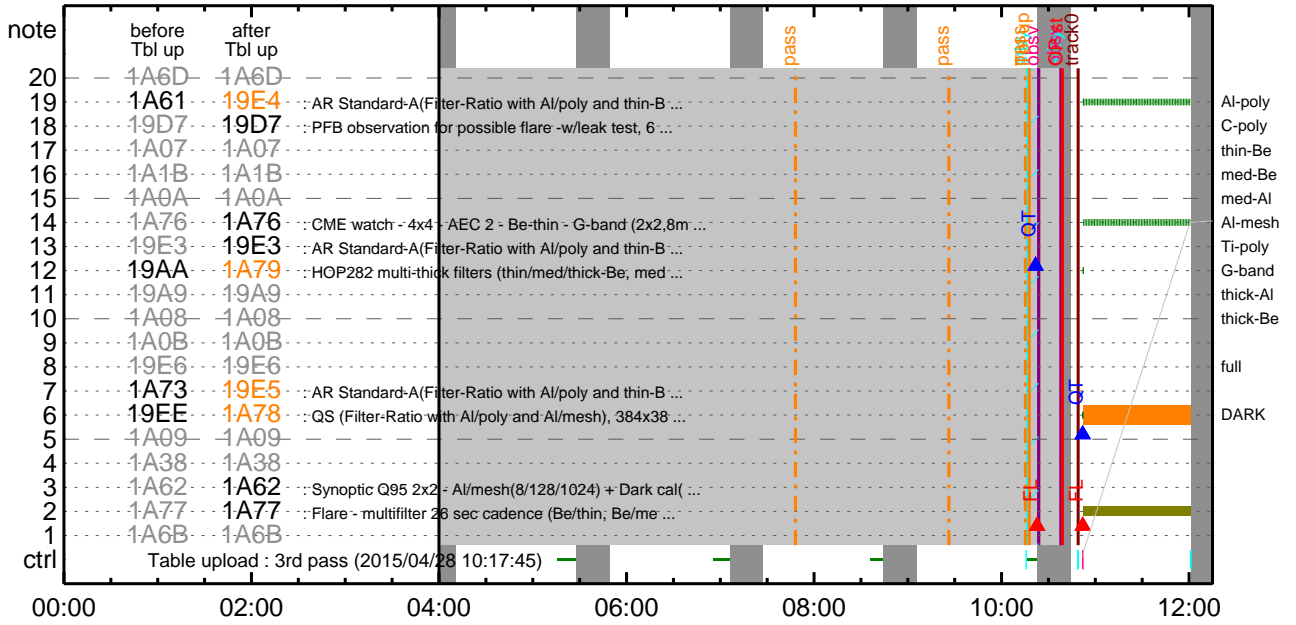
Flare Detection

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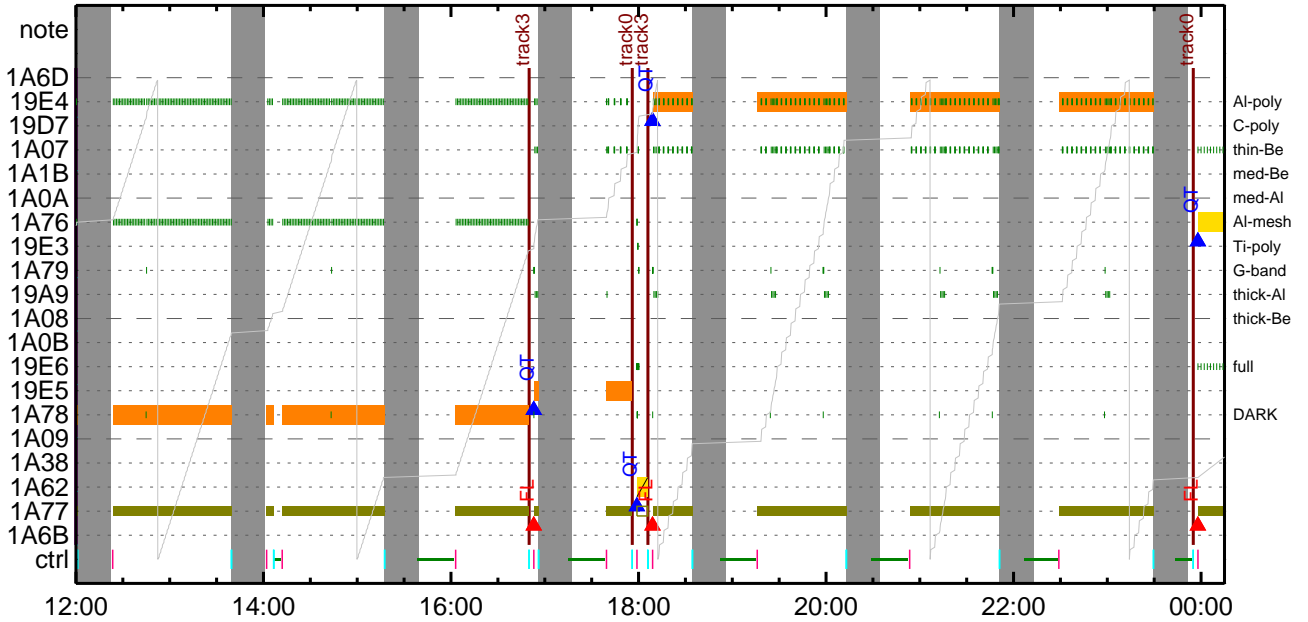
FLD Patrol

Term	Pointing (x, y)	Comment
04/28 18:06:18 - 04/29 06:28:18	Track (127.5, 382.2) ^{Ⓜ 04/28 18:06:00}	# track AR 12333
04/29 06:38:18 - 04/29 10:30:18	Track (233.0, 379.7) ^{Ⓜ 04/29 06:38:00}	# track AR 12333
04/29 10:40:18 - 04/29 13:30:18	Track (266.3, 378.7) ^{Ⓜ 04/29 10:40:00}	# HOP282 @AR12333
04/29 13:40:18 - 04/29 18:14:18	Track (290.9, 377.9) ^{Ⓜ 04/29 13:40:00}	# track AR12333
04/29 18:24:18 - 04/30 06:00:18	Track (329.0, 376.5) ^{Ⓜ 04/29 18:24:00}	# track AR 12333
04/30 06:10:18 - 05/02 09:47:00	Track (420.9, 372.6) ^{Ⓜ 04/30 06:10:00}	# track AR 12333
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec

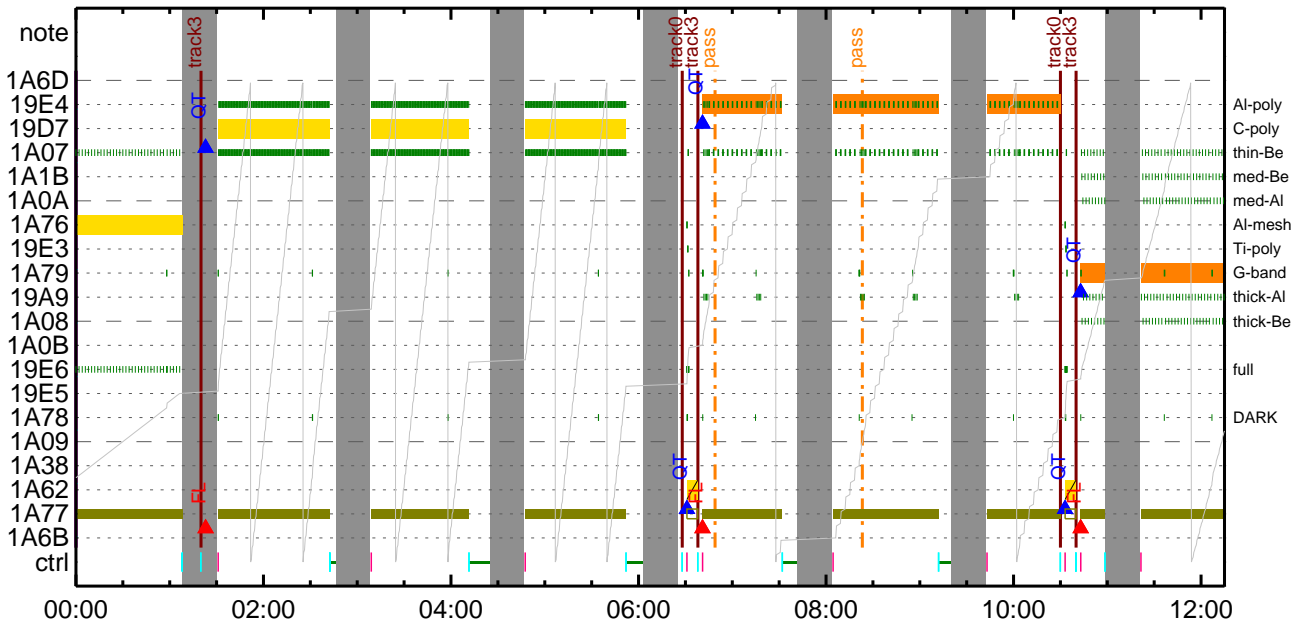
CMDI #0088 2015/04/28



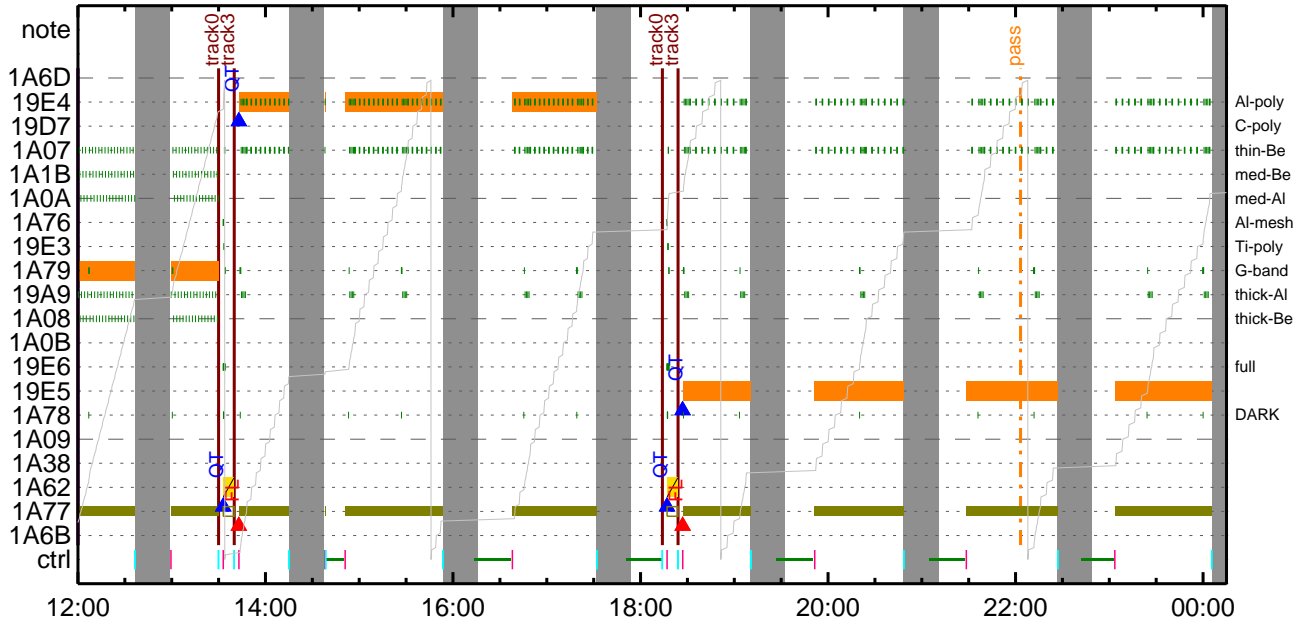
CMDI #0088 2015/04/28



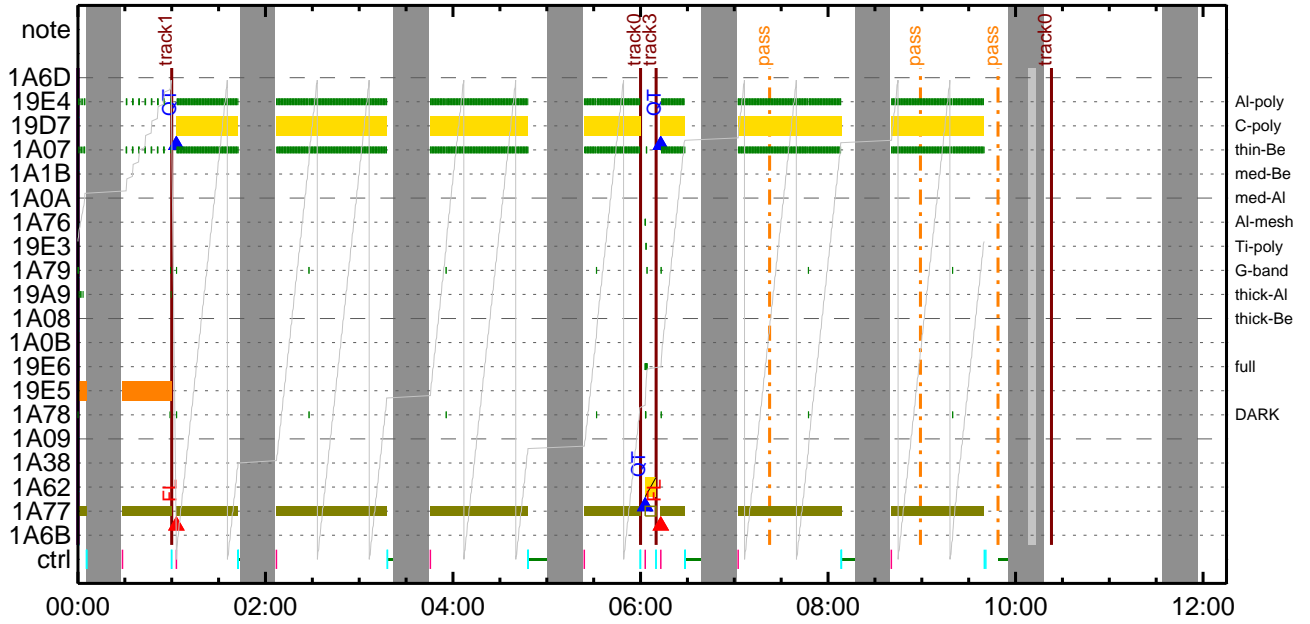
CMDI #0088 2015/04/29



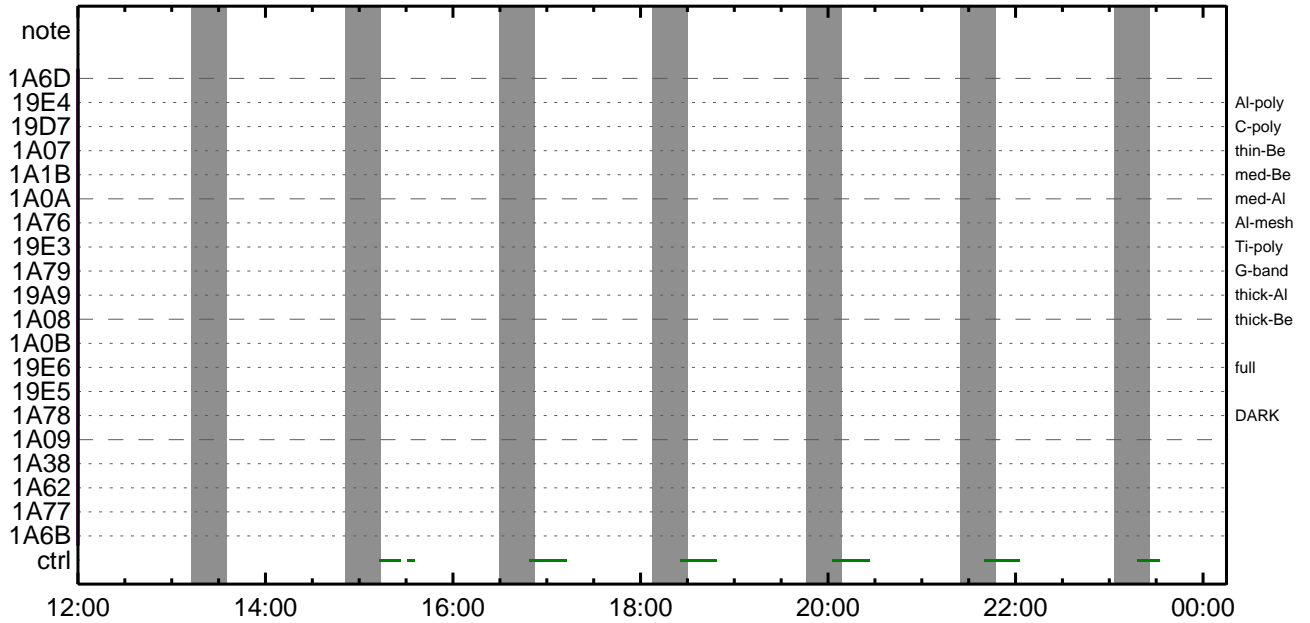
CMDI #0088 2015/04/29



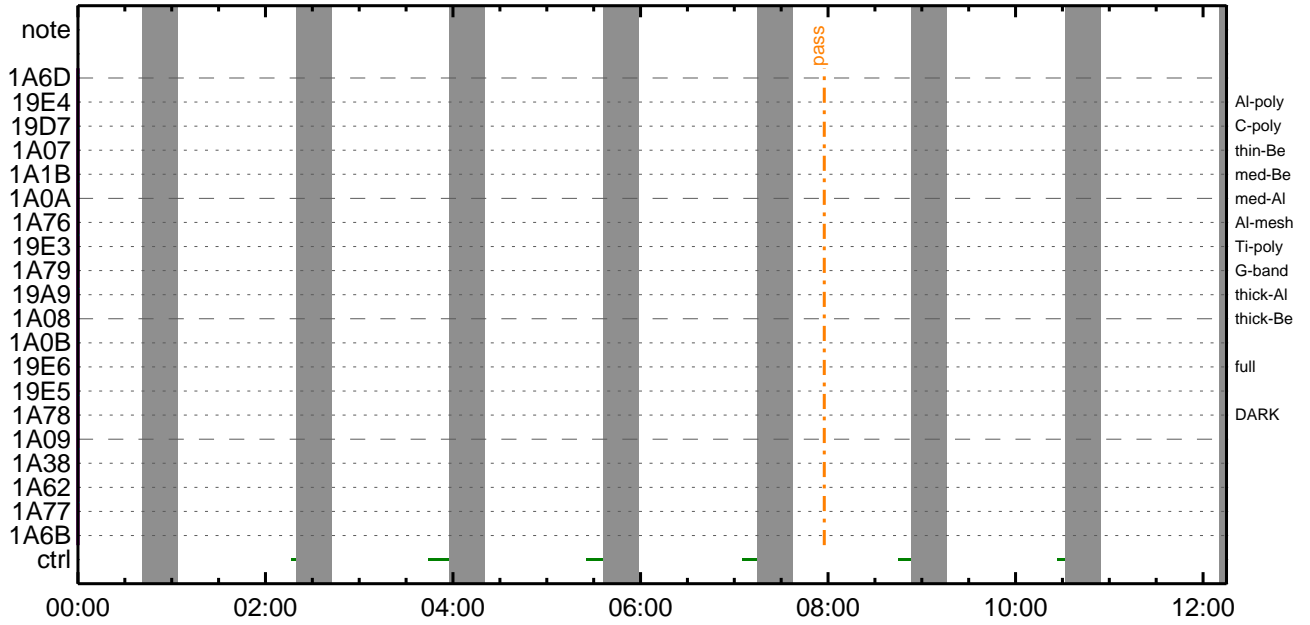
CMDI #0088 2015/04/30



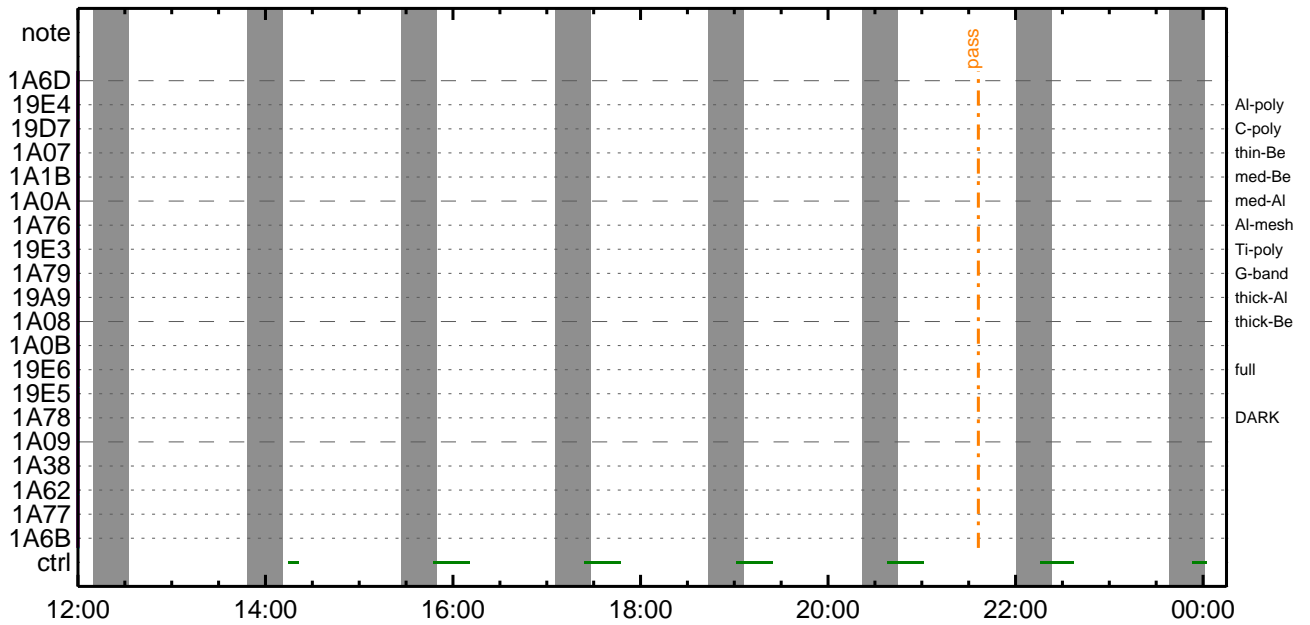
CMDI #0088 2015/04/30



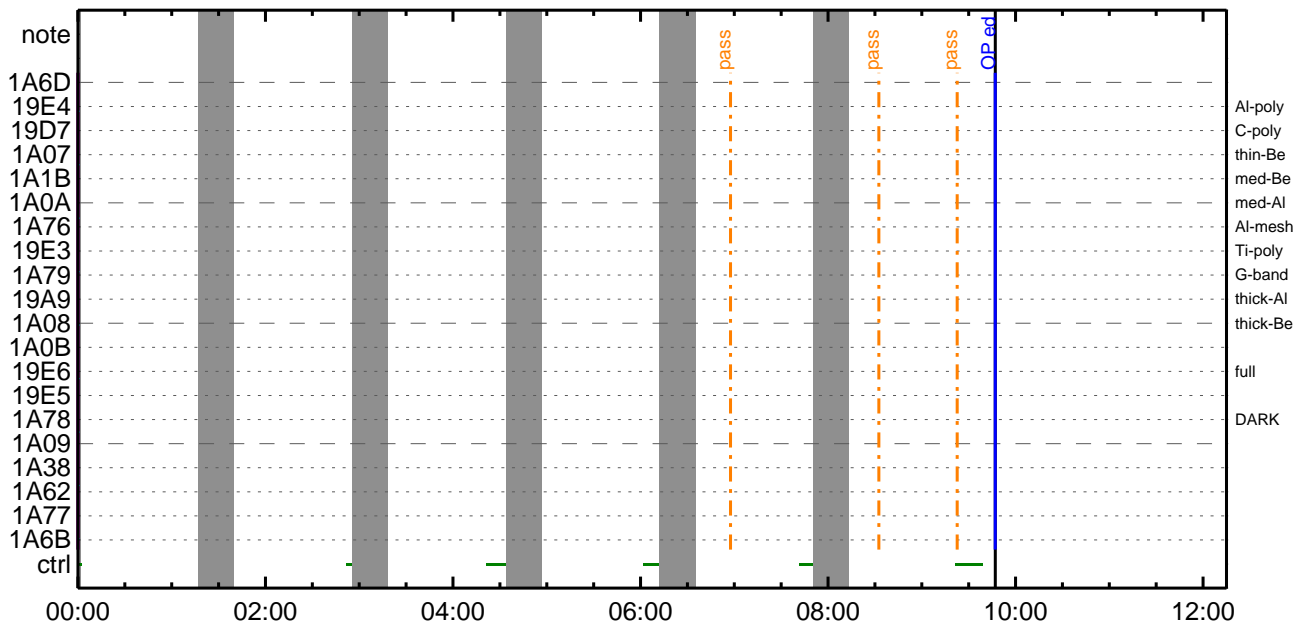
CMDI #0088 2015/05/01

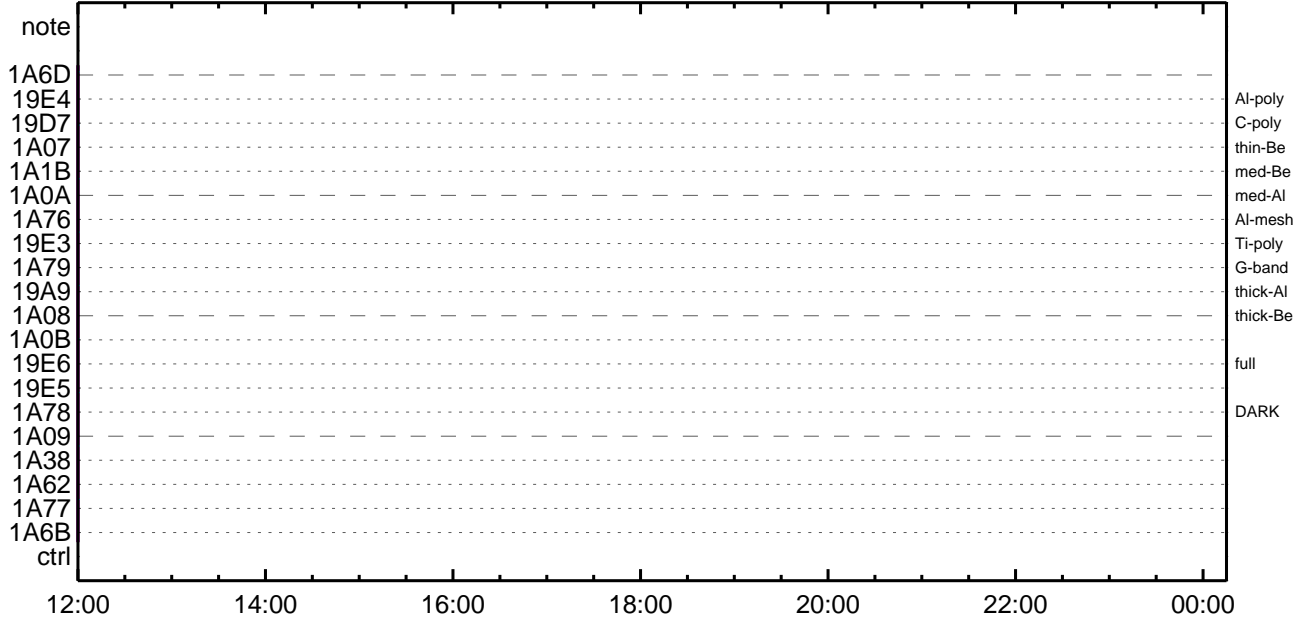


CMDI #0088 2015/05/01



CMDI #0088 2015/05/02






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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YA6Yx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-105:OP
0104 ( )
0105 . S. OG og-105:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPfî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAY6Yx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAY6Yx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAY6Yx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 . C. ***** °E²¼òî¼A´¶Á°òEÉ-ò°Á÷¿® (¼âµ-YAY6Yx½ê¼çòðÁÓÆòÇ¼ª°²òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOAD²-Á÷¿®NG²î¼î¹ç;ç°E²¼òî¼TI-CMDÁ÷¿®²î¼Á¹Ô²°²E²ò³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²î¼±°²î¼Y¹²ç¹Ô²|²³òE;f
0181 C.
0182 . C. TIY³Y²Y6YÉ²òðÁDî¿(UT)
0183 +. TI 2015-04-28 10:34:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-04-28 10:34:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-04-28 10:34:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

0194 C.
0195 +. TI 2015-04-28 10:38:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. 꺝꺝[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼αĩÄē%ĩĨñαĩŷÄŷ\$ŷÄŷ-¹ãĩÛ
0200 C. 꺝꺝[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. 꺝꺝[HK1_TI_CMD_NUM] EQ 4
0202 C. 꺝꺝[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. 꺝꺝[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 . C. *****
0206 C. TIĩŧ°èŷÄŷÖŷx
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. 꺝꺝[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. 꺝꺝[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. 꺝꺝[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. 꺝꺝[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. 꺝꺝[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. 꺝꺝[HK1_PKT_FORM_NO] EQ 7
0220 C. 꺝꺝[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. 꺝꺝[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. 꺝꺝[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. 꺝꺝[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 . C. ŷÄŷÖŷx½ªĩ»αð³ĩÇ\$
0226 C. 꺝꺝[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 . C. RAM ID=TI_TBLαĩ¼Ê¹ç•è²İOKαð³ĩÇ\$
0229 C.
0230 . C. DHUŷã;¼ŷÊ;Ê¼ŷ¼. ŷi;¼ŷË;Êαðĩãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. 꺝꺝[HK1_PKT_FORM_NO] EQ 2
0234 C. 꺝꺝[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. 꺝꺝[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. 꺝꺝[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2015-04-28 10:38:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 . C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2015-04-28 10:38:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2015-04-28 10:38:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2015-04-28 10:38:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´ũÃĩαĩ»ö¼ŷαÊÄα¹αēDCBC•x²è *****
0276 C. (¼ã°ĩŷÖŷÄŷËŷŷŷËŷáŷçŷèè¼αα¼Ã»Ûα¹è)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷĐŷ¹•İ Daily±çİñèË'Øα¹αēDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLŌŷÄŷ\$ŷÄŷ-¼Ã»Û;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-107 2015-04-28 13:17:54 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁY$;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ªÁª•µ°È»Í×ÁÇªÍY¿YÁ×YÍ;¼YÉ;ÈÈ½µ•íÉ;ÈªÈ¼°ÇÕª•ª¿¼í¹¿ªÍ;¿À®, ùª¹ªÈªªªÇÁ+¿®ª•ªÈªªªªªÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-268:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. Y¿YÁY×YÍ;¼YÉªÍÁª°ªÈSTS_CHKªðOFFªÈª¹ªÈ
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKªðONªÈª¹ªÈ
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 . C.
0059 . C. < Resume FG table (auto mode) >
0060 +. DC 07-F0 MDP_FG_CTRL_AUTO
0061 BC (50)
0062 . C. -----
0063 C. MDP_FG_CTRL_MODE = AUTO [ ]
0064 C. -----
0065 C.
0066 C. *****
0067 C. SOT TI command set
0068 C. *****
0069 C. Execute, after the success of TBL upload.
0070 +. TI 2015-04-28 10:38:18.0
0071 DC 07-F0 MDP_SOT_MODE_OBSV
0072 BC (40)
0073 . C. -----
0074 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0075 C. -----
0076 C.
0077 C. Only when FG_CTRL_AUTO is used in RT.
0078 +. TI 2015-04-28 10:38:20.0
0079 DC 07-F0 MDP_FG_CTRL_AUTO
0080 BC (50)
0081 . C. -----
0082 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0083 C. -----
0084 C. ***** SOT END *****
0085 C.
0086 C. ***** XRT START *****
0087 C.
0088 +. DC 07-F0 MDP_XRT_CTRL_MANU
0089 BC (c1)
0090 +. DC 07-F0 MDP_XRT_MODE_STBY
0091 BC (c3)
0092 . C. ----- Success Verify ? OK / NG_____
0093 C.
0094 C. XRT Obs. Table Upload
0095 . S. RAM ram-291:MDP_OBS_X
```


*** OP Sequence for XRT ***

```

2015/04/28 10:48:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 10:48:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 10:48:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2015/04/28 10:49:00.0 AOCS_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 c6 f2 3c cc
2015/04/28 10:49:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/04/28 10:49:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2015/04/28 10:49:22.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2015/04/28 10:49:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2015/04/28 10:49:26.0 XRT_FLD_RESET_407_OG [0x197]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/04/28 10:51:56.0 XRT_QT_PROG_SET_442_OG [0x1ba]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 06
2015/04/28 10:51:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 02
2015/04/28 10:52:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/04/28 12:01:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 12:01:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 12:01:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/04/28 12:01:06.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/04/28 12:04:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/04/28 12:22:30.0 XRT_Custom_430_OG [0x1ae]
2015/04/28 12:23:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/04/28 13:39:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 13:39:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 13:39:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/04/28 13:39:36.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/04/28 13:42:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/04/28 14:01:00.0 XRT_Custom_430_OG [0x1ae]
2015/04/28 14:02:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/04/28 14:06:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 14:06:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 14:06:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/04/28 14:06:36.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/04/28 14:09:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/04/28 14:11:00.0 XRT_Custom_430_OG [0x1ae]
2015/04/28 14:12:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/04/28 15:17:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 15:17:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 15:17:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/04/28 15:17:36.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/04/28 15:20:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/04/28 16:02:00.0 XRT_Custom_430_OG [0x1ae]
2015/04/28 16:03:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/04/28 16:49:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 16:49:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/04/28 16:49:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2015/04/28 16:50:00.0 AOCS_Ore-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 03 00 00 00 00
2015/04/28 16:50:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/04/28 16:50:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2015/04/28 16:50:22.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2015/04/28 16:50:24.0 XRT_ARS_DIS_423_OG [0x1a7]

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2015/04/28	16:50:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/28	16:52:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/28	16:52:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2015/04/28	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/04/28	16:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/28	16:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	16:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	16:56:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/28	16:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/28	17:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/28	17:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	e9
2015/04/28	17:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/28	17:55:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	17:55:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	17:56:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/04/28	17:56:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00
2015/04/28	17:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/04/28	17:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/04/28	17:58:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/28	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/04/28	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/28	18:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	18:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	18:06:00.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/04/28	18:06:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2015/04/28	18:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/04/28	18:06:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/04/28	18:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/04/28	18:06:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/28	18:08:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/28	18:08:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2015/04/28	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/04/28	18:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/28	18:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	18:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	18:34:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/28	18:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/28	19:15:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/28	19:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	e9
2015/04/28	20:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/28	20:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	20:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/28	20:13:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/28	20:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/28	20:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/28	20:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	e9
2015/04/28	21:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/28	21:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2015/04/28	21:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/28	21:51:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/28	21:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/04/28	22:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/04/28	22:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/28	23:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/28	23:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/28	23:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/28	23:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/28	23:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/04/28	23:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/04/28	23:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/28	23:54:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/28	23:55:00.0	AOCs_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/04/28	23:55:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 c6 f2 3c cc			
2015/04/28	23:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/04/28	23:55:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/04/28	23:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/04/28	23:55:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/04/28	23:57:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/28	23:57:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2015/04/28	23:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2015/04/29	01:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/29	01:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/29	01:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/29	01:08:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/29	01:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/04/29	01:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/04/29	01:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/29	01:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/29	01:20:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/04/29	01:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00			
2015/04/29	01:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/04/29	01:20:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/04/29	01:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/04/29	01:20:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/04/29	01:22:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/29	01:22:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2015/04/29	01:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2015/04/29	01:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/29	02:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/29	02:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/29	02:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/29	02:42:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/29	02:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/04/29	03:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/04/29	03:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/29	04:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			

2015/04/29	04:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	04:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	04:11:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/29	04:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/29	04:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/29	04:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/29	05:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	05:52:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	05:52:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	05:52:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/29	05:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/29	06:27:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/29	06:27:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	06:27:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	06:28:00.0	AOCS_Or-e-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/04/29	06:28:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/04/29	06:30:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/04/29	06:30:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/04/29	06:30:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/29	06:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/04/29	06:37:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/29	06:37:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	06:37:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	06:38:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/04/29	06:38:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/04/29	06:38:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/04/29	06:38:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/04/29	06:38:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/04/29	06:38:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/04/29	06:40:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/29	06:40:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2015/04/29	06:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/04/29	07:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/29	07:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	07:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	07:32:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/29	07:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/29	08:03:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/29	08:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/29	09:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	09:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	09:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/04/29	09:12:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/04/29	09:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/04/29	09:42:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/04/29	09:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/29	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/04/29	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2015/04/29	10:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	10:30:00.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/04/29	10:30:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00
2015/04/29	10:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/04/29	10:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/04/29	10:32:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/29	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/04/29	10:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/29	10:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	10:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	10:40:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/04/29	10:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2015/04/29	10:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/04/29	10:40:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/04/29	10:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/04/29	10:40:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/29	10:42:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/29	10:42:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2015/04/29	10:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/04/29	10:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/29	10:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	10:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	10:58:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/29	11:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/29	11:20:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/29	11:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/04/29	12:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/29	12:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	12:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	12:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/29	12:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/29	12:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/29	12:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/04/29	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/29	13:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	13:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	13:30:00.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/04/29	13:30:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00
2015/04/29	13:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/04/29	13:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/04/29	13:32:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/29	13:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/04/29	13:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/29	13:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	13:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/29	13:40:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/04/29	13:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
			MDP_XRT_FLD_ENA	1	07-F0	d8

2015/04/29	13:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/29	13:40:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/29	13:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/29	13:40:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/29	13:42:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2015/04/29	13:42:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/04/29	13:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/29	14:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	14:15:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	14:15:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/29	14:15:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/29	14:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/29	14:37:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/29	14:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/29	14:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	14:38:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	14:38:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/29	14:38:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/29	14:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/29	14:50:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/29	14:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/29	15:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	15:53:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	15:53:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/29	15:53:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/29	15:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/29	16:37:00.0	XRT_Custom_430_OG [0x1ae]							
2015/04/29	16:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/29	17:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	17:32:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	17:32:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/29	17:32:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/29	17:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/29	18:13:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	18:13:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	18:13:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/04/29	18:14:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/04/29	18:14:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/04/29	18:16:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/04/29	18:16:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/29	18:16:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2015/04/29	18:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/29	18:23:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	18:23:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	18:23:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/04/29	18:24:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03	00	00	00	00
2015/04/29	18:24:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/29	18:24:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				

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2015/04/29	18:24:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/29	18:24:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/29	18:24:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/29	18:26:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2015/04/29	18:26:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/04/29	18:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/29	19:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	19:10:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	19:10:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/29	19:10:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/29	19:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/29	19:50:30.0	XRT_Custom_430_OG [0x1ae]							
2015/04/29	19:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/29	20:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	20:48:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	20:48:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/29	20:48:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/29	20:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/29	21:27:30.0	XRT_Custom_430_OG [0x1ae]							
2015/04/29	21:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/29	22:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	22:27:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/29	22:27:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/29	22:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/29	22:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/29	23:02:30.0	XRT_Custom_430_OG [0x1ae]							
2015/04/29	23:03:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/30	00:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/30	00:05:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/30	00:05:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/30	00:05:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/04/30	00:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/04/30	00:27:30.0	XRT_Custom_430_OG [0x1ae]							
2015/04/30	00:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/30	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/30	00:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/30	00:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2015/04/30	01:00:00.5	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01	00 00 00 00			
2015/04/30	01:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/04/30	01:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/04/30	01:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/04/30	01:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/04/30	01:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/04/30	01:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2015/04/30	01:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/04/30	01:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/04/30	01:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/30	01:42:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/04/30	01:42:34.0	XRT_FLD_RESET_415_OG [0x19f]							

2015/04/30	01:42:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/30	01:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/30	02:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/30	02:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/30	03:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	03:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	03:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/30	03:18:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/30	03:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/30	03:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/30	03:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	04:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	04:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	04:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/30	04:48:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/30	04:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/30	05:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/30	05:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/04/30	06:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2015/04/30	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/04/30	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/04/30	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/30	06:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/04/30	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/30	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/04/30	06:10:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00
2015/04/30	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/04/30	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/04/30	06:10:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/04/30	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/04/30	06:10:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/30	06:12:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2015/04/30	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/04/30	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/30	06:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	06:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	06:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/04/30	06:28:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/04/30	06:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/04/30	07:01:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/04/30	07:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	08:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/04/30	08:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2015/04/30	08:08:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/04/30	08:08:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/04/30	08:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/04/30	08:39:30.0	XRT_Custom_430_OG [0x1ae]						
2015/04/30	08:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/04/30	09:40:00.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/30	09:41:00.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/04/30	10:23:00.0	AOCS_Or-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			