

# XRT Timeline to be uploaded on 2015/05/12

Period: 2015/05/12 09:44:00 - 2015/05/16 10:29:00

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## Normal mode

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XOB #1A76: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,8ms) - Leak (33ms) - 120s cad												
Term	Pointing (x, y)		Comment									
05/12 09:57:00 - 05/12 11:59:30	Fixed ( 764.0, -571.0)		# OP start + 10min, HOP255 prominence at SW limb									
05/12 19:26:30 - 05/12 20:22:30	Track ( 131.7, 0.4) @ 05/12 19:00:00		EIS sensitivity monitoring									
05/13 08:18:30 - 05/13 11:59:54	Fixed ( 764.0, -571.0)		HOP255 coordination with MSDP prominence at SW limb									
05/14 08:03:00 - 05/14 10:00:00	Fixed ( 740.0, -611.0)		HOP255 coordination with MSDP prominence at SW limb									
<b>PROG= 14 Inf.-time(s)</b>												
└ Subr= 1 30-time(s) 120.0sec												
└└ Seqn= 29 1-time(s) 4.0sec												
└└└ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└ Subr= 2 1-time(s) 2.0sec												
└└ Seqn= 6 1-time(s) 2.0sec												
└└└ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└└└ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1A83: Stray light study 2015-9 ;Ti-poly and C-poly, 2x2 full FOV(1min-cad) and 2x2 256 on S-pole												
Term	Pointing (x, y)		Comment									
05/12 12:07:30 - 05/12 12:30:00	Fixed ( -13.0, -960.0)		HOP081 S-pole									
<b>PROG= 08 1-time(s)</b>												
└ Subr= 1 1-time(s) 2.0sec												
└└ Seqn= 17 1-time(s) 150.0sec												
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
└└└ C-poly/Open C-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
└└└ C-poly/Open C-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
└ Subr= 2 21-time(s) 2.0sec												
└└ Seqn= 68 1-time(s) 10.0sec												
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 500ms Obs 2x2 512x512 (1600, 1400) Q=95 0 0 2.0sec												
└└└ Open/G-band Open/G-band close Safe Norm 16ms Obs 2x2 512x512 (1600, 1400) Q=95 0 0 2.0sec												
└ Subr= 3 6-time(s) 2.0sec												
└└ Seqn= 49 1-time(s) 2.0sec												
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
└└└ C-poly/Open C-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec												
└└ Seqn= 68 5-time(s) 10.0sec												
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 500ms Obs 2x2 512x512 (1600, 1400) Q=95 0 0 2.0sec												
└└└ Open/G-band Open/G-band close Safe Norm 16ms Obs 2x2 512x512 (1600, 1400) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1A1B: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 33ms												
Term	Pointing (x, y)		Comment									
05/12 12:37:30 - 05/12 17:05:30	Fixed ( -13.0, -960.0)		HOP081 S-pole									
<b>PROG= 12 Inf.-time(s)</b>												
└ Subr= 1 1-time(s) 2.0sec												
└└ Seqn= 8 2-time(s) 2.0sec												
└└└ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└ Subr= 2 1-time(s) 2.0sec												
└└ Seqn= 48 1-time(s) 30.0sec												
└└└ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└ Subr= 3 30-time(s) 2.0sec												
└└ Seqn= 57 1-time(s) 60.0sec												
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└└└ Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi												
Term	Pointing (x, y)		Comment									
05/12 17:53:00 - 05/12 17:59:54	Fixed ( 0.0, 0.0)		synoptic, shifted manually									
05/13 05:38:00 - 05/13 05:44:54	Fixed ( 0.0, 0.0)		synoptic, shifted -25.0 min									
05/13 18:27:30 - 05/13 18:34:24	Fixed ( 0.0, 0.0)		synoptic, shifted 24.5 min									
05/14 05:58:30 - 05/14 06:05:24	Fixed ( 0.0, 0.0)		synoptic, shifted -4.5 min									
<b>PROG= 03 1-time(s)</b>												
└ Subr= 1 1-time(s) 12.0sec												
└└ Seqn= 33 1-time(s) 4.0sec												
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└└ Seqn= 5 1-time(s) 2.0sec												
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												

	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 40</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 77</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 6</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19E4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)**

Term	Pointing (x, y)	Comment
05/12 18:03:00 - 05/12 18:44:00	Track ( -780.3, -173.6) <sup>05/12 18:00:00</sup>	coordination w/DST AR12344
05/13 12:03:00 - 05/13 13:59:54	Track ( -681.6, -168.0) <sup>05/13 12:00:00</sup>	AR12344
05/13 14:03:00 - 05/13 17:19:45	Track ( -669.3, -167.4) <sup>05/13 14:00:00</sup>	coordination w/DST AR12344
05/14 03:59:00 - 05/14 05:55:24	Track ( -579.9, -164.2) <sup>05/14 03:30:00</sup>	AR12344
05/14 06:08:30 - 05/14 07:59:54	Track ( -561.6, -163.7) <sup>05/14 06:05:30</sup>	Cont.

<b>PROG= 19</b>	<b>Inf.-time(s)</b>												
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 8</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 24</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 42</b>	<b>4-time(s)</b>	<b>2.0sec</b>											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 62</b>	<b>10-time(s)</b>	<b>180.0sec</b>											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1A74: AR Standard-B(Morphology), Be/thin 48s-cad, multifilter PFB (Al/poly, Be/thin, Be/med(4096ms)), multifilter context, 384x384, 1064x1048, G-band**

Term	Pointing (x, y)	Comment
05/12 23:03:00 - 05/13 02:53:00	Track ( -755.2, -171.9) <sup>05/12 23:00:00</sup>	HOP245 at AR12344
05/13 23:13:30 - 05/14 03:26:00	Track ( -610.9, -165.2) <sup>05/13 23:00:00</sup>	HOP245 AR12344

<b>PROG= 05</b>	<b>Inf.-time(s)</b>												
<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 8</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Seqn= 24</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
<b>Seqn= 7</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 58</b>	<b>50-time(s)</b>	<b>2.0sec</b>											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19E5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)**

Term	Pointing (x, y)	Comment
05/13 03:23:00 - 05/13 05:34:54	Track ( -733.7, -170.6) <sup>05/13 03:00:00</sup>	AR12344

05/13 05:48:00 - 05/13 07:43:00 Track ( -718.4, -169.8) @ 05/13 05:45:00 Cont.  
 05/13 18:37:30 - 05/13 22:24:45 Track ( -640.2, -166.3) @ 05/13 18:34:30 AR12344

**PROG= 07 Inf-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 8 2-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2 2-time(s) 2.0sec</b>													
└─ <b>Seqn= 24 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ <b>Seqn= 42 4-time(s) 2.0sec</b>													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ <b>Seqn= 62 8-time(s) 240.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1A82: Stray light study 2015-7/8 ;Ti-poly and C-poly, 2x2 full FOV(1min-cad) and 2x2 256 on AR(10sec-cad)**

Term	Pointing (x, y)	Comment
05/13 17:38:30 - 05/13 18:24:24	Fixed ( 945.0, 0.0)	stray light measurments on W limb
05/13 22:33:30 - 05/13 22:59:54	Fixed ( -945.0, 0.0)	stray light measurments on E limb

**PROG= 01 1-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 41 1-time(s) 150.0sec</b>													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	C-poly/Open	C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 21-time(s) 2.0sec</b>													
└─ <b>Seqn= 87 1-time(s) 10.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	512x512 (1024, 840)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	8ms	Obs	2x2	512x512 (1024, 840)	Q=95	0	0	2.0sec
<b>Subr= 3 6-time(s) 2.0sec</b>													
└─ <b>Seqn= 79 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	C-poly/Open	C-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ <b>Seqn= 87 5-time(s) 10.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	512x512 (1024, 840)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	8ms	Obs	2x2	512x512 (1024, 840)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1A77: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
05/12 09:57:00 - 05/12 11:59:30	Fixed ( 764.0, -571.0)	# OP start + 10min, HOP255 prominence at SW limb
05/12 12:37:30 - 05/12 17:05:30	Fixed ( -13.0, -960.0)	HOP081 S-pole
05/12 18:03:00 - 05/12 18:44:00	Track ( -780.3, -173.6) @ 05/12 18:00:00	coordination w/DST AR12344
05/12 19:26:30 - 05/12 20:22:30	Track ( 131.7, 0.4) @ 05/12 19:00:00	EIS sensitivity monitoring
05/12 23:03:00 - 05/13 02:53:00	Track ( -755.2, -171.9) @ 05/12 23:00:00	HOP245 at AR12344
05/13 03:23:00 - 05/13 05:34:54	Track ( -733.7, -170.6) @ 05/13 03:00:00	AR12344
05/13 05:48:00 - 05/13 07:43:00	Track ( -718.4, -169.8) @ 05/13 05:45:00	Cont.
05/13 08:18:30 - 05/13 11:59:54	Fixed ( 764.0, -571.0)	HOP255 coordination with MSDP prominence at SW limb
05/13 12:03:00 - 05/13 13:59:54	Track ( -681.6, -168.0) @ 05/13 12:00:00	AR12344
05/13 14:03:00 - 05/13 17:19:45	Track ( -669.3, -167.4) @ 05/13 14:00:00	coordination w/DST AR12344
05/13 18:37:30 - 05/13 22:24:45	Track ( -640.2, -166.3) @ 05/13 18:34:30	AR12344
05/13 23:13:30 - 05/14 03:26:00	Track ( -610.9, -165.2) @ 05/13 23:00:00	HOP245 AR12344
05/14 03:59:00 - 05/14 05:55:24	Track ( -579.9, -164.2) @ 05/14 03:30:00	AR12344
05/14 06:08:30 - 05/14 07:59:54	Track ( -561.6, -163.7) @ 05/14 06:05:30	Cont.
05/14 08:03:00 - 05/14 10:00:00	Fixed ( 740.0, -611.0)	HOP255 coordination with MSDP prominence at SW limb

**PROG= 02 30-time(s)**

<b>Subr= 1 20-time(s) 2.0sec</b>													
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn=100 1-time(s) 10.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 10 1-time(s) 2.0sec</b>													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11	1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn= 38	1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

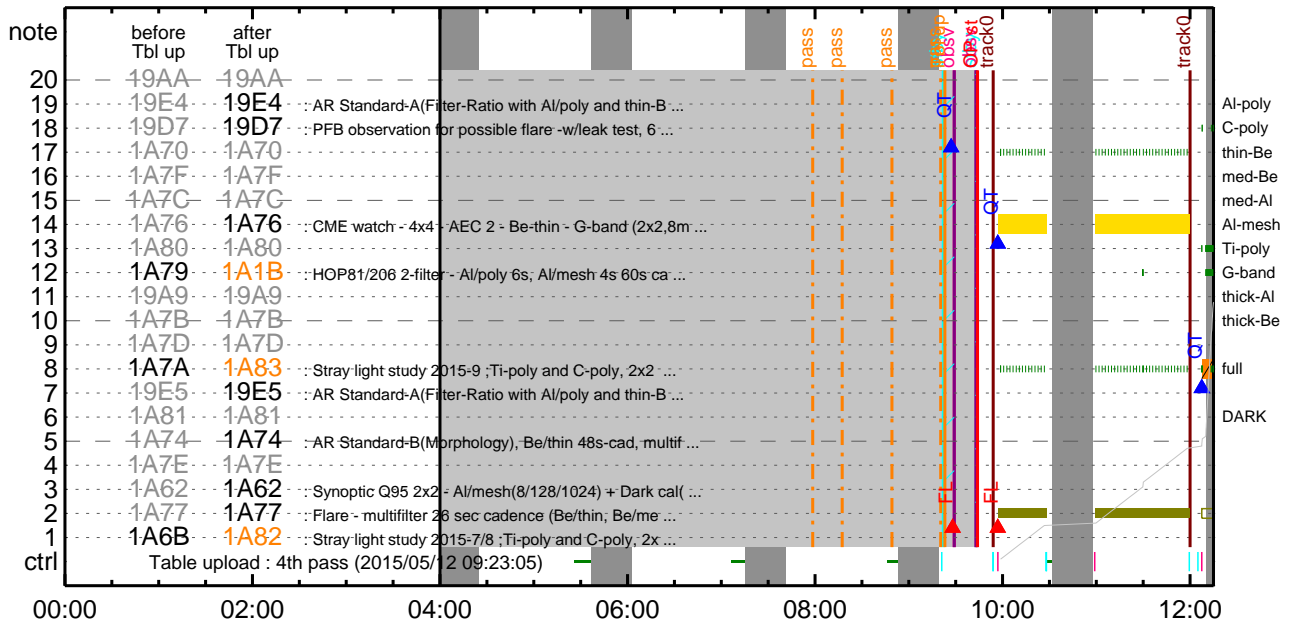
\* \* \* \* \*

### Flare Detection

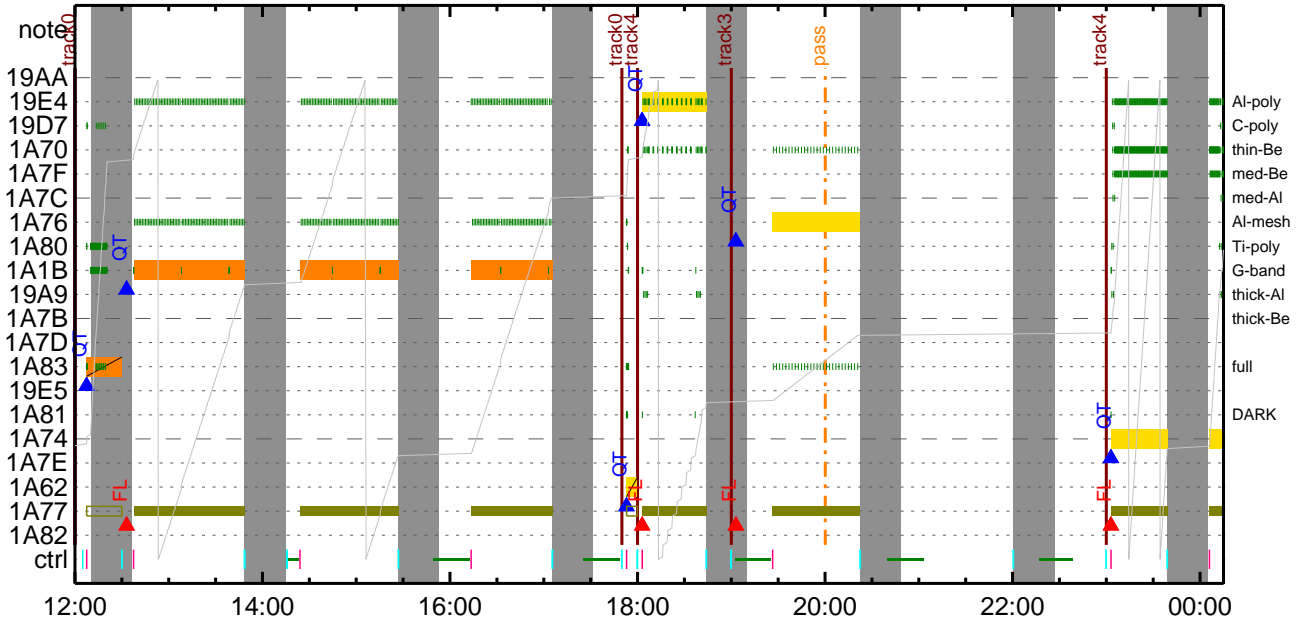
\* \* \* \* \*

FLD Patrol													
Term		Pointing (x, y)				Comment							
05/12 12:30:24 - 05/12 17:50:18		Fixed ( -13.0, -960.0)				HOP081 S-pole							
05/12 18:00:18 - 05/13 05:35:18		Track ( -780.3, -173.6) <small>@ 05/12 18:00:00</small>				coordination w/DST AR12344							
05/13 05:45:18 - 05/13 17:36:22		Track ( -718.4, -169.8) <small>@ 05/13 05:45:00</small>				Cont.							
05/13 18:34:48 - 05/13 22:31:22		Track ( -640.2, -166.3) <small>@ 05/13 18:34:30</small>				AR12344							
05/13 23:00:18 - 05/14 05:55:48		Track ( -610.9, -165.2) <small>@ 05/13 23:00:00</small>				HOP245 AR12344							
05/14 06:05:48 - 05/16 10:29:00		Track ( -561.6, -163.7) <small>@ 05/14 06:05:30</small>				Cont.							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec		
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

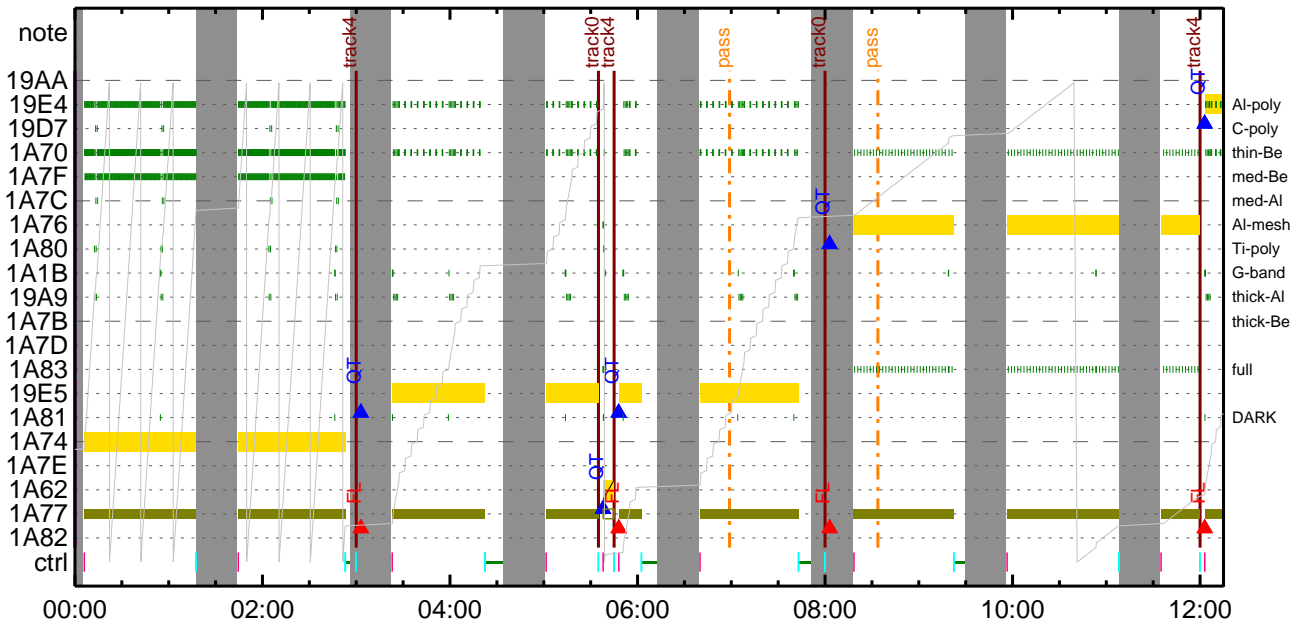
### CMDI #0125 2015/05/12



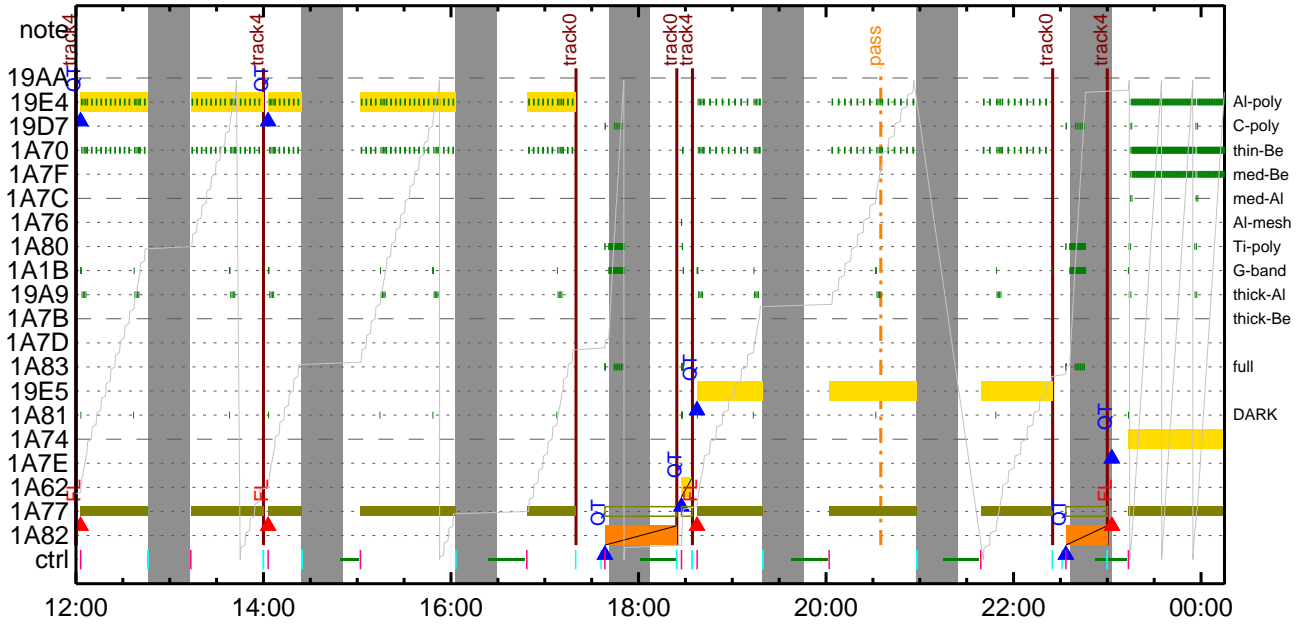
### CMDI #0125 2015/05/12



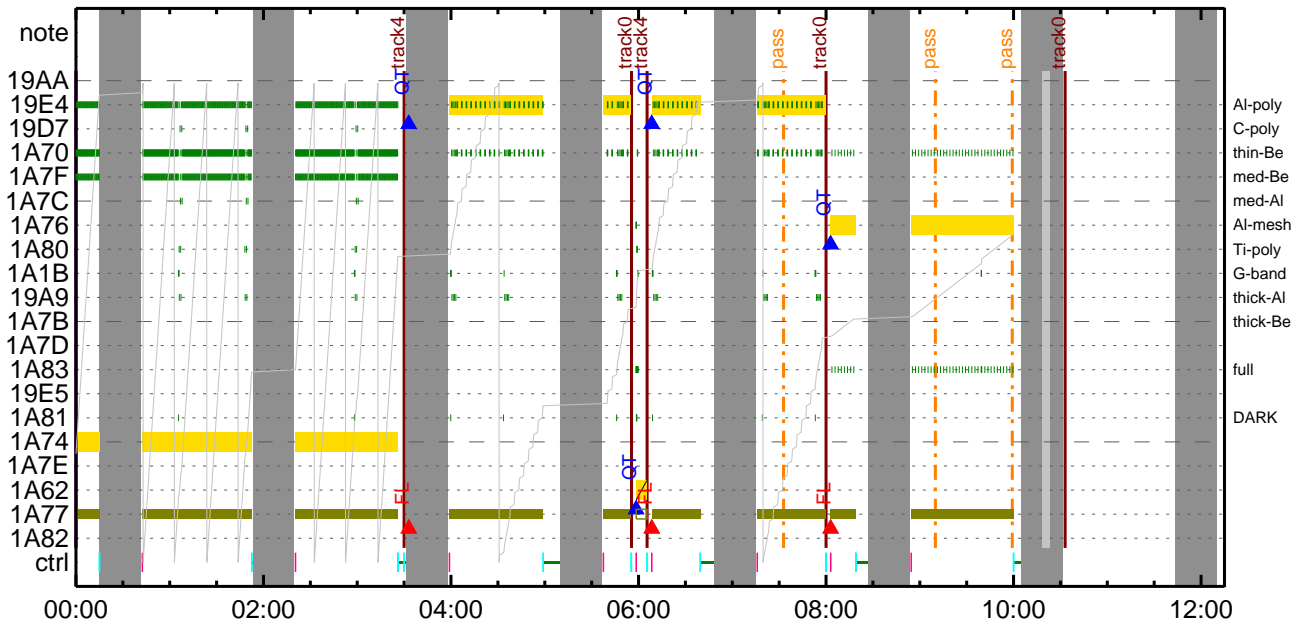
### CMDI #0125 2015/05/13



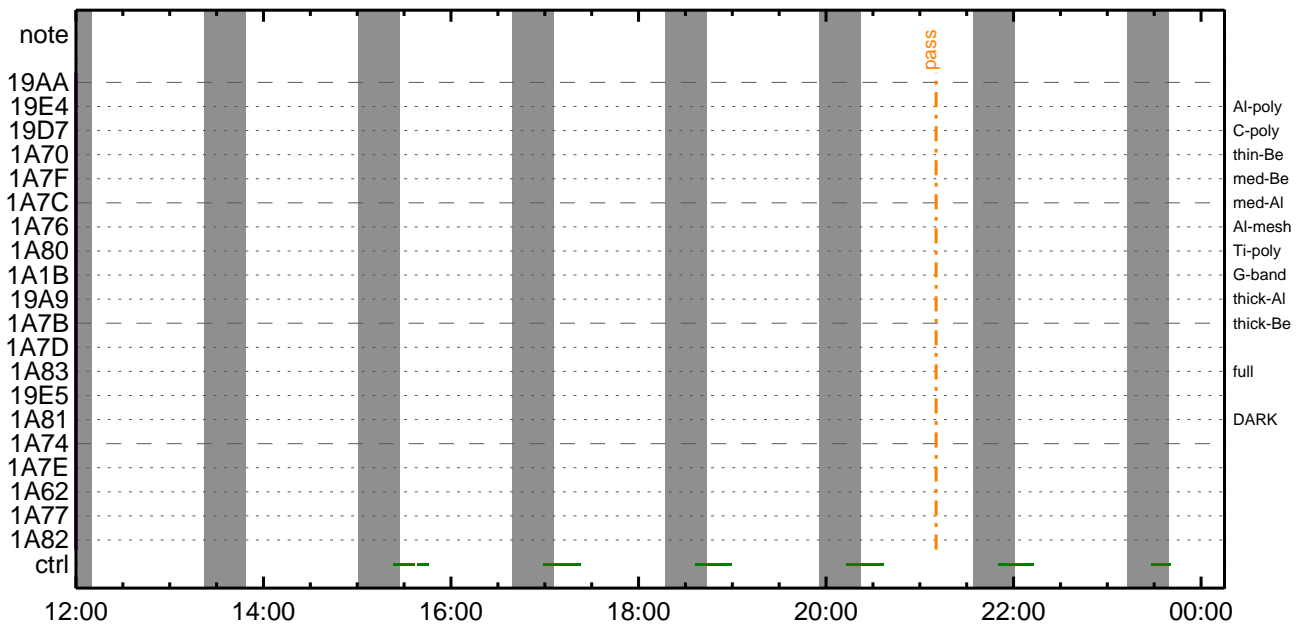
CMDI #0125 2015/05/13



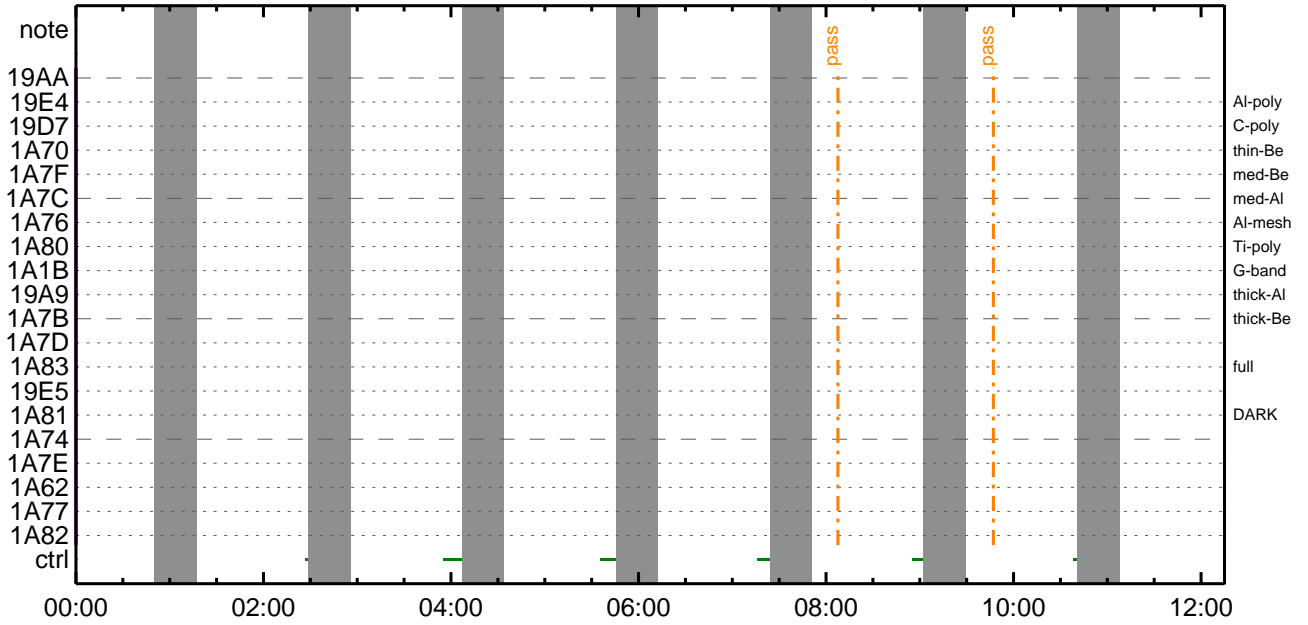
CMDI #0125 2015/05/14



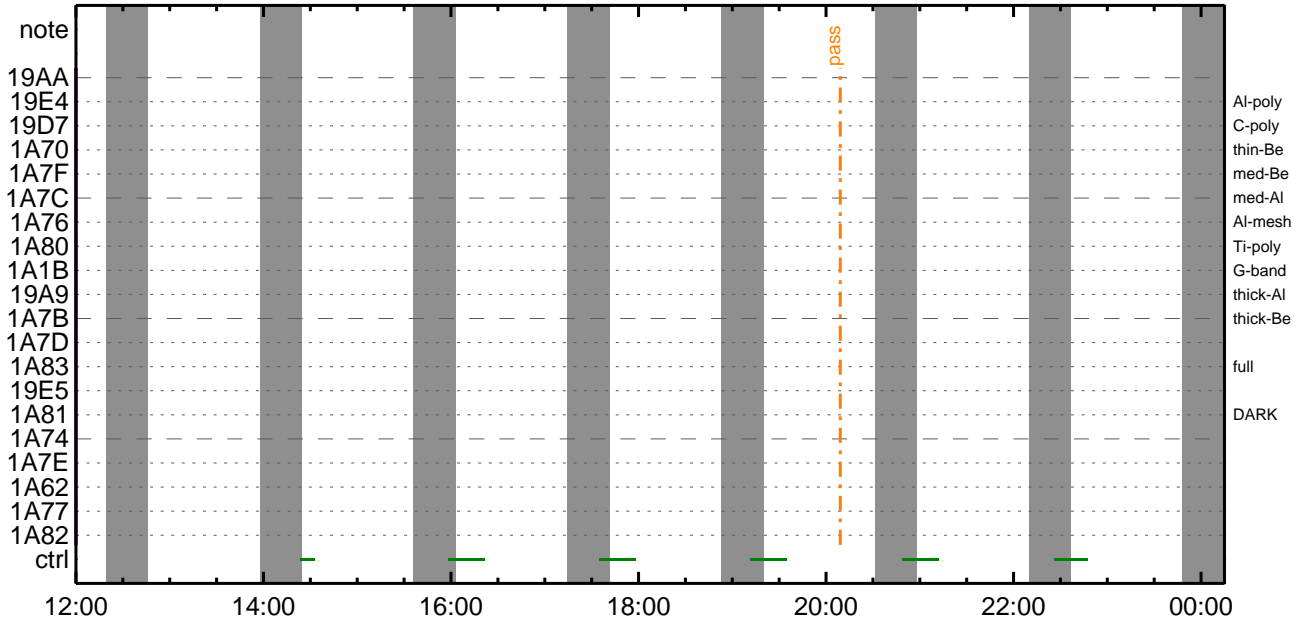
CMDI #0125 2015/05/14



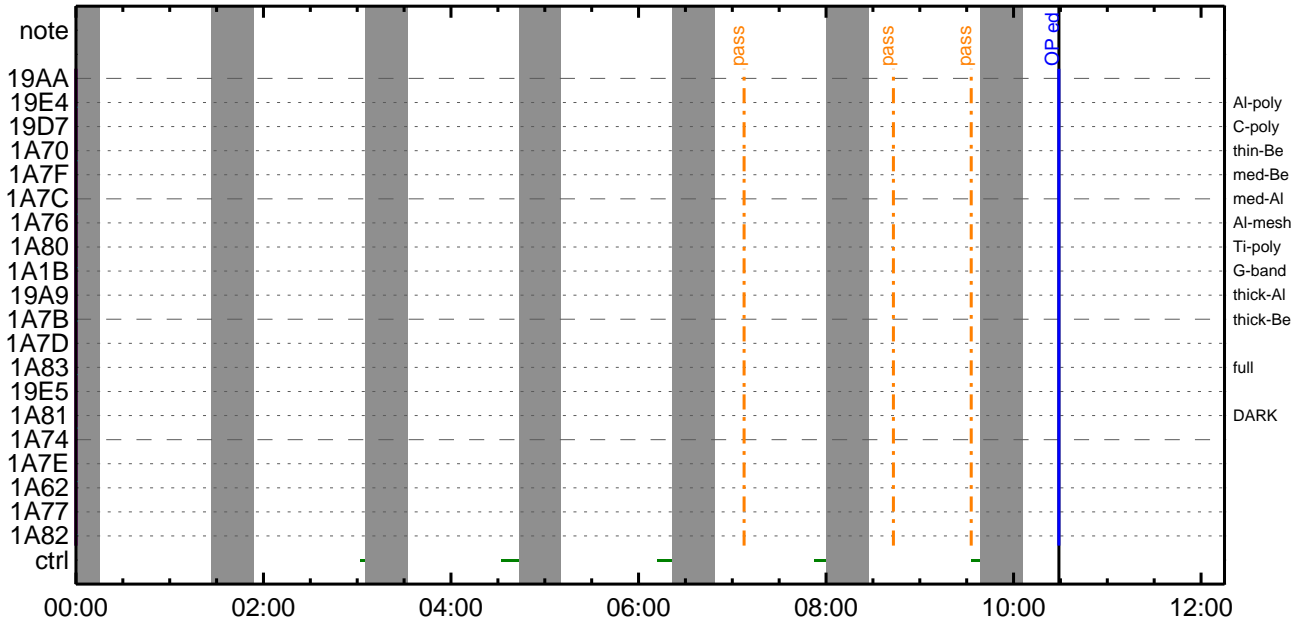
### CMDI #0125 2015/05/15



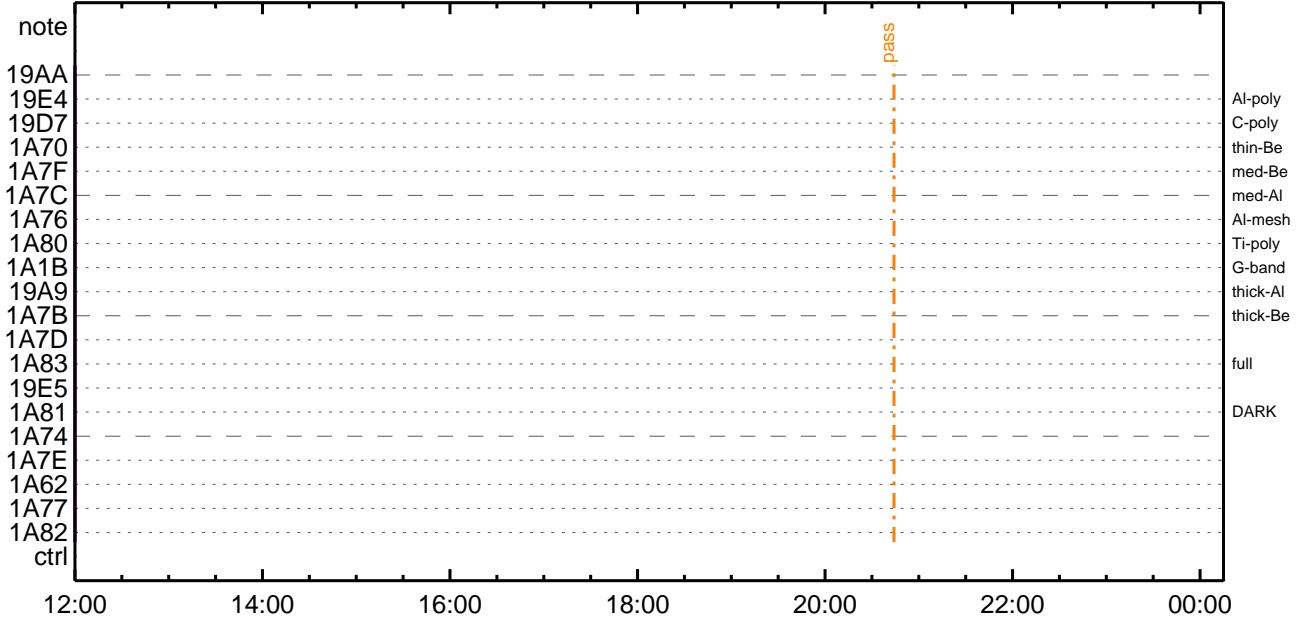
### CMDI #0125 2015/05/15



### CMDI #0125 2015/05/16



CMDI #0125 2015/05/16







0096 C.  
0097 C.  
0098 C. \*\*\*\*\*  
0099 C. OP/OGY1;4YE;|YAYOYx  
0100 C. \*\*\*\*\*  
0101 C.  
0102 C. ;ãOP/OGY1;4YE;ã  
0103 S. OP op-132:OP  
0104 (  
0105 S. OG og-132:OG  
0106 (  
0107 C.  
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã  
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)  
0110 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0111 BC (20 00 7f 01 02)  
0112 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 40  
0113 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0114 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0115 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0116 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0117 +. DC 01-22 DHU\_MODE\_CHNG  
0118 BC (07 0b f8)  
0119 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0120 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0121 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0122 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0123 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0124 C. YAYOYx½ªî»ð³îÇ§  
0125 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0126 C. RAM ID=NMOGãî½ª¹ç•è²îOKð³îÇ§  
0127 C.  
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)  
0129 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0130 BC (20 80 7f 01 02)  
0131 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 41  
0132 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0133 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0134 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0135 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0136 +. DC 01-22 DHU\_MODE\_CHNG  
0137 BC (07 0b f8)  
0138 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0139 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0140 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0141 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0142 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0143 C. YAYOYx½ª¹î»ð³îÇ§  
0144 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0145 C. RAM ID=NMOGãî½ª¹ç•è²îOKð³îÇ§  
0146 C.  
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
0148 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0149 BC (21 00 41 01 02)  
0150 C. çç[HK1\_DMP\_TOP\_ADRS\_1] EQ 42  
0151 C. çç[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0152 C. çç[HK1\_DMP\_BLOCK\_NUM] EQ 65  
0153 C. çç[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0154 C. çç[HK1\_DMA\_DMP\_PIM] EQ DHU  
0155 +. DC 01-22 DHU\_MODE\_CHNG  
0156 BC (07 0b f8)  
0157 C. çç[HK1\_PKT\_FORM\_NO] EQ 7  
0158 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0159 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0160 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0161 C. çç[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0162 C. YAYOYx½ª¹î»ð³îÇ§  
0163 C. çç[HK1\_DMP\_CHK\_FLG] EQ NON  
0164 C. RAM ID=NMOG, RAM ID=OPãî½ª¹ç•è²îOKð³îÇ§  
0165 C.  
0166 C. \*\*\*\*\* òê²¼òî½ª¹¶Á°òêê¬ò°Á÷¿@ (½ãµ-YAYOYx½ª¹ç•è²îOKð³îÇ§ª°ã°òê²¼òî½ª¹¶Á°òêê¬ò°Á÷¿@) \*\*\*\*\*  
0167 C. DHUYã;4YE;ê½½;Y;½YE;êòîã¹  
0168 +. DC 01-22 DHU\_MODE\_CHNG  
0169 BC (02 0a f8)  
0170 C. çç[HK1\_PKT\_FORM\_NO] EQ 2  
0171 C. çç[HK1\_PKT\_GEN\_TIME] EQ 0.5S  
0172 C. çç[HK1\_S\_TLM\_BIT\_RATE] EQ 32K  
0173 C. çç[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0174 C.  
0175 C. \*\*\*\*\*  
0176 C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 C. \*\*\*\*\*  
0178 C.  
0179 C. NOTICE ;§ OPOG UPLOADã¬Á÷¿@NGUî½ª¹ç;ç°ê²¼òî½ª¹¶Á°òêê¬ò°Á÷¿@ª°ã°òê²¼òî½ª¹¶Á°òêê¬ò°Á÷¿@ \*\*\*\*\*  
0180 C. ã½ª¹ç;ç°ê²¼òî½ª¹¶Á°òêê¬ò°Á÷¿@ª°ã°òê²¼òî½ª¹¶Á°òêê¬ò°Á÷¿@ \*\*\*\*\*  
0181 C.  
0182 C. TIY³Y½YóYêòðÁDî¿(UT)  
0183 +. TI 2015-05-12 09:39:00.0  
0184 DC 01-B3 DHU\_OP\_STOP  
0185 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0186 C.  
0187 +. TI 2015-05-12 09:39:01.0  
0188 DC 01-B4 DHU\_OP\_COPY  
0189 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0190 C.  
0191 +. TI 2015-05-12 09:39:01.0  
0192 DC 01-B5 DHU\_OPOG\_COPY  
0193 C. çç[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

```
0194 C.
0195 +. TI 2015-05-12 09:43:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ÷÷[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C.      °Ê²¼îãĒē%ĩĩñîîŷĀŷ$ŷĀŷ¹ãîŭ
0200 C.      ÷÷[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.      ÷÷[HK1_TI_CMD_NUM]                        EQ      4
0202 C.      ÷÷[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.      ÷÷[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C.      *****
0206 C.      TIîŷ°êŷĀŷōŷ×
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ÷÷[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.      ÷÷[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.      ÷÷[HK1_DMP_BLOCK_NUM]                     EQ      3
0215 C.      ÷÷[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.      ÷÷[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ÷÷[HK1_PKT_FORM_NO]                       EQ      7
0220 C.      ÷÷[HK1_PKT_GEN_TIME]                      EQ      0.25 s
0221 C.      ÷÷[HK1_S_TLM_BIT_RATE]                   EQ      32k
0222 C.      ÷÷[HK1_X_TLM_BIT_RATE]                   EQ      4M
0223 C.      ÷÷[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C.      ŷĀŷōŷ×½ªî»ò³îçš
0226 C.      ÷÷[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C.      RAM ID=TI_TBLîŷ°êŷĀŷōŷ×½ªî»ò³îçš
0229 C.
0230 C.      DHUŷã;¼ŷĒ;Ē¼ŷ¼.ŷî;¼ŷĒ;ĒòòĪãª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ÷÷[HK1_PKT_FORM_NO]                       EQ      2
0234 C.      ÷÷[HK1_PKT_GEN_TIME]                      EQ      0.5S
0235 C.      ÷÷[HK1_S_TLM_BIT_RATE]                   EQ      32K
0236 C.      ÷÷[HK1_X_TLM_BIT_RATE]                   EQ      4M
0237 C.
0238 C.      Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C.      ***** Start EIS operation (TI set) *****
0242 C.      Execute, after the success of OP upload.
0243 C.      Set EIS TI-commands
0244 +. TI 2015-05-12 09:43:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2015-05-12 09:43:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.      [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0251 C.      ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.      *****
0255 C.      SOT TI command set
0256 C.      *****
0257 C.      Execute, after the success of OP upload.
0258 +. TI 2015-05-12 09:43:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C.      -----
0262 C.      HK1_TI_CMD_NUM = 1 CNTUP [ ]
0263 C.      -----
0264 C.      ***** SOT END *****
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2015-05-12 09:43:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 C.      ***** MDP `ũĀîãĪ»ō¼ŷ¼ĒĀĒª¹ĒēDCBC•x²ê *****
0276 C.      (¼ª°îŷōŷĀŷēŷŷŷĀŷŷçŷēòĒ¼ªª¼Ā»Ūª¹òē)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C.      ***** ŷĐŷ¹•Ī Daily±çĪñîĒē'òª¹ĒēDCBC•x²ê *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.      ;ãLOSŷĀŷ$ŷĀŷ¹¼Ā»Ū;ã
0287 C.
0288 C.      ***** LOS *****
0289 C.
```





```
0096 C.
0097 . S. RAM ram-271:MDP_DPL
0098 ( )
0099 C.
0100 . C. < Dump RAMID=MDP_DPL >
0101 +. DC 07-F0 MDP_DUMP_FGTBL
0102 BC (82 07 00 38 b8 00 40)
0103 C. -----
0104 C. MDP_DPL verify = OK [ ]
0105 C. -----
0106 C.
0107 C. STS_CHK=ON=Ë=ë
0108 C.
0109 . C. < Update MDP DSC PAR1 >
0110 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0111 BC (4c)
0112 C. MDP_CMD_CODE = F04C0700[ ]
0113 C. MDP_CMD_CNT (count-up 1) [ ]
0114 C. -----
0115 C.
0116 . C.
0117 C. *****
0118 C. SOT TI command set
0119 C. *****
0120 C. Execute, after the success of TBL upload.
0121 +. TI 2015-05-12 09:43:18.0
0122 DC 07-F0 MDP_SOT_MODE_OBSV
0123 BC (40)
0124 . C. -----
0125 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0126 C. -----
0127 C.
0128 C.
0129 . C. ***** MDP 'ÛÃîî'õ%Ý=ËÃ=ëDCBC•x²è *****
0130 C. (%Á°îÝÓYÃYÈYpYÛYáYçYè=Ë%¼=¼¼Û¼¼)
0131 . S. DC-BC dcbc-402:DCBC
0132 (MDP_known_event)
0133 C.
0134 C.
0135 . C. ***** YDY!•Ï Daily±¿ÍÑ=Ë'Ø=ëDCBC•x²è *****
0136 . S. DC-BC dcbc-153:DCBC
0137 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0138 C.
0139 C.
0140 . C. ;ãLOS¥Á¥$¥Ã¥~¼Û¼»Û¼;ã
0141 C.
0142 . C. ***** LOS *****
0143 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-135 2015-05-12 13:18:32 127 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈøÈ¼°ÇÇø•ø¿¼í¹çøí;çÀ®, ùø¹øèøøøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 c8 af 08 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 69 08 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 + DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 + DC 07-F0 MDP_XRT_AEC_RESET
0065 BC (d0)
0066 + DC 07-F0 MDP_XRT_ARS_DIS
0067 BC (d5)
0068 + DC 07-F0 MDP_XRT_FLD_RESET
0069 BC (da)
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071 BC (c4 12)
0072 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0073 BC (c5 02)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2015-05-12 09:43:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0088 +. DC 07-FC EIS_MODE_CHG_ENA
0089 BC (20)
0090 . C. Verify EIS_MODE_CHG_FLG is ENA
0091 +. DC 07-FC EIS_MODE_MANU
0092 BC (21 02)
0093 . C. Verify EIS in MANUAL mode
0094 . C. Estimated OBSTBL upload time is 37s
0095 C. *****
```

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0096 C. EIS START OBSTBL LOAD
0097 C. *****
0098 . S. RAM ram-820:EIS_OBSTBL
0099 ( )
0100 +. DC 07-FC EIS_DUMP_OBSTBL
0101 BC (07 07 07 00 00 70 00)
0102 C.
0103 C. Execute, after the success of OBSTBL upload.
0104 C. Set EIS TI-commands
0105 +. TI 2015-05-12 09:43:50.0
0106 DC 07-FC EIS_MODE_CHG_ENA
0107 BC (20)
0108 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0109 C. *****
0110 C. EIS END OBSTBL LOAD
0111 C. *****
0112 C.
0113 . C. ***** MDP 'úÃîâî»ö¼ÝðËÂð¹æDCBC•x²è *****
0114 C. (¼å°îÏÖ¥Ã¥Ë¥Þ¥Ë¥á¥ç¥èæ¼¼¼¼¼¼»Û¹æè)
0115 . S. DC-BC dcbc-402:DCBC
0116 (MDP_known_event)
0117 C.
0118 C.
0119 . C. ***** ¥Ð¥¹•İ Daily±¿İÑæ'Ø¹æèDCBC•x²è *****
0120 . S. DC-BC dcbc-153:DCBC
0121 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0122 C.
0123 C.
0124 . C. ;ãLOS¥Á¥$¥Ã¥-¼Â»Û;ã
0125 C.
0126 . C. ***** LOS *****
0127 C.

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\*\*\* OP Sequence for XRT \*\*\*

2015/05/12	09:53:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	09:53:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	09:53:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/12	09:54:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 32 c0 bc 19				
2015/05/12	09:54:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/12	09:54:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/12	09:54:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/12	09:54:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/12	09:54:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/12	09:56:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/05/12	09:56:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/05/12	09:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/12	10:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	10:28:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	10:28:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/12	10:28:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/12	10:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/12	10:58:00.5	XRT_Custom_430_OG [0x1ae]							
2015/05/12	10:59:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/12	11:59:30.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	12:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 55 58 01 26				
2015/05/12	12:05:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	12:05:02.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/05/12	12:05:22.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/12	12:07:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/12	12:07:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/12	12:07:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2015/05/12	12:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/12	12:30:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	12:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	12:30:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/12	12:30:24.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/12	12:30:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/12	12:30:28.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/12	12:30:30.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/12	12:30:32.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/12	12:33:02.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2015/05/12	12:33:04.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/05/12	12:36:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/12	12:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/12	13:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	13:48:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	13:48:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/12	13:48:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/12	13:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/12	14:15:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/12	14:15:32.0	XRT_CTRL_MANU_402_OG [0x192]							

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2015/05/12	14:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	14:15:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/12	14:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/12	14:23:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/12	14:24:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/12	15:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	15:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	15:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/12	15:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/12	15:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/12	16:12:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/12	16:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	17:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	17:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	17:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/12	17:05:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/12	17:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/12	17:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	17:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	17:49:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/05/12	17:50:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/05/12	17:50:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/05/12	17:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/05/12	17:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/12	17:52:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/05/12	17:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/12	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	17:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/05/12	18:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 00 00 00 00	
2015/05/12	18:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/05/12	18:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/05/12	18:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/05/12	18:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/12	18:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/12	18:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2015/05/12	18:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/05/12	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/12	18:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	18:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	18:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/12	18:44:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/12	18:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/12	18:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	18:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/12	18:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/05/12	19:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	03 00 00 00 00	

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2015/05/12	19:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/05/12	19:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/05/12	19:00:22.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/05/12	19:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/12	19:00:26.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/12	19:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/05/12	19:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/05/12	19:25:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/12	19:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/12	20:22:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/12	20:22:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/12	20:22:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/12	20:22:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/12	20:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/12	22:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/12	22:00:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/12	22:00:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/12	22:00:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/12	22:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/12	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/12	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/12	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/12	23:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/05/12	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/05/12	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/05/12	23:00:22.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/05/12	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/12	23:00:26.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/12	23:02:56.0	XRT_QT_PROG_SET_406_OG [0x196]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/05/12	23:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/05/12	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/12	23:39:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/12	23:39:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/12	23:39:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/12	23:39:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/12	23:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/13	00:05:00.0	XRT_Custom_430_OG [0x1ae]			
2015/05/13	00:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/13	01:17:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/13	01:17:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/13	01:17:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/13	01:17:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/13	01:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/13	01:43:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/13	01:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/13	02:53:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/13	02:53:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/13	02:53:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da

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2015/05/13	02:53:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/13	02:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/13	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	02:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/13	03:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/05/13	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/13	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/13	03:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/13	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/13	03:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	03:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2015/05/13	03:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/05/13	03:22:00.0	XRT_Custom_430_OG [0x1ae]							
2015/05/13	03:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	04:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	04:22:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	04:22:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	04:22:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/13	04:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/13	05:00:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/13	05:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	05:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	05:34:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	05:34:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/05/13	05:35:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/05/13	05:35:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/13	05:37:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/13	05:37:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/13	05:37:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/05/13	05:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	05:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	05:44:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	05:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/13	05:45:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/05/13	05:45:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/13	05:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/13	05:45:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/13	05:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/13	05:45:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	05:47:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2015/05/13	05:47:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/05/13	05:48:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	06:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	06:02:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	06:02:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	06:02:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/13	06:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2015/05/13	06:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/13	06:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/05/13	07:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/13	07:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	07:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	07:43:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/13	07:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/13	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/13	07:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	07:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	08:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/05/13	08:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 32 c0 bc 19		
2015/05/13	08:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/05/13	08:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/05/13	08:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/05/13	08:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/05/13	08:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/13	08:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2015/05/13	08:17:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2015/05/13	08:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/05/13	09:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/13	09:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	09:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	09:22:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/13	09:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/13	09:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/13	09:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/05/13	11:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/13	11:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	11:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	11:08:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/13	11:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/13	11:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/13	11:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/05/13	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/13	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	11:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	12:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/05/13	12:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00		
2015/05/13	12:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/05/13	12:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/05/13	12:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/05/13	12:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/05/13	12:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/13	12:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2015/05/13	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2015/05/13	12:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/13	12:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/13	12:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2015/05/13	12:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	12:46:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/13	12:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/13	13:12:30.0	XRT_Custom_430_OG [0x1ae]								
2015/05/13	13:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	13:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/13	14:00:00.0	AOCS_Orе-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 00 00 00 00				
2015/05/13	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/13	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/13	14:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/13	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/13	14:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	14:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2015/05/13	14:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/05/13	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	14:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	14:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	14:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	14:24:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/13	14:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/13	15:01:00.5	XRT_Custom_430_OG [0x1ae]								
2015/05/13	15:02:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	16:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	16:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	16:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	16:03:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/13	16:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/13	16:47:30.0	XRT_Custom_430_OG [0x1ae]								
2015/05/13	16:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	17:19:45.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	17:20:00.0	AOCS_Orе-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 00 00 ac 00				
2015/05/13	17:36:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	17:36:02.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/05/13	17:36:22.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/13	17:38:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/13	17:38:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/13	17:38:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2015/05/13	17:38:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	18:24:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	18:24:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	18:24:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/05/13	18:24:30.0	AOCS_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/05/13	18:24:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/13	18:27:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/13	18:27:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/13	18:27:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				

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2015/05/13	18:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	18:34:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	18:34:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	18:34:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/13	18:34:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/05/13	18:34:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/13	18:34:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/13	18:34:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/13	18:34:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/13	18:34:56.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	18:37:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2015/05/13	18:37:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/05/13	18:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	19:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	19:19:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	19:19:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	19:19:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/13	19:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/13	20:01:00.0	XRT_Custom_430_OG [0x1ae]							
2015/05/13	20:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	20:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	20:58:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	20:58:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	20:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/13	21:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/13	21:38:00.0	XRT_Custom_430_OG [0x1ae]							
2015/05/13	21:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	22:24:45.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	22:25:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2015/05/13	22:31:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	22:31:02.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/05/13	22:31:22.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/13	22:33:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/13	22:33:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/13	22:33:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2015/05/13	22:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/13	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/13	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/13	23:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/05/13	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/13	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/13	23:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/13	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/13	23:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/13	23:02:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/05/13	23:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/05/13	23:12:30.0	XRT_Custom_430_OG [0x1ae]							

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2015/05/13	23:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/14	00:15:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	00:15:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	00:15:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/14	00:15:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/05/14	00:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/05/14	00:41:30.0	XRT_Custom_430_OG [0x1ae]								
2015/05/14	00:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/14	01:52:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	01:52:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	01:52:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/14	01:52:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/05/14	01:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/05/14	02:19:30.0	XRT_Custom_430_OG [0x1ae]								
2015/05/14	02:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/14	03:26:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	03:26:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	03:26:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/14	03:26:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/05/14	03:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/05/14	03:29:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	03:29:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	03:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2015/05/14	03:30:00.0	AOCS_Ore-point_Start_4_OG [0x09a]								
		AOCU_NM	5	02-76	04 00 00 00 00					
2015/05/14	03:30:18.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2015/05/14	03:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2015/05/14	03:30:22.0	XRT_AEC_RESET_413_OG [0x19d]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2015/05/14	03:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/05/14	03:30:26.0	XRT_FLD_RESET_407_OG [0x197]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/14	03:32:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13					
2015/05/14	03:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02					
2015/05/14	03:58:00.0	XRT_Custom_430_OG [0x1ae]								
2015/05/14	03:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/14	04:59:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	04:59:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	04:59:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/14	04:59:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/05/14	05:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/05/14	05:36:30.0	XRT_Custom_430_OG [0x1ae]								
2015/05/14	05:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/14	05:55:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	05:55:26.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/14	05:55:28.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2015/05/14	05:55:30.0	AOCS_ORe-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2015/05/14	05:55:48.0	XRT_FLD_DIS_422_OG [0x1a6]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2015/05/14	05:58:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2015/05/14	05:58:26.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/05/14	05:58:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03					
2015/05/14	05:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]								



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2015/05/14	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	06:05:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	06:05:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	06:05:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/14	06:05:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00
2015/05/14	06:05:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/05/14	06:05:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/05/14	06:05:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/05/14	06:05:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/14	06:08:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/14	06:08:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2015/05/14	06:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/05/14	06:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	06:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	06:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	06:39:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/14	06:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/14	07:15:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/14	07:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/05/14	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	07:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	07:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	08:00:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/14	08:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 36 4d be 36
2015/05/14	08:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/05/14	08:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/05/14	08:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/05/14	08:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/14	08:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/14	08:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/05/14	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/05/14	08:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	08:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	08:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	08:19:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/14	08:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/14	08:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/14	08:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/05/14	10:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	10:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	10:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	10:00:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/14	10:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/14	10:33:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM	AOCS_ORe-point_Start_3_OG [0x099]	5	02-76	00 00 00 00 00