

XRT Timeline to be uploaded on 2015/05/14

Period: 2015/05/14 10:23:00 - 2015/05/19 10:06:00

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Normal mode

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XOB #1A7E: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,8ms) - Leak (33ms) - 120s cad													
Term	Pointing (x, y)								Comment				
05/14 10:36:00 - 05/14 11:43:30	Fixed (-830.0, 435.0)	# OP start + 10min,HOP255 NElimb											
PROG= 14 Inf.-time(s)													
└─ Subr= 1 30-time(s) 120.0sec													
└─ Seqn= 29 1-time(s) 4.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs 4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #1A1B: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 33ms													
Term	Pointing (x, y)								Comment				
05/14 12:11:00 - 05/14 16:33:30	Fixed (-13.0, 860.0)	HOP81 North pole											
PROG= 12 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 48 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
└─ Subr= 3 30-time(s) 2.0sec													
└─ Seqn= 57 1-time(s) 60.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec	
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #1A84: Stray light study 2015-9 ;Ti-poly and C-poly, 2x2 full FOV(1min-cad) and 2x2 256 on N-pole													
Term	Pointing (x, y)								Comment				
05/14 16:36:00 - 05/14 17:04:54	Fixed (-13.0, 860.0)	HOP81 North pole											
PROG= 04 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 17 1-time(s) 150.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	C-poly/Open	C-poly/Open	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	C-poly/Open	C-poly/Open	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Subr= 2 21-time(s) 2.0sec													
└─ Seqn= 22 1-time(s) 10.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	500ms	Obs 2x2	512x512 (1600, 400)	Q=95	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	16ms	Obs 2x2	512x512 (1600, 400)	Q=95	0	0	2.0sec	
└─ Subr= 3 6-time(s) 2.0sec													
└─ Seqn= 49 1-time(s) 2.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
	C-poly/Open	C-poly/Open	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 22 5-time(s) 10.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	500ms	Obs 2x2	512x512 (1600, 400)	Q=95	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	16ms	Obs 2x2	512x512 (1600, 400)	Q=95	0	0	2.0sec	
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #19E1: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)													
Term	Pointing (x, y)								Comment				
05/14 17:24:00 - 05/14 17:59:54	Track (478.4, 185.2) ^{@ 05/14 17:05:00}	coordination with DST at AR12345											
05/15 03:33:00 - 05/15 06:15:54	Track (557.2, 182.0) ^{@ 05/15 03:30:00}	AR12345 obs.											
PROG= 15 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
└─ Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
└─ Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
└─ Seqn= 62 30-time(s) 60.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A7B: Synoptic 9 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Thick-Al(32768), Med-Al(4096/23142), Al-poly(45/1024),

Term	Pointing (x, y)	Comment
05/14 18:03:00 - 05/14 18:09:54	Fixed (0.0, 0.0)	synoptic
PROG= 10 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 33 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 40 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 77 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 2 1-time(s) 12.0sec		
└─ Seqn= 72 1-time(s) 2.0sec		
Open/thick-Al	Open/thick-Al close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 37 1-time(s) 2.0sec		
med-Al/Open	med-Al/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 21 1-time(s) 4.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 59 1-time(s) 2.0sec		
C-poly/Open	C-poly/Open close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
C-poly/Open	C-poly/Open close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Seqn= 71 1-time(s) 2.0sec		
med-Be/Open	med-Be/Open close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 16.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2

Term	Pointing (x, y)	Comment
05/14 18:25:00 - 05/14 20:09:54	Fixed (0.0, 945.0)	co-alignment at N limb
PROG= 11 1-time(s)		
└─ Subr= 1 1-time(s) 360.0sec		
└─ Seqn= 76 24-time(s) 300.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec- 8 min cadence-wNGT-2

Term	Pointing (x, y)	Comment
05/14 20:25:00 - 05/14 22:09:54	Fixed (-945.0, 0.0)	co-alignment at E limb
PROG= 20 1-time(s)		
└─ Subr= 1 1-time(s) 360.0sec		
└─ Seqn= 60 15-time(s) 480.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A70: HOP 278 - Al/Poly, Ti/Poly, Al/mesh with FOV 512x512 at 1064 1048 with AEC 2, With G-band (33ms/45ms leak) and Dark (16sec), 2min cad

Term	Pointing (x, y)	Comment
05/14 22:14:00 - 05/15 03:29:54	Fixed (-13.0, -985.0)	HOP280 polar CH spicules at S pole
PROG= 17 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 20 2-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 44ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
└─ Seqn= 75 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 0 0 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 94 30-time(s) 120.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A6B: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45

Term	Pointing (x, y)	Comment
05/15 06:19:00 - 05/15 06:25:54	Fixed (0.0, 0.0)	synoptic, shifted 16.0 min

PROG= 09 1-time(s)

Subr= 1 1-time(s) 12.0sec												
Seqn= 33 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 40 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 12.0sec												
Seqn= 46 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25 1-time(s) 4.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 21 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 37 1-time(s) 2.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25 1-time(s) 4.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 21 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 37 1-time(s) 2.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 46 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1A77: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
05/14 10:36:00 - 05/14 11:43:30	Fixed (-830.0, 435.0)	# OP start + 10min, HOP255 NElimb
05/14 12:11:00 - 05/14 16:33:30	Fixed (-13.0, 860.0)	HOP81 North pole
05/14 17:24:00 - 05/14 17:59:54	Track (478.4, 185.2) @ 05/14 17:05:00	coordination with DST at AR12345
05/15 03:33:00 - 05/15 06:15:54	Track (557.2, 182.0) @ 05/15 03:30:00	AR12345 obs.

PROG= 02 30-time(s)

Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11			1-time(s)			2.0sec						
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 38			1-time(s)			2.0sec						
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

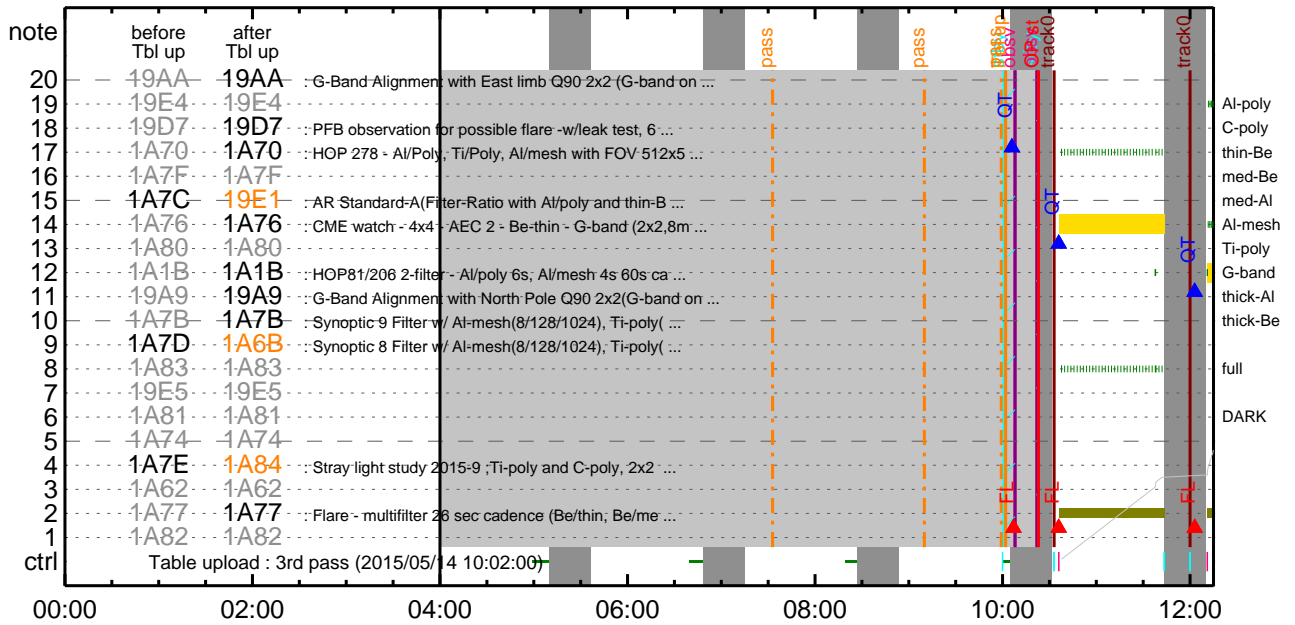
* * * * *

Flare Detection

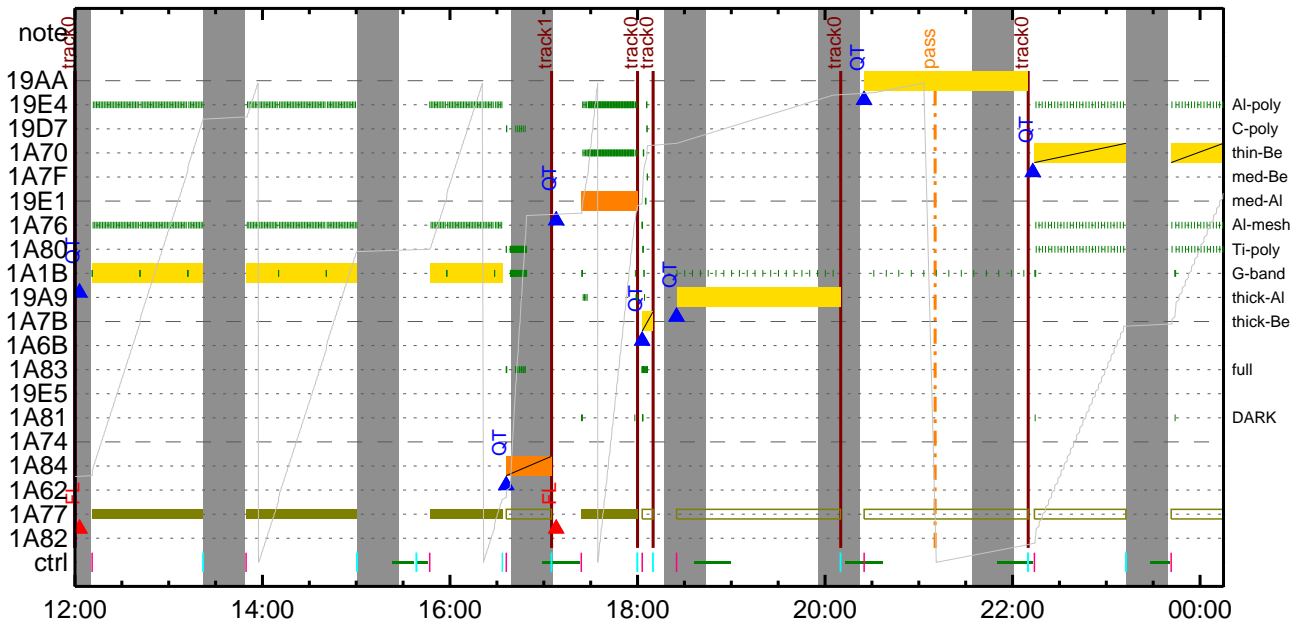
* * * * *

FLD Patrol														
Term	Pointing (x, y)		Comment											
05/14 17:05:18 - 05/14 18:00:18	Track (478.4,	185.2)	<small>@ 05/14 17:05:00</small>	coordination with DST at AR12345									
05/15 03:30:18 - 05/15 06:16:18	Track (557.2,	182.0)	<small>@ 05/15 03:30:00</small>	AR12345 obs.									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

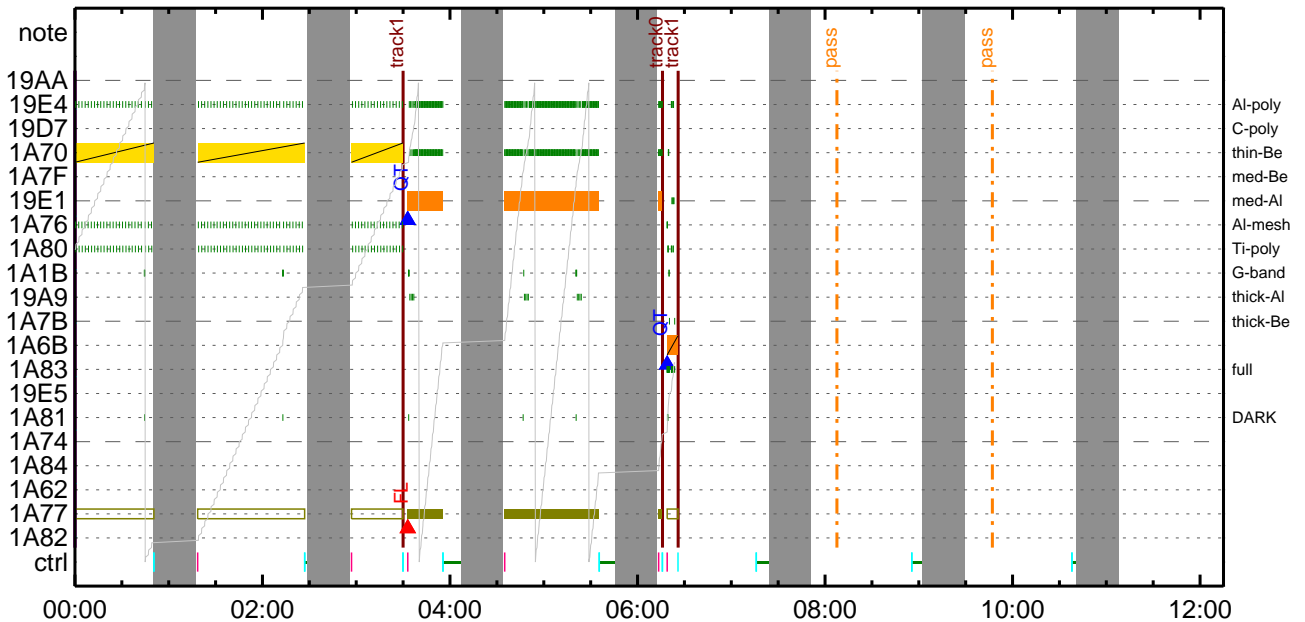
CMDI #0131 2015/05/14



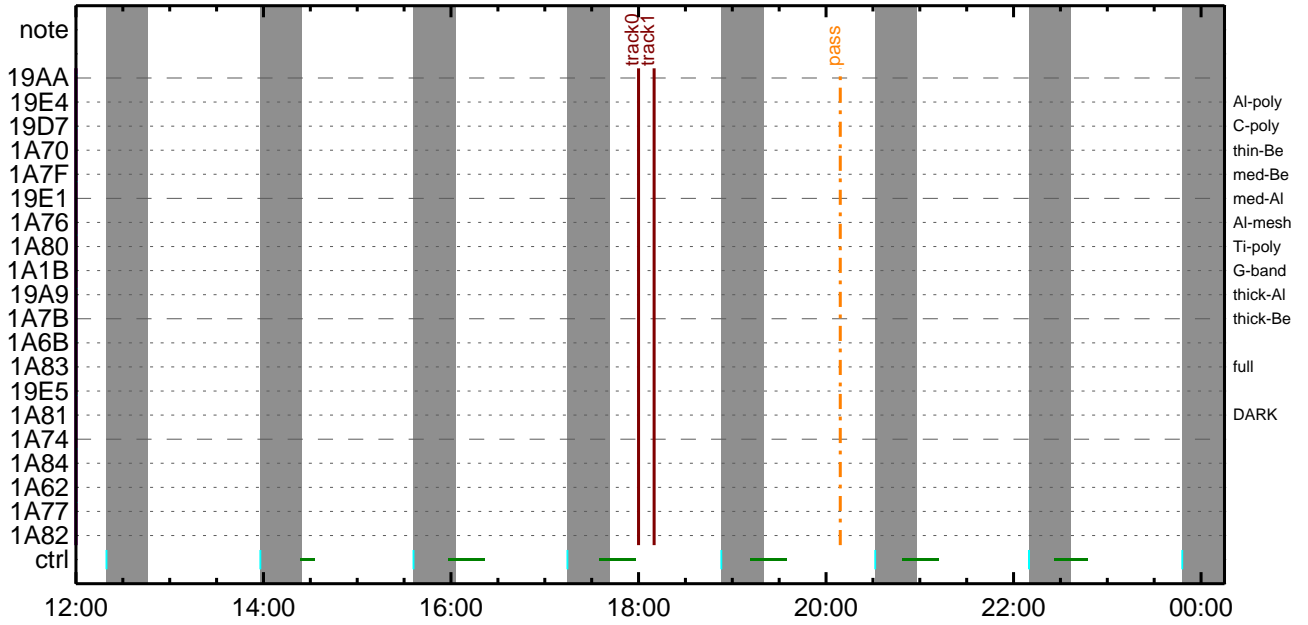
CMDI #0131 2015/05/14



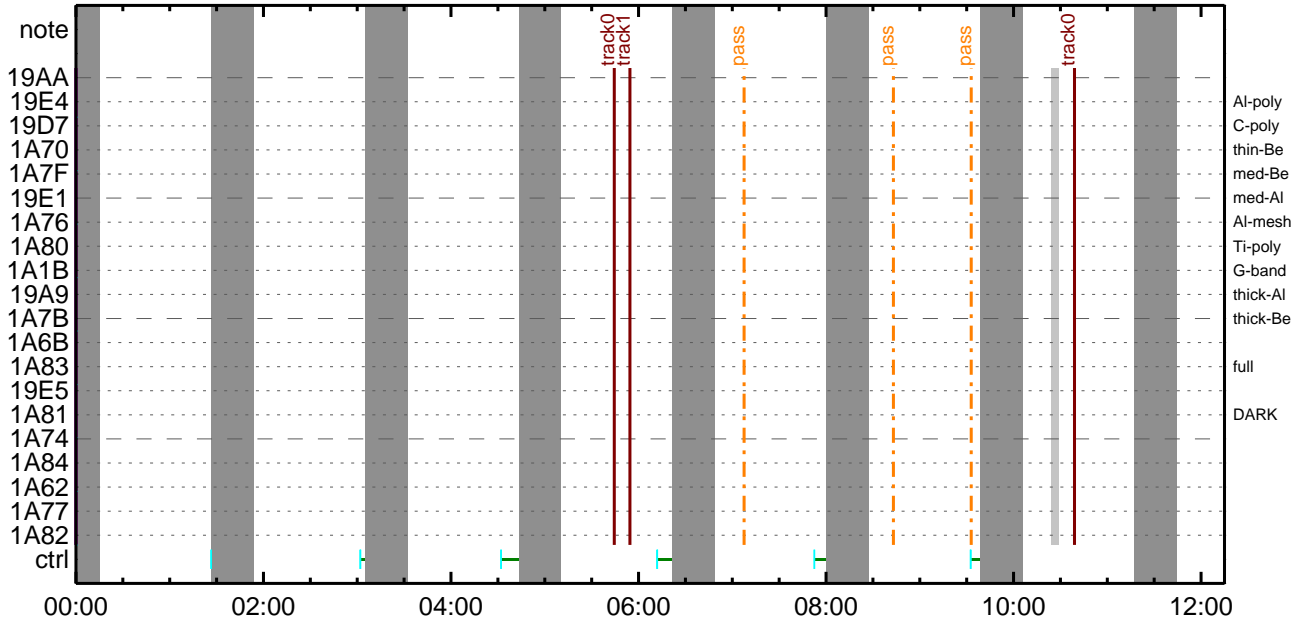
CMDI #0131 2015/05/15



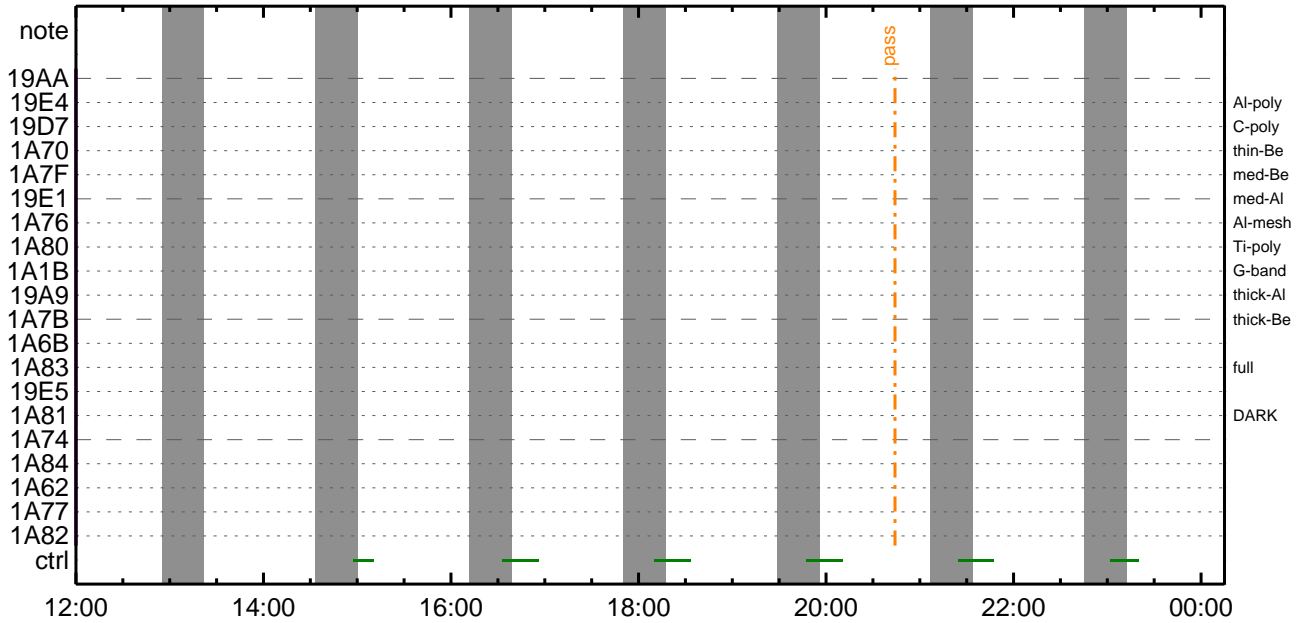
CMDI #0131 2015/05/15



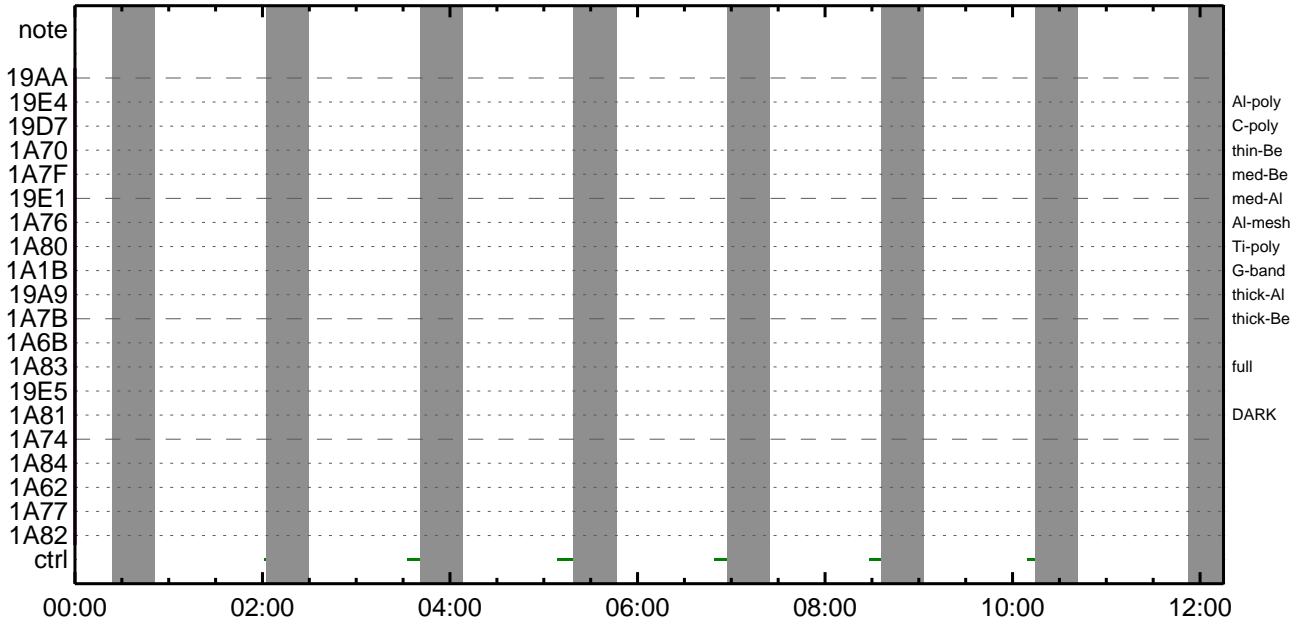
CMDI #0131 2015/05/16



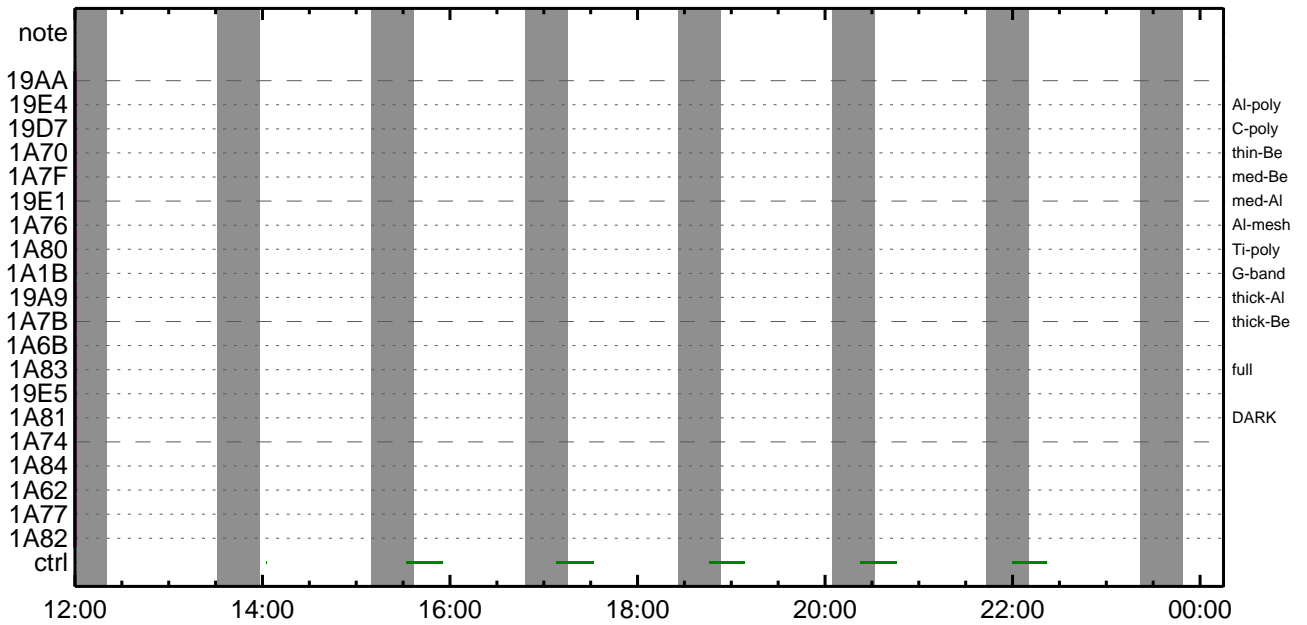
CMDI #0131 2015/05/16



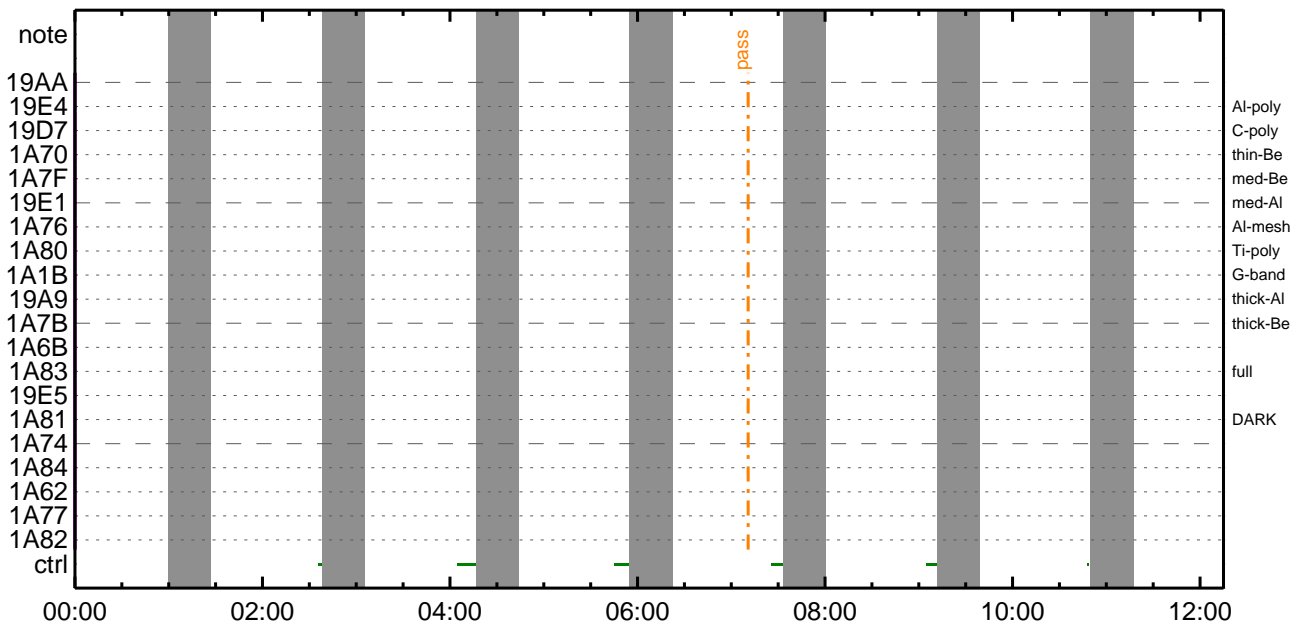
CMDI #0131 2015/05/17



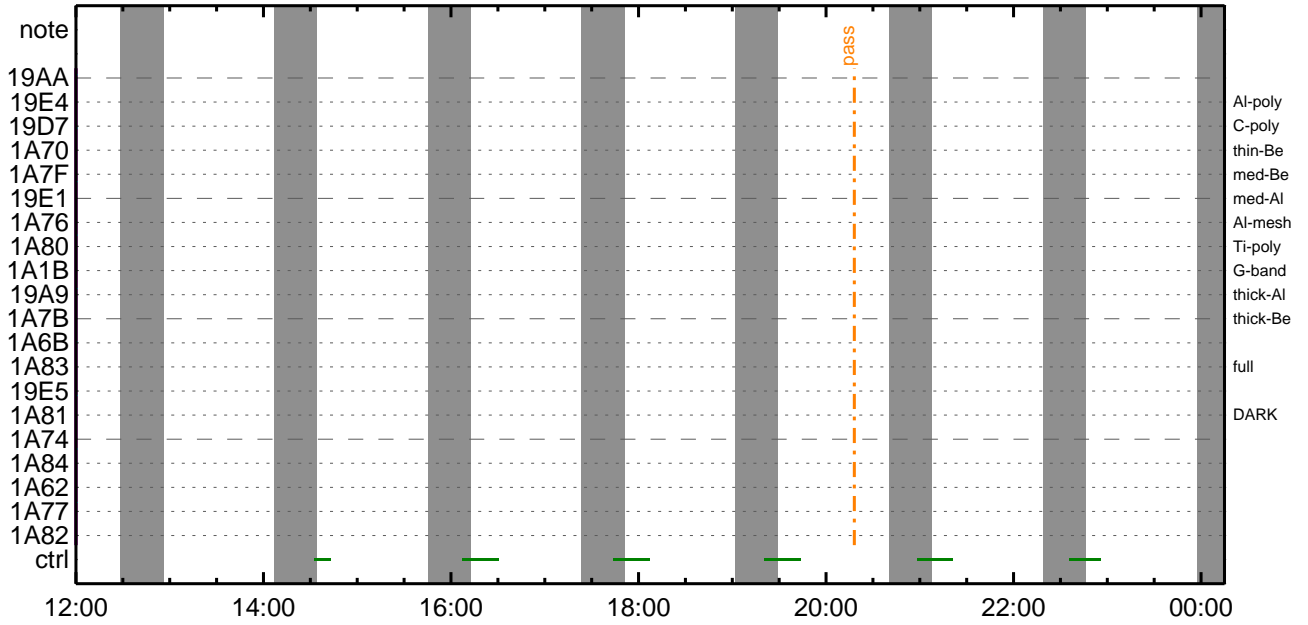
CMDI #0131 2015/05/17



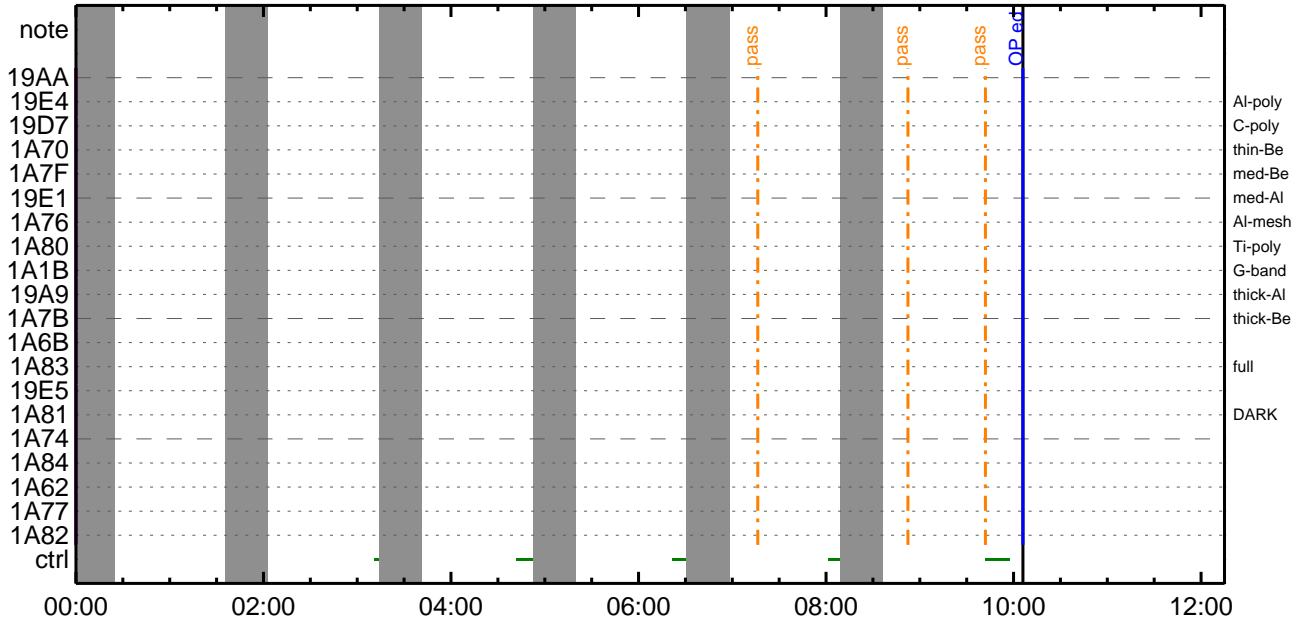
CMDI #0131 2015/05/18



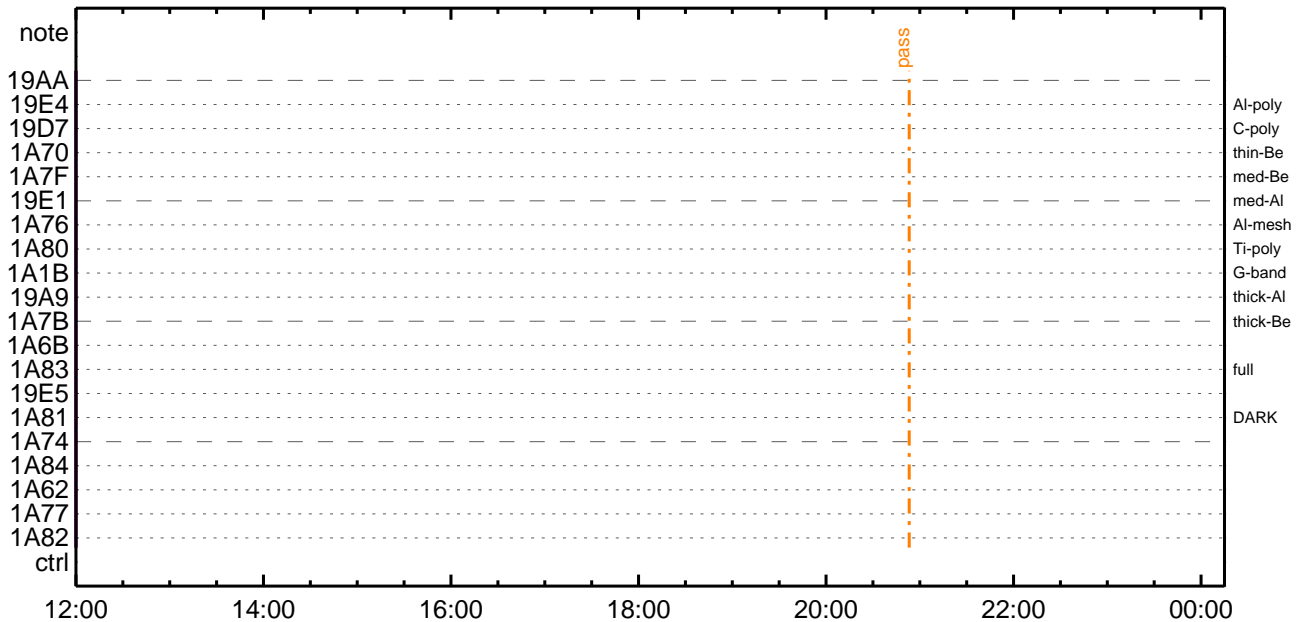
CMDI #0131 2015/05/18



CMDI #0131 2015/05/19



CMDI #0131 2015/05/19



0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;Y4E;YAY6Yx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;Y4E;ã
0103 . S. OP op-138:OP
0104 ()
0105 . S. OG og-138:OG
0106 ()
0107 C.
0108 . C. ;ãNMOG&OPfî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAY6YxY4E;ãî»òð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGãîîî¹ç·è²îOKòð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAY6YxY4E;ãî»òð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGãîîî¹ç·è²îOKòð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAY6YxY4E;ãî»òð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG,RAM ID=OPãîîî¹ç·è²îOKòð³îÇ§
0165 C.
0166 . C. ***** òè²¼òîîî¹ç·è²îOKòð³îÇ§ *****
0167 C. DHUYã;Y4E;EY4E;YAY6Yx;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOADã-Á÷çîNGUîîî¹ç;ç°è²¼òîîî¹ç·è²îOKòð³îÇ§ã¹òã·òè²¼òîîî¹ç·è²îOKòð³îÇ§;f
0180 C. òè²¼òîîî¹ç·è²îOKòð³îÇ§;çSETòEDUMPAîîî¹ç·è²îOKòð³îÇ§;f
0181 C.
0182 . C. TIY³Y4E;ãî»òð³îÇ§(UT)
0183 +. TI 2015-05-14 10:18:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-05-14 10:18:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-05-14 10:18:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

0194 C.
0195 +. TI 2015-05-14 10:22:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïÄë%îîñïîŷÄŷ§ŷÄŷ-¹âiÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]       EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî¾È¹ç•è²îOKò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èòðîâï¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2015-05-14 10:22:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2015-05-14 10:22:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2015-05-14 10:22:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2015-05-14 10:22:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîî»ö¼ŷ¼ÈÄò¹èDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÄŷçŷèè¼ò¼ª»Ûò¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñèË'ò¹èèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼ª»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. *****
0130 C. SOT table upload
0131 C. *****
0132 . C. < Stop FG table >
0133 +. DC 07-F0 MDP_FG_CTRL_MANU
0134 BC (51)
0135 . C. -----
0136 C. MDP_FG_CTRL_MODE = MANU [ ]
0137 C. -----
0138 C.
0139 . C. <Upload FG Observation Table>
0140 . S. RAM ram-266:MDP_OBS_F
0141 ( )
0142 C.
0143 . C. < Dump RAMID=MDP_OBS_F >
0144 +. DC 07-F0 MDP_DUMP_FGTBL
0145 BC (82 07 00 00 00 38 b8)
0146 C. -----
0147 C. MDP_OBS_F verify = OK/NG [ ]
0148 C. -----
0149 C.
0150 C. *****
0151 C. SOT TI command set
0152 C. *****
0153 C. Execute, after the success of TBL upload.
0154 +. TI 2015-05-14 10:22:18.0
0155 DC 07-F0 MDP_SOT_MODE_OBSV
0156 BC (40)
0157 . C. -----
0158 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0159 C. -----
0160 C.
0161 C.
0162 . C. ***** MDP `úÃîïï»ö%ÝðËÃð¹ñëDCBC•x²è *****
0163 C. (%ã°ìÿÓYÁYËYBYËYáYçYèøE%¼ø%Á»Ûñ¹è)
0164 . S. DC-BC dcbc-402:DCBC
0165 (MDP_known_event)
0166 C.
0167 C.
0168 . C. ***** YDÿ¹•Ï Daily±¿ÎñøE´Øñ¹øëDCBC•x²è *****
0169 . S. DC-BC dcbc-153:DCBC
0170 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0171 C.
0172 C.
0173 . C. ¡ãLOS¥Á¥$¥Ã¥~¼Á»Û;ã
0174 C.
0175 . C. ***** LOS *****
0176 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-140 2015-05-14 13:30:56 129 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíËò¿òÁò•µ°È»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÓò•ò¿¼í¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG_____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR _____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 c8 32 08 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 85 83 08 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 20 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 80 08 20)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 60 20 18)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d a0 80 18 20)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0f 80 80 06 06)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 10 80 80 08 08)
0062 + DC 07-F0 MDP_XRT_FLD_ENA
0063 BC (d8)
0064 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0065 BC (c8)
0066 + DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 + DC 07-F0 MDP_XRT_ARS_DIS
0069 BC (d5)
0070 + DC 07-F0 MDP_XRT_FLD_RESET
0071 BC (da)
0072 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0073 BC (c4 12)
0074 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0075 BC (c5 02)
0076 . C. ----- Success Verify ? OK / NG _____
0077 C.
0078 C.
0079 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0080 C.
0081 +. DC 07-F0 MDP_XRT_MODE_OBSV
0082 BC (c2)
0083 +. TI 2015-05-14 10:22:02.0
0084 DC 07-F0 MDP_XRT_MODE_OBSV
0085 BC (c2)
0086 . C. ----- Success Verify ? OK / NG _____
0087 C.
0088 C. ***** XRT END *****
0089 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0090 +. DC 07-FC EIS_MODE_CHG_ENA
0091 BC (20)
0092 . C. Verify EIS_MODE_CHG_FLG is ENA
0093 +. DC 07-FC EIS_MODE_MANU
0094 BC (21 02)
0095 . C. Verify EIS in MANUAL mode
```


May 14, 15 13:31

XRT_OGLIST_0131.chk

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*** OP Sequence for XRT ***

2015/05/14	10:32:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	10:32:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	10:32:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/05/14	10:33:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 d9 59 49 ca		
2015/05/14	10:33:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/05/14	10:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/05/14	10:33:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/05/14	10:33:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/05/14	10:33:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/14	10:35:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2015/05/14	10:35:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2015/05/14	10:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/14	11:43:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	11:43:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	11:43:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/14	11:43:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/14	11:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/14	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	11:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/05/14	12:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 b3 8e 01 26		
2015/05/14	12:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/05/14	12:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/05/14	12:00:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/05/14	12:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/05/14	12:00:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/14	12:02:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2015/05/14	12:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2015/05/14	12:10:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/14	12:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/14	13:22:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	13:22:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	13:22:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/14	13:22:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/14	13:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/14	13:48:30.0	XRT_Custom_430_OG [0x1ae]					
2015/05/14	13:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/14	15:00:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	15:00:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	15:00:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/14	15:00:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/14	15:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/14	15:38:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	15:38:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/14	15:38:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/14	15:38:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/14	15:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					

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2015/05/14	15:46:01.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/14	15:47:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/05/14	16:33:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	16:33:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	16:33:52.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/05/14	16:35:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/05/14	16:35:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/05/14	16:35:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/14	16:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/05/14	17:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	17:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	17:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	17:05:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/14	17:05:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2015/05/14	17:05:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/05/14	17:05:22.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/05/14	17:05:24.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/05/14	17:05:26.5	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/14	17:07:56.5	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/14	17:07:58.5	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/05/14	17:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/05/14	17:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/05/14	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	18:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/05/14	18:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00
2015/05/14	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/05/14	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/05/14	18:02:58.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/14	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/05/14	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	18:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	18:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/14	18:10:18.0	XRT_FLD_DIS_446_OG [0x1be]	AOCU_NM	5	02-76	00 ac 00 00 00
2015/05/14	18:10:20.0	XRT_FLRCTRL_DIS_431_OG [0x1af]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/05/14	18:10:22.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/05/14	18:24:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/14	18:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2015/05/14	20:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/14	20:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	20:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/14	20:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/14	20:10:18.0	XRT_FLD_DIS_446_OG [0x1be]	AOCU_NM	5	02-76	00 00 00 54 00
2015/05/14	20:10:20.0	XRT_FLRCTRL_DIS_431_OG [0x1af]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/05/14	20:10:22.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/05/14			MDP_XRT_ARS_DIS	1	07-F0	d5

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2015/05/14	20:24:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2015/05/14	20:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/14	22:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/14	22:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/14	22:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/05/14	22:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	57	8d	01	26
2015/05/14	22:10:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/14	22:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/14	22:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/14	22:12:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2015/05/14	22:13:00.0	XRT_Custom_430_OG [0x1ae]							
2015/05/14	22:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/14	23:12:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/14	23:12:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/14	23:12:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/14	23:12:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/14	23:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/14	23:40:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/14	23:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/15	00:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	00:50:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	00:50:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	00:50:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/15	00:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/15	01:17:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/15	01:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/15	02:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	02:27:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	02:27:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	02:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/15	02:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/15	02:56:00.0	XRT_Custom_430_OG [0x1ae]							
2015/05/15	02:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/15	03:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	03:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	03:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/05/15	03:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	00	00	00	00
2015/05/15	03:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/15	03:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/15	03:30:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/15	03:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/15	03:30:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	03:32:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2015/05/15	03:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/05/15	03:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/15	03:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	03:55:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	03:55:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	03:55:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							

2015/05/15	03:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/15	04:34:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/15	04:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/15	05:35:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	05:35:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	05:35:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/15	05:35:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/15	05:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/15	06:12:30.0	XRT_Custom_430_OG [0x1ae]					
2015/05/15	06:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/15	06:15:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	06:15:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	06:15:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/05/15	06:16:00.0	AOCS_ORe-point_Start_4_OG [0x09a]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2015/05/15	06:16:18.0	XRT_FLD_DIS_422_OG [0x1a6]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/05/15	06:18:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/05/15	06:18:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/15	06:18:58.0	XRT_QT_PROG_SET_401_OG [0x191]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2015/05/15	06:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/15	06:25:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	06:26:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2015/05/15	06:30:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_432_OG [0x1b0]					
			TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2015/05/15	07:16:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	07:16:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	07:16:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/15	07:16:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/15	07:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/15	08:55:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	08:55:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	08:55:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/15	08:55:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/15	08:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/15	10:38:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	10:38:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	10:38:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/15	10:38:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/15	10:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/15	12:19:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	12:19:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	12:19:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/15	12:19:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/15	12:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/15	13:58:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	13:58:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/15	13:58:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/15	13:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/15	14:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2015/05/15	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	15:36:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	15:36:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	15:36:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/15	15:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/15	17:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	17:14:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	17:14:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	17:14:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/15	17:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/15	18:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/05/15	18:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/05/15	18:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	18:53:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	18:53:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	18:53:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/15	18:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/15	20:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	20:31:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	20:31:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	20:31:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/15	20:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/15	22:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	22:10:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	22:10:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	22:10:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/15	22:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/15	23:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	23:48:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/15	23:48:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/15	23:48:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/15	23:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/16	01:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/16	01:26:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/16	01:26:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/16	01:26:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/16	01:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/16	03:02:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/16	03:02:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/16	03:02:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/16	03:02:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/16	03:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/16	04:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/16	04:32:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/16	04:32:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/16	04:32:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/16	04:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

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2015/05/16	05:44:30.5	AOCS_ORe-point_Start_4_OG [0x09a]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			AOCU_NM	5	02-76	00 00 00 00 00
2015/05/16	05:54:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00
2015/05/16	06:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/16	06:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/16	06:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/16	06:12:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/16	06:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/16	07:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/16	07:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/16	07:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/16	07:52:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/16	07:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/16	09:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/16	09:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/16	09:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/16	09:32:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/16	09:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/16	10:39:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00