

XRT Timeline to be uploaded on 2015/05/26

Period: 2015/05/26 09:22:00 - 2015/06/03 10:52:00

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Normal mode

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XOB #1A89: HOP285 Al/Poly FOV 384x384 30s cadence															
Term		Pointing (x, y)					Comment								
05/26 09:35:00 - 05/26 19:39:56		Track (-54.0, -39.7) @ 05/26 09:32:00					# OP start + 10min HOP 285 with VTT on disk center								
05/27 09:03:00 - 05/27 18:59:56		Track (-58.9, -39.7) @ 05/27 09:00:00					HOP 285 with VTT on disk center								
PROG= 14 Inf.-time(s)															
└ Subr= 1		1-time(s)		2.0sec											
└└ Seqn= 73		1-time(s)		30.0sec											
└└└ Al-poly/Open		Open/thick-Al		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=92	2	0	2.0sec
└└└ Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1A6B: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45)															
Term		Pointing (x, y)					Comment								
05/26 19:43:00 - 05/26 19:49:56		Fixed (0.0, 0.0)					synoptic shifted manually								
PROG= 09 1-time(s)															
└ Subr= 1		1-time(s)		12.0sec											
└└ Seqn= 33		1-time(s)		4.0sec											
└└└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└└└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└└└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└└└ Seqn= 5		1-time(s)		2.0sec											
└└└└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec
└└└└ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec
└└└ Seqn= 40		1-time(s)		4.0sec											
└└└└ Open/Ti-poly		Open/Ti-poly		close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└└└└ Open/Ti-poly		Open/Ti-poly		close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└└└└ Open/Ti-poly		Open/Ti-poly		close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└└└ Seqn= 77		1-time(s)		2.0sec											
└└└└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└└└└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└└└└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└└└ Seqn= 6		1-time(s)		2.0sec											
└└└└ Open/G-band		Open/G-band		open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec
└└└└ Open/G-band		Open/G-band		close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Subr= 2		1-time(s)		12.0sec											
└└ Seqn= 46		1-time(s)		2.0sec											
└└└ Open/thick-Be		Open/thick-Be		close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└ Seqn= 25		1-time(s)		4.0sec											
└└└└ Al-poly/Ti-poly		Al-poly/thick-Al		close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└└ Al-poly/Ti-poly		Al-poly/thick-Al		close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└ Seqn= 21		1-time(s)		4.0sec											
└└└└ Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└ Seqn= 37		1-time(s)		2.0sec											
└└└└ med-Al/Open		med-Al/Open		close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└└ med-Al/Open		med-Al/Open		close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└ Seqn= 25		1-time(s)		4.0sec											
└└└└ Al-poly/Ti-poly		Al-poly/thick-Al		close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└└ Al-poly/Ti-poly		Al-poly/thick-Al		close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└ Seqn= 21		1-time(s)		4.0sec											
└└└└ Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└ Seqn= 37		1-time(s)		2.0sec											
└└└└ med-Al/Open		med-Al/Open		close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└└ med-Al/Open		med-Al/Open		close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└ Seqn= 46		1-time(s)		2.0sec											
└└└└ Open/thick-Be		Open/thick-Be		close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└└└ Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1A38: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m)															
Term		Pointing (x, y)					Comment								
05/26 19:53:00 - 05/27 05:45:26		Track (-308.2, -162.9) @ 05/26 19:50:00					# track AR 12355								
05/27 05:58:30 - 05/27 08:59:56		Track (-221.2, -163.1) @ 05/27 05:55:30					# track AR 12355								
05/27 19:13:00 - 05/28 05:59:56		Track (-104.0, -163.7) @ 05/27 19:10:00					# track AR 12355								
PROG= 04 Inf.-time(s)															
└ Subr= 1		1-time(s)		2.0sec											
└└ Seqn= 8		2-time(s)		2.0sec											
└└└ Open/G-band		Open/G-band		close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
└ Subr= 2		2-time(s)		2.0sec											
└└ Seqn= 24		1-time(s)		2.0sec											

├	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42		4-time(s)		2.0sec									
├	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62		60-time(s)		30.0sec									
├	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi

Term	Pointing (x, y)	Comment
05/27 05:48:30 - 05/27 05:55:26	Fixed (0.0, 0.0)	synoptic, shifted -14.5 min
05/27 19:03:00 - 05/27 19:09:56	Fixed (0.0, 0.0)	synoptic, shifted -23.5 min
05/28 06:03:00 - 05/28 06:09:56	Fixed (0.0, 0.0)	synoptic
05/28 18:07:00 - 05/28 18:13:56	Fixed (0.0, 0.0)	synoptic, shifted 4.0 min
05/29 06:32:30 - 05/29 06:39:26	Fixed (0.0, 0.0)	synoptic, shifted 29.5 min
05/29 19:53:00 - 05/29 19:59:56	Fixed (0.0, 0.0)	synoptic, shifted 8.0 min
05/30 05:57:30 - 05/30 06:04:26	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min
05/30 19:03:00 - 05/30 19:09:54	Fixed (0.0, 0.0)	synoptic, shifted -14.5 min
05/31 06:06:00 - 05/31 06:13:00	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min

PROG= 03 1-time(s)

├ **Subr= 1 1-time(s) 12.0sec**

├ **Seqn= 33 1-time(s) 4.0sec**

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

├ **Seqn= 5 1-time(s) 2.0sec**

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

├ **Seqn= 40 1-time(s) 4.0sec**

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

├ **Seqn= 77 1-time(s) 2.0sec**

thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

├ **Seqn= 6 1-time(s) 2.0sec**

Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19E1: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m

Term	Pointing (x, y)	Comment
05/28 06:13:00 - 05/28 10:10:30	Track (-5.3, -164.5) @ 05/28 06:10:00	# track AR 12355
05/28 14:01:30 - 05/28 18:03:56	Track (65.1, -165.2) @ 05/28 14:00:00	track AR 12355
05/28 18:17:00 - 05/29 06:29:26	Track (103.0, -165.6) @ 05/28 18:14:00	# track AR 12355
05/29 06:42:30 - 05/29 08:56:56	Track (213.0, -166.9) @ 05/29 06:39:30	# track AR 12355, HOP 285 from 0900UT
05/29 20:03:00 - 05/30 05:54:26	Track (327.6, -168.5) @ 05/29 20:00:00	# track AR 12355
05/30 06:07:30 - 05/30 08:56:54	Track (410.7, -169.8) @ 05/30 06:04:30	# track AR 12355, HOP 285 from 0900UT
05/30 23:28:30 - 05/31 06:02:56	Track (542.1, -172.2) @ 05/30 23:10:00	# track AR 12355

PROG= 13 Inf.-time(s)

├ **Subr= 1 1-time(s) 2.0sec**

├ **Seqn= 8 2-time(s) 2.0sec**

Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
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├ **Subr= 2 2-time(s) 2.0sec**

├ **Seqn= 24 1-time(s) 2.0sec**

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

├ **Seqn= 42 4-time(s) 2.0sec**

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

├ **Seqn= 62 30-time(s) 60.0sec**

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A8A: HOP285 (AEC3) Al/Poly FOV 384x384 30s cadence

Term	Pointing (x, y)	Comment										
05/29 09:00:00 - 05/29 19:49:56	Track (213.0, -166.9) @ 05/29 06:39:30	# track AR 12355, HOP 285 from 0900UT										
05/30 08:59:58 - 05/30 18:59:56	Track (410.7, -169.8) @ 05/30 06:04:30	# track AR 12355, HOP 285 from 0900UT										
PROG= 18 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 50 1-time(s) 30.0sec												
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=92	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2

Term	Pointing (x, y)	Comment										
05/30 19:24:58 - 05/30 21:09:54	Fixed (0.0, 930.0)	# Coalignment obs at N pole										
PROG= 15 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 76 24-time(s) 300.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec- 8 min cadence-wNGT-2

Term	Pointing (x, y)	Comment										
05/30 21:24:58 - 05/30 23:09:54	Fixed (-945.0, 0.0)	Coalignment obs at E limb										
PROG= 20 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 60 15-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1A77: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment										
05/26 09:35:00 - 05/26 19:39:56	Track (-54.0, -39.7) @ 05/26 09:32:00	# OP start + 10min HOP 285 with VTT on disk center										
05/26 19:53:00 - 05/27 05:45:26	Track (-308.2, -162.9) @ 05/26 19:50:00	# track AR 12355										
05/27 05:58:30 - 05/27 08:59:56	Track (-221.2, -163.1) @ 05/27 05:55:30	# track AR 12355										
05/27 09:03:00 - 05/27 18:59:56	Track (-58.9, -39.7) @ 05/27 09:00:00	HOP 285 with VTT on disk center										
05/27 19:13:00 - 05/28 05:59:56	Track (-104.0, -163.7) @ 05/27 19:10:00	# track AR 12355										
05/28 06:13:00 - 05/28 10:10:30	Track (-5.3, -164.5) @ 05/28 06:10:00	# track AR 12355										
05/28 14:01:30 - 05/28 18:03:56	Track (65.1, -165.2) @ 05/28 14:00:00	track AR 12355										
05/28 18:17:00 - 05/29 06:29:26	Track (103.0, -165.6) @ 05/28 18:14:00	# track AR 12355										
05/29 06:42:30 - 05/29 08:56:56	Track (213.0, -166.9) @ 05/29 06:39:30	# track AR 12355, HOP 285 from 0900UT										
05/29 09:00:00 - 05/29 19:49:56	Track (213.0, -166.9) @ 05/29 06:39:30	# track AR 12355, HOP 285 from 0900UT										
05/29 20:03:00 - 05/30 05:54:26	Track (327.6, -168.5) @ 05/29 20:00:00	# track AR 12355										
05/30 06:07:30 - 05/30 08:56:54	Track (410.7, -169.8) @ 05/30 06:04:30	# track AR 12355, HOP 285 from 0900UT										
05/30 08:59:58 - 05/30 18:59:56	Track (410.7, -169.8) @ 05/30 06:04:30	# track AR 12355, HOP 285 from 0900UT										
05/30 23:28:30 - 05/31 06:02:56	Track (542.1, -172.2) @ 05/30 23:10:00	# track AR 12355										
PROG= 02 30-time(s)												
└─ Subr= 1 20-time(s) 2.0sec												
└─ Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 38 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

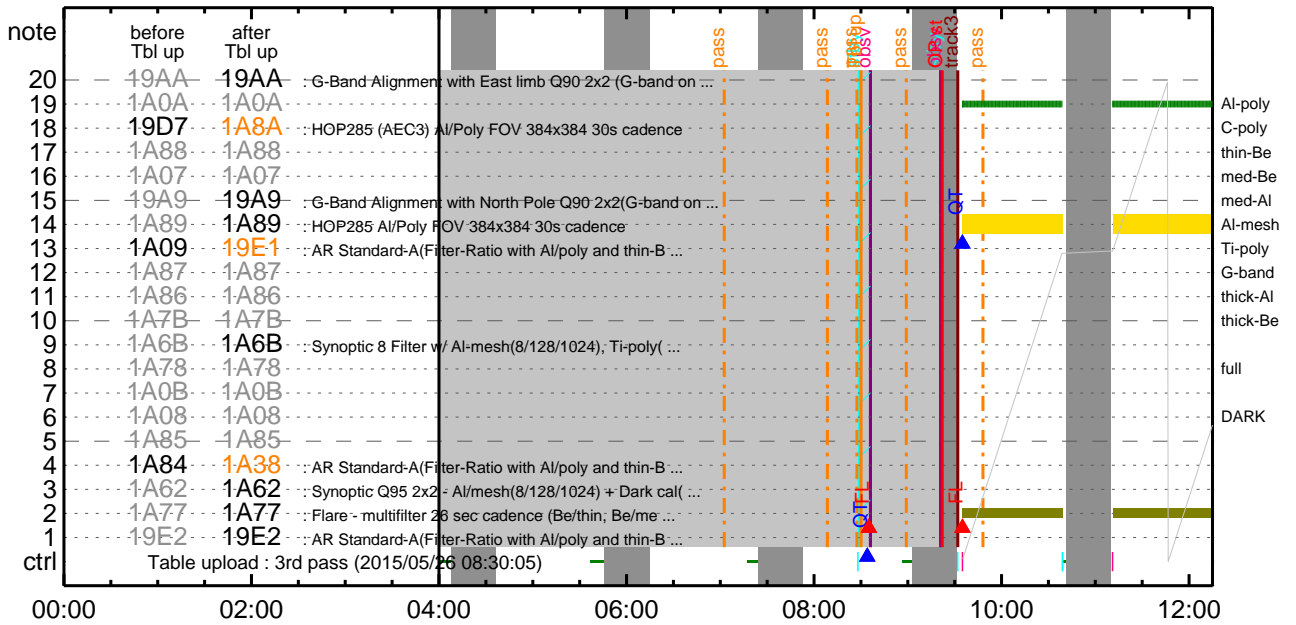
Flare Detection

* * * * *

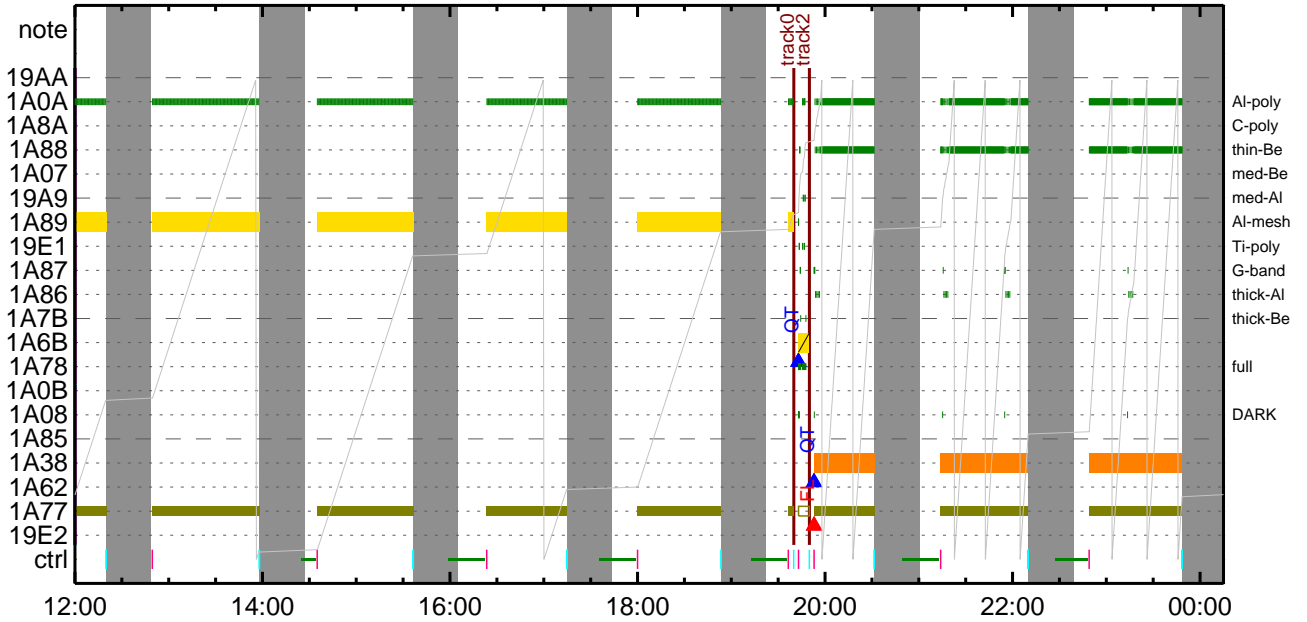
FLD Patrol

Term	Pointing (x, y)	Comment
05/26 19:50:18 - 05/27 05:45:48	Track (-308.2, -162.9) @ 05/26 19:50:00	# track AR 12355
05/27 05:55:48 - 05/27 19:00:18	Track (-221.2, -163.1) @ 05/27 05:55:30	# track AR 12355
05/27 19:10:18 - 05/28 06:00:18	Track (-104.0, -163.7) @ 05/27 19:10:00	# track AR 12355
05/28 06:10:18 - 05/28 18:04:18	Track (-5.3, -164.5) @ 05/28 06:10:00	# track AR 12355
05/28 18:14:18 - 05/29 06:29:48	Track (103.0, -165.6) @ 05/28 18:14:00	# track AR 12355
05/29 06:39:48 - 05/29 19:50:18	Track (213.0, -166.9) @ 05/29 06:39:30	# track AR 12355, HOP 285 from 0900UT
05/29 20:00:18 - 05/30 05:54:48	Track (327.6, -168.5) @ 05/29 20:00:00	# track AR 12355
05/30 06:04:48 - 05/30 19:00:18	Track (410.7, -169.8) @ 05/30 06:04:30	# track AR 12355, HOP 285 from 0900UT
05/30 23:10:16 - 05/31 06:03:18	Track (542.1, -172.2) @ 05/30 23:10:00	# track AR 12355
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms	Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

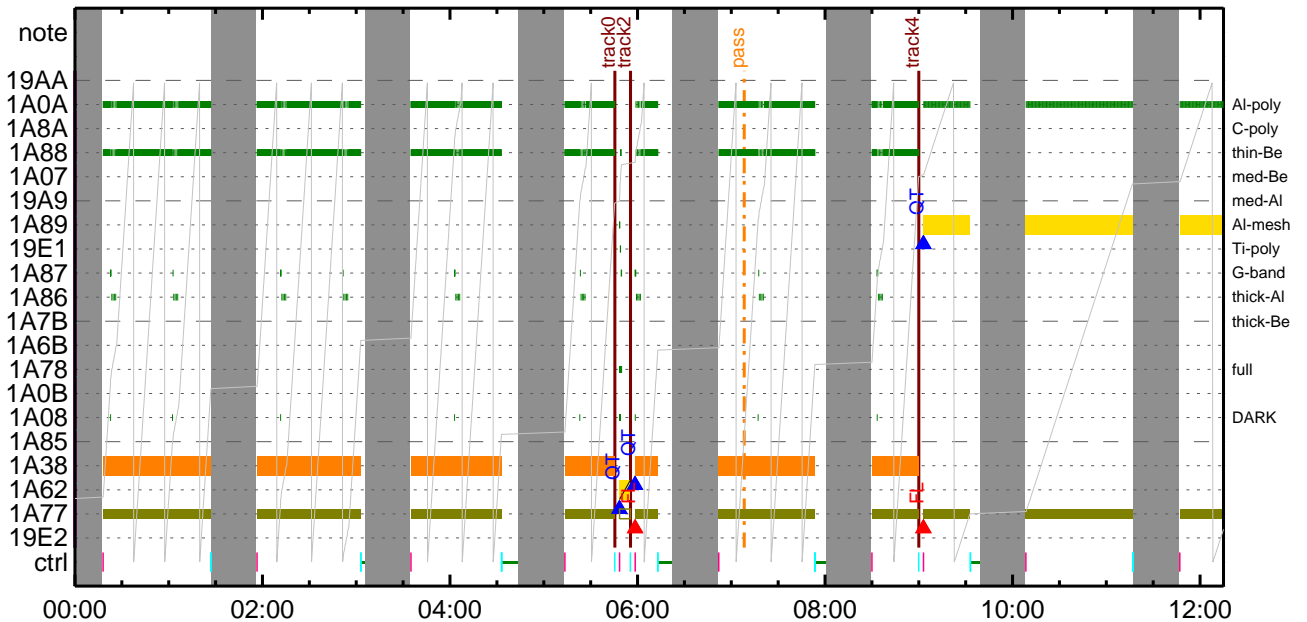
CMDI #0155 2015/05/26



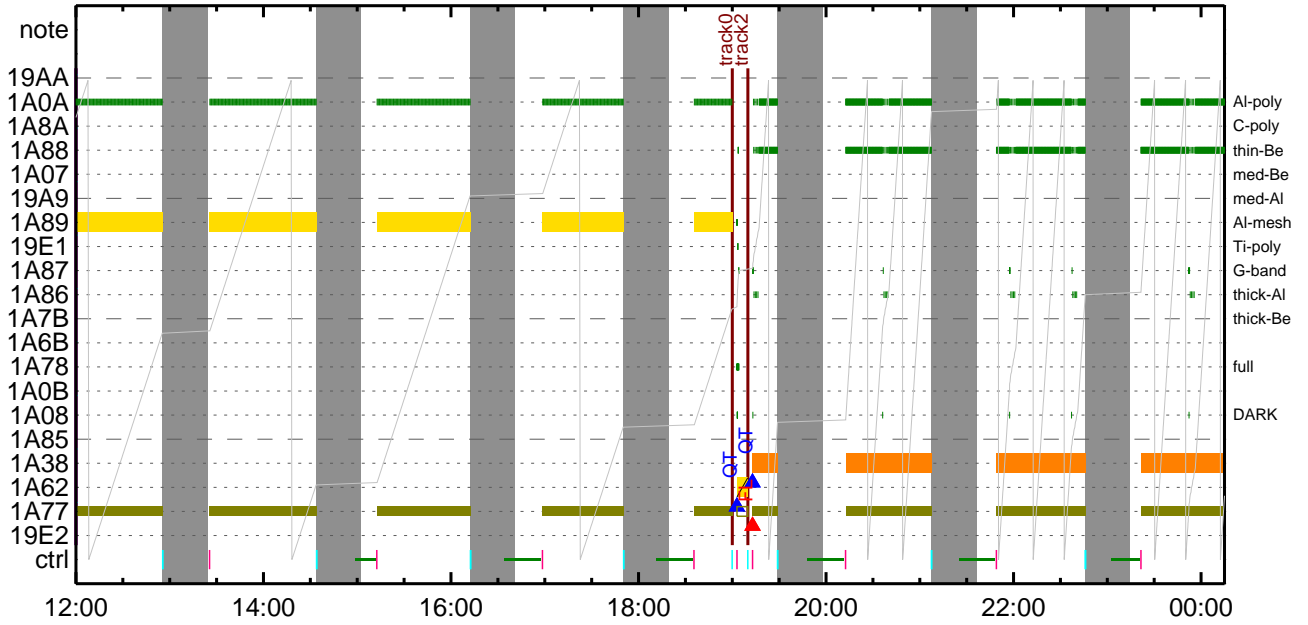
CMDI #0155 2015/05/26



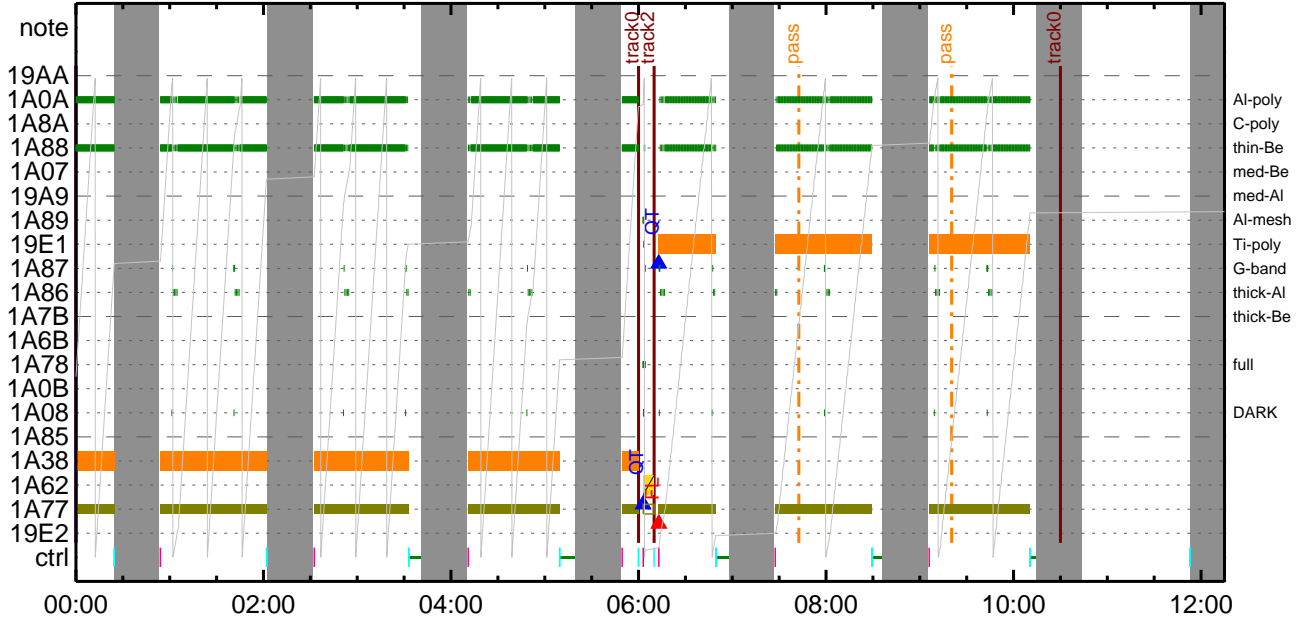
CMDI #0155 2015/05/27



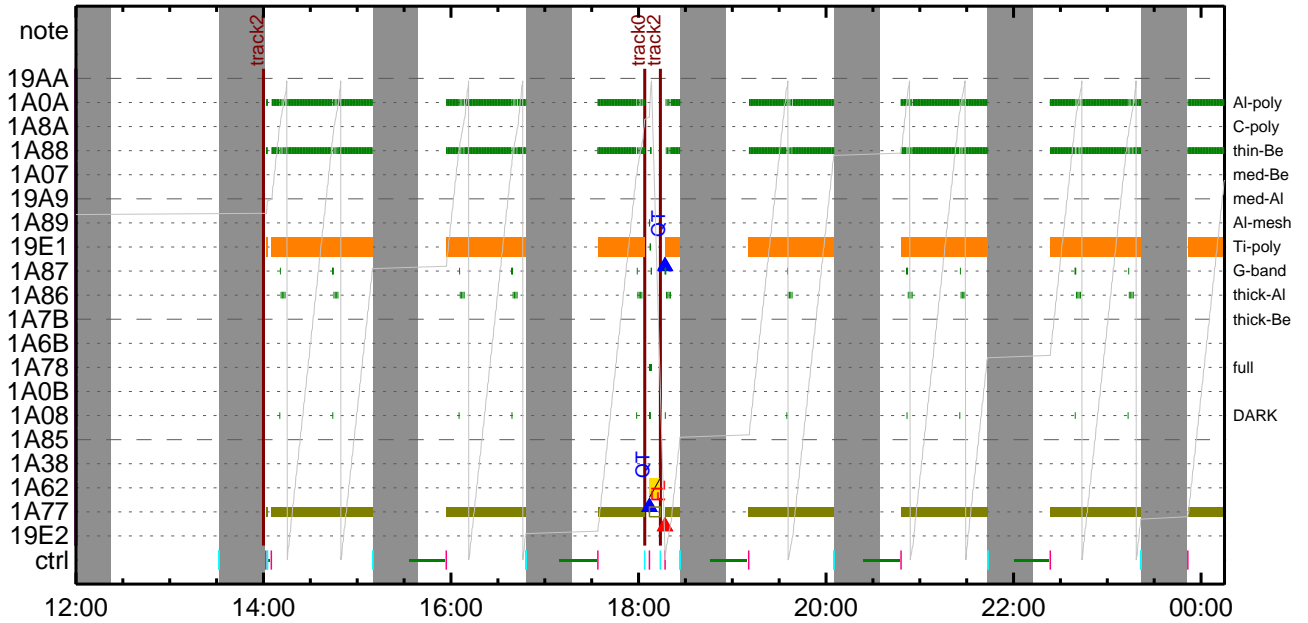
CMDI #0155 2015/05/27



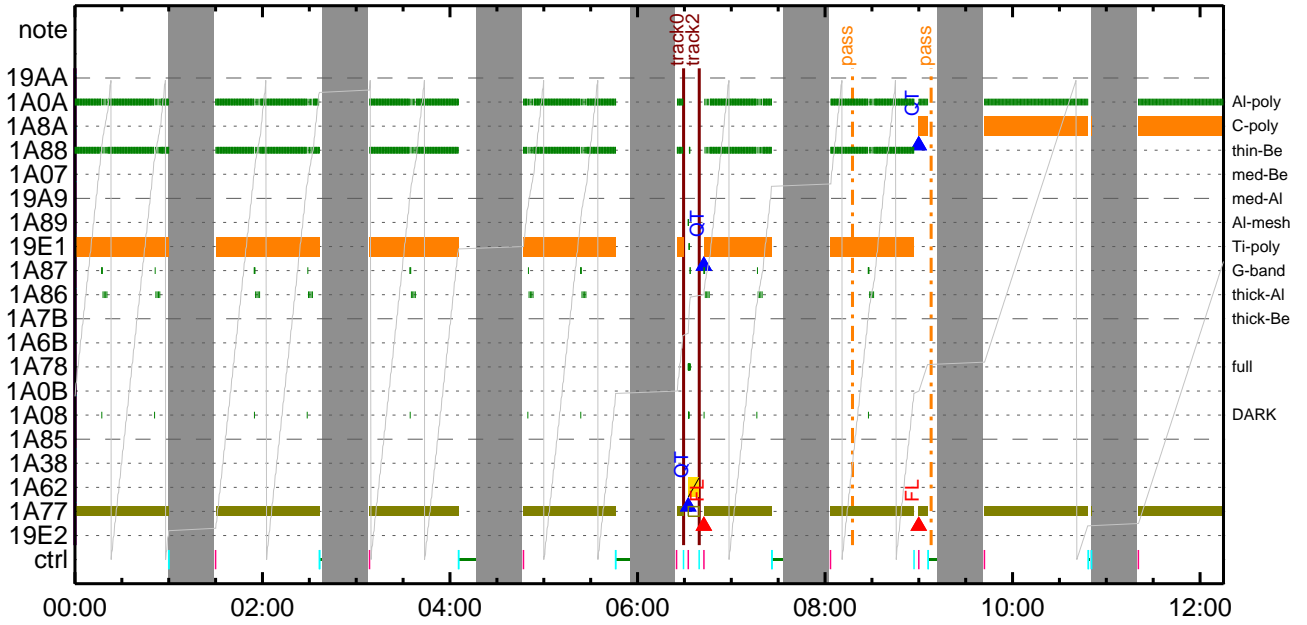
CMDI #0155 2015/05/28



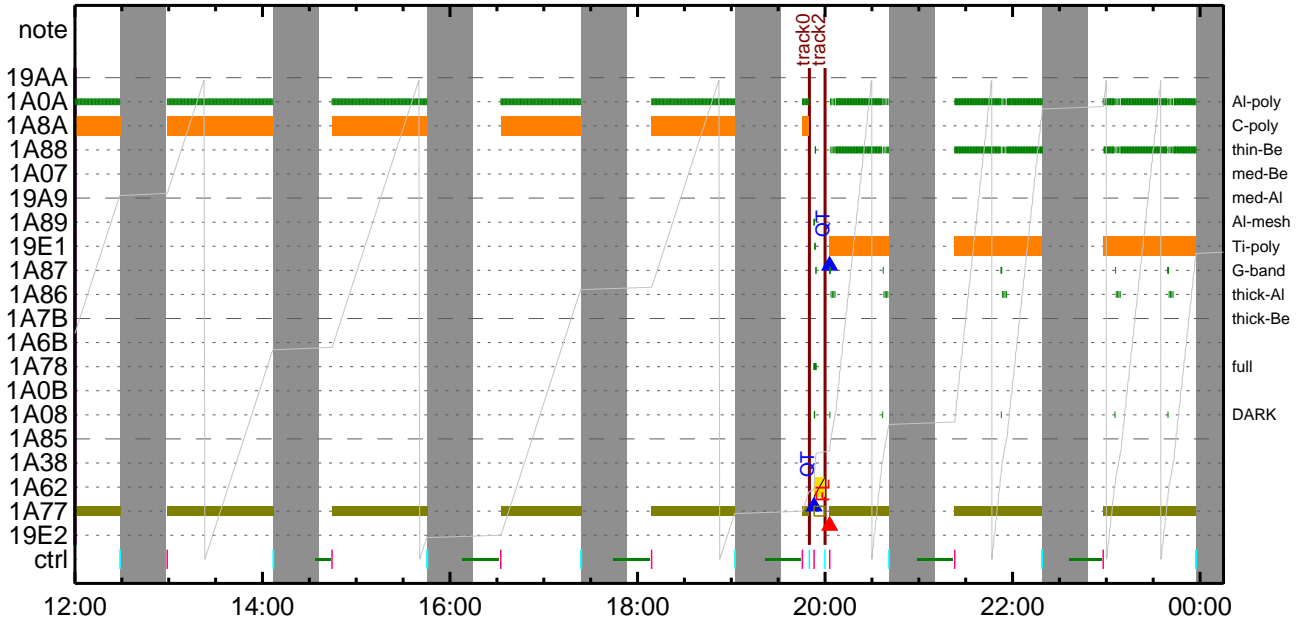
CMDI #0155 2015/05/28



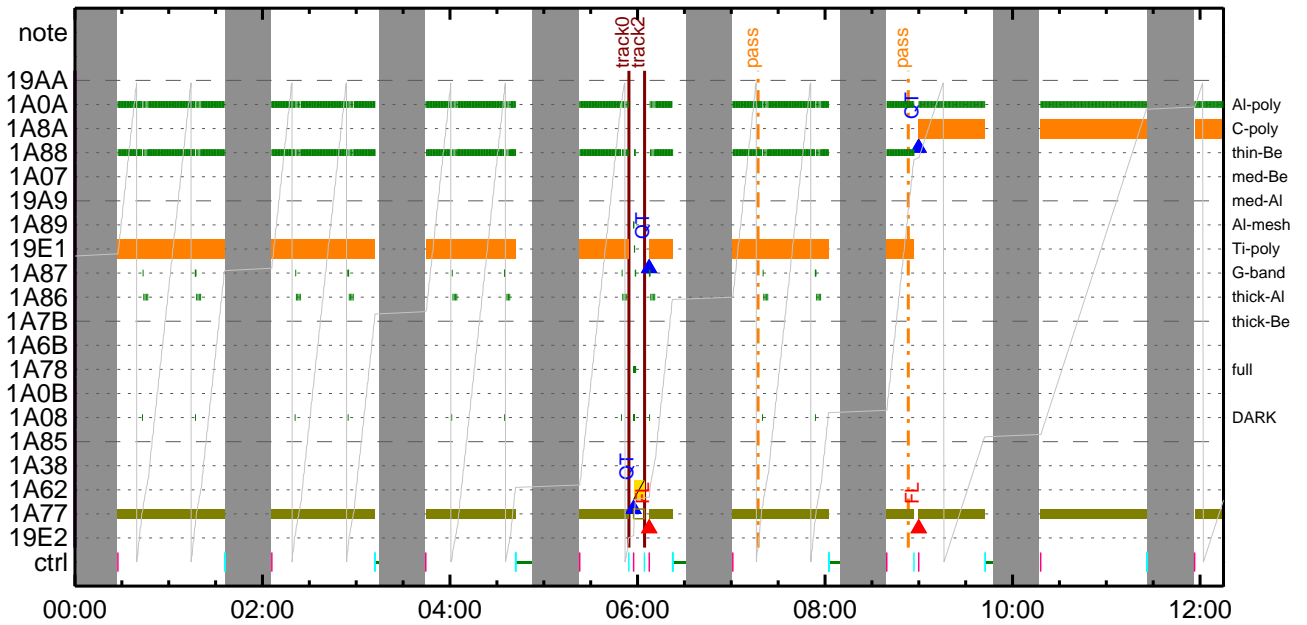
CMDI #0155 2015/05/29



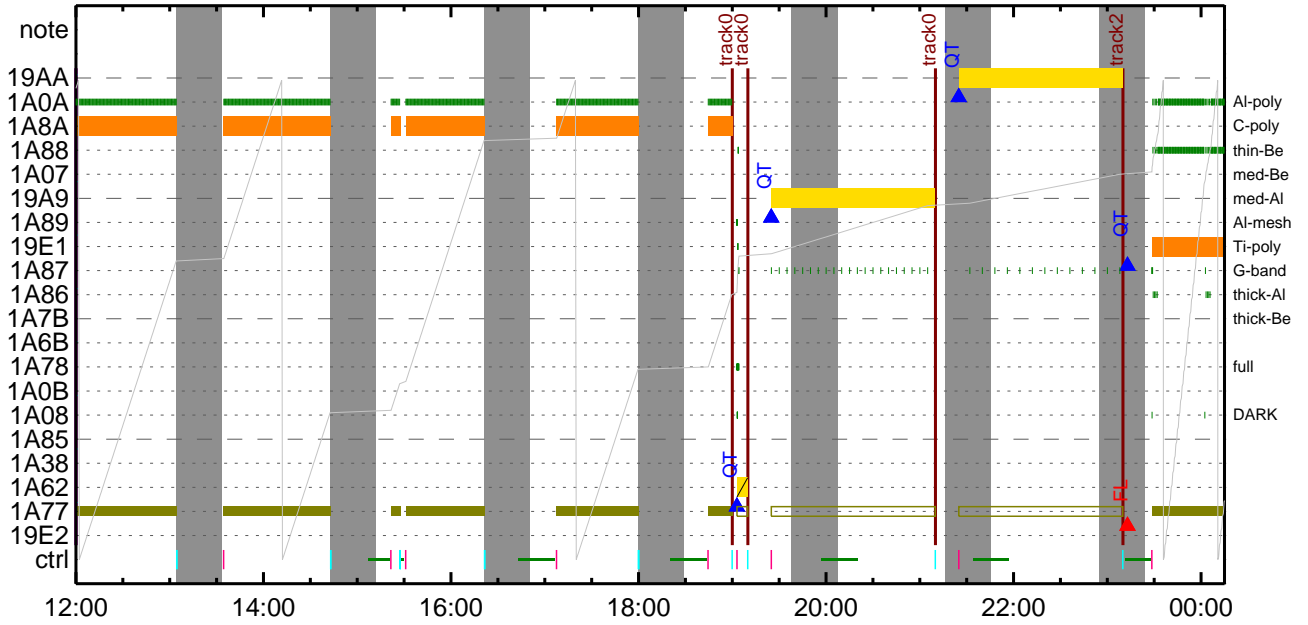
CMDI #0155 2015/05/29



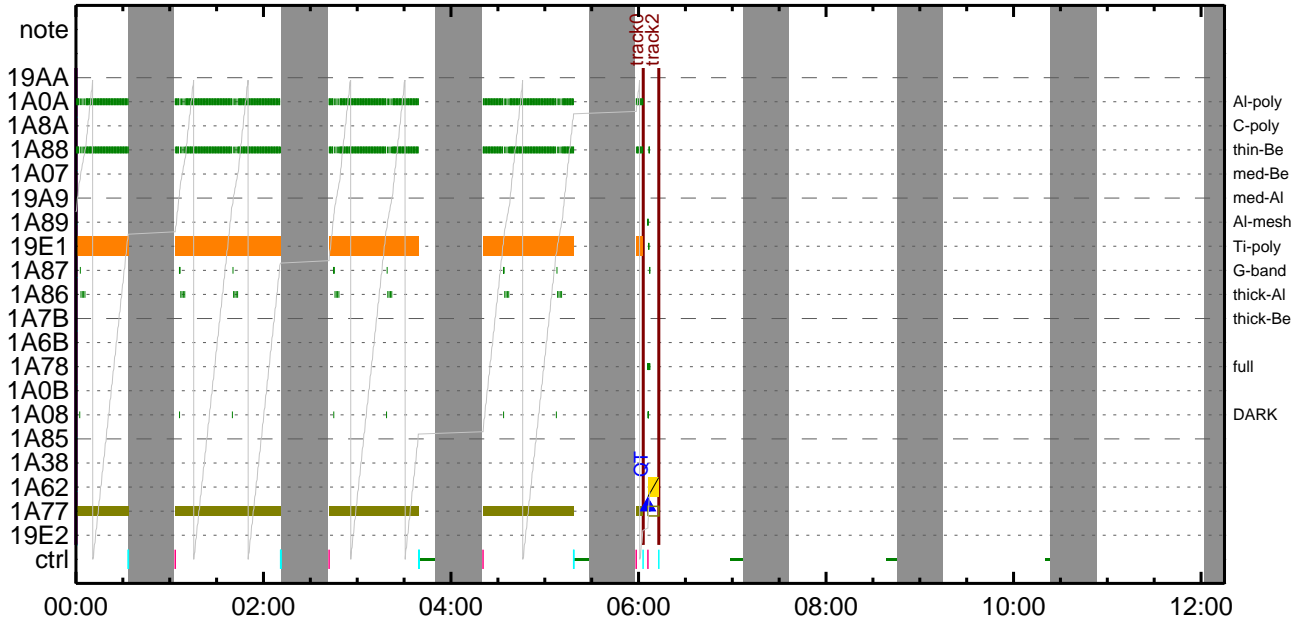
CMDI #0155 2015/05/30



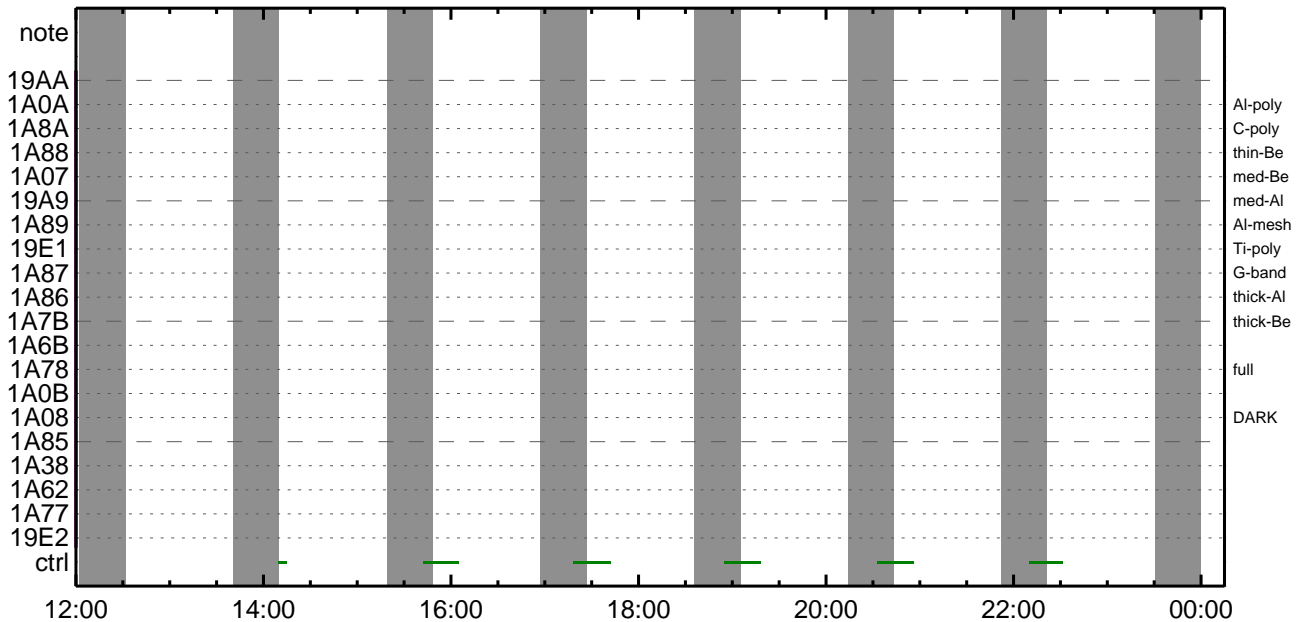
CMDI #0155 2015/05/30



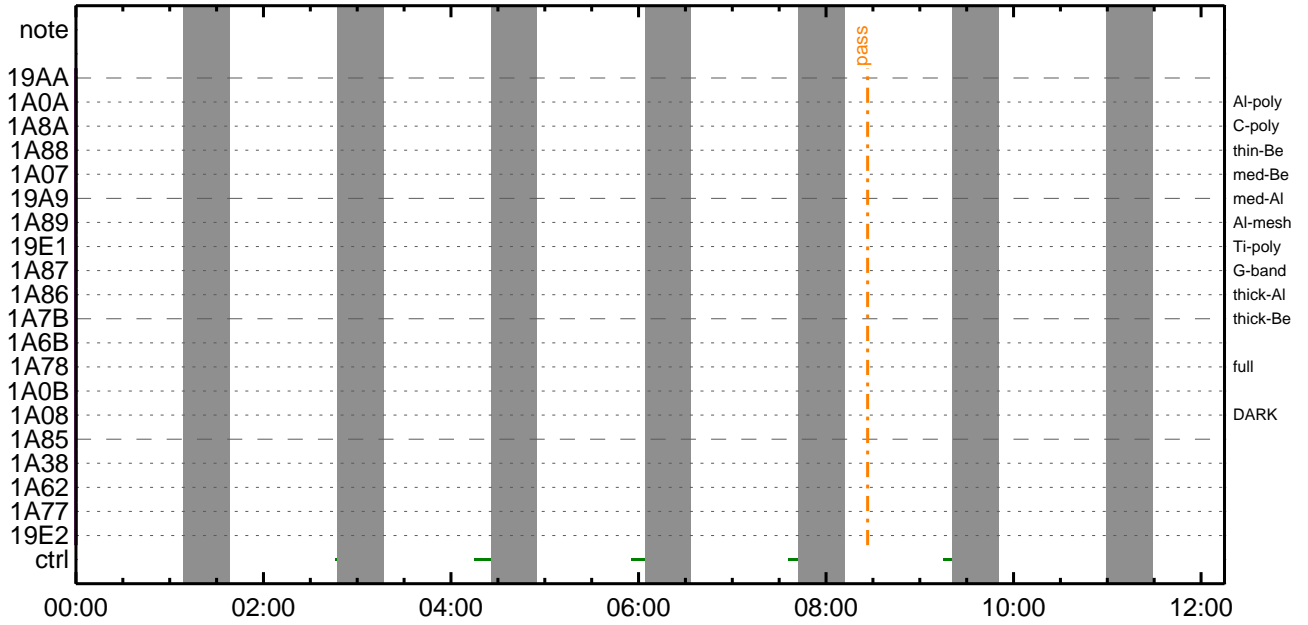
CMDI #0155 2015/05/31



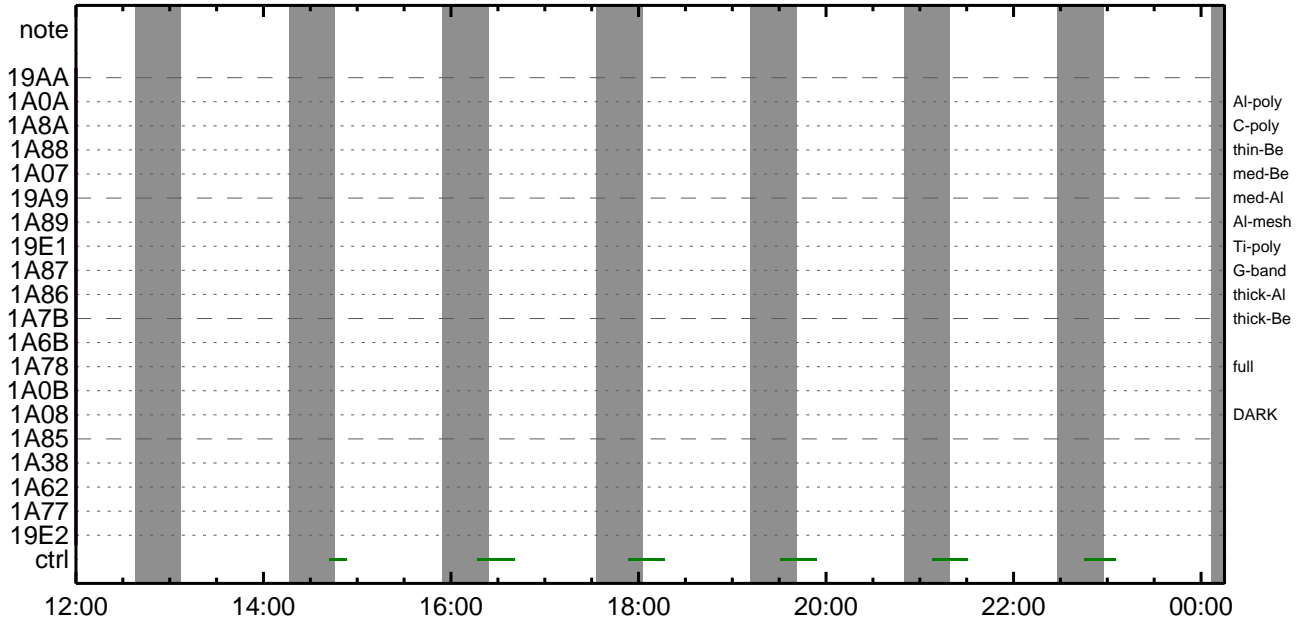
CMDI #0155 2015/05/31



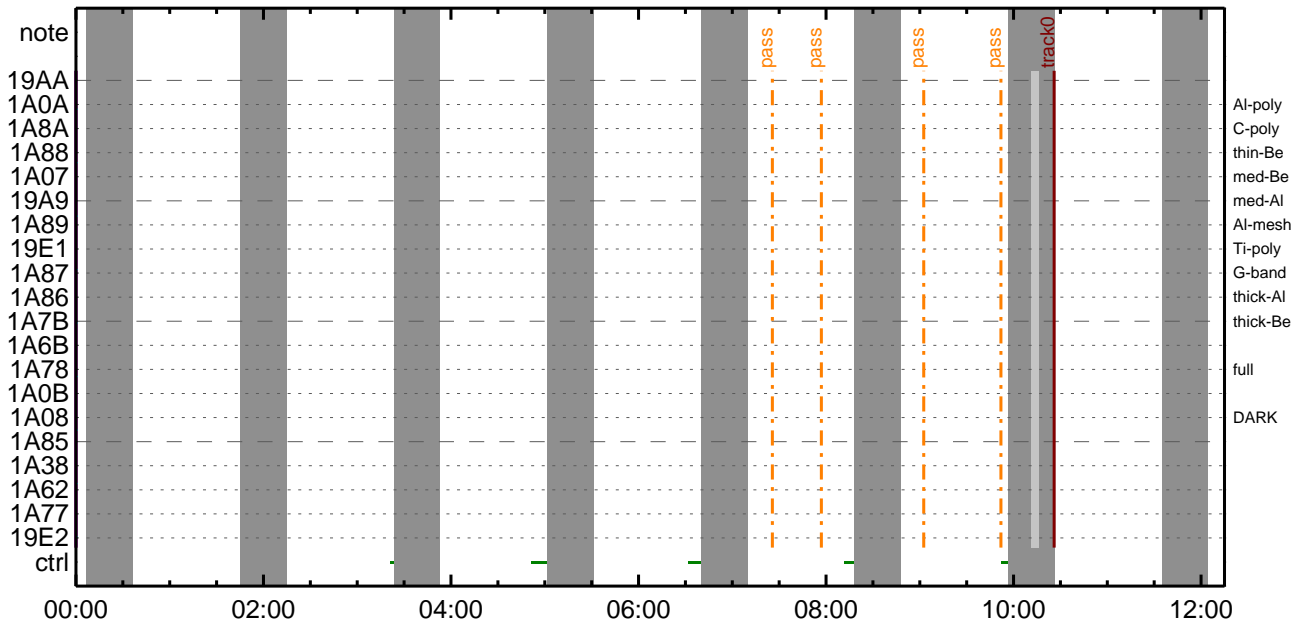
CMDI #0155 2015/06/01



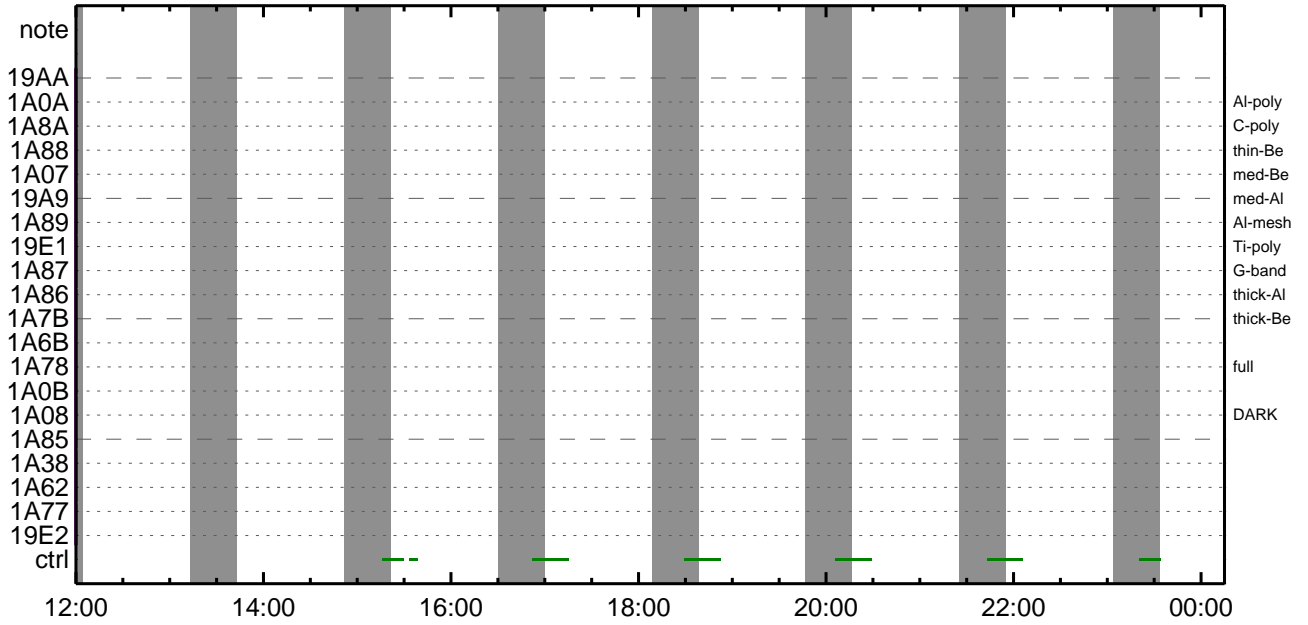
CMDI #0155 2015/06/01



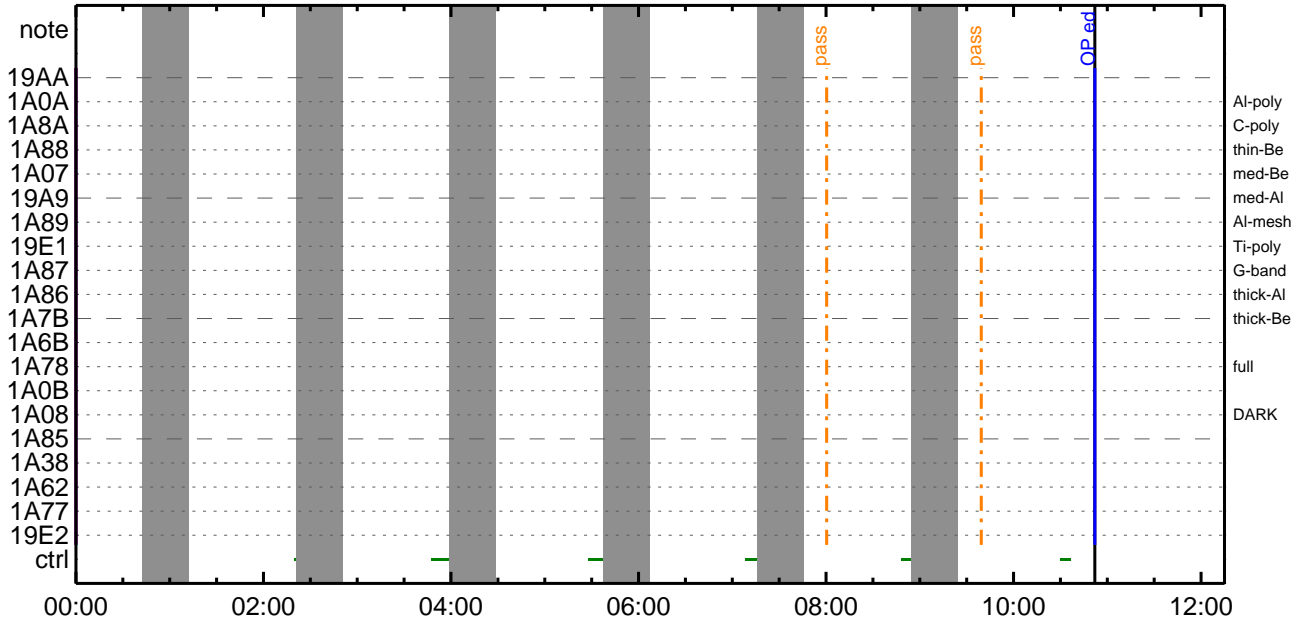
CMDI #0155 2015/06/02



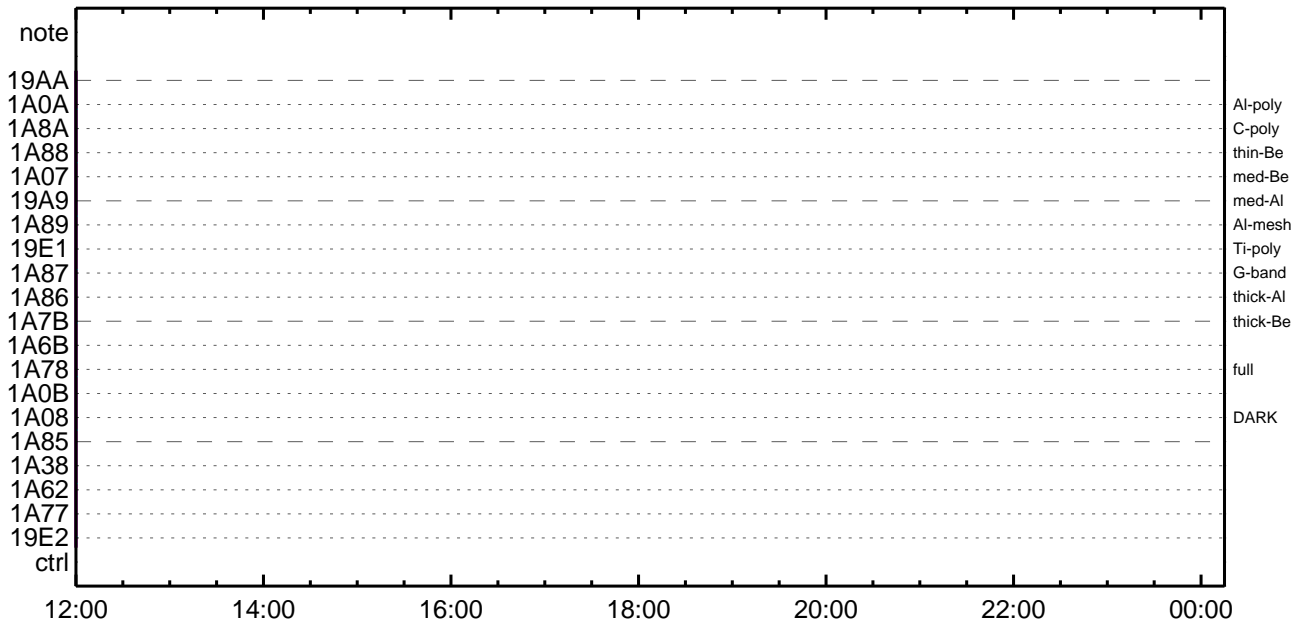
CMDI #0155 2015/06/02



CMDI #0155 2015/06/03



CMDI #0155 2015/06/03



(a) Spacecraft Operation Procedure (real-commands)

```
main-161 2015-05-26 12:58:15 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY`¼Ä»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÁò•μ°Æ»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈè¿μ•íÉ;ÈòÈ¼°ÇÓò•ò¿¼í¹çòÍ;çÀ®, ùò¹òÈòòòçÁ+¿®ò•òÈòòòòòÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-161:OP
0020 ( )
0021 . S. OG og-161:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPî°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYóY×¼ªªî»òð³îÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòî¿È¹ç•è²ïOKòð³îÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYóY×¼ªªî»òð³îÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòî¿È¹ç•è²ïOKòð³îÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYóY×¼ªªî»òð³îÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPòî¿È¹ç•è²ïOKòð³îÇ§
0081 C.
0082 . C. ***** òÈ²¼òî¿Ä´¶¼òÈÈ-òòÁ+¿®(¼áμ-YÁYóY×¼È¿çòðÁÓÁæòçªªò°ò¼¿í¹çòçòâ) *****
0083 C. DHUYá;¼YÉ;È¿Y½;Yí;¼YÉ;ÈòðÍáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADò-Á+¿®NGòî¿í¹ç;ç°È²¼òî¿TI-CMDÁ+¿®òî¿Á¹Ôò•òÈòòòòòÈ;f
```

```

0096 C.      0300; SET 0EDUMP 01A±0iYNY¹0Ç¹0a|0³0E; f
0097 C.
0098 . C.   TIY³YF¥0YÉ0dÁDİ¿ (UT)
0099 +. TI  2015-05-26 09:17:00.0
0100 DC  01-B3 DHU_OP_STOP
0101 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0102 C.
0103 +. TI  2015-05-26 09:17:01.0
0104 DC  01-B4 DHU_OP_COPY
0105 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0106 C.
0107 +. TI  2015-05-26 09:17:01.0
0108 DC  01-B5 DHU_OPOG_COPY
0109 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0110 C.
0111 +. TI  2015-05-26 09:21:59.5
0112 DC  01-B2 DHU_OP_START
0113 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0114 C.
0115 C.   0E²¼0İÄè%îÍÑ0İYÁ¥§YÁY-¹àİÜ
0116 C.      çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0117 C.      çç[HK1_TI_CMD_NUM]         EQ      4
0118 C.      çç[HK1_NEXT_EXEC_PIM]      EQ      DHU
0119 C.      çç[HK1_NEXT_EXEC_DC]      EQ      0xB3
0120 C.
0121 . C.   *****
0122 C.   TIİİ°èYÁY0Y×
0123 C.   *****
0124 C.
0125 C.   TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0126 +. DC  01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.      çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0129 C.      çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0130 C.      çç[HK1_DMP_BLOCK_NUM]      EQ      3
0131 C.      çç[HK1_DMP_REPEAT_NUM]     EQ      0
0132 C.      çç[HK1_DMA_DMP_PIM]       EQ      DHU
0133 +. DC  01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.      çç[HK1_PKT_FORM_NO]        EQ      7
0136 C.      çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0137 C.      çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0138 C.      çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0139 C.      çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0140 C.
0141 . C.   YÁY0Y×½ªİ»0d³İÇ§
0142 C.      çç[HK1_DMP_CHK_FLG]       EQ      NON
0143 C.
0144 . C.   RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇ§
0145 C.
0146 . C.   DHU¥â;¼¥É;È¼Y½.¥İ;¼¥È;È0dİá0¹
0147 +. DC  01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.      çç[HK1_PKT_FORM_NO]        EQ      2
0150 C.      çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0151 C.      çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0152 C.      çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0153 C.
0154 C.   *****
0155 C.   SOT TI command set
0156 C.   *****
0157 C.   Execute, after the success of OP upload.
0158 +. TI  2015-05-26 09:21:16.0
0159 DC  07-F0 MDP_SOT_MODE_STBY
0160 BC      (41)
0161 . C.   -----
0162 C.   HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C.   -----
0164 C.   ***** SOT END *****
0165 C.
0166 C.   ***** XRT START *****
0167 C.   Execute, after the success of OP upload.
0168 +. TI  2015-05-26 09:21:00.0
0169 DC  07-F0 MDP_XRT_MODE_STBY
0170 BC      (c3)
0171 . C.   [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0172 C.
0173 C.   ***** XRT END *****
0174 . C.   Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C.   ***** Start EIS operation (TI set) *****
0178 C.   Execute, after the success of OP upload.
0179 C.   Set EIS TI-commands
0180 +. TI  2015-05-26 09:21:30.0
0181 DC  07-FC EIS_MODE_MANU
0182 BC      (21 02)
0183 +. TI  2015-05-26 09:21:40.0
0184 DC  07-FC EIS_MODE_CHG_DIS
0185 BC      (22)
0186 . C.   [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0187 C.   ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 . C.   ***** MDP `0Ãİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C.   (¼0İY0YÁYÉY0Y¥YÁYçYè0E¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCSS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCSS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCSS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 4817948.9 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 1m54s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2015-05-26 09:21:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP `úÄîñî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0165 C. (¼á°ìÝÖÝÄÝËÝÛÝËÝÄÝ¼ÝËè¼¼¼¼¼»Ü¹¼è)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝD¥¹•ï Daily±;íññÈ´Ø¹ñèDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÄÝSÝÄÝ-¼Ä»Ü;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-163 2015-05-26 12:58:15 176 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀY$;¼Y³YFYóYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈççðÁð•µºÈ»Í×ÁÇçíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÈ;ÈÈÈºÇÓç•çç¼l¹ççí;çÀ®,ùç¹çèçðçÁ+çç®ç•çÈçççççÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-262:MDP_OBS_F
0026 ()
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-289:MDP_OBS_S
0044 ()
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 . C. < Upload DPL table >
0054 C.
0055 C. YçYÁY×Yí;¼YÉçíÁºçÈSTS_CHKçðOFFçÈç¹çè
0056 C.
0057 . S. RAM ram-271:MDP_DPL
0058 ()
0059 C.
0060 . C. < Dump RAMID=MDP_DPL >
0061 +. DC 07-F0 MDP_DUMP_FGTBL
0062 BC (82 07 00 38 b8 00 40)
0063 C. -----
0064 C. MDP_DPL verify = OK [ ]
0065 C. -----
0066 C.
0067 C. STS_CHKçðONçÈç¹çè
0068 C.
0069 . C. < Update MDP DSC PAR1 >
0070 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0071 BC (4c)
0072 C. MDP_CMD_CODE = F04C0700 [ ]
0073 C. MDP_CMD_CNT (count-up 1) [ ]
0074 C. -----
0075 C.
0076 . C.
0077 C. *****
0078 C. SOT TI command set
0079 C. *****
0080 C. Execute, after the success of TBL upload.
0081 +. TI 2015-05-26 09:21:18.0
0082 DC 07-F0 MDP_SOT_MODE_OBSV
0083 BC (40)
0084 . C. -----
0085 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0086 C. -----
0087 C.
0088 C.
0089 C. ***** XRT START *****
0090 C.
0091 +. DC 07-F0 MDP_XRT_CTRL_MANU
0092 BC (c1)
0093 +. DC 07-F0 MDP_XRT_MODE_STBY
0094 BC (c3)
0095 . C. ----- Success Verify ?
```

OK / NG_____


```

0096 C.
0097 C. XRT Obs. Table Upload
0098 . S. RAM ram-291:MDP_OBS_X
0099 ( )
0100 C.
0101 +. DC 07-F0 MDP_DUMP_XRTTBL
0102 BC (84 07 00 00 00 3a d4)
0103 . C. ----- Comparison Check ?           OK / ERR ____
0104 C.
0105 C.
0106 +. DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 01 b1 b1 04 04)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 02 b1 b1 08 08)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 03 b1 b1 08 08)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 04 b1 b1 06 06)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 05 85 83 06 06)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 06 85 83 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 07 85 83 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 08 80 80 20 20)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 09 80 80 20 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 0a 80 80 08 20)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 0b 80 60 20 18)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 0c a0 80 18 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 0f 80 80 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 10 80 80 08 08)
0134 + DC 07-F0 MDP_XRT_FLD_ENA
0135 BC (d8)
0136 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0137 BC (c8)
0138 + DC 07-F0 MDP_XRT_AEC_RESET
0139 BC (d0)
0140 + DC 07-F0 MDP_XRT_ARS_DIS
0141 BC (d5)
0142 + DC 07-F0 MDP_XRT_FLD_RESET
0143 BC (da)
0144 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0145 BC (c4 01)
0146 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0147 BC (c5 02)
0148 . C. ----- Success Verify ?           OK / NG ____
0149 C.
0150 C.
0151 . C. All OK?  Yes--> Please Proceed. / No --> Stop here.
0152 C.
0153 +. DC 07-F0 MDP_XRT_MODE_OBSV
0154 BC (c2)
0155 +. TI 2015-05-26 09:21:02.0
0156 DC 07-F0 MDP_XRT_MODE_OBSV
0157 BC (c2)
0158 . C. ----- Success Verify ?           OK / NG ____
0159 C.
0160 C. ***** XRT END *****
0161 C.
0162 . C. ***** MDP ^úĀîĉī»ô%ŶȉÉĀĐŋ¹ᵋēDCBC•x²è *****
0163 C. (%ă°îÿÓŶĀŷŒÛŲŲŲŒŷĀŶçŶèĉ%¼ĉĉ%Ā»Ūŋ¹ᵋè)
0164 . S. DC-BC dcbc-402:DCBC
0165 (MDP_known_event)
0166 C.
0167 C.
0168 . C. ***** ŷĐŶ¹•İ Daily±ĵİñŋÉ´Ųŋ¹ᵋēDCBC•x²è *****
0169 . S. DC-BC dcbc-153:DCBC
0170 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0171 C.
0172 C.
0173 . C. ĵăLOŶŷĀŶŶŶĀŶ^-¼Ā»Ū;ă
0174 C.
0175 . C. ***** LOS *****
0176 C.

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*** OP Sequence for XRT ***

2015/05/26	09:31:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	09:31:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/26	09:32:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2015/05/26	09:32:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/05/26	09:32:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/05/26	09:32:22.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/05/26	09:32:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/26	09:32:26.0	XRT_FLD_RESET_449_OG [0x1c1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/26	09:34:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/05/26	09:34:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/05/26	09:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/26	10:39:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	10:39:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	10:39:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/26	10:39:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/26	10:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/26	11:10:00.0	XRT_Custom_430_OG [0x1ae]			
2015/05/26	11:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/26	12:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	12:20:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	12:20:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/26	12:20:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/26	12:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/26	12:48:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/26	12:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/26	13:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	13:58:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	13:58:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/26	13:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/26	14:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/26	14:34:00.0	XRT_Custom_430_OG [0x1ae]			
2015/05/26	14:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/26	15:36:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	15:36:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	15:36:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/26	15:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/26	15:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/26	16:22:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/26	16:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/26	17:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	17:15:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	17:15:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/26	17:15:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/26	17:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/26	17:59:00.0	XRT_Custom_430_OG [0x1ae]			
2015/05/26	18:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/26	18:53:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/26	18:53:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2015/05/26	18:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/26	18:53:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/26	18:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/26	19:35:30.0	XRT_Custom_430_OG [0x1ae]					
2015/05/26	19:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/26	19:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/26	19:39:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/05/26	19:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/05/26	19:40:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/05/26	19:42:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/05/26	19:42:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/26	19:42:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2015/05/26	19:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/26	19:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/26	19:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/05/26	19:50:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00	
2015/05/26	19:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/05/26	19:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/05/26	19:50:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/05/26	19:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/26	19:50:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/26	19:52:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2015/05/26	19:52:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/05/26	19:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/26	20:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/26	20:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/26	20:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/26	20:31:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/26	20:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/26	21:13:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/26	21:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/26	22:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/26	22:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/26	22:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/26	22:10:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/26	22:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/26	22:48:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/26	22:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/26	23:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/26	23:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/26	23:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/26	23:48:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/26	23:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/27	00:17:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/27	00:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/27	01:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	01:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	01:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/27	01:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					

2015/05/27	01:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/27	01:55:30.0	XRT_Custom_430_OG [0x1ae]					
2015/05/27	01:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/27	03:03:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	03:03:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	03:03:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/27	03:03:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/27	03:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/27	03:34:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/27	03:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/27	04:33:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	04:33:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	04:33:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/27	04:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/27	04:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/27	05:12:30.0	XRT_Custom_430_OG [0x1ae]					
2015/05/27	05:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/27	05:45:26.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	05:45:28.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/05/27	05:45:30.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2015/05/27	05:45:48.0	XRT_FLD_DIS_422_OG [0x1a6]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/05/27	05:48:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/05/27	05:48:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/27	05:48:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/05/27	05:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/27	05:55:26.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	05:55:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/05/27	05:55:30.0	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2015/05/27	05:55:48.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/05/27	05:55:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/05/27	05:55:52.0	XRT_AEC_RESET_441_OG [0x1b9]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/05/27	05:55:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/27	05:55:56.0	XRT_FLD_RESET_449_OG [0x1c1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/27	05:58:26.0	XRT_QT_PROG_SET_433_OG [0x1b1]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2015/05/27	05:58:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/05/27	05:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/27	06:13:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	06:13:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	06:13:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/27	06:13:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/27	06:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/27	06:51:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/27	06:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/27	07:53:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	07:53:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/27	07:53:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/27	07:53:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/27	07:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2015/05/27	08:29:00.0	XRT_Custom_430_OG [0x1ae]			
2015/05/27	08:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/27	08:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	08:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/27	09:00:00.0	AOCS_Or-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/05/27	09:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/05/27	09:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/05/27	09:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/05/27	09:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/27	09:00:26.0	XRT_FLD_RESET_449_OG [0x1c1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/27	09:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/05/27	09:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/05/27	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/27	09:33:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	09:33:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	09:33:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/27	09:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/27	09:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/27	10:07:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/27	10:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/27	11:17:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	11:17:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	11:17:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/27	11:17:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/27	11:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/27	11:46:00.0	XRT_Custom_430_OG [0x1ae]			
2015/05/27	11:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/27	12:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	12:55:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	12:55:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/27	12:55:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/27	12:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/27	13:24:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/27	13:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/27	14:34:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	14:34:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	14:34:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/27	14:34:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/27	14:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/27	15:11:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/27	15:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/27	16:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	16:12:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	16:12:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/27	16:12:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/27	16:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/27	16:57:31.0	XRT_Custom_430_OG [0x1ae]			
2015/05/27	16:58:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/27	17:50:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/27	17:50:32.0	XRT_CTRL_MANU_402_OG [0x192]			

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2015/05/27	17:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/05/27	17:50:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/05/27	17:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/05/27	18:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/05/27	18:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2015/05/27	18:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/05/27	18:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/05/27	19:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2015/05/27	19:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCs_OrE-point_Start_2_OG [0x098]						
2015/05/27	19:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	AOCU_NM	5	02-76	00 00 00 00 00			
2015/05/27	19:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/05/27	19:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/05/27	19:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/05/27	19:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2015/05/27	19:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/05/27	19:10:00.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/05/27	19:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00			
2015/05/27	19:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/05/27	19:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/05/27	19:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/05/27	19:10:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/05/27	19:12:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/05/27	19:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2015/05/27	19:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2015/05/27	19:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/05/27	19:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/05/27	19:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/05/27	19:29:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/05/27	19:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/05/27	20:11:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/05/27	20:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2015/05/27	21:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/05/27	21:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/05/27	21:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/05/27	21:07:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/05/27	21:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/05/27	21:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/05/27	21:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2015/05/27	22:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/05/27	22:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/05/27	22:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/05/27	22:46:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/05/27	22:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/05/27	23:20:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/05/27	23:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2015/05/28	00:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/05/28	00:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/05/28	00:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			

2015/05/28	00:24:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/05/28	00:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/05/28	00:53:00.0	XRT_Custom_430_OG [0x1ae]								
2015/05/28	00:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/28	02:02:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	02:02:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	02:02:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/28	02:02:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/05/28	02:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/05/28	02:31:30.0	XRT_Custom_430_OG [0x1ae]								
2015/05/28	02:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/28	03:33:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	03:33:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	03:33:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/28	03:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/05/28	03:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/05/28	04:10:00.0	XRT_Custom_430_OG [0x1ae]								
2015/05/28	04:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/28	05:09:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	05:09:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	05:09:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/28	05:09:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/05/28	05:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/05/28	05:48:30.0	XRT_Custom_430_OG [0x1ae]								
2015/05/28	05:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/28	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2015/05/28	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2015/05/28	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2015/05/28	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2015/05/28	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/05/28	06:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03					
2015/05/28	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/28	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2015/05/28	06:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	02 00 00 00 00					
2015/05/28	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2015/05/28	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2015/05/28	06:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2015/05/28	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/05/28	06:10:26.0	XRT_FLD_RESET_449_OG [0x1c1]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/28	06:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d					
2015/05/28	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02					
2015/05/28	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/05/28	06:49:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	06:49:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/05/28	06:49:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/05/28	06:49:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/05/28	06:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								

2015/05/28	07:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/28	07:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2015/05/28	08:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/28	08:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	08:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	08:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/28	08:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/28	09:05:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/28	09:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2015/05/28	10:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/28	10:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	10:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	10:10:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/28	10:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/28	10:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/28	11:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/05/28	11:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	11:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	11:53:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/28	11:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/28	13:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/28	13:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	13:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	13:31:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/28	13:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/28	14:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/28	14:00:30.0	XRT_Custom_430_OG [0x1ae]	AOCU_NM	5	02-76	02 00 00 00 00				
2015/05/28	14:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2015/05/28	14:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/28	14:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	14:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	14:02:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/28	14:04:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/28	14:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2015/05/28	14:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/28	15:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/28	15:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	15:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	15:10:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/28	15:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/28	15:56:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/28	15:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2015/05/28	16:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/28	16:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	16:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/28	16:48:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/28	16:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/28	17:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/28	17:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2015/05/28	18:03:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	18:03:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/05/28	18:04:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2015/05/28	18:04:18.0	XRT_FLD_DIS_422_OG [0x1a6] MDP_XRT_FLD_DIS	1	07-F0	d9
2015/05/28	18:06:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/05/28	18:06:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/28	18:06:58.0	XRT_QT_PROG_SET_438_OG [0x1b6] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/05/28	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/28	18:13:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	18:13:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/28	18:14:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	02 00 00 00 00
2015/05/28	18:14:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/05/28	18:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/05/28	18:14:22.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/05/28	18:14:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/28	18:14:26.0	XRT_FLD_RESET_449_OG [0x1c1] MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/28	18:16:56.0	XRT_QT_PROG_SET_417_OG [0x1a1] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2015/05/28	18:16:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/05/28	18:17:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/28	18:26:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	18:26:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	18:26:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/28	18:26:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/28	18:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/28	19:09:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/28	19:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/28	20:05:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	20:05:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	20:05:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/28	20:05:06.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/28	20:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/28	20:47:00.0	XRT_Custom_430_OG [0x1ae]			
2015/05/28	20:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/28	21:43:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	21:43:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	21:43:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/28	21:43:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/28	21:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/28	22:22:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/28	22:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/28	23:21:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	23:21:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/28	23:21:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/28	23:21:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/28	23:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/28	23:50:30.0	XRT_Custom_430_OG [0x1ae]			
2015/05/28	23:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/29	01:00:00.5	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/29	01:00:02.5	XRT_CTRL_MANU_402_OG [0x192]			

2015/05/29	01:00:04.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	01:00:06.5	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/29	01:03:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/29	01:29:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/29	01:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/29	02:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	02:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	02:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	02:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/29	02:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/29	03:07:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/29	03:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/29	04:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	04:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	04:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	04:05:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/29	04:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/29	04:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/29	04:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/29	05:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	05:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	05:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	05:46:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/29	05:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/05/29	06:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/05/29	06:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/29	06:29:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	06:29:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	06:29:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/05/29	06:29:48.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/05/29	06:32:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/05/29	06:32:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/05/29	06:32:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/05/29	06:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2015/05/29	06:39:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/29	06:39:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	06:39:30.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/05/29	06:39:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00		
2015/05/29	06:39:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/05/29	06:39:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/05/29	06:39:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/05/29	06:39:56.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/05/29	06:42:26.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/05/29	06:42:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2015/05/29	06:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2015/05/29	07:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/05/29	07:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/05/29	07:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		

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2015/05/29	07:26:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/29	07:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/29	08:02:30.0	XRT_Custom_430_OG [0x1ae]				
2015/05/29	08:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/29	08:56:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	08:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/05/29	08:57:18.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/05/29	08:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/05/29	08:57:22.0	XRT_AEC_RESET_441_OG [0x1b9]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/05/29	08:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/29	08:57:26.0	XRT_FLD_RESET_449_OG [0x1c1]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/29	08:59:56.0	XRT_QT_PROG_SET_404_OG [0x194]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2015/05/29	08:59:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/05/29	09:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/29	09:06:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	09:06:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	09:06:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/29	09:06:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/29	09:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/29	09:41:00.0	XRT_Custom_430_OG [0x1ae]				
2015/05/29	09:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/29	10:48:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	10:48:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	10:48:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/29	10:48:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/29	10:49:30.0	XRT_Custom_430_OG [0x1ae]				
2015/05/29	10:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/29	10:50:30.5	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	10:50:32.5	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	10:50:34.5	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/29	10:50:36.5	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/29	10:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/29	10:53:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/29	11:19:30.0	XRT_Custom_430_OG [0x1ae]				
2015/05/29	11:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/29	12:29:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	12:29:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	12:29:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/29	12:29:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/29	12:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/29	12:58:00.0	XRT_Custom_430_OG [0x1ae]				
2015/05/29	12:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/29	14:07:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	14:07:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	14:07:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/29	14:07:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/29	14:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/29	14:43:31.0	XRT_Custom_430_OG [0x1ae]				
2015/05/29	14:44:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/29	15:45:30.0	XRT_CTRL_MANU_400_OG [0x190]				

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2015/05/29	15:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29	15:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29	15:45:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/29	15:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/29	16:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/29	16:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2015/05/29	17:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/05/29	17:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/29	17:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29	17:24:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29	17:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/29	18:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/29	18:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/29	19:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_430_OG [0x1ae]							
2015/05/29	19:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/05/29	19:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/29	19:02:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29	19:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29	19:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/29	19:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/29	19:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/29	19:49:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_Custom_430_OG [0x1ae]							
2015/05/29	19:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/05/29	19:50:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/05/29	19:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/29	19:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/29	19:52:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/29	19:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/05/29	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_408_OG [0x198]							
2015/05/29	19:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/29	20:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29	20:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/29	20:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	5	02-76	02 00 00 00 00				
2015/05/29	20:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/29	20:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/29	20:00:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/29	20:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/29	20:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/29	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2015/05/29	20:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/05/29	20:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_408_OG [0x198]							
2015/05/29	20:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/29	20:41:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29	20:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29	21:22:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/29	21:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/29	22:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/29	22:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]							
2015/05/29			XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/05/29			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/29			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/29			MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2015/05/29	22:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/29	22:19:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/29	22:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/29	22:57:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/29	22:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/29	23:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	23:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/29	23:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/29	23:57:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/30	00:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/30	00:26:30.0	XRT_Custom_430_OG [0x1ae]					
2015/05/30	00:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/30	01:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/30	01:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/30	01:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/30	01:36:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/30	01:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/30	02:05:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/30	02:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/30	03:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/30	03:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/30	03:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/30	03:12:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/30	03:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/30	03:43:30.0	XRT_Custom_430_OG [0x1ae]					
2015/05/30	03:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/30	04:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/30	04:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/30	04:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/05/30	04:42:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/05/30	04:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/05/30	05:22:00.0	XRT_Custom_430_OG [0x1ae]					
2015/05/30	05:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/30	05:54:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/30	05:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/05/30	05:54:30.5	AOCs_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/05/30	05:54:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/05/30	05:57:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/05/30	05:57:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/30	05:57:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/05/30	05:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/30	06:04:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/30	06:04:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/05/30	06:04:30.0	AOCs_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00	
2015/05/30	06:04:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/05/30	06:04:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/05/30	06:04:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/05/30	06:04:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/05/30	06:04:56.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2015/05/30	06:07:26.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2015/05/30	06:07:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/05/30	06:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	06:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	06:22:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	06:22:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	06:22:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/30	06:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/30	07:00:00.0	XRT_Custom_430_OG [0x1ae]							
2015/05/30	07:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	08:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	08:02:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	08:02:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	08:02:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/30	08:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/30	08:38:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/30	08:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	08:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	08:56:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2015/05/30	08:57:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/05/30	08:57:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/05/30	08:57:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/05/30	08:57:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/30	08:57:24.0	XRT_FLD_RESET_449_OG [0x1c1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	08:59:54.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2015/05/30	08:59:56.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/05/30	08:59:58.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	09:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	09:42:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	09:42:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	09:42:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/30	09:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/30	10:17:00.0	XRT_Custom_430_OG [0x1ae]							
2015/05/30	10:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	11:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	11:26:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	11:26:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	11:26:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/30	11:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/30	11:55:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/30	11:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	13:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	13:04:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	13:04:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	13:04:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/30	13:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/30	13:33:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/30	13:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	14:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2015/05/30	14:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	14:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	14:43:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/30	14:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/30	15:20:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/30	15:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	15:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	15:27:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	15:27:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	15:27:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/30	15:30:00.0	XRT_Custom_430_OG [0x1ae]							
2015/05/30	15:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/30	15:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	16:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	16:21:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	16:21:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	16:21:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/30	16:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/30	17:06:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/30	17:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	18:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	18:00:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	18:00:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/05/30	18:00:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/05/30	18:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/05/30	18:43:30.0	XRT_Custom_430_OG [0x1ae]							
2015/05/30	18:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	18:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	18:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/05/30	19:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/05/30	19:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/30	19:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/30	19:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/30	19:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/05/30	19:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	19:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	19:09:56.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/30	19:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 ad 59 00 00				
2015/05/30	19:10:16.0	XRT_FLD_DIS_446_OG [0x1be]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/30	19:10:18.0	XRT_FLRCTRL_DIS_431_OG [0x1af]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/30	19:10:20.0	XRT_ARS_DIS_407_OG [0x197]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/05/30	19:24:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2015/05/30	19:24:58.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/05/30	21:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/05/30	21:09:56.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/05/30	21:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2015/05/30	21:10:16.0	XRT_FLD_DIS_446_OG [0x1be]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/05/30	21:10:18.0	XRT_FLRCTRL_DIS_431_OG [0x1af]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/05/30	21:10:20.0	XRT_ARS_DIS_407_OG [0x197]							

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2015/05/30	21:24:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2015/05/30	21:24:58.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/30	23:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/30	23:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/05/30	23:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00
2015/05/30	23:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/05/30	23:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/05/30	23:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/05/30	23:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/30	23:10:24.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/30	23:12:54.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2015/05/30	23:12:56.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/05/30	23:27:30.0	XRT_Custom_430_OG [0x1ae]				
2015/05/30	23:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/31	00:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/31	00:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/31	00:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/31	00:33:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/31	00:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/31	01:02:30.0	XRT_Custom_430_OG [0x1ae]				
2015/05/31	01:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/31	02:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/31	02:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/31	02:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/31	02:11:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/31	02:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/31	02:41:00.0	XRT_Custom_430_OG [0x1ae]				
2015/05/31	02:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/31	03:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/31	03:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/31	03:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/31	03:39:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/31	03:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/31	04:19:30.0	XRT_Custom_430_OG [0x1ae]				
2015/05/31	04:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/31	05:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/31	05:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/31	05:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/05/31	05:18:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/05/31	05:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/05/31	05:57:30.0	XRT_Custom_430_OG [0x1ae]				
2015/05/31	05:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/05/31	06:02:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/05/31	06:02:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/05/31	06:03:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/05/31	06:03:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/05/31	06:05:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/05/31	06:05:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/05/31	06:05:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]				

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2015/05/31	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/05/31	06:13:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02	00 00 00 00
2015/05/31	06:13:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/05/31	06:15:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_432_OG [0x1b0]	TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2015/06/02	10:26:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00