

XRT Timeline to be uploaded on 2015/06/02

Period: 2015/06/02 10:16:00 - 2015/06/10 09:34:00

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Normal mode

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XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
06/03 12:48:00 - 06/03 12:54:54	Fixed (-528.4, -528.4)	#Q1
PROG= 06 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 88 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
06/03 12:58:00 - 06/03 13:04:54	Fixed (528.4, -528.4)	#Q2
PROG= 05 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 31 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
06/03 13:08:00 - 06/03 13:14:54	Fixed (528.4, 528.4)	#Q3
PROG= 19 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 81 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 12 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
06/03 13:18:00 - 06/03 13:24:54	Fixed (-528.4, 528.4)	#Q4
PROG= 07 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 28 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec

	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
	Seqn= 23	2-time(s)	2.0sec										
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3	2-time(s)	2.0sec											
	Seqn= 12	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Seqn= 14	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19E2: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m)

Term	Pointing (x, y)	Comment
06/03 13:28:00 - 06/03 18:06:54	Track (113.9, -330.0) @ 06/03 13:25:00	#AR12356
06/03 18:20:00 - 06/03 22:01:30	Track (154.5, -330.4) @ 06/03 18:17:00	#AR12356
06/04 05:50:00 - 06/04 17:42:00	Track (248.7, -331.3) @ 06/04 05:47:00	#AR12356
06/05 06:10:00 - 06/05 08:21:00	Track (436.8, -333.3) @ 06/05 06:07:00	#AR12356
06/05 17:25:30 - 06/05 17:59:54	Track (500.2, -333.9) @ 06/05 15:00:00	#AR12356
06/06 06:35:00 - 06/06 08:57:30	Track (602.0, -334.9) @ 06/06 06:32:00	#AR12356
06/06 14:43:00 - 06/06 17:15:30	Track (650.2, -335.3) @ 06/06 14:40:00	#AR12356
06/06 18:13:00 - 06/06 22:10:30	Track (669.8, -335.4) @ 06/06 18:10:00	#AR12356
06/07 05:59:00 - 06/07 08:59:54	Track (729.9, -335.9) @ 06/07 05:56:00	#AR12356
06/07 15:23:00 - 06/07 17:36:24	Track (770.1, -336.0) @ 06/07 15:00:00	#AR12356
06/08 06:13:00 - 06/08 18:04:54	Fixed (882.0, -291.0)	#AR12356
06/08 18:18:00 - 06/09 06:31:24	Fixed (882.0, -291.0)	#AR12356
06/09 06:44:30 - 06/09 07:27:00	Fixed (882.0, -291.0)	#AR12356

PROG= 01 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec											
	Seqn= 8	2-time(s)	2.0sec										
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec											
	Seqn= 24	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Seqn= 42	4-time(s)	2.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Seqn= 62	20-time(s)	90.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi

Term	Pointing (x, y)	Comment
06/03 18:10:00 - 06/03 18:16:54	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min
06/04 05:40:00 - 06/04 05:46:54	Fixed (0.0, 0.0)	synoptic, shifted -23.0 min
06/04 18:29:00 - 06/04 18:35:54	Fixed (0.0, 0.0)	synoptic, shifted 26.0 min
06/05 06:00:00 - 06/05 06:06:54	Fixed (0.0, 0.0)	synoptic, shifted -3.0 min
06/06 06:25:00 - 06/06 06:31:54	Fixed (0.0, 0.0)	synoptic, shifted 22.0 min
06/06 18:03:00 - 06/06 18:09:54	Fixed (0.0, 0.0)	synoptic
06/07 05:49:00 - 06/07 05:55:54	Fixed (0.0, 0.0)	synoptic, shifted -14.0 min
06/07 17:39:30 - 06/07 17:46:24	Fixed (0.0, 0.0)	synoptic, shifted -23.5 min
06/08 06:03:00 - 06/08 06:09:54	Fixed (0.0, 0.0)	synoptic
06/08 18:08:00 - 06/08 18:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min
06/09 06:34:30 - 06/09 06:41:24	Fixed (0.0, 0.0)	synoptic, shifted 31.5 min

PROG= 03 1-time(s)

Subr= 1	1-time(s)	12.0sec											
	Seqn= 33	1-time(s)	4.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 5	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Seqn= 40	1-time(s)	4.0sec										
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 77		1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec		
Seqn= 6		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1A02: AR standard-B (thin-Be) with PFB (384FOV), 512x512 at 1064 1048, thick Al and Al/Poly context, With G-band (33ms/45ms leak), 5min cad

Term	Pointing (x, y)	Comment
06/03 22:41:30 - 06/04 05:36:54	Fixed (826.0, 363.0)	# prominence with IRIS
06/04 23:15:30 - 06/05 05:56:54	Fixed (786.0, 436.0)	# prominence with IRIS
06/05 23:48:00 - 06/06 06:21:54	Fixed (739.0, 508.0)	# prominence with IRIS
06/06 22:50:00 - 06/07 05:45:54	Fixed (815.0, -466.0)	# prominence with IRIS

PROG= 08 Inf.-time(s)

Subr= 1		1-time(s)		2.0sec											
Seqn= 20		2-time(s)		2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	512x512 (1064, 1048)		DPCM	0	0	2.0sec		
Seqn= 75		1-time(s)		2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)		Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)		Q=98	0	0	2.0sec		
Subr= 2		2-time(s)		2.0sec											
Seqn= 66		2-time(s)		2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec		
Seqn= 85		7-time(s)		300.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	17.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	17.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	17.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	17.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	17.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	17.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	17.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1A38: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
06/04 18:39:00 - 06/04 22:37:30	Track (350.1, -332.4) ^{© 06/04 18:36:00}	#AR12356
06/05 18:13:00 - 06/05 23:13:00	Track (521.9, -334.1) ^{© 06/05 18:10:00}	#AR12356
06/07 17:49:30 - 06/08 05:59:54	Track (781.2, -336.1) ^{© 06/07 17:46:30}	#AR12356

PROG= 04 Inf.-time(s)

Subr= 1		1-time(s)		2.0sec											
Seqn= 8		2-time(s)		2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec		
Subr= 2		2-time(s)		2.0sec											
Seqn= 24		1-time(s)		2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec		
Seqn= 42		4-time(s)		2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec		
Seqn= 62		60-time(s)		30.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1A1B: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 33ms

Term	Pointing (x, y)	Comment
06/05 09:03:00 - 06/05 14:59:54	Fixed (-13.0, -960.0)	#HOP81 S-pole
06/07 09:03:00 - 06/07 14:34:30	Fixed (-13.0, 860.0)	#HOP 81 N-pole

PROG= 11 Inf.-time(s)

Subr= 1		1-time(s)		2.0sec											
Seqn= 8		2-time(s)		2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 48		1-time(s)		30.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec		
Subr= 3		30-time(s)		2.0sec											
Seqn= 57		1-time(s)		60.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec		

XOB #1A6B: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/2048), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45

Term	Pointing (x, y)		Comment										
06/05 18:03:00 - 06/05 18:09:54	Fixed (0.0, 0.0)		synoptic										
PROG= 09 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
Seqn= 33 1-time(s) 4.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
Seqn= 40 1-time(s) 4.0sec													
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 12.0sec													
Seqn= 46 1-time(s) 2.0sec													
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25 1-time(s) 4.0sec													
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 21 1-time(s) 4.0sec													
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 37 1-time(s) 2.0sec													
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25 1-time(s) 4.0sec													
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 21 1-time(s) 4.0sec													
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 37 1-time(s) 2.0sec													
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 46 1-time(s) 2.0sec													
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec

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Flare mode

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XOB #1A77: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)		Comment									
06/03 13:28:00 - 06/03 18:06:54	Track (113.9, -330.0) @ 06/03 13:25:00		#AR12356									
06/03 18:20:00 - 06/03 22:01:30	Track (154.5, -330.4) @ 06/03 18:17:00		#AR12356									
06/03 22:41:30 - 06/04 05:36:54	Fixed (826.0, 363.0)		# prominence with IRIS									
06/04 05:50:00 - 06/04 17:42:00	Track (248.7, -331.3) @ 06/04 05:47:00		#AR12356									
06/04 18:39:00 - 06/04 22:37:30	Track (350.1, -332.4) @ 06/04 18:36:00		#AR12356									
06/04 23:15:30 - 06/05 05:56:54	Fixed (786.0, 436.0)		# prominence with IRIS									
06/05 06:10:00 - 06/05 08:21:00	Track (436.8, -333.3) @ 06/05 06:07:00		#AR12356									
06/05 09:03:00 - 06/05 14:59:54	Fixed (-13.0, -960.0)		#HOP81 S-pole									
06/05 17:25:30 - 06/05 17:59:54	Track (500.2, -333.9) @ 06/05 15:00:00		#AR12356									
06/05 18:13:00 - 06/05 23:13:00	Track (521.9, -334.1) @ 06/05 18:10:00		#AR12356									
06/05 23:48:00 - 06/06 06:21:54	Fixed (739.0, 508.0)		# prominence with IRIS									
06/06 06:35:00 - 06/06 08:57:30	Track (602.0, -334.9) @ 06/06 06:32:00		#AR12356									
06/06 14:43:00 - 06/06 17:15:30	Track (650.2, -335.3) @ 06/06 14:40:00		#AR12356									
06/06 18:13:00 - 06/06 22:10:30	Track (669.8, -335.4) @ 06/06 18:10:00		#AR12356									
06/06 22:50:00 - 06/07 05:45:54	Fixed (815.0, -466.0)		# prominence with IRIS									
06/07 05:59:00 - 06/07 08:59:54	Track (729.9, -335.9) @ 06/07 05:56:00		#AR12356									
06/07 09:03:00 - 06/07 14:34:30	Fixed (-13.0, 860.0)		#HOP 81 N-pole									
06/07 15:23:00 - 06/07 17:36:24	Track (770.1, -336.0) @ 06/07 15:00:00		#AR12356									
06/07 17:49:30 - 06/08 05:59:54	Track (781.2, -336.1) @ 06/07 17:46:30		#AR12356									
06/08 06:13:00 - 06/08 18:04:54	Fixed (882.0, -291.0)		#AR12356									
06/08 18:18:00 - 06/09 06:31:24	Fixed (882.0, -291.0)		#AR12356									

PROG= 02		30-time(s)													
Subr= 1		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn=100		1-time(s)		10.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec		
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11		1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 38		1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

* * * * *

Active Region Search

* * * * *

NOT USED

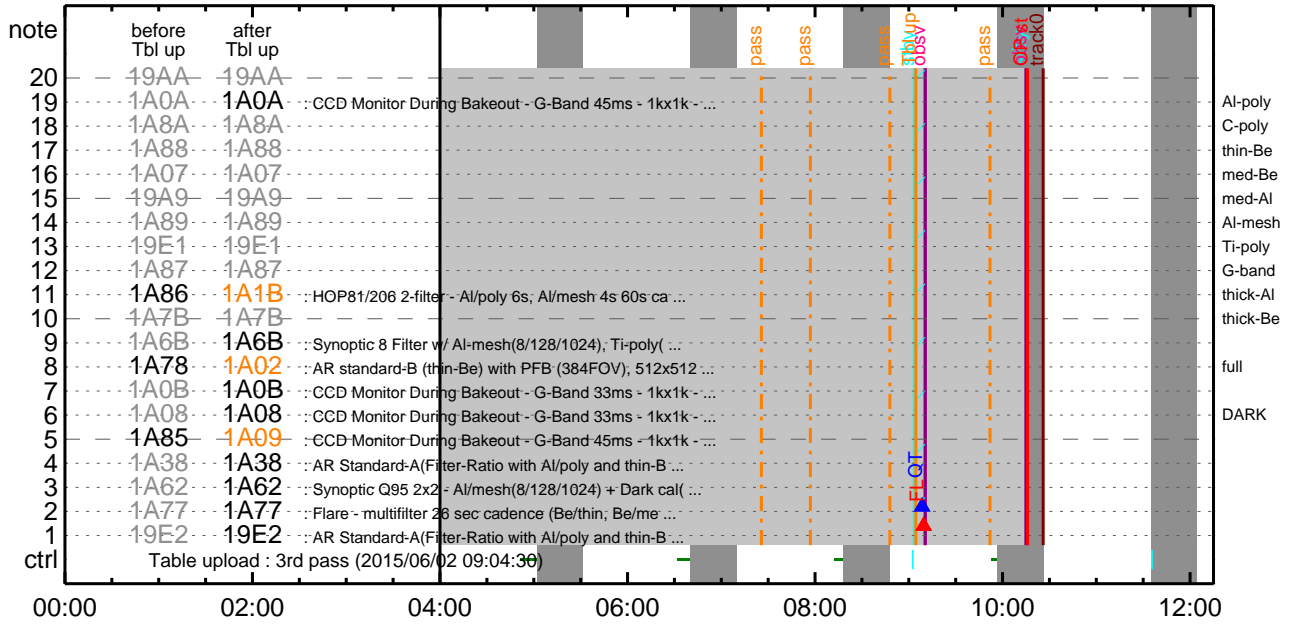
* * * * *

Flare Detection

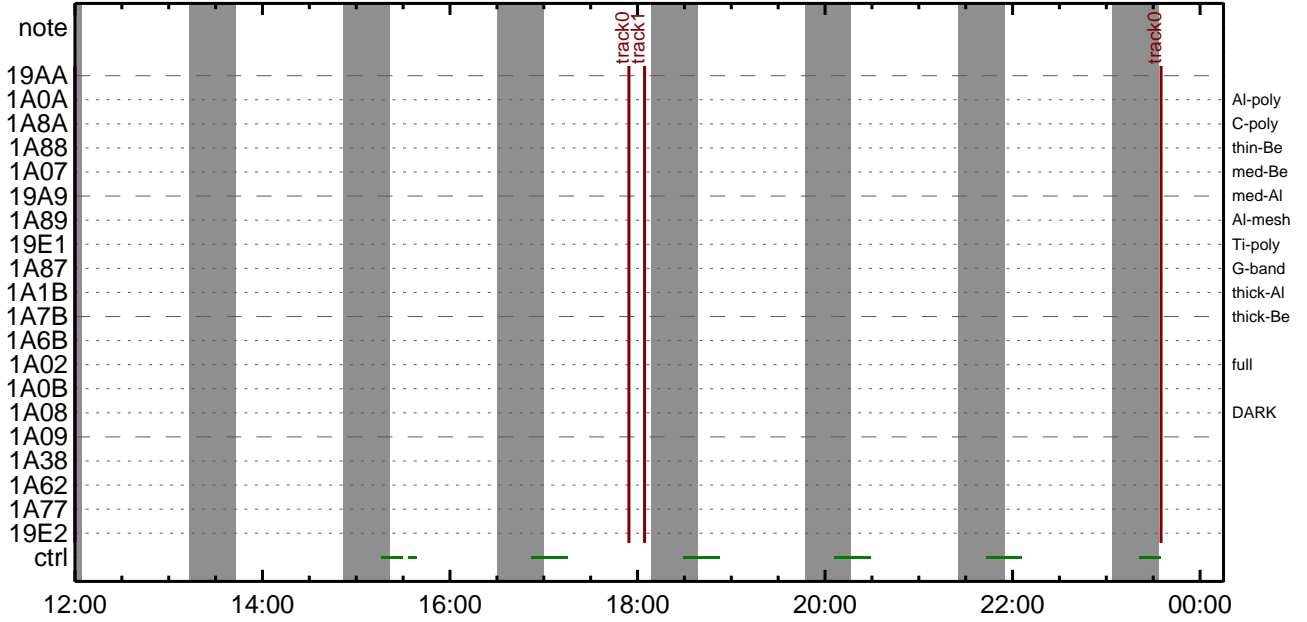
* * * * *

FLD Patrol												
Term	Pointing (x, y)					Comment						
06/03 13:25:18 - 06/03 18:07:18	Track (113.9,	-330.0)	@ 06/03 13:25:00	#AR12356							
06/03 18:17:18 - 06/04 05:37:18	Track (154.5,	-330.4)	@ 06/03 18:17:00	#AR12356							
06/04 05:47:18 - 06/04 18:26:18	Track (248.7,	-331.3)	@ 06/04 05:47:00	#AR12356							
06/04 18:36:18 - 06/05 05:57:18	Track (350.1,	-332.4)	@ 06/04 18:36:00	#AR12356							
06/05 06:07:18 - 06/05 18:00:18	Track (436.8,	-333.3)	@ 06/05 06:07:00	#AR12356							
06/05 18:10:18 - 06/06 06:22:18	Track (521.9,	-334.1)	@ 06/05 18:10:00	#AR12356							
06/06 06:32:18 - 06/06 18:00:18	Track (602.0,	-334.9)	@ 06/06 06:32:00	#AR12356							
06/06 18:10:18 - 06/07 05:46:18	Track (669.8,	-335.4)	@ 06/06 18:10:00	#AR12356							
06/07 05:56:18 - 06/07 17:36:48	Track (729.9,	-335.9)	@ 06/07 05:56:00	#AR12356							
06/07 17:46:48 - 06/08 06:00:18	Track (781.2,	-336.1)	@ 06/07 17:46:30	#AR12356							
06/08 06:10:18 - 06/08 18:05:18	Fixed (882.0,	-291.0)	#AR12356								
06/08 18:15:18 - 06/09 06:31:48	Fixed (882.0,	-291.0)	#AR12356								
06/09 06:41:48 - 06/10 09:34:00	Fixed (882.0,	-291.0)	#AR12356								
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

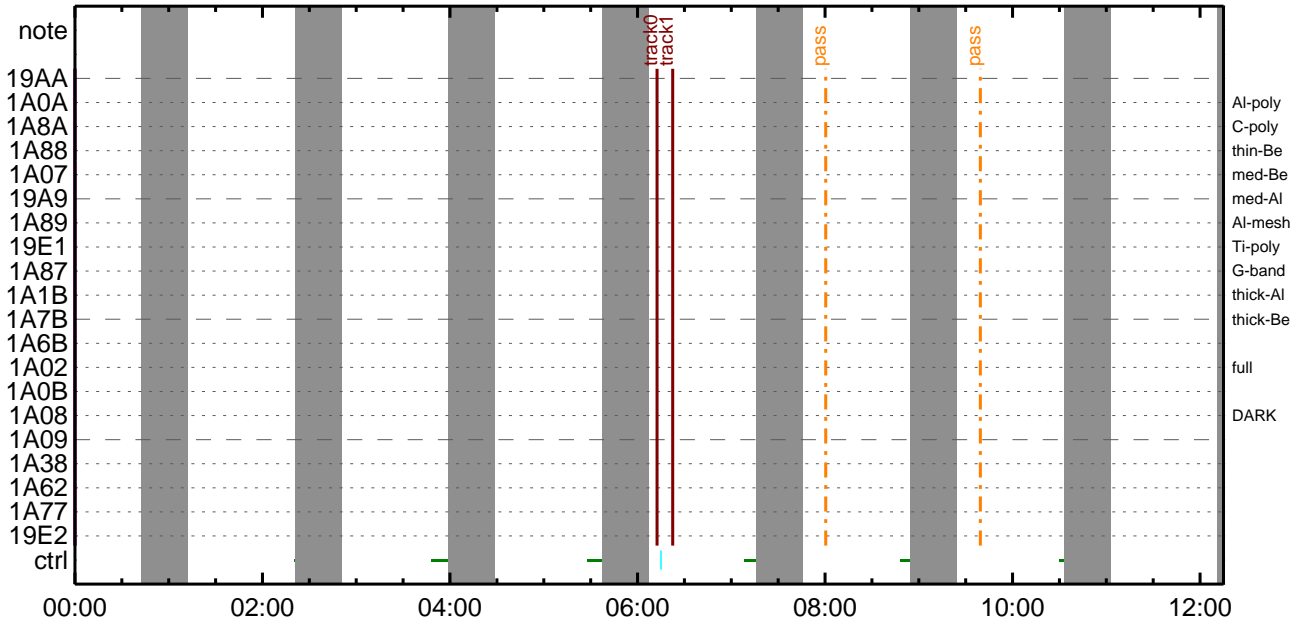
CMDI #0168 2015/06/02



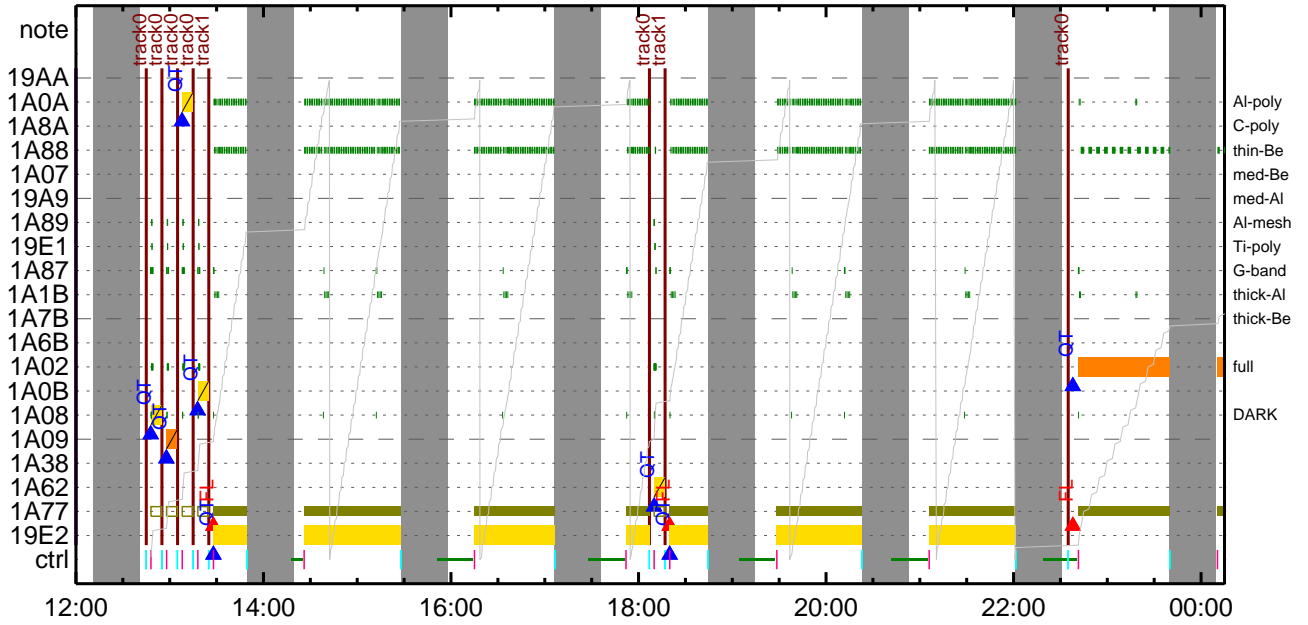
CMDI #0168 2015/06/02



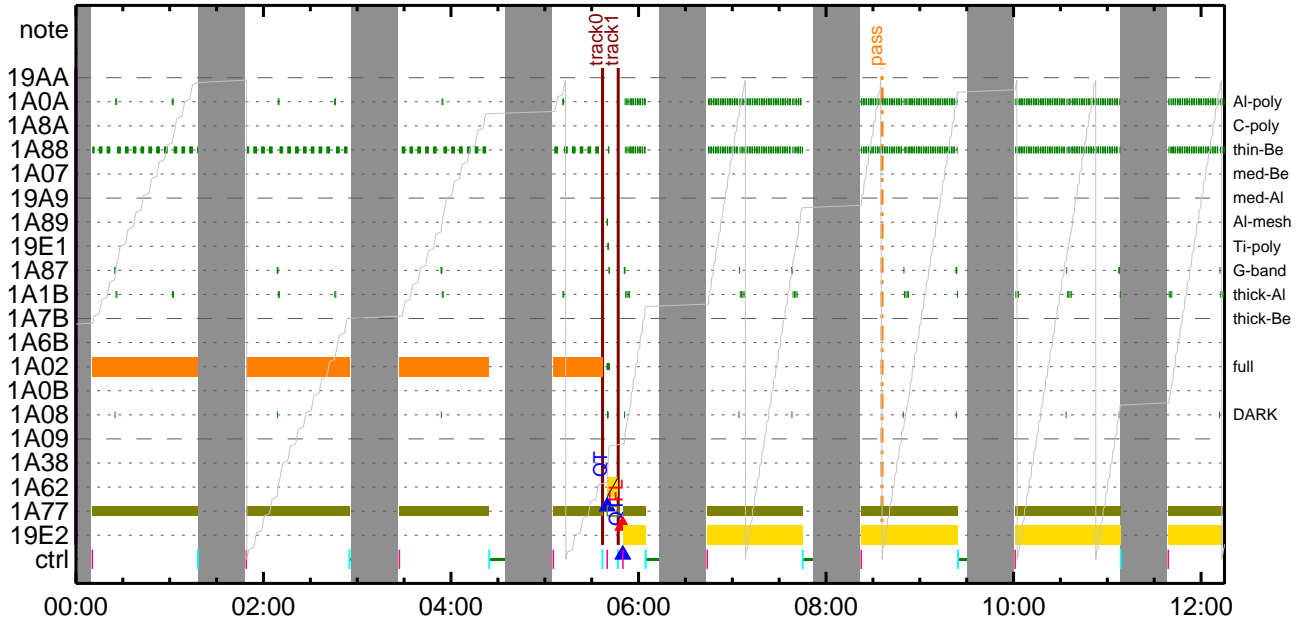
CMDI #0168 2015/06/03



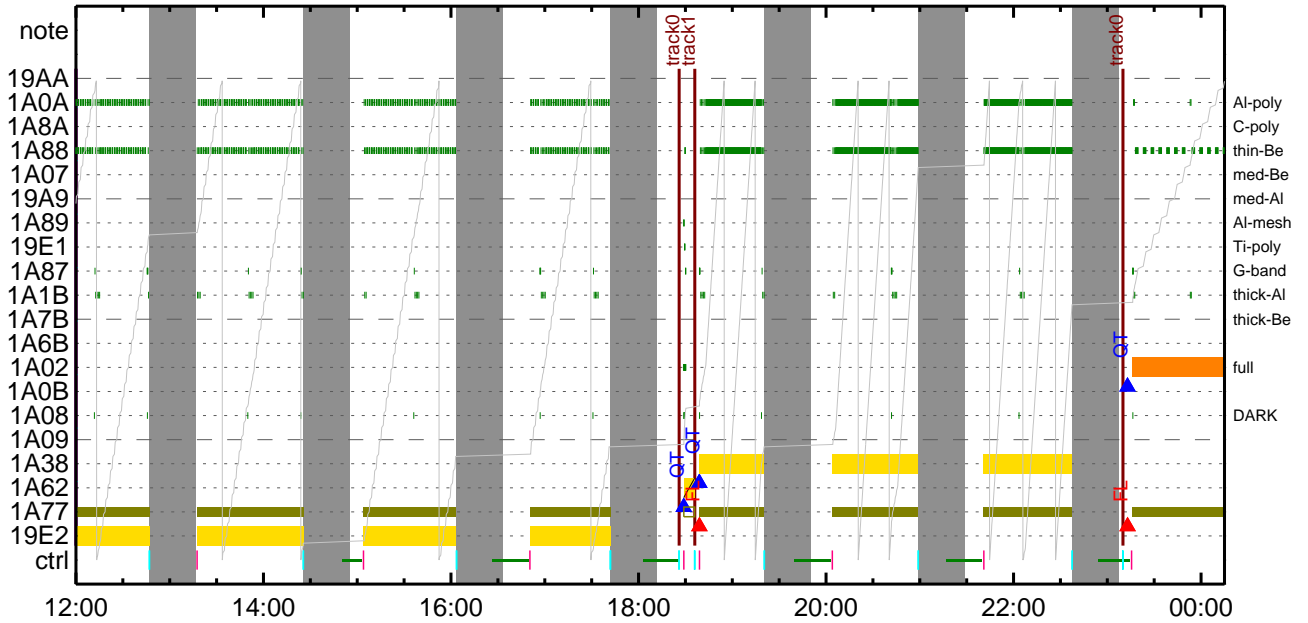
CMDI #0168 2015/06/03



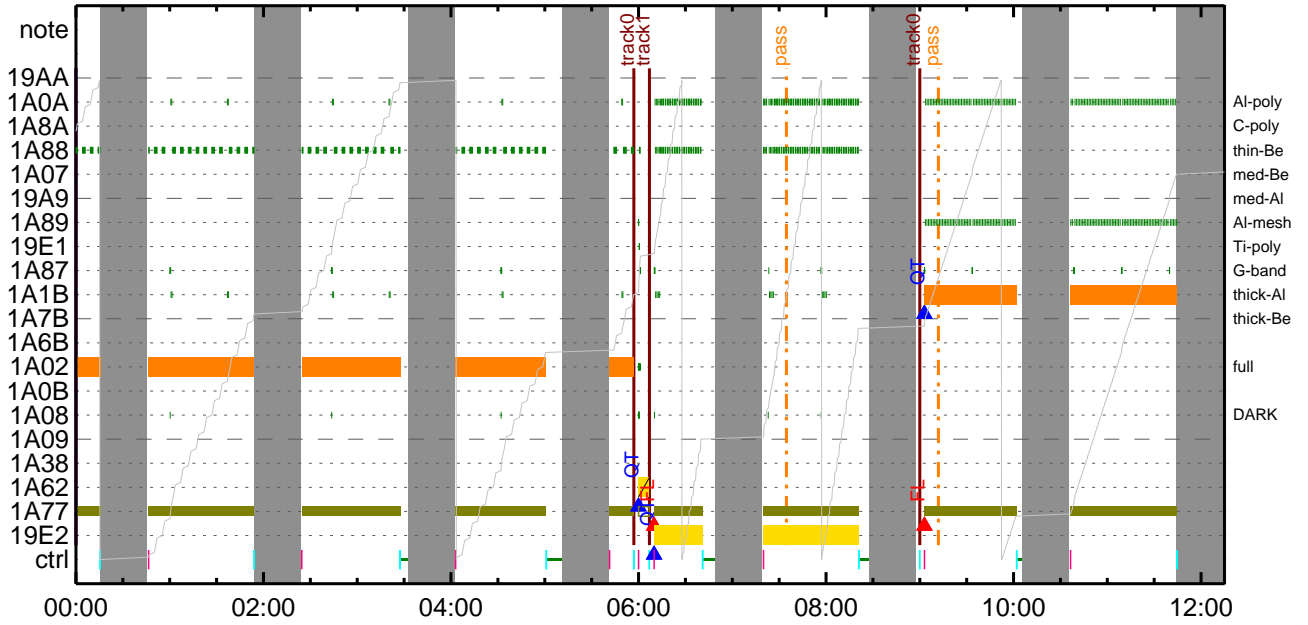
CMDI #0168 2015/06/04



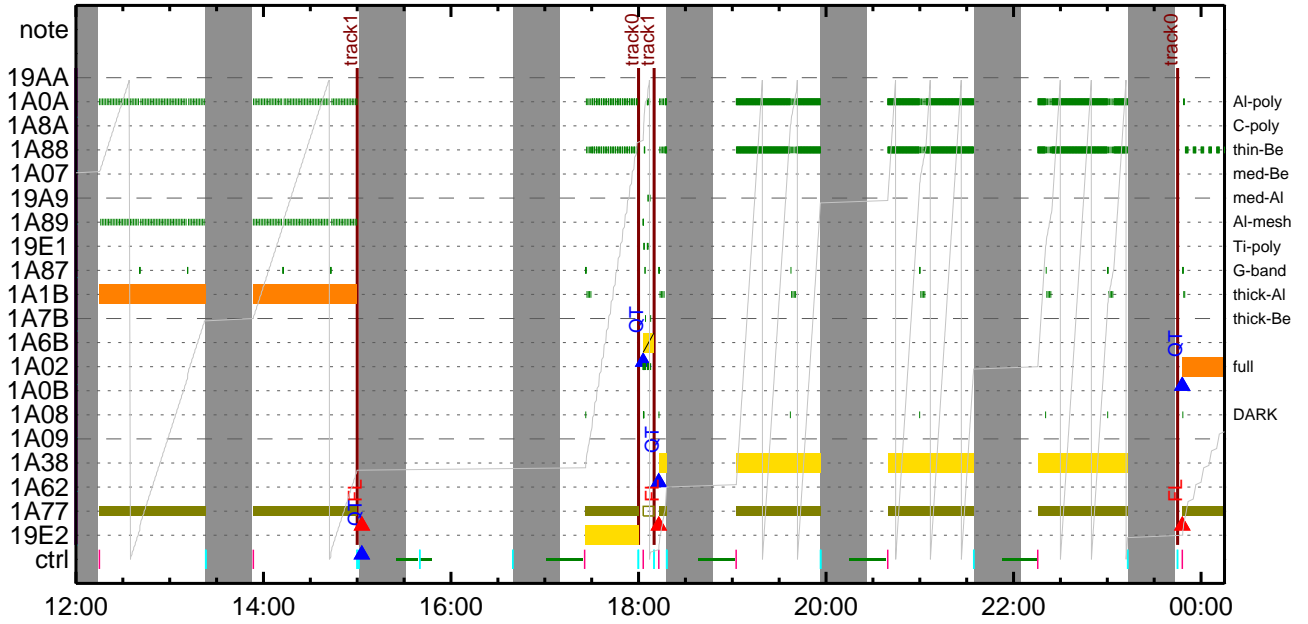
CMDI #0168 2015/06/04



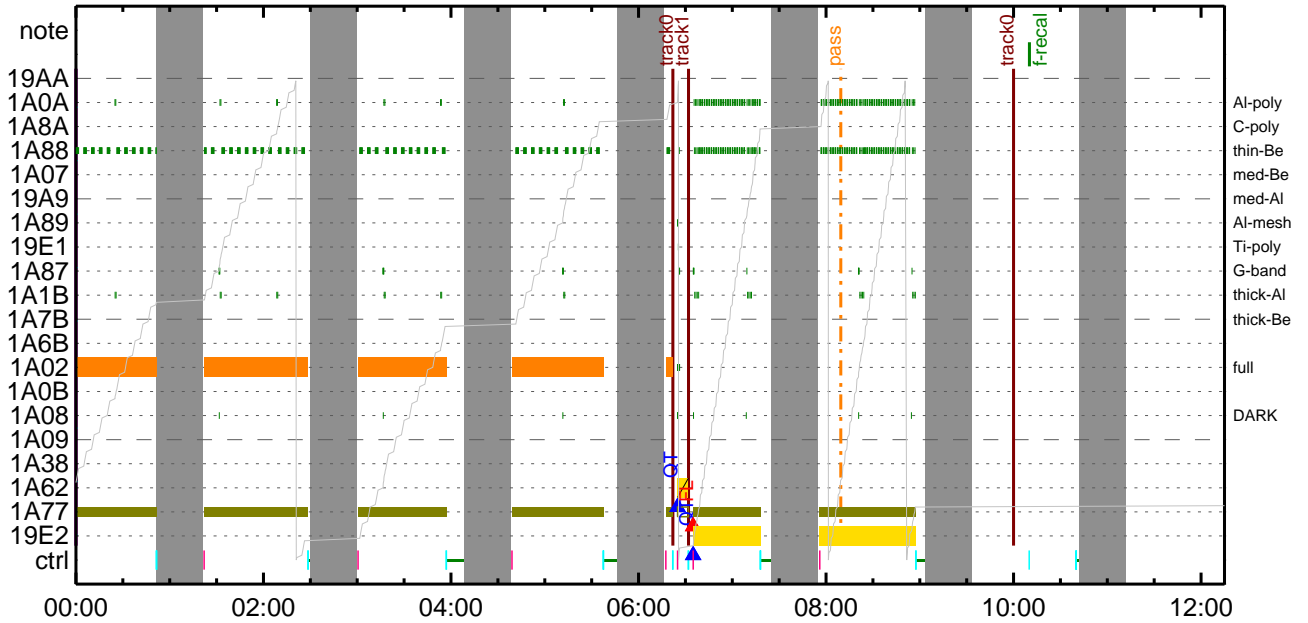
CMDI #0168 2015/06/05



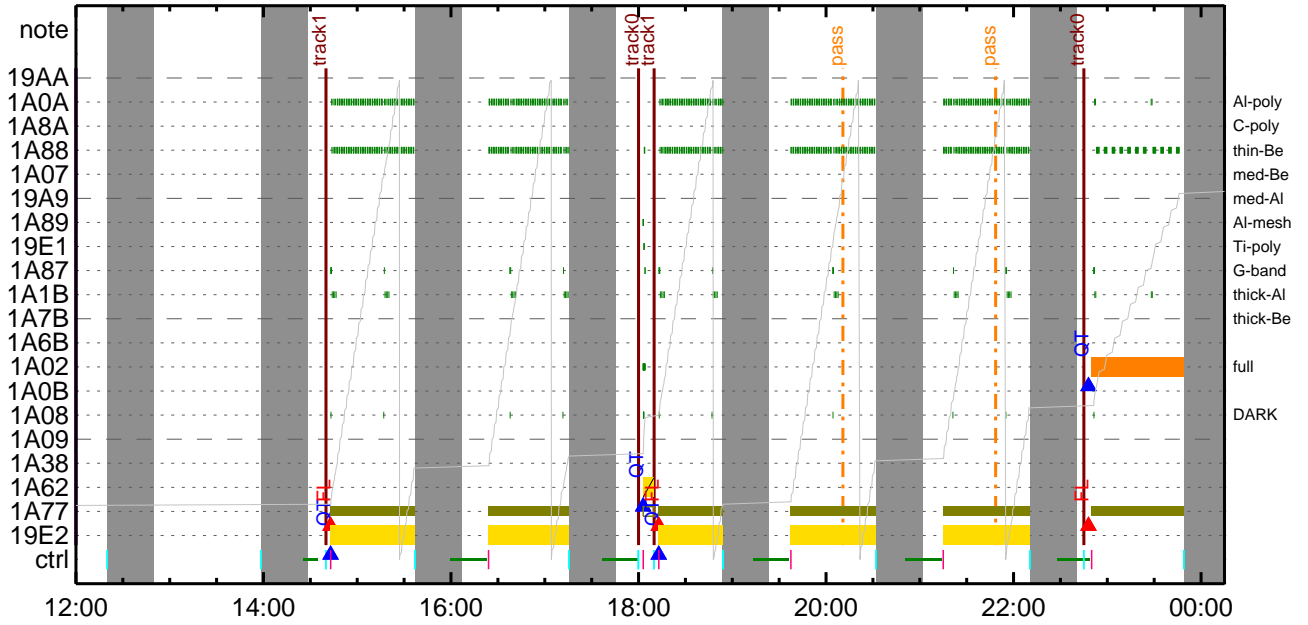
CMDI #0168 2015/06/05



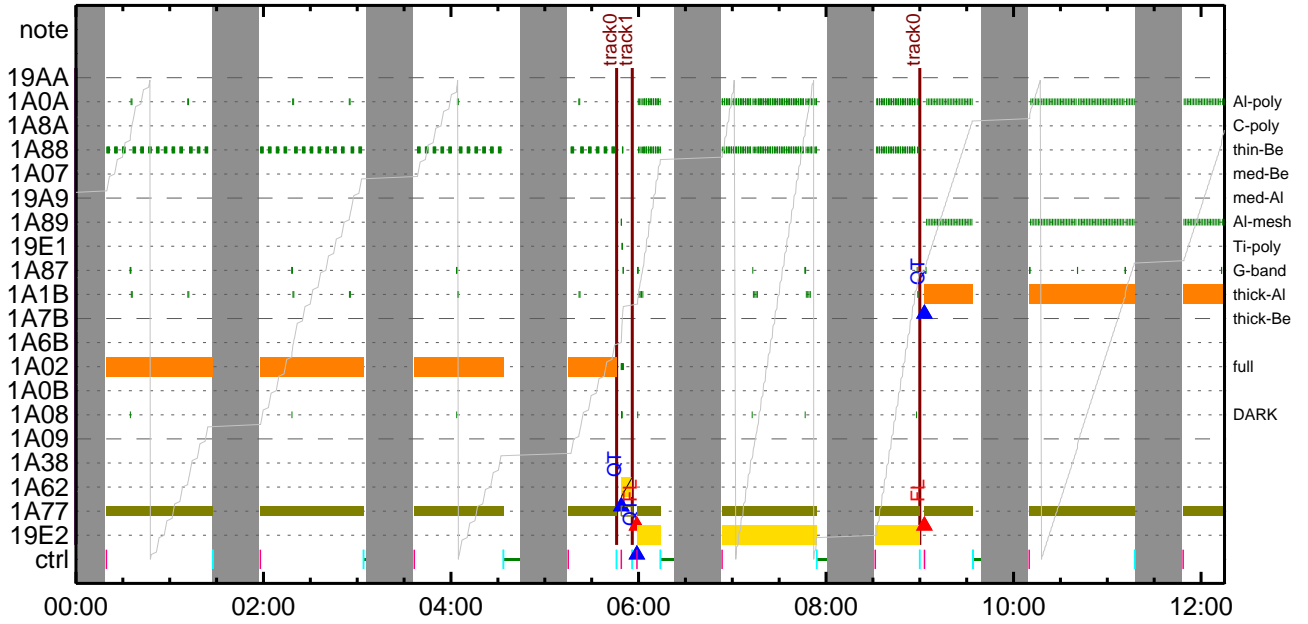
CMDI #0168 2015/06/06



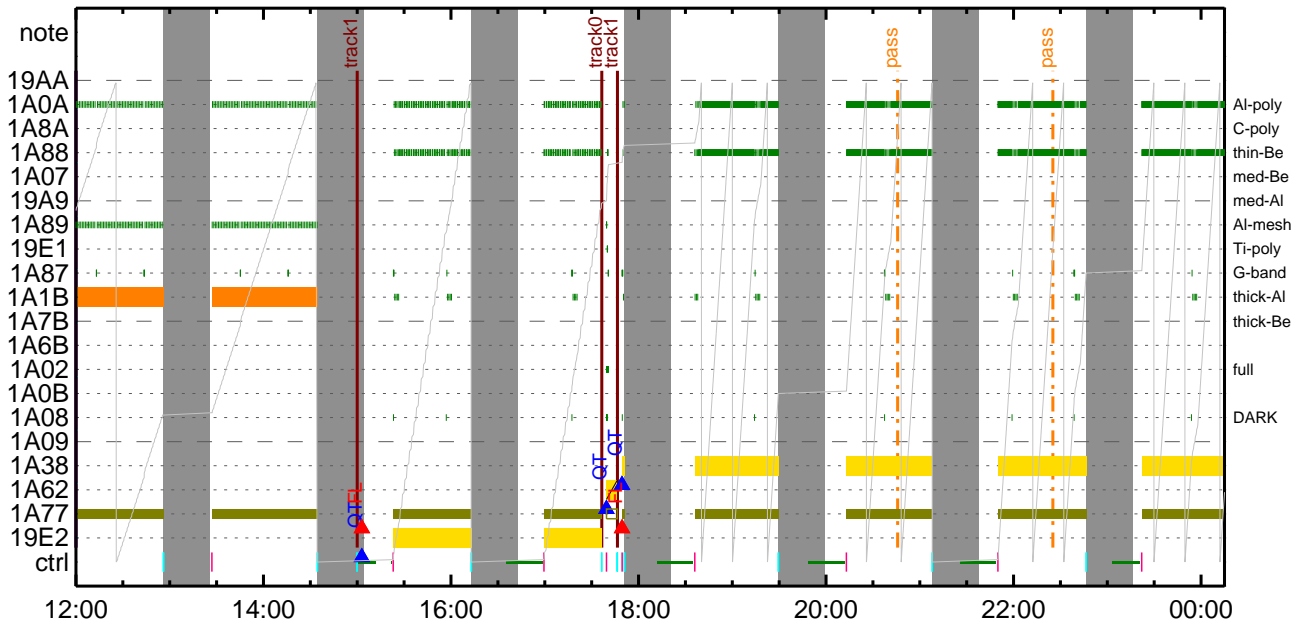
CMDI #0168 2015/06/06



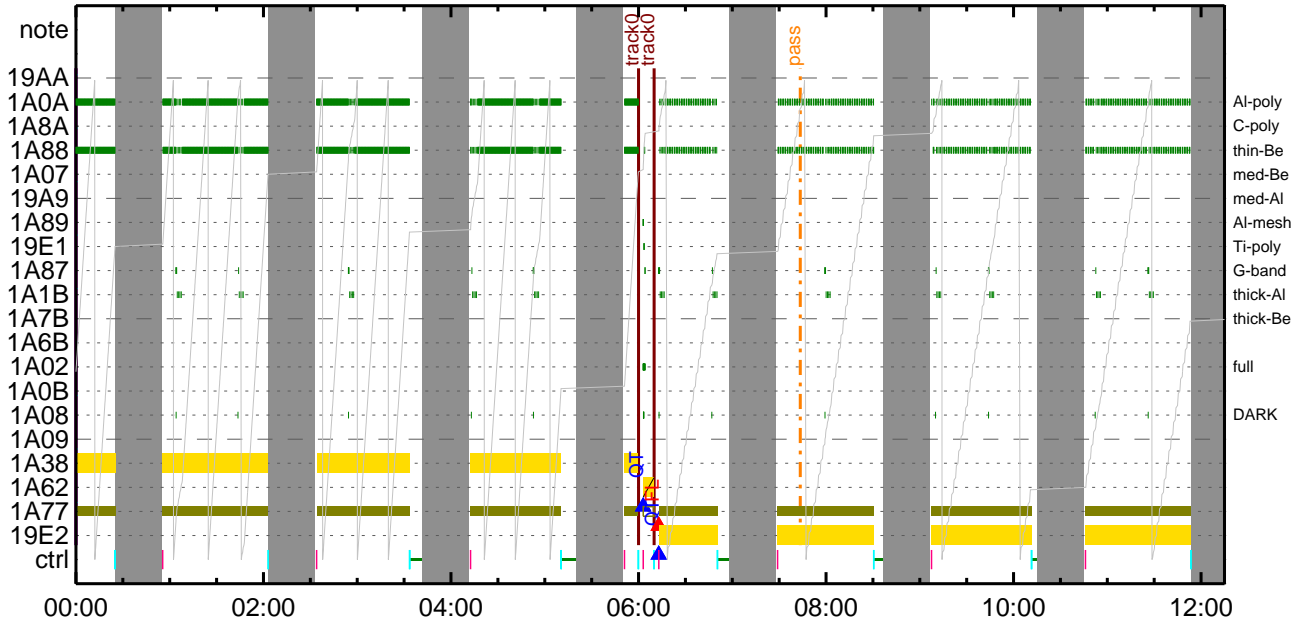
CMDI #0168 2015/06/07



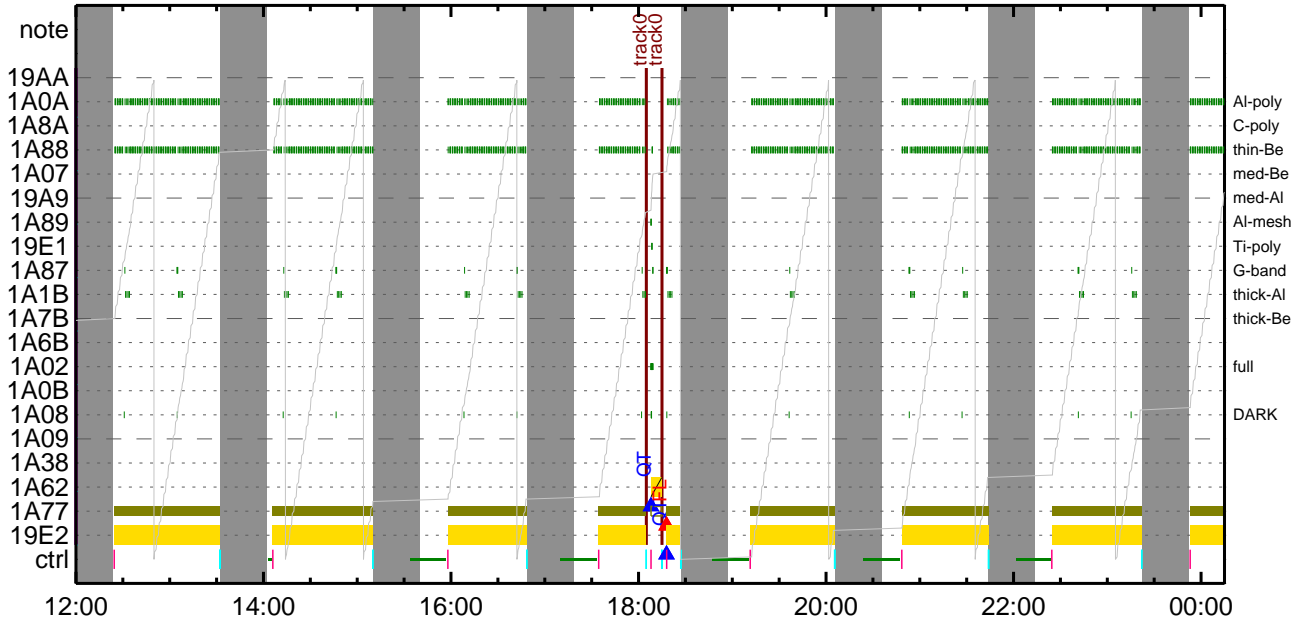
CMDI #0168 2015/06/07



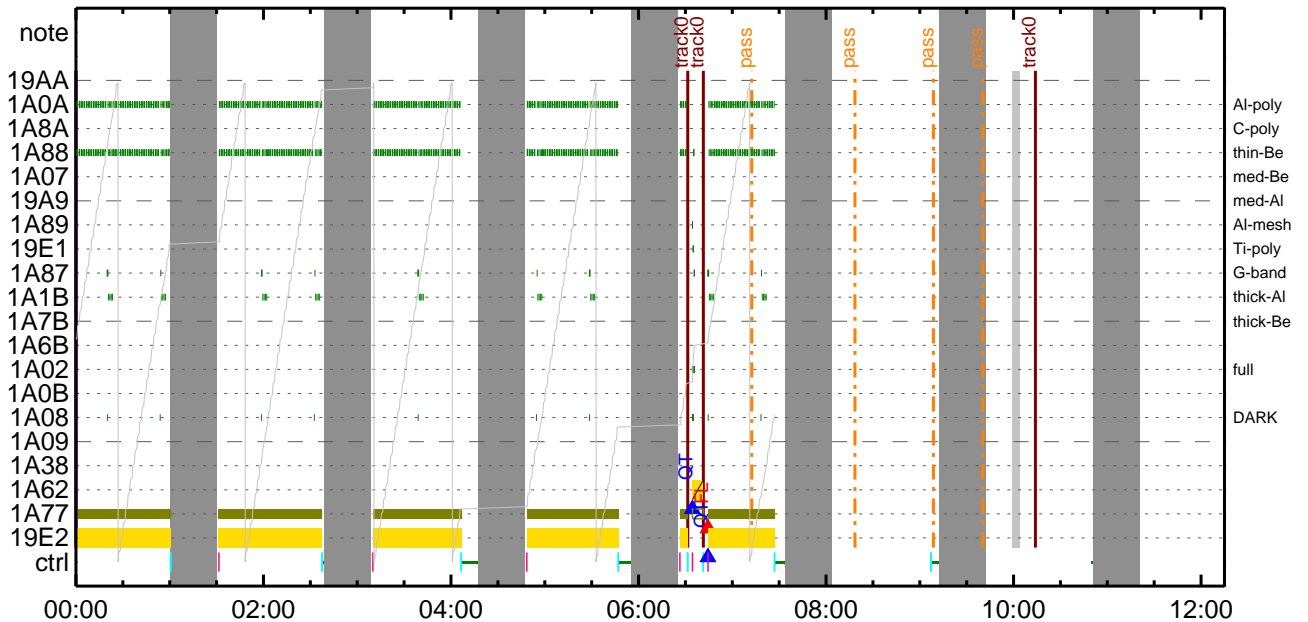
CMDI #0168 2015/06/08



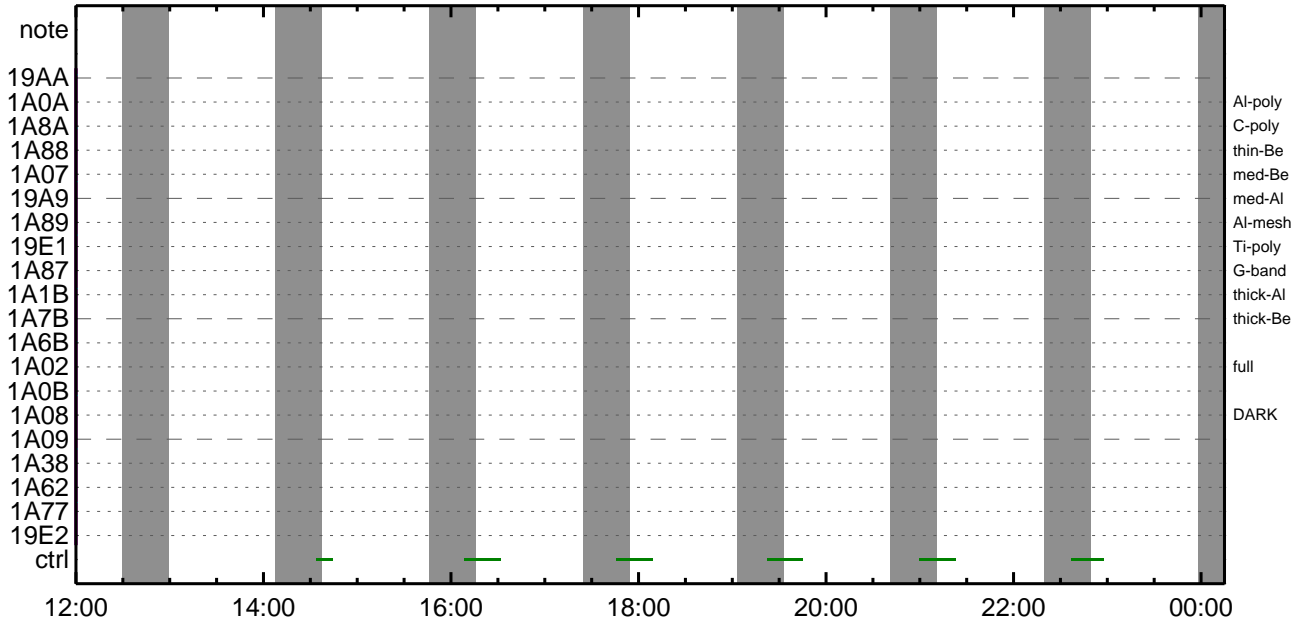
CMDI #0168 2015/06/08



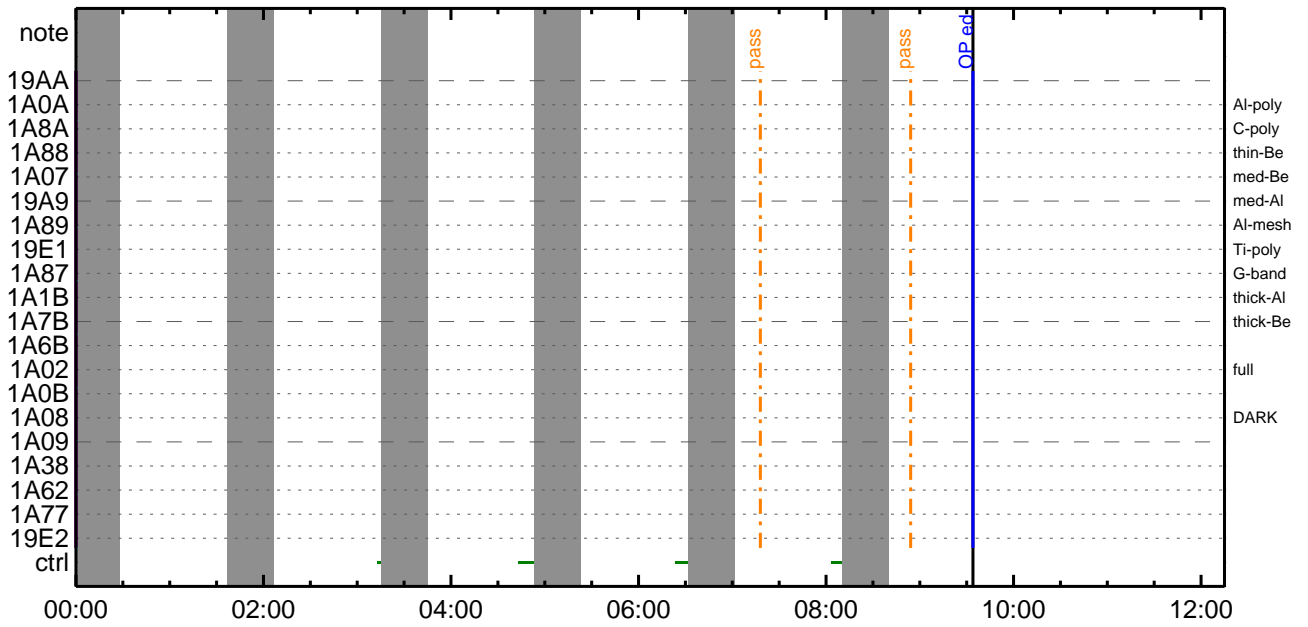
CMDI #0168 2015/06/09



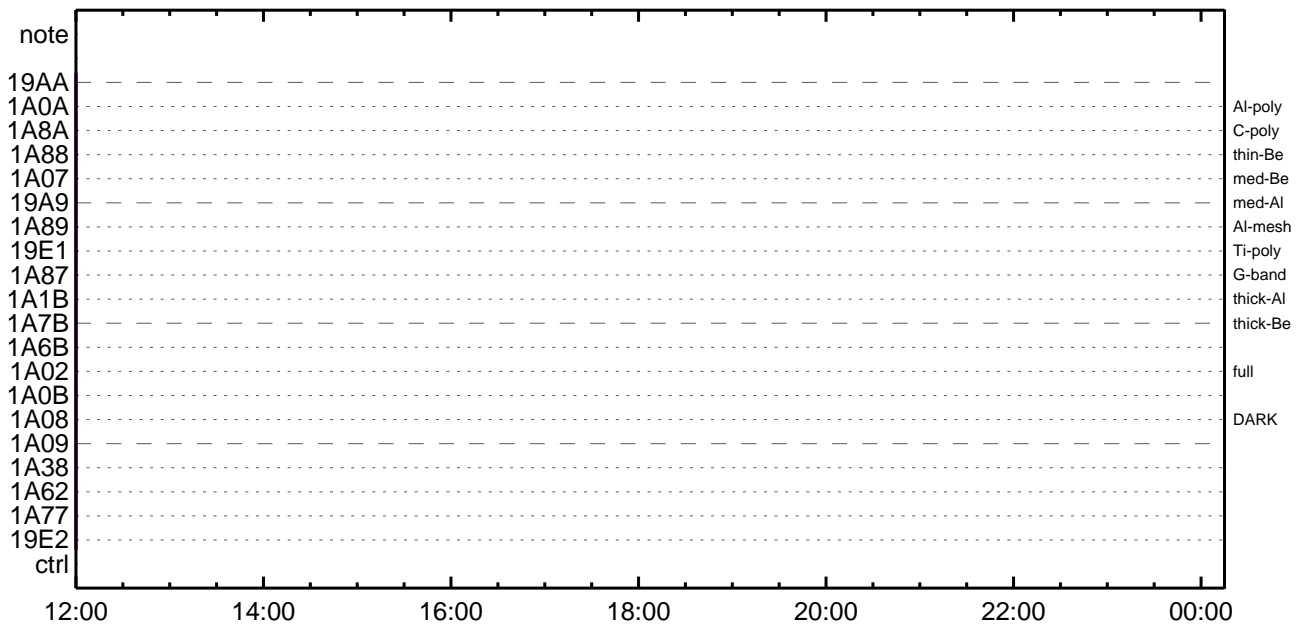
CMDI #0168 2015/06/09



CMDI #0168 2015/06/10



CMDI #0168 2015/06/10



0096 C. 0p0z;çSET0EDUMPAİÆ±°iYÑY¹aÇ¹Ôa|a³aE;f

0097 C.

0098 . C. TIY³YFÿÖYÉ0dADİç(UT)

0099 +. TI 2015-06-02 10:11:00.0

0100 DC 01-B3 DHU_OP_STOP

0101 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

0102 C.

0103 +. TI 2015-06-02 10:11:01.0

0104 DC 01-B4 DHU_OP_COPY

0105 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

0106 C.

0107 +. TI 2015-06-02 10:11:01.0

0108 DC 01-B5 DHU_OPOG_COPY

0109 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

0110 C.

0111 +. TI 2015-06-02 10:15:59.5

0112 DC 01-B2 DHU_OP_START

0113 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

0114 C.

0115 C. °E²¼aİÄê%îİÑaİYÁY§YÁY-¹àİÜ

0116 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA

0117 C. çç[HK1_TI_CMD_NUM] EQ 4

0118 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU

0119 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3

0120 C.

0121 . C. *****

0122 C. TIİİî°èYÀYÖY×

0123 C. *****

0124 C.

0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)

0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET

0127 BC (03 ab 03 01 02)

0128 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07

0129 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B

0130 C. çç[HK1_DMP_BLOCK_NUM] EQ 3

0131 C. çç[HK1_DMP_REPEAT_NUM] EQ 0

0132 C. çç[HK1_DMA_DMP_PIM] EQ DHU

0133 +. DC 01-22 DHU_MODE_CHNG

0134 BC (07 0b f8)

0135 C. çç[HK1_PKT_FORM_NO] EQ 7

0136 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s

0137 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k

0138 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M

0139 C. çç[HK1_DMP_CHK_FLG] EQ EXEC

0140 C.

0141 . C. YÀYÖY×½¹İ»0d³İÇ§

0142 C. çç[HK1_DMP_CHK_FLG] EQ NON

0143 C.

0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇ§

0145 C.

0146 . C. DHUYâ;¼YÉ;È¼Y½, Yİ;¼YÈ;È0dİá0¹

0147 +. DC 01-22 DHU_MODE_CHNG

0148 BC (02 0a f8)

0149 C. çç[HK1_PKT_FORM_NO] EQ 2

0150 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S

0151 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K

0152 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M

0153 C.

0154 C. *****

0155 C. SOT TI command set

0156 C. *****

0157 C. Execute, after the success of OP upload.

0158 +. TI 2015-06-02 10:15:16.0

0159 DC 07-F0 MDP_SOT_MODE_STBY

0160 BC (41)

0161 . C. -----

0162 C. HK1_TI_CMD_NUM = 1 CNTUP []

0163 C. -----

0164 C. ***** SOT END *****

0165 . C. Stop EIS observation and temporarily disable EIS mode changes

0166 C.

0167 C.

0168 C. ***** Start EIS operation (TI set) *****

0169 C. Execute, after the success of OP upload.

0170 C. Set EIS TI-commands

0171 +. TI 2015-06-02 10:15:30.0

0172 DC 07-FC EIS_MODE_MANU

0173 BC (21 02)

0174 +. TI 2015-06-02 10:15:40.0

0175 DC 07-FC EIS_MODE_CHG_DIS

0176 BC (22)

0177 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP

0178 C. ***** End EIS operation (TI set) *****

0179 C.

0180 C.

0181 C.

0182 C. ***** XRT START *****

0183 C. Execute, after the success of OP upload.

0184 +. TI 2015-06-02 10:15:00.0

0185 DC 07-F0 MDP_XRT_MODE_STBY

0186 BC (c3)

0187 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP

0188 C.

0189 C. ***** XRT END *****

0190 C.

0191 . C. ***** MDP `úÃİaİ»ö¼Y0ÈÄ0d¹0èDCBC•×²è *****

0192 C. (¼a°İYÖYÀYÉYÿYáYçYè0È¼a¼A»Ü0¹0é)

0193 . S. DC-BC dcbc-402:DCBC

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-175 2015-06-02 14:34:04 94 33 SOLAR-B MAIN //  
0001 . C.  
0002 . C. ***** AOS *****  
0003 . C.  
0004 . C. ;ãAOSYÁYŞYÄY~¼Ä»Û;ã  
0005 . C.  
0006 . C. YÄYB;¼Y³YBYÖYÉÁ+¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 . C.  
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****  
0010 . C. Áí;Ëó¿òÁó•µ°È»Í×ÁÇóÍYçYÄY×Yí;¼YÉ;ËÈ%µ•ííË;ËóÈ¼°ÇÔó•ó¿¼í¹çóÍ;çÄ®, ùó¹óÈóóóóçÁ+¿®ó•óÈóóóóóÈ;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 . C.  
0013 . C.  
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****  
0015 . C. Upload the Orbit Element and the Target Attitude  
0016 . C. RAM-ID:TARGET_ATT  
0017 . S. RAM ram-150:TARGET_ATT  
0018 . C.  
0019 . C.  
0020 . C.  
0021 . C. Set the dump memory area of TARGET_ATT  
0022 +. DC 02-48 AOCU_DUMP_SET  
0023 . BC (07 00 00 00 18 00)  
0024 . C.  
0025 . C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]  
0026 . C.  
0027 . C.  
0028 . C. Change the TLMFormatNo for the AOCs Dump Format  
0029 +. DC 01-22 DHU_MODE_CHNG  
0030 . BC (04 0b f8)  
0031 . C.  
0032 . C. Wait for AOCSDUMP to end  
0033 . C.  
0034 . C. Check the dump memory  
0035 . C.  
0036 . C. Result = OK [ ]  
0037 . C.  
0038 +. DC 01-22 DHU_MODE_CHNG  
0039 . BC (02 0a f8)  
0040 . C.  
0041 . C. <A_***>[TLM STS] FMT = 2 [ ]  
0042 . C.  
0043 +. DC 02-8E AOCU_ORB_UPD  
0044 . C.  
0045 . C. ***** AOCs Commands (Orbital Element Update) *****  
0046 . C. Update the orbital element  
0047 +. DC 02-50 AOCU_ORB_PRPGT_START  
0048 . BC (16)  
0049 +. DC 02-8E AOCU_ORB_UPD  
0050 . C.  
0051 . C. <A_ORB>[ORBIT] EPC = 5419153.1 +- 1.0 (s) [ ]  
0052 . C.  
0053 . C.  
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0055 +. DC 07-FC EIS_MODE_CHG_ENA  
0056 . BC (20)  
0057 . C. Verify EIS_MODE_CHG_FLG is ENA  
0058 +. DC 07-FC EIS_MODE_MANU  
0059 . BC (21 02)  
0060 . C. Verify EIS in MANUAL mode  
0061 . C. Estimated OBSTBL upload time is 58s  
0062 . C. *****  
0063 . C. EIS START OBSTBL LOAD  
0064 . C. *****  
0065 . S. RAM ram-820:EIS_OBSTBL  
0066 . C.  
0067 +. DC 07-FC EIS_DUMP_OBSTBL  
0068 . BC (07 07 07 00 00 70 00)  
0069 . C.  
0070 . C. Execute, after the success of OBSTBL upload.  
0071 . C. Set EIS TI-commands  
0072 +. TI 2015-06-02 10:15:50.0  
0073 . DC 07-FC EIS_MODE_CHG_ENA  
0074 . BC (20)  
0075 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0076 . C. *****  
0077 . C. EIS END OBSTBL LOAD  
0078 . C. *****  
0079 . C.  
0080 . C. ***** MDP ^ûÁíòí»ò¼YóÈÁðó¹óÈDCBC•×²è *****  
0081 . C. (¼á°íYÓYÄYËYþYËYáYçYèóÈ¼¾¼Ä»Ûó¹óÈ)  
0082 . S. DC-BC dcbc-402:DCBC  
0083 . (MDP_known_event)  
0084 . C.  
0085 . C.  
0086 . C. ***** YDÝ¹•í Daily±¿íÑóÈ`øó¹óÈDCBC•×²è *****  
0087 . S. DC-BC dcbc-153:DCBC  
0088 . (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0089 . C.  
0090 . C.  
0091 . C. ;ãLOSÝÁYŞYÄY~¼Ä»Û;ã  
0092 . C.  
0093 . C. ***** LOS *****  
0094 . C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-176 2015-06-02 14:34:04 264 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Ü;ã
0005 C.
0006 C. YÀYŞ;¼Y³YFÝÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Éç¿òÁò•µ°È»Í×ÁÇóÍYçYÁY×YÍ;¼YÉ;ÉÈè%µ•íÉ;ÈòÈ¼°ÇÓà•òç¼í¹çòí;çÀ®, ùò¹àèòÞàÇÁ+ç®•òÉòòò³òÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®µ;ON
0016 C. *****
0017 C. ç“ °ÆÀ, Í×ÈÝòáLOSòÞàÇóí»Þ´Ôòð¹íí, ò•; çÉÔÍ×òÈXÁÓONòí¹ÔòÉòíòÉòòò³òÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYÍYÁY~¼ÔÁÖò~òÁÂèò•òç; ç°È²¼òí°ÆÀ, ¼è%çòð¼Á¹Ôò¹òÉ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç“ RESTART; ÈPT1; Èò•òçò¼í¹çòí; ç°È²¼òí°ÆÀ¹Ôò»ò°; çDCBC-150òØçÈòà; f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãYçYóYÉYÉÁÚÁØ; ÈÁ•Á°²óÈò; È, àòí°ÆÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ç~¼«Æ°Áã»ßò•òç, á; ç°È²¼òð¼Á¹Ôò¹òÉ; f
0055 C. YçYóYÉYÉÁÚÁØòáÁ•Á°²óÈò~¼áòò¼í¹çòí´°í»ò¹òèòÞàÇÁÓòÁ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç“ RESTART; ÈPT2; Èò•òçò¼í¹çòí; ç°È²¼òí°ÆÀ¹Ôò»ò°; çDCBC-151òØçÈòà; f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãYçYóYÉYÉÁÚÁØ; ÈÁ•Á°²óÈò; È, àòí°ÆÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áã»ß; çXÁ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```



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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-263:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-281:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¥ç¥Ã¥×¥í;¼¥É°âÊSTS_CHKðOFFðËð¹ðë
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHKðONðËð¹ðë
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700[ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2015-06-02 10:15:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 . C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** XRT START *****
0174 C.
0175 +. DC 07-F0 MDP_XRT_CTRL_MANU
0176 BC (c1)
0177 + DC 07-F0 MDP_XRT_MODE_STBY
0178 BC (c3)
0179 . C. ----- Success Verify ? OK / NG____
0180 C.
0181 C. XRT Obs. Table Upload
0182 . S. RAM ram-291:MDP_OBS_X
0183 ( )
0184 C.
0185 +. DC 07-F0 MDP_DUMP_XRTTBL
0186 BC (84 07 00 00 00 3a d4)
0187 . C. ----- Comparison Check ? OK / ERR ____
0188 C.
0189 C.
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 01 b1 b1 04 04)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 02 b1 b1 08 08)

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```

0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 03 b1 b1 08 08)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 04 b1 b1 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 05 85 83 06 06)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 06 80 80 20 20)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 07 80 80 20 08)
0204 + DC 07-F0 MDP_XRT_ROI_SET
0205 BC (cd 08 80 80 08 20)
0206 + DC 07-F0 MDP_XRT_ROI_SET
0207 BC (cd 09 c0 c0 10 10)
0208 + DC 07-F0 MDP_XRT_ROI_SET
0209 BC (cd 0a 40 c0 10 10)
0210 + DC 07-F0 MDP_XRT_ROI_SET
0211 BC (cd 0b 40 40 10 10)
0212 + DC 07-F0 MDP_XRT_ROI_SET
0213 BC (cd 0c c0 40 10 10)
0214 + DC 07-F0 MDP_XRT_ROI_SET
0215 BC (cd 0d 85 83 06 06)
0216 + DC 07-F0 MDP_XRT_ROI_SET
0217 BC (cd 0e 85 83 08 08)
0218 + DC 07-F0 MDP_XRT_ROI_SET
0219 BC (cd 0f 80 80 06 06)
0220 + DC 07-F0 MDP_XRT_ROI_SET
0221 BC (cd 10 80 80 08 08)
0222 + DC 07-F0 MDP_XRT_FLD_ENA
0223 BC (d8)
0224 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0225 BC (c8)
0226 + DC 07-F0 MDP_XRT_AEC_RESET
0227 BC (d0)
0228 + DC 07-F0 MDP_XRT_ARS_DIS
0229 BC (d5)
0230 + DC 07-F0 MDP_XRT_FLD_RESET
0231 BC (da)
0232 + DC 07-F0 MDP_XRT_QT_PROG_SET
0233 BC (c4 03)
0234 + DC 07-F0 MDP_XRT_FL_PROG_SET
0235 BC (c5 02)
0236 . C. ----- Success Verify ? OK / NG ____
0237 C.
0238 C.
0239 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0240 C.
0241 + DC 07-F0 MDP_XRT_MODE_OBSV
0242 BC (c2)
0243 + TI 2015-06-02 10:15:02.0
0244 DC 07-F0 MDP_XRT_MODE_OBSV
0245 BC (c2)
0246 . C. ----- Success Verify ? OK / NG ____
0247 C.
0248 C. ***** XRT END *****
0249 C.
0250 . C. ***** MDP (ũãîñî»ö¼ÿñëÄðñ¹ñëDCBC•x²è *****
0251 C. (%ã°îÿÓÿÄÿËÿÏÿËÿÄÿÇÿËëñ¼¼¼¼»Ûñ¹ñë)
0252 . S. DC-BC dcbc-402:DCBC
0253 (MDP_known_event)
0254 C.
0255 C.
0256 . C. ***** ÿÐÿ¹•Ï Daily±¿Îñë´Øñ¹ñëDCBC•x²è *****
0257 . S. DC-BC dcbc-153:DCBC
0258 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0259 C.
0260 C.
0261 . C. ;ãLOSÿÄÿÏÿÄÿ¹¼Ä»Û;ã
0262 C.
0263 . C. ***** LOS *****
0264 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-177 2015-06-02 14:34:04 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µ°Æ»Í×ÁÇúÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÍÉ;ÈÈÈ¼°ÇÔα•α¿¼i¹çαÍ;çÀ®, ùα¹αèαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP `úÁÍσÍ»ò¼YαÈÁĐα¹αèDCBC•x²è *****
0016 C. (¼á°íYÓYÁYÈYB¥ÈYáYçYèαÈ¼αα¼Á»Ûα¹αè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YD¥¹•İ Daily+¿ÍÑαÈ´Øα¹αèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

*** OP Sequence for XRT ***

```

2015/06/02 10:26:00.0 AOCs_Ore-point_Start_1_OG [0x097]
                    AOCU_NM 5 02-76 00 ed 0f b1 9a
2015/06/02 11:35:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/02 11:35:32.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/02 11:35:34.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET 1 07-F0 da
2015/06/02 11:35:36.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                    MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/06/02 11:38:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/06/02 17:54:30.0 AOCs_Ore-point_Start_2_OG [0x098]
                    AOCU_NM 5 02-76 00 00 00 00 00
2015/06/02 18:04:30.0 AOCs_Ore-point_Start_3_OG [0x099]
                    AOCU_NM 5 02-76 01 00 00 00 00
2015/06/02 23:35:00.0 AOCs_Ore-point_Start_4_OG [0x09a]
                    AOCU_NM 5 02-76 00 ea f2 b2 36
2015/06/03 06:12:30.0 AOCs_Ore-point_Start_2_OG [0x098]
                    AOCU_NM 5 02-76 00 00 00 00 00
2015/06/03 06:15:00.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/03 06:15:02.0 XRT_TCIB_XRT_S_HTR_A_DIS_418_OG [0x1a2]
                    TCIB_XRT_S_HTR_A_DIS 0 04-C0
2015/06/03 06:22:30.0 AOCs_Ore-point_Start_3_OG [0x099]
                    AOCU_NM 5 02-76 01 00 00 00 00
2015/06/03 12:44:54.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/03 12:44:56.0 XRT_CTRL_MANU_444_OG [0x1bc]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/03 12:45:00.0 AOCs_Ore-point_Start_5_OG [0x09b]
                    AOCU_NM 5 02-76 00 2e f9 2e f9
2015/06/03 12:47:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                    XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2015/06/03 12:47:52.0 XRT_QT_PROG_SET_442_OG [0x1ba]
                    MDP_XRT_QT_PROG_SET 2 07-F0 c4 06
2015/06/03 12:47:54.0 XRT_FLD_DIS_443_OG [0x1bb]
                    MDP_XRT_FLD_DIS 1 07-F0 d9
2015/06/03 12:47:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                    MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2015/06/03 12:47:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                    MDP_XRT_ARS_DIS 1 07-F0 d5
2015/06/03 12:48:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                    MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/06/03 12:54:54.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/03 12:54:56.0 XRT_CTRL_MANU_444_OG [0x1bc]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/03 12:55:00.0 AOCs_Ore-point_Start_6_OG [0x09c]
                    AOCU_NM 5 02-76 00 2e f9 d1 07
2015/06/03 12:57:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                    XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2015/06/03 12:57:52.0 XRT_QT_PROG_SET_406_OG [0x196]
                    MDP_XRT_QT_PROG_SET 2 07-F0 c4 05
2015/06/03 12:57:54.0 XRT_FLD_DIS_443_OG [0x1bb]
                    MDP_XRT_FLD_DIS 1 07-F0 d9
2015/06/03 12:57:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                    MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2015/06/03 12:57:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                    MDP_XRT_ARS_DIS 1 07-F0 d5
2015/06/03 12:58:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                    MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/06/03 13:04:54.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/03 13:04:56.0 XRT_CTRL_MANU_444_OG [0x1bc]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/03 13:05:00.0 AOCs_Ore-point_Start_7_OG [0x09d]
                    AOCU_NM 5 02-76 00 d1 07 d1 07
2015/06/03 13:07:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                    XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2015/06/03 13:07:52.0 XRT_QT_PROG_SET_437_OG [0x1b5]
                    MDP_XRT_QT_PROG_SET 2 07-F0 c4 13
2015/06/03 13:07:54.0 XRT_FLD_DIS_443_OG [0x1bb]
                    MDP_XRT_FLD_DIS 1 07-F0 d9
2015/06/03 13:07:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                    MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2015/06/03 13:07:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                    MDP_XRT_ARS_DIS 1 07-F0 d5
2015/06/03 13:08:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                    MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/06/03 13:14:54.0 XRT_CTRL_MANU_402_OG [0x192]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/03 13:14:56.0 XRT_CTRL_MANU_444_OG [0x1bc]
                    MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/06/03 13:15:00.0 AOCs_Ore-point_Start_8_OG [0x09e]
                    AOCU_NM 5 02-76 00 d1 07 2e f9
2015/06/03 13:17:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                    XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2015/06/03 13:17:52.0 XRT_QT_PROG_SET_429_OG [0x1ad]
                    MDP_XRT_QT_PROG_SET 2 07-F0 c4 07
2015/06/03 13:17:54.0 XRT_FLD_DIS_443_OG [0x1bb]

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2015/06/03	13:17:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/06/03	13:17:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/06/03	13:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/03	13:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/03	13:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	13:24:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	13:25:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/06/03	13:25:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/06/03	13:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/06/03	13:25:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/06/03	13:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/06/03	13:25:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/03	13:27:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/03	13:27:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2015/06/03	13:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/06/03	13:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/03	13:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	13:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	13:49:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/03	13:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/03	14:25:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/03	14:26:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/06/03	15:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/03	15:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	15:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	15:28:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/03	15:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/03	16:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/03	16:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/06/03	17:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/03	17:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	17:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	17:06:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/03	17:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/03	17:51:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/03	17:52:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/06/03	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/03	18:06:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	18:06:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	18:07:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/06/03	18:07:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/06/03	18:09:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/06/03	18:09:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/06/03	18:09:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/03	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/06/03	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/03	18:16:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/03	18:16:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2015/06/03	18:17:00.0	AOCS_Or-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	01	00	00	00
2015/06/03	18:17:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/06/03	18:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/06/03	18:17:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/06/03	18:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/06/03	18:17:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/03	18:19:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2015/06/03	18:19:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2015/06/03	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/03	18:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	18:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	18:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/03	18:44:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/03	18:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/03	19:27:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/03	19:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/03	20:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	20:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	20:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/03	20:23:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/03	20:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/03	21:05:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/03	21:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/03	22:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	22:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	22:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/03	22:01:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/03	22:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/03	22:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	22:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	22:34:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/06/03	22:35:00.0	AOCS_Or-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	df	bf	b6
2015/06/03	22:35:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/06/03	22:35:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/06/03	22:35:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/06/03	22:35:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/06/03	22:35:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/03	22:37:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2015/06/03	22:37:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2015/06/03	22:40:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/03	22:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/03	23:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	23:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/03	23:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/03	23:40:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/03	23:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/04	00:09:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/04	00:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2015/06/04	01:18:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	01:18:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	01:18:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/06/04	01:18:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/06/04	01:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/06/04	01:48:00.0	XRT_Custom_430_OG [0x1ae]								
2015/06/04	01:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/06/04	02:55:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	02:55:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	02:55:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/06/04	02:55:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/06/04	02:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/06/04	03:26:00.0	XRT_Custom_430_OG [0x1ae]								
2015/06/04	03:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/06/04	04:24:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	04:24:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	04:24:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/06/04	04:24:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/06/04	04:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/06/04	05:04:30.0	XRT_Custom_430_OG [0x1ae]								
2015/06/04	05:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/06/04	05:36:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	05:36:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	05:36:58.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2015/06/04	05:37:00.0	AOCS_ORe-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2015/06/04	05:37:18.0	XRT_FLD_DIS_422_OG [0x1a6]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2015/06/04	05:39:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2015/06/04	05:39:56.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/06/04	05:39:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03					
2015/06/04	05:40:00.5	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/06/04	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	05:46:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	05:46:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2015/06/04	05:47:00.0	AOCS_ORe-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	01 00 00 00 00					
2015/06/04	05:47:18.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2015/06/04	05:47:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2015/06/04	05:47:22.0	XRT_AEC_RESET_441_OG [0x1b9]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2015/06/04	05:47:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/06/04	05:47:26.0	XRT_FLD_RESET_449_OG [0x1c1]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/06/04	05:49:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01					
2015/06/04	05:49:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02					
2015/06/04	05:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/06/04	06:04:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	06:04:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/06/04	06:04:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/06/04	06:04:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/06/04	06:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/06/04	06:43:00.0	XRT_Custom_430_OG [0x1ae]								
2015/06/04	06:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								

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2015/06/04	07:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	07:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	07:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/04	07:45:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/04	07:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/04	08:21:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/04	08:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/04	09:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	09:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	09:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/04	09:24:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/04	09:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/04	10:00:00.0	XRT_Custom_430_OG [0x1ae]				
2015/06/04	10:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/04	11:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	11:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	11:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/04	11:08:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/04	11:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/04	11:38:00.0	XRT_Custom_430_OG [0x1ae]				
2015/06/04	11:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/04	12:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	12:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	12:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/04	12:47:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/04	12:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/04	13:16:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/04	13:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/04	14:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	14:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	14:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/04	14:25:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/04	14:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/04	15:03:00.0	XRT_Custom_430_OG [0x1ae]				
2015/06/04	15:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/04	16:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	16:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	16:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/04	16:03:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/04	16:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/04	16:49:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/04	16:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/04	17:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	17:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	17:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/04	17:42:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/04	17:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/04	18:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/04	18:25:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2015/06/04	18:25:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/06/04	18:26:00.0	AOCs_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/06/04	18:26:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/04	18:28:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/04	18:28:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/04	18:28:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2015/06/04	18:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/04	18:35:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	18:35:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	18:35:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/06/04	18:36:00.0	AOCs_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	00	00	00	00
2015/06/04	18:36:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/04	18:36:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/04	18:36:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/04	18:36:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/04	18:36:26.0	XRT_FLD_RESET_449_OG [0x1c1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/04	18:38:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2015/06/04	18:38:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/06/04	18:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/04	19:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	19:20:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	19:20:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/04	19:20:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/04	19:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/04	20:03:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/04	20:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/04	20:59:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	20:59:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	20:59:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/04	20:59:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/04	21:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/04	21:40:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/04	21:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/04	22:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	22:37:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	22:37:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/04	22:37:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/04	22:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/04	23:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	23:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/04	23:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/06/04	23:10:00.0	AOCs_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d9	40	ba	25
2015/06/04	23:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/04	23:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/04	23:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/04	23:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/04	23:10:26.0	XRT_FLD_RESET_449_OG [0x1c1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/04	23:12:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							

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2015/06/04	23:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2015/06/04	23:14:30.5	XRT_Custom_430_OG [0x1ae]					
2015/06/04	23:15:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/05	00:15:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	00:15:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	00:15:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/05	00:15:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/05	00:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/05	00:45:30.0	XRT_Custom_430_OG [0x1ae]					
2015/06/05	00:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/05	01:54:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	01:54:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	01:54:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/05	01:54:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/05	01:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/05	02:23:30.0	XRT_Custom_430_OG [0x1ae]					
2015/06/05	02:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/05	03:27:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	03:27:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	03:27:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/05	03:27:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/05	03:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/05	04:02:00.0	XRT_Custom_430_OG [0x1ae]					
2015/06/05	04:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/05	05:01:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	05:01:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	05:01:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/05	05:01:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/05	05:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/05	05:40:30.0	XRT_Custom_430_OG [0x1ae]					
2015/06/05	05:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/05	05:56:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	05:56:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	05:56:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/06/05	05:57:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2015/06/05	05:57:18.0	XRT_FLD_DIS_422_OG [0x1a6]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/06/05	05:59:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/06/05	05:59:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/05	05:59:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/06/05	06:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/05	06:06:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	06:06:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/05	06:06:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/06/05	06:07:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2015/06/05	06:07:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/06/05	06:07:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/06/05	06:07:22.0	XRT_AEC_RESET_441_OG [0x1b9]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/06/05	06:07:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/05	06:07:26.0	XRT_FLD_RESET_449_OG [0x1c1]					

2015/06/05	06:09:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/05	06:09:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2015/06/05	06:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2015/06/05	06:41:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/05	06:41:02.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	06:41:04.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	06:41:06.5	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/05	06:44:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/05	07:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/05	07:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2015/06/05	08:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/06/05	08:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/05	08:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	08:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	08:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/05	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/05	08:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/05	08:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	09:00:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2015/06/05	09:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	55 58 01 26		
2015/06/05	09:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/06/05	09:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/06/05	09:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/06/05	09:00:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/06/05	09:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/05	09:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2015/06/05	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2015/06/05	10:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/05	10:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	10:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	10:02:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/05	10:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/05	10:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/05	10:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2015/06/05	11:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/06/05	11:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/05	11:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	11:44:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	11:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/05	12:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/05	12:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/05	13:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/06/05	13:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/05	13:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	13:23:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/05	13:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/05	13:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/05	13:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2015/06/05	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/06/05	15:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 00 00 00 00
2015/06/05	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/06/05	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/06/05	15:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/06/05	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/05	15:00:26.0	XRT_FLD_RESET_449_OG [0x1c1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/05	15:01:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	15:01:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	15:01:04.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/05	15:01:06.5	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/05	15:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/06/05	15:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/06/05	15:04:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/05	15:40:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	15:40:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	15:40:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/05	15:40:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/05	15:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/05	16:39:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	16:39:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	16:39:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/05	16:39:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/05	16:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/05	17:24:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/05	17:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/05	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/06/05	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/06/05	18:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/06/05	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/06/05	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/05	18:02:58.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2015/06/05	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/05	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/05	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/06/05	18:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 00 00 00 00
2015/06/05	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/06/05	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/06/05	18:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/06/05	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/05	18:10:26.0	XRT_FLD_RESET_449_OG [0x1c1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/05	18:12:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04

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2015/06/05	18:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/06/05	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/05	18:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	18:18:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	18:18:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/05	18:18:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/05	18:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/05	19:01:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/05	19:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/05	19:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	19:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	19:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/05	19:56:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/05	19:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/05	20:38:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/05	20:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/05	21:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	21:34:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	21:34:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/05	21:34:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/05	21:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/05	22:14:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/05	22:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/05	23:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	23:13:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	23:13:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/05	23:13:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/05	23:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/05	23:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	23:44:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/05	23:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/06/05	23:45:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	d2	da	be	4e
2015/06/05	23:45:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/05	23:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/05	23:45:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/05	23:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/05	23:45:26.0	XRT_FLD_RESET_449_OG [0x1c1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/05	23:47:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2015/06/05	23:47:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/06/05	23:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/06	00:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	00:51:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	00:51:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/06	00:51:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/06	00:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/06	01:21:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/06	01:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/06	02:28:30.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	02:28:33.0	XRT_CTRL_MANU_402_OG [0x192]							

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2015/06/06	02:28:35.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	02:28:37.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	02:31:45.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	02:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/06	03:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/06/06	03:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/06	03:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	03:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	03:57:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	04:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	04:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/06	04:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/06/06	05:37:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/06	05:37:32.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	05:37:34.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	05:37:36.5	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	05:40:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	06:16:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/06	06:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/06/06	06:21:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/06	06:21:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	06:21:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	06:22:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/06/06	06:22:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/06/06	06:24:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/06/06	06:24:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/06/06	06:24:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/06	06:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/06/06	06:31:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/06	06:31:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	06:31:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	06:32:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/06/06	06:32:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/06/06	06:32:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/06/06	06:32:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/06/06	06:32:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/06/06	06:32:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/06	06:34:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	06:34:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2015/06/06	06:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/06/06	07:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/06	07:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	07:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	07:18:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	07:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	07:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/06	07:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/06/06	08:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2015/06/06	08:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	08:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	08:57:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	09:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/06	10:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/06/06	10:10:00.0	XRT_CTRL_MANU_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	10:10:10.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78 00	
2015/06/06	10:14:10.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/06/06	10:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	10:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	10:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	10:40:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	10:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/06	12:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	12:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	12:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	12:20:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	12:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/06	13:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	13:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	13:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	13:58:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	14:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/06	14:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	14:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	14:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/06/06	14:40:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/06/06	14:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/06/06	14:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/06/06	14:40:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/06/06	14:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/06	14:40:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	14:42:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2015/06/06	14:42:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2015/06/06	14:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/06	15:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	15:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	15:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	15:37:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	15:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/06	16:23:00.0	XRT_Custom_430_OG [0x1ae]					
2015/06/06	16:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/06	17:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	17:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/06	17:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/06	17:15:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/06	17:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2015/06/06	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/06/06	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/06/06	18:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/06	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/06	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/06	18:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/06/06	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/06	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/06/06	18:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/06/06	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/06	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/06	18:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/06	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/06	18:10:26.0	XRT_FLD_RESET_449_OG [0x1c1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/06	18:12:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2015/06/06	18:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/06/06	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/06	18:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	18:54:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	18:54:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/06	18:54:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/06	18:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/06	19:36:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/06	19:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/06	20:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	20:32:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	20:32:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/06	20:32:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/06	20:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/06	21:14:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/06	21:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/06	22:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	22:10:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	22:10:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/06	22:10:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/06	22:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/06	22:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	22:44:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/06	22:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/06/06	22:45:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 29 68 b7 8e				
2015/06/06	22:45:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/06	22:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/06	22:45:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/06	22:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]							

2015/06/06	22:45:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/06/06	22:47:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/06	22:47:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08	
2015/06/06	22:49:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02	
2015/06/06	22:50:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/06	23:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/06	23:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/06	23:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/06	23:49:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/06	23:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/07	00:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/07	00:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	01:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	01:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	01:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/07	01:27:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/07	01:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/07	01:57:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/07	01:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	03:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	03:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	03:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/07	03:04:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/07	03:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/07	03:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/07	03:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	04:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	04:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	04:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/07	04:33:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/07	04:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/07	05:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/07	05:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	05:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	05:45:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	05:45:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2015/06/07	05:46:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00	
2015/06/07	05:46:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/06/07	05:48:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/06/07	05:48:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/06/07	05:48:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2015/06/07	05:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/07	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	05:55:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	05:55:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2015/06/07	05:56:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00 00 00 00	
2015/06/07	05:56:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/06/07	05:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/06/07	05:56:22.0	XRT_AEC_RESET_441_OG [0x1b9]						

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2015/06/07	05:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/07	05:56:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/07	05:58:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2015/06/07	05:58:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/06/07	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/07	06:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	06:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	06:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/07	06:14:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/07	06:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/07	06:52:30.0	XRT_Custom_430_OG [0x1ae]								
2015/06/07	06:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/07	07:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	07:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	07:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/07	07:54:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/07	07:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/07	08:30:30.0	XRT_Custom_430_OG [0x1ae]								
2015/06/07	08:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/07	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	08:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	08:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/06/07	09:00:00.5	AOCS_OrE-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	b3	8e	01	26
2015/06/07	09:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/07	09:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/07	09:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/07	09:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/07	09:00:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/07	09:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2015/06/07	09:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2015/06/07	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/07	09:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	09:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	09:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/07	09:34:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/07	09:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/07	10:09:00.0	XRT_Custom_430_OG [0x1ae]								
2015/06/07	10:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/07	11:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	11:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	11:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/07	11:17:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/07	11:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/07	11:47:30.0	XRT_Custom_430_OG [0x1ae]								
2015/06/07	11:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/07	12:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	12:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/07	12:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				

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2015/06/07	12:56:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/07	12:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/07	13:26:00.0	XRT_Custom_430_OG [0x1ae]						
2015/06/07	13:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/07	14:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	14:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	14:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/07	14:34:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/07	14:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/07	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/06/07	15:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00		
2015/06/07	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/06/07	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/06/07	15:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/06/07	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/06/07	15:00:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/07	15:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2015/06/07	15:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2015/06/07	15:22:00.0	XRT_Custom_430_OG [0x1ae]						
2015/06/07	15:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/07	16:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	16:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	16:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/07	16:13:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/07	16:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/07	16:58:30.0	XRT_Custom_430_OG [0x1ae]						
2015/06/07	16:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/07	17:36:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	17:36:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	17:36:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/06/07	17:36:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/06/07	17:36:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/06/07	17:39:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/06/07	17:39:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/06/07	17:39:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2015/06/07	17:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/07	17:46:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	17:46:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/07	17:46:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/06/07	17:46:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00		
2015/06/07	17:46:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/06/07	17:46:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/06/07	17:46:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/06/07	17:46:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/06/07	17:46:56.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/07	17:49:26.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		

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2015/06/07	17:49:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/06/07	17:49:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/07	17:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/07	17:51:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/07	17:51:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/07	17:51:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/07	17:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/07	18:35:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/07	18:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/07	19:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/07	19:29:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/07	19:29:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/07	19:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/07	19:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/07	20:12:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/07	20:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/07	21:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/07	21:08:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/07	21:08:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/07	21:08:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/07	21:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/07	21:49:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/07	21:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/07	22:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/07	22:46:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/07	22:46:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/07	22:46:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/07	22:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/07	23:21:00.5	XRT_Custom_430_OG [0x1ae]			
2015/06/07	23:22:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	00:25:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	00:25:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	00:25:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	00:25:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	00:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	00:54:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	00:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	02:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	02:03:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	02:03:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	02:03:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	02:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	02:33:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	02:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	03:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	03:33:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	03:33:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	03:33:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	03:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	04:11:30.0	XRT_Custom_430_OG [0x1ae]			

2015/06/08	04:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/08	05:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	05:10:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	05:10:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/08	05:10:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/08	05:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/08	05:50:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/08	05:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/08	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/06/08	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/06/08	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/08	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/08	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/08	06:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/06/08	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/08	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/06/08	06:10:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 19 db b1 9a				
2015/06/08	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/08	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/08	06:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/08	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/08	06:10:26.0	XRT_FLD_RESET_449_OG [0x1c1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/08	06:12:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2015/06/08	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/06/08	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/08	06:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	06:50:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	06:50:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/08	06:50:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/08	06:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/08	07:28:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/08	07:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/08	08:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	08:30:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	08:30:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/08	08:30:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/08	08:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/08	09:06:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/08	09:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/08	10:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	10:11:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/08	10:11:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/08	10:11:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/08	10:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2015/06/08	10:45:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	10:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	11:53:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	11:53:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	11:53:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	11:53:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	11:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	12:23:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	12:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	13:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	13:32:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	13:32:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	13:32:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	13:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	14:05:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	14:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	15:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	15:10:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	15:10:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	15:10:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	15:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	15:57:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	15:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	16:48:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	16:48:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	16:48:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	16:48:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	16:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	17:33:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	17:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	18:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/06/08	18:05:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/06/08	18:05:18.0	XRT_FLD_DIS_422_OG [0x1a6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/06/08	18:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/06/08	18:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/08	18:07:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2015/06/08	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	18:14:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	18:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/06/08	18:15:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]			
		AOCU_NM	5	02-76	00 19 db b1 9a
2015/06/08	18:15:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/06/08	18:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/06/08	18:15:22.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/06/08	18:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/08	18:15:26.0	XRT_FLD_RESET_449_OG [0x1c1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	18:17:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01

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2015/06/08	18:17:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2015/06/08	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	18:27:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	18:27:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	18:27:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	18:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	18:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	19:10:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	19:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	20:05:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	20:05:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	20:05:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	20:05:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	20:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	20:47:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	20:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	21:44:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	21:44:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	21:44:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	21:44:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	21:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	22:23:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	22:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/08	23:22:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	23:22:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/08	23:22:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/08	23:22:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/08	23:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/08	23:52:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/08	23:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/09	01:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/09	01:00:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/09	01:00:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/09	01:00:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/09	01:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/09	01:30:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/09	01:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/09	02:37:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/09	02:37:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/09	02:37:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/09	02:37:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/09	02:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/09	03:09:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/09	03:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/09	04:06:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/09	04:06:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/09	04:06:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/09	04:06:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/09	04:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/09	04:47:30.0	XRT_Custom_430_OG [0x1ae]			

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2015/06/09	04:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/09	05:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	05:47:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	05:47:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/09	05:47:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/09	05:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/09	06:25:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/09	06:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/09	06:31:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	06:31:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	06:31:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/06/09	06:31:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/06/09	06:31:48.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/09	06:34:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/09	06:34:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/09	06:34:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/06/09	06:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/09	06:41:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	06:41:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	06:41:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/06/09	06:41:30.0	AOCS_Ore-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 19 db b1 9a				
2015/06/09	06:41:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/09	06:41:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/09	06:41:52.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/09	06:41:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/09	06:41:56.0	XRT_FLD_RESET_449_OG [0x1c1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/09	06:44:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2015/06/09	06:44:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2015/06/09	06:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/09	07:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	07:27:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	07:27:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/09	07:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/09	07:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/09	09:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	09:07:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/09	09:07:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/09	09:07:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/09	09:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/09	10:14:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				