

XRT Timeline to be uploaded on 2015/06/20

Period: 2015/06/20 10:05:00 - 2015/06/30 10:36:00

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Normal mode

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XOB #1A95: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (2ms)													
Term	Pointing (x, y)				Comment								
06/20 10:18:00 - 06/20 15:46:30	Track (-382.9, 133.0) @ 06/20 10:15:00				# OP start + 10min, AR 12371 flare observations. EIS CH engineering obs.								
06/20 19:47:00 - 06/21 05:55:54	Track (-307.4, 131.5) @ 06/20 19:15:00				# AR cont.								
06/21 06:09:00 - 06/21 17:45:24	Track (-213.4, 129.8) @ 06/21 06:06:00				# AR cont.								
06/21 18:45:30 - 06/22 05:20:30	Track (-108.3, 128.4) @ 06/21 17:55:30				# AR cont.								
06/22 06:18:30 - 06/22 17:59:54	Track (2.8, 127.2) @ 06/22 06:15:30				# AR cont.								
06/22 18:13:00 - 06/23 05:29:24	Track (110.0, 126.5) @ 06/22 18:10:00				# AR cont.								
06/23 05:42:30 - 06/23 08:35:00	Track (212.1, 126.1) @ 06/23 05:39:30				# AR cont.								
PROG= 05 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 30 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	2ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 69 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	2ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 62 8-time(s) 160.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A97: Stray light 2015-12 ; Al-poly(1s) Thin-Be(5.6s) 2x2 full FOV(1min-cad) and 2x2 256 on AR(10sec-cad)													
Term	Pointing (x, y)				Comment								
06/20 17:24:00 - 06/20 18:11:54	Fixed (0.0, 0.0)				synoptic, extended for X-ray stray light measurements and SOT flat fields.								
PROG= 07 1-time(s)													
Subr= 2 10-time(s) 2.0sec													
└─ Seqn= 52 1-time(s) 10.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	512x512 (672, 1144)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	512x512 (1192, 648)	Q=95	0	0	2.0sec
Subr= 3 7-time(s) 2.0sec													
└─ Seqn= 83 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 52 3-time(s) 10.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	512x512 (672, 1144)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	512x512 (1192, 648)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A94: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi													
Term	Pointing (x, y)				Comment								
06/20 18:15:00 - 06/20 19:00:00	Fixed (0.0, 0.0)				synoptic, extended for X-ray stray light measurements and SOT flat fields.								
06/21 05:59:00 - 06/21 06:05:54	Fixed (0.0, 0.0)				synoptic, shifted -4.0 min								
06/21 17:48:30 - 06/21 17:55:24	Fixed (0.0, 0.0)				synoptic, shifted -14.5 min								
06/22 06:08:30 - 06/22 06:15:24	Fixed (0.0, 0.0)				synoptic, shifted 5.5 min								
06/22 18:03:00 - 06/22 18:09:54	Fixed (0.0, 0.0)				synoptic								
06/23 05:34:30 - 06/23 05:39:24	Fixed (0.0, 0.0)				synoptic, shifted -30.5 min								
PROG= 06 1-time(s)													
Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 33 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 40 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 39 1-time(s) 2.0sec													

thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 91 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	2ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	2ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A98: Stray light 2015-13 ; Ti-poly(2s) C-poly(2s) 2x2 full FOV(1min-cad) and 2x2 256 on AR(10sec-cad)

Term	Pointing (x, y)	Comment
06/20 19:02:30 - 06/20 19:14:54	Fixed (0.0, 0.0)	synoptic, extended for X-ray stray light measurements and SOT flat fields.
PROG= 11 1-time(s)		
Subr= 2 10-time(s) 2.0sec		
Seqn= 52 1-time(s) 10.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Norm 250ms Obs 2x2 512x512 (672, 1144) Q=95 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 2x2 512x512 (1192, 648) Q=95 0 0 2.0sec
Subr= 3 7-time(s) 2.0sec		
Seqn= 79 1-time(s) 2.0sec		
Open/Ti-poly	Open/Ti-poly	close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
C-poly/Open	C-poly/Open	close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 52 3-time(s) 10.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Norm 250ms Obs 2x2 512x512 (672, 1144) Q=95 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 2x2 512x512 (1192, 648) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1A96: Flare - multfilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
06/20 10:18:00 - 06/20 15:46:30	Track (-382.9, 133.0) ^{Ⓜ 06/20 10:15:00}	# OP start + 10min, AR 12371 flare observations. EIS CH engineering obs.
06/20 19:47:00 - 06/21 05:55:54	Track (-307.4, 131.5) ^{Ⓜ 06/20 19:15:00}	# AR cont.
06/21 06:09:00 - 06/21 17:45:24	Track (-213.4, 129.8) ^{Ⓜ 06/21 06:06:00}	# AR cont.
06/21 18:45:30 - 06/22 05:20:30	Track (-108.3, 128.4) ^{Ⓜ 06/21 17:55:30}	# AR cont.
06/22 06:18:30 - 06/22 17:59:54	Track (2.8, 127.2) ^{Ⓜ 06/22 06:15:30}	# AR cont.
06/22 18:13:00 - 06/23 05:29:24	Track (110.0, 126.5) ^{Ⓜ 06/22 18:10:00}	# AR cont.
06/23 05:42:30 - 06/23 08:35:00	Track (212.1, 126.1) ^{Ⓜ 06/23 05:39:30}	# AR cont.
PROG= 19 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 80 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 2ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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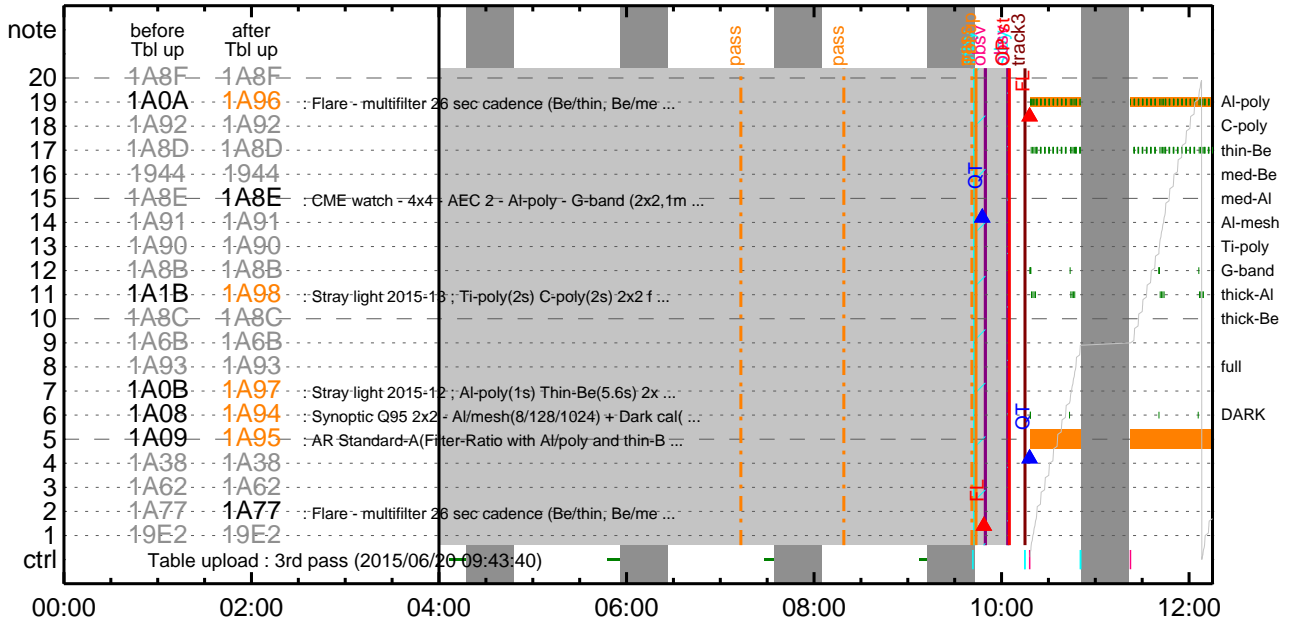
Flare Detection

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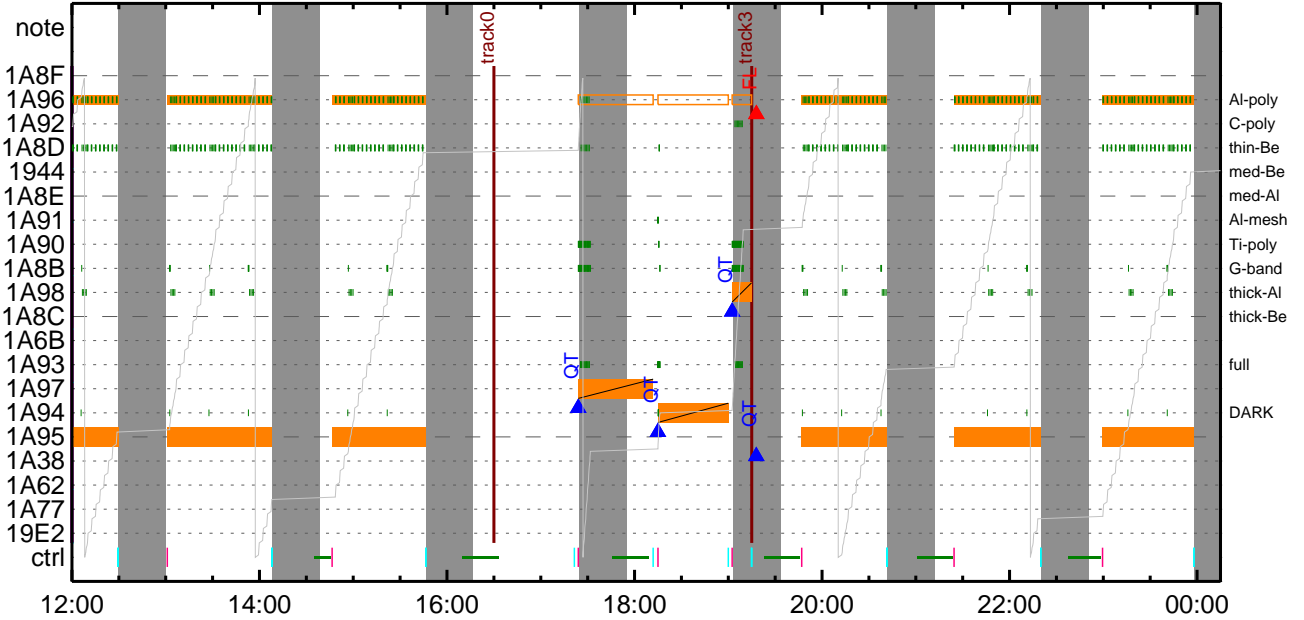
FLD Patrol

Term	Pointing (x, y)	Comment
06/20 19:15:18 - 06/21 05:56:16	Track (-307.4, 131.5) ^{Ⓜ 06/20 19:15:00}	# AR cont.
06/21 06:06:18 - 06/21 17:45:46	Track (-213.4, 129.8) ^{Ⓜ 06/21 06:06:00}	# AR cont.
06/21 17:55:48 - 06/22 06:05:46	Track (-108.3, 128.4) ^{Ⓜ 06/21 17:55:30}	# AR cont.
06/22 06:15:48 - 06/22 18:00:16	Track (2.8, 127.2) ^{Ⓜ 06/22 06:15:30}	# AR cont.
06/22 18:10:18 - 06/23 05:33:46	Track (110.0, 126.5) ^{Ⓜ 06/22 18:10:00}	# AR cont.
06/23 05:39:48 - 06/30 10:36:00	Track (212.1, 126.1) ^{Ⓜ 06/23 05:39:30}	# AR cont.
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

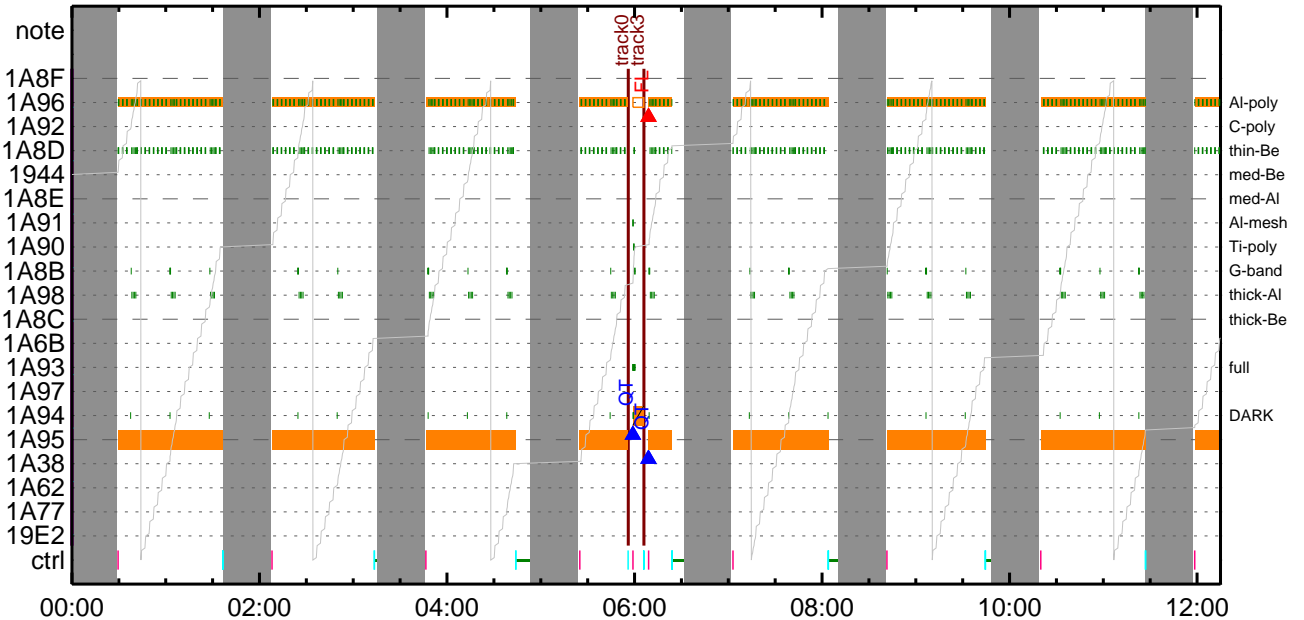
CMDI #0210 2015/06/20



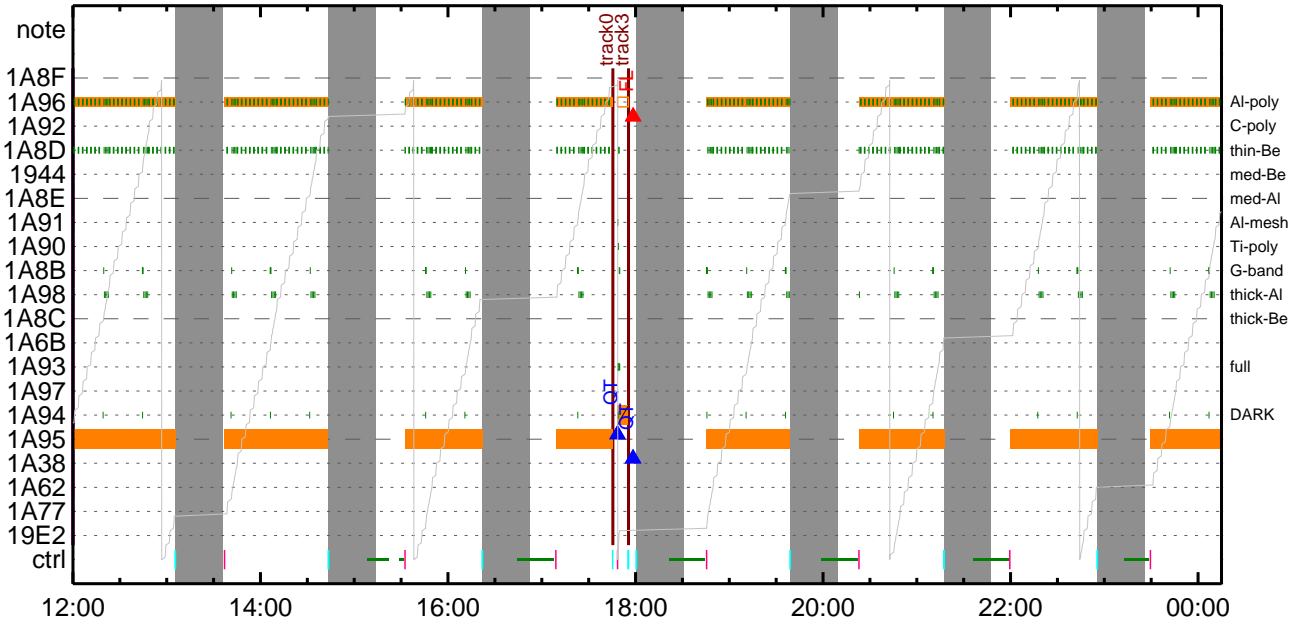
CMDI #0210 2015/06/20



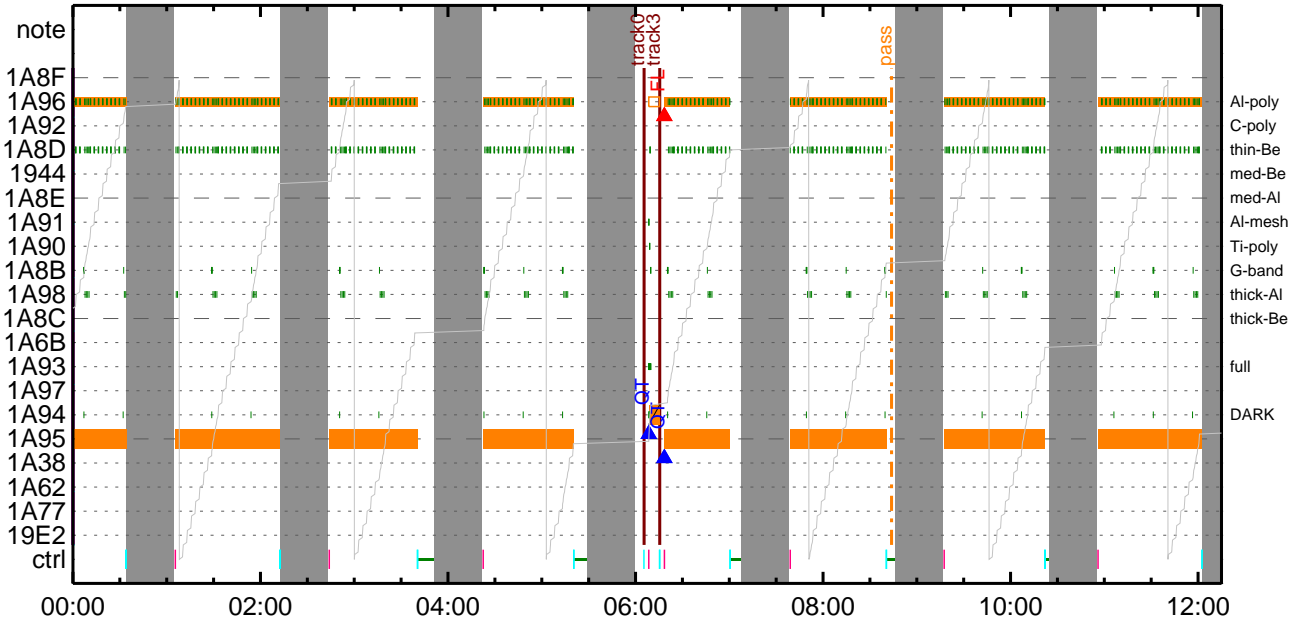
CMDI #0210 2015/06/21



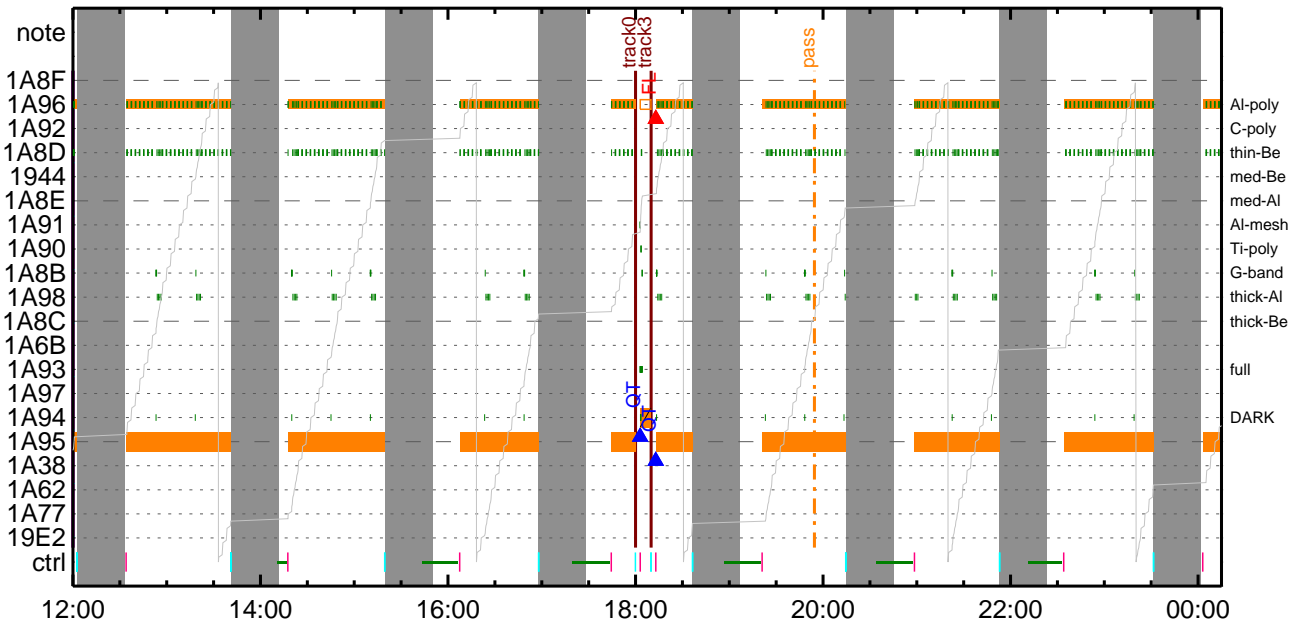
CMDI #0210 2015/06/21



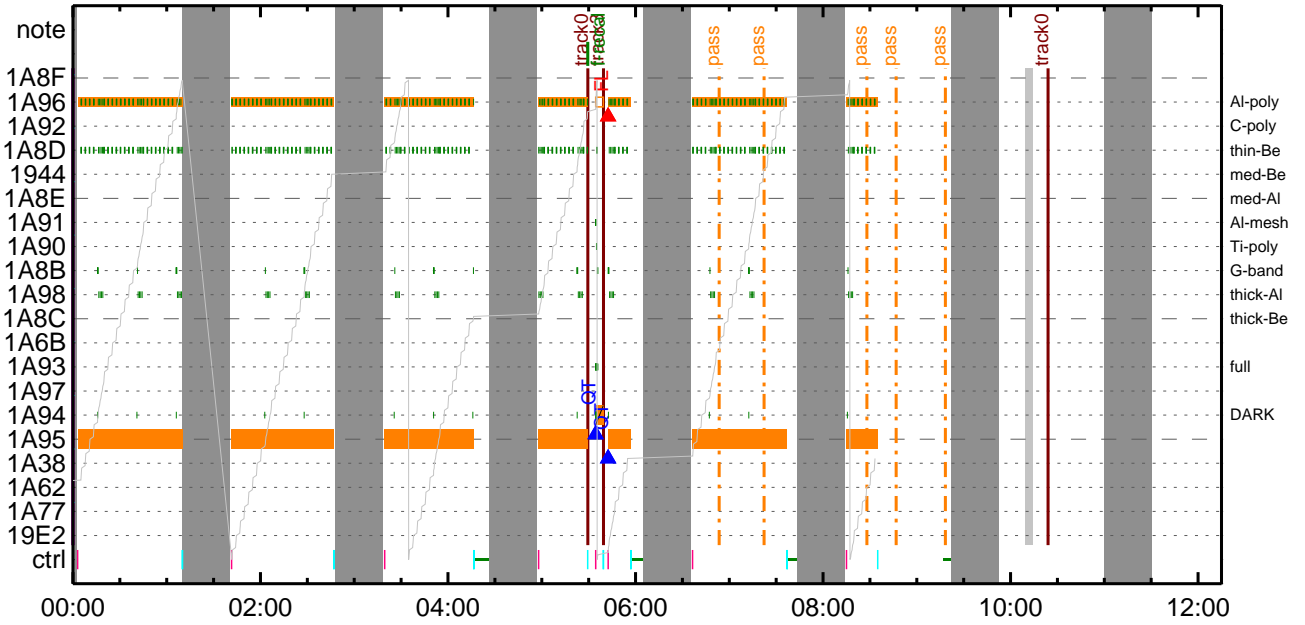
CMDI #0210 2015/06/22



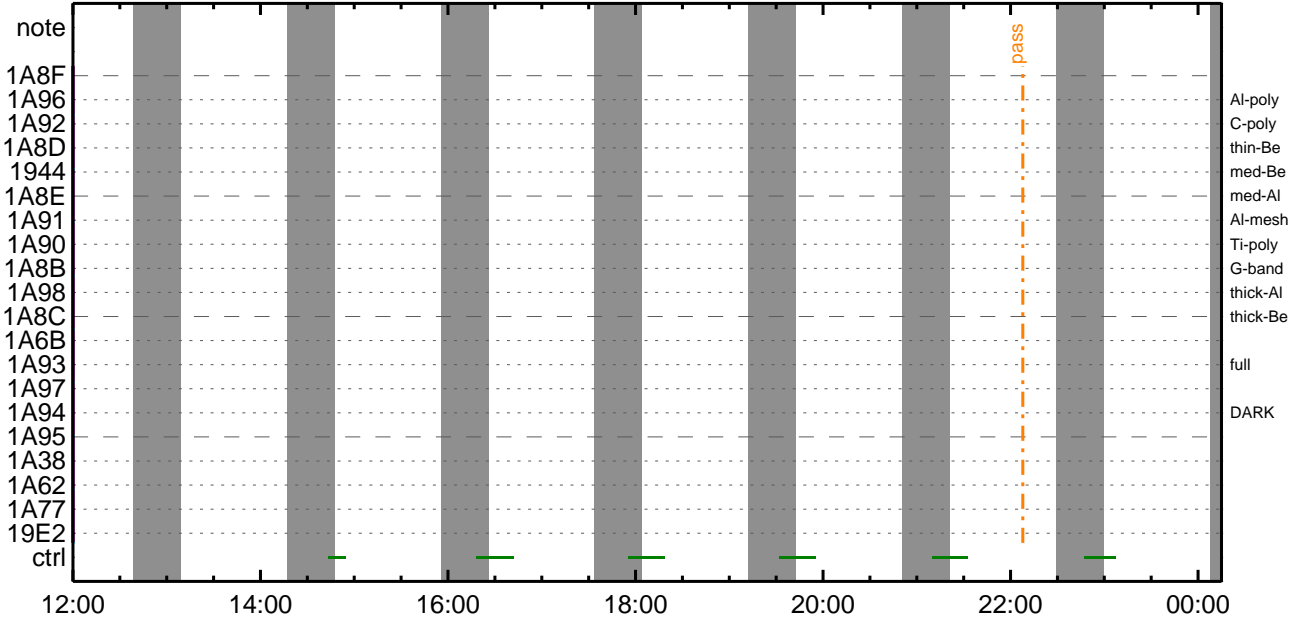
CMDI #0210 2015/06/22



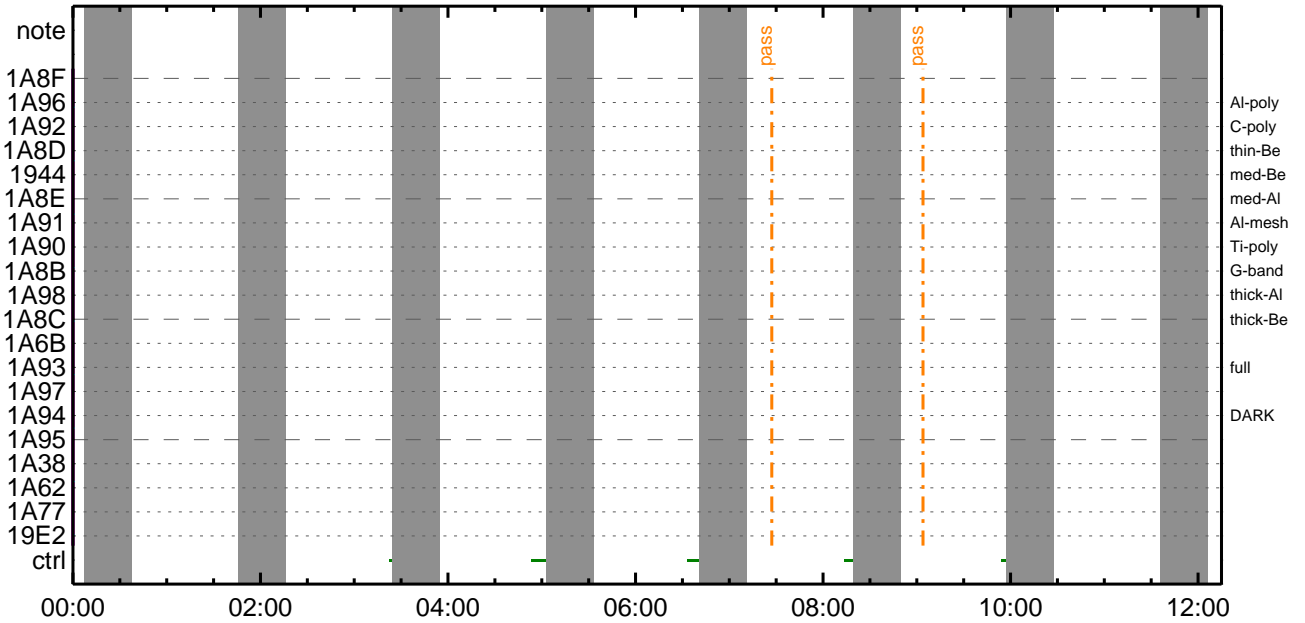
CMDI #0210 2015/06/23



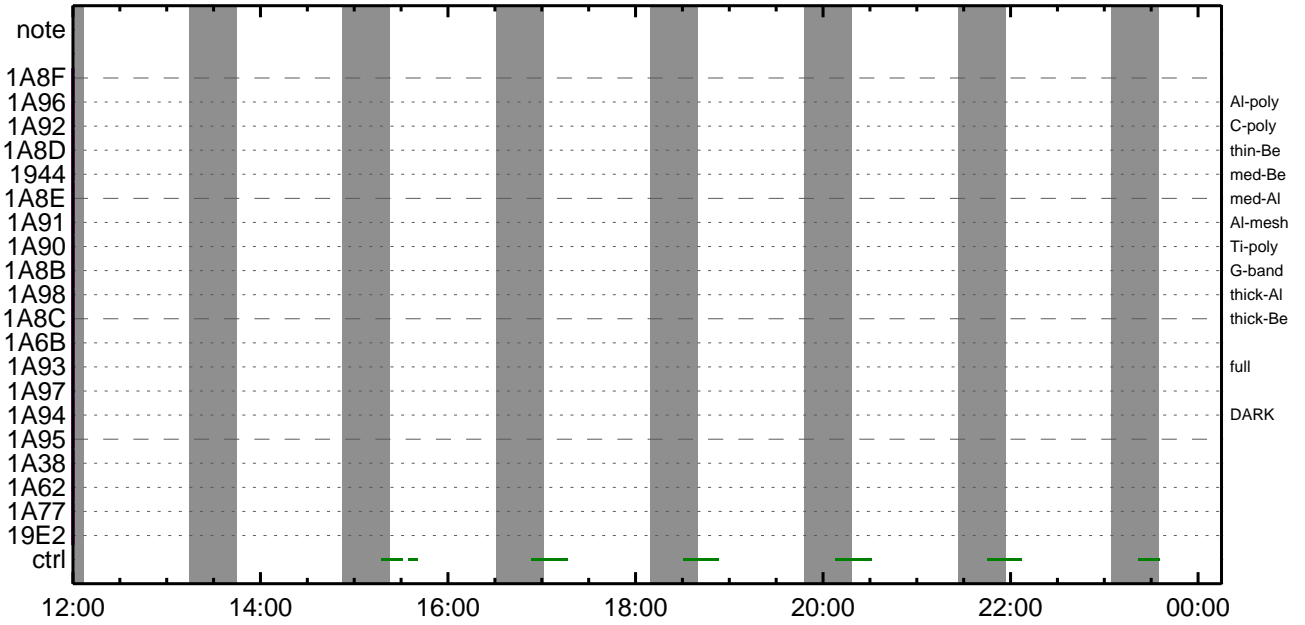
CMDI #0210 2015/06/23



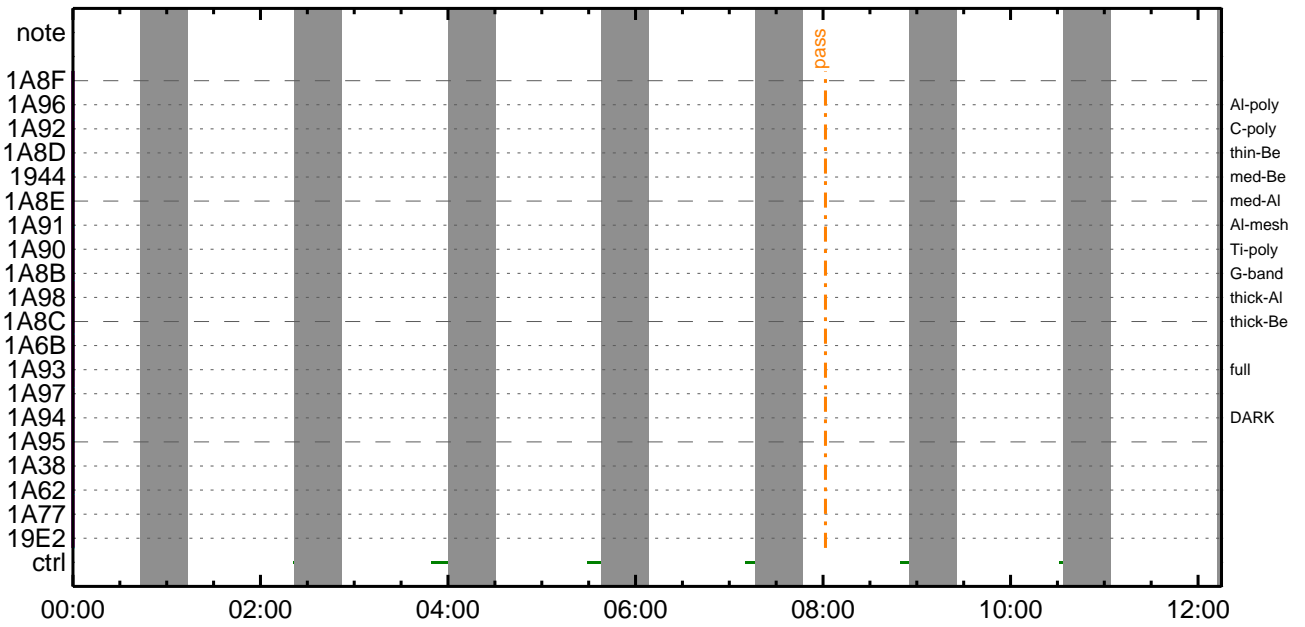
CMDI #0210 2015/06/24



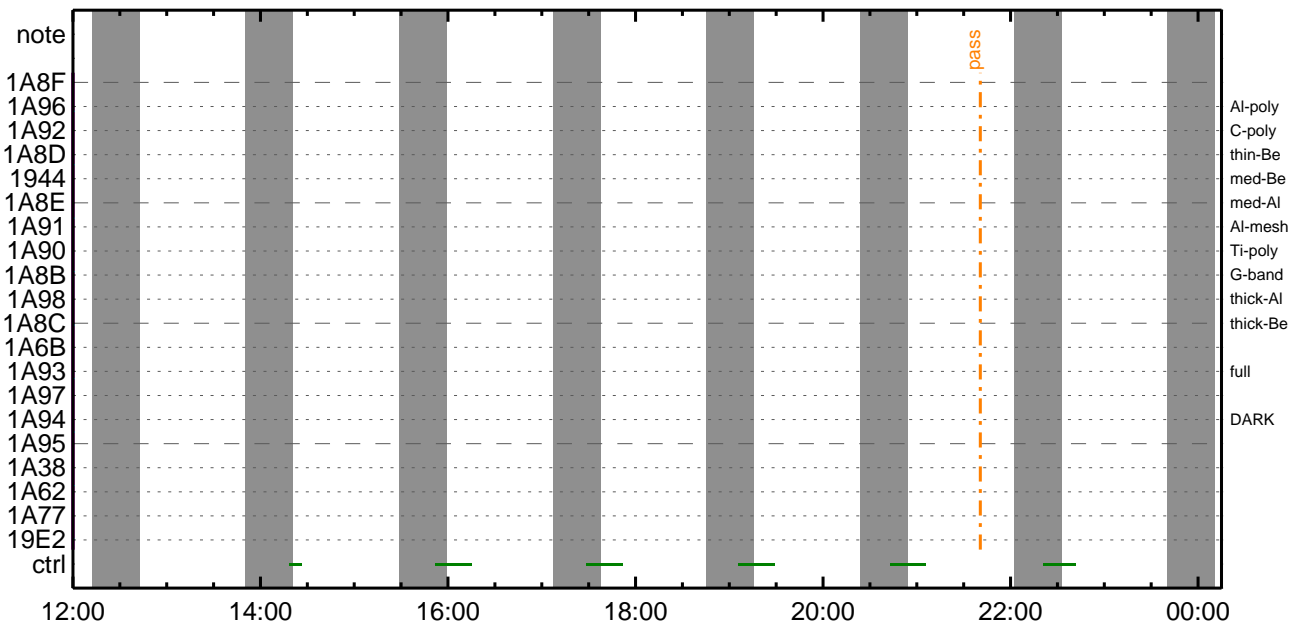
CMDI #0210 2015/06/24



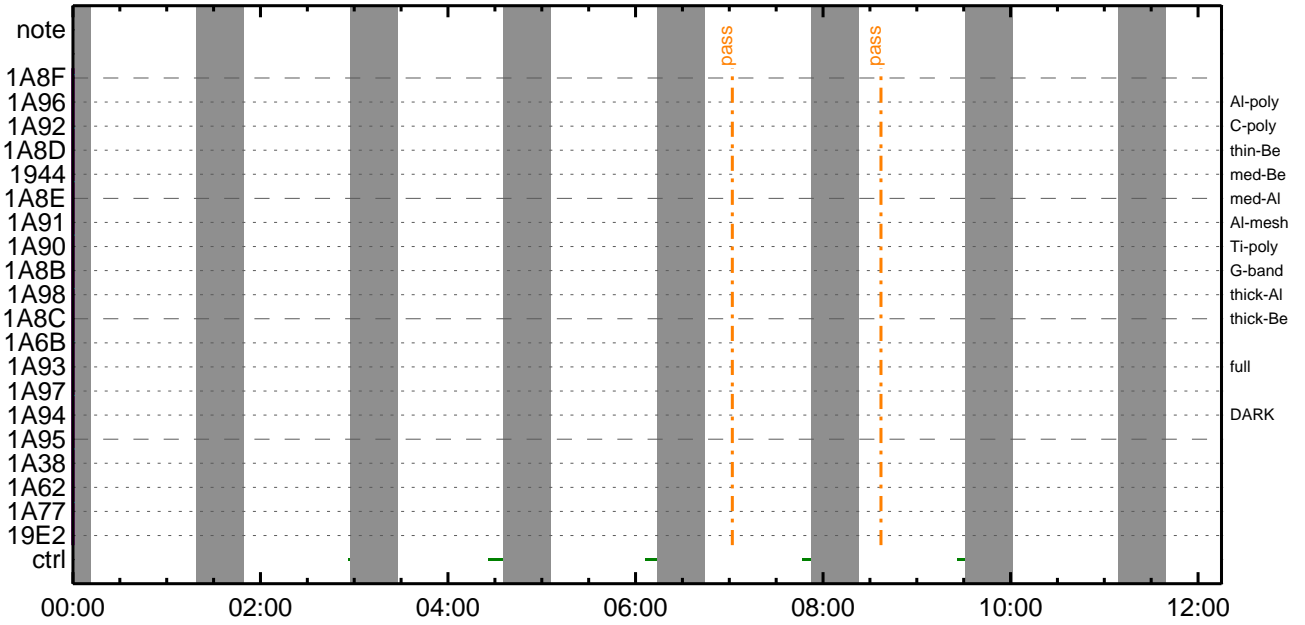
CMDI #0210 2015/06/25



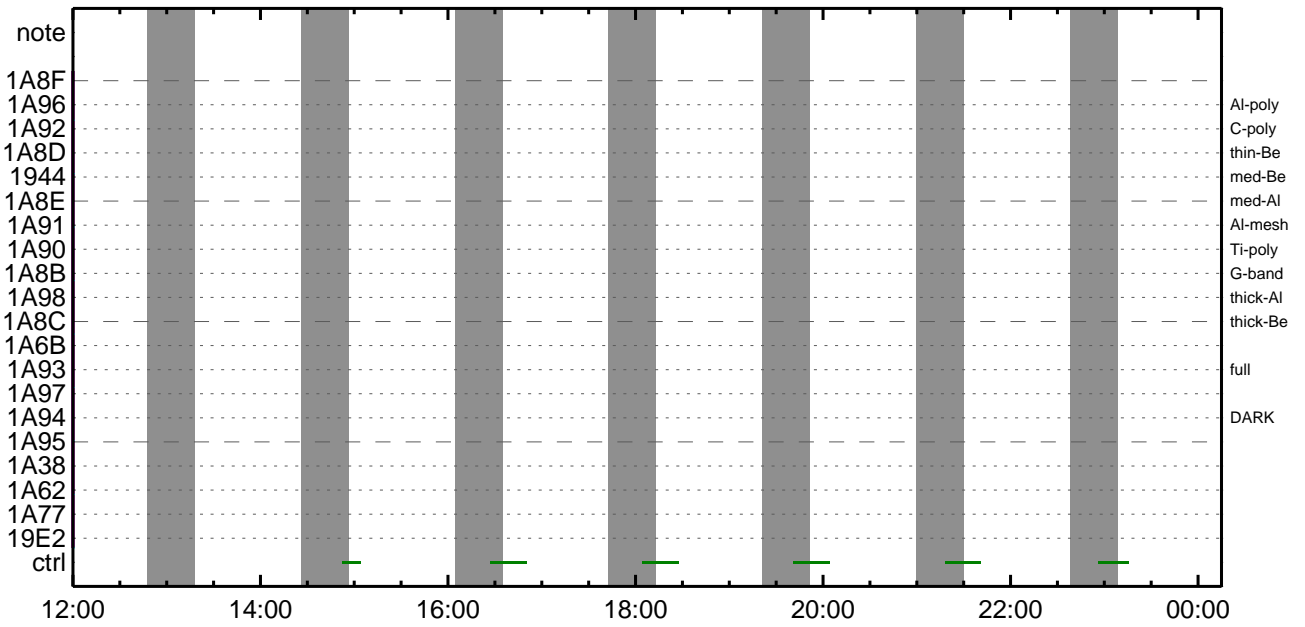
CMDI #0210 2015/06/25



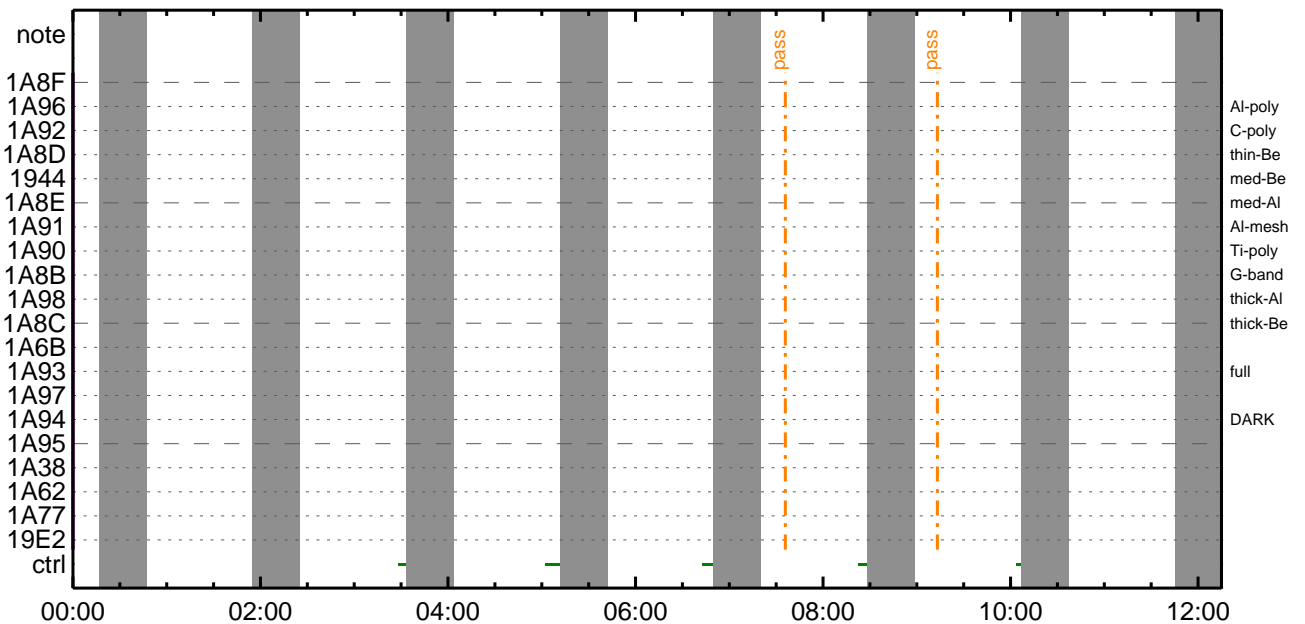
CMDI #0210 2015/06/26



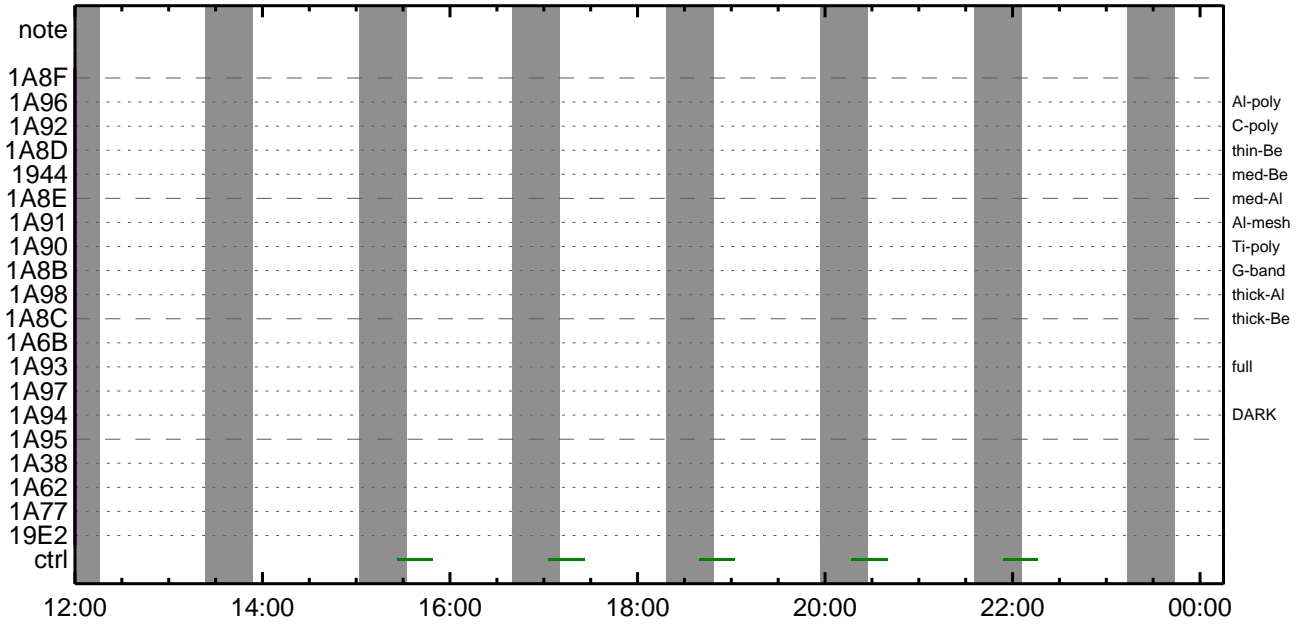
CMDI #0210 2015/06/26



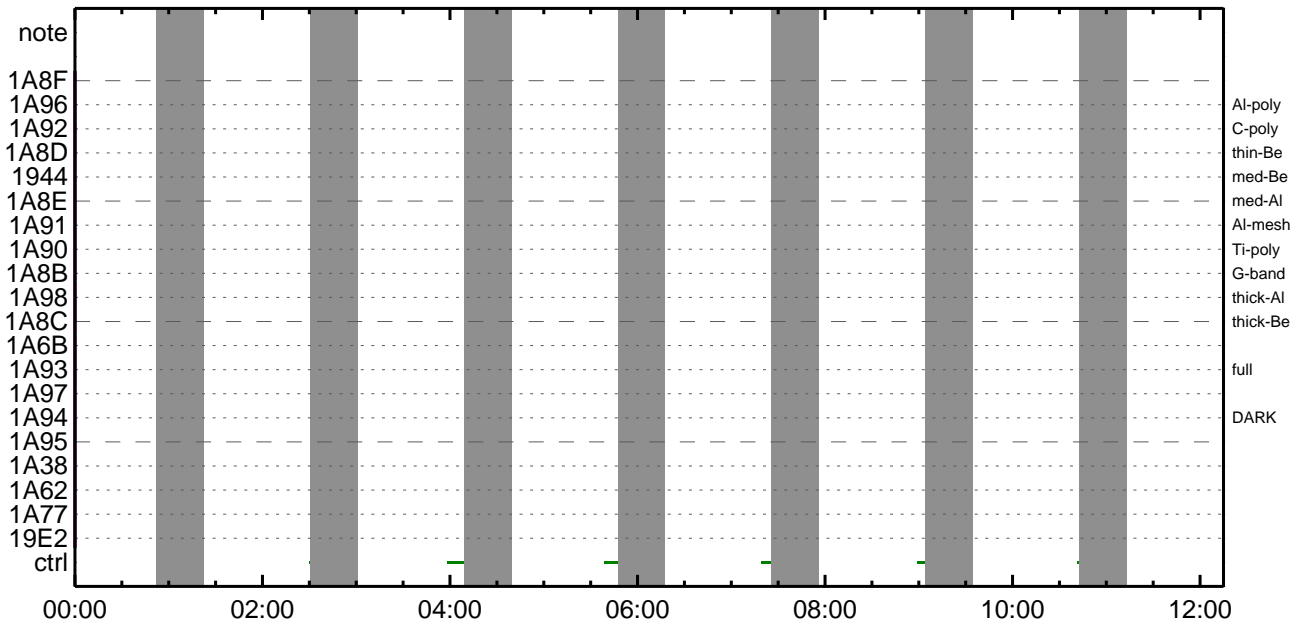
CMDI #0210 2015/06/27



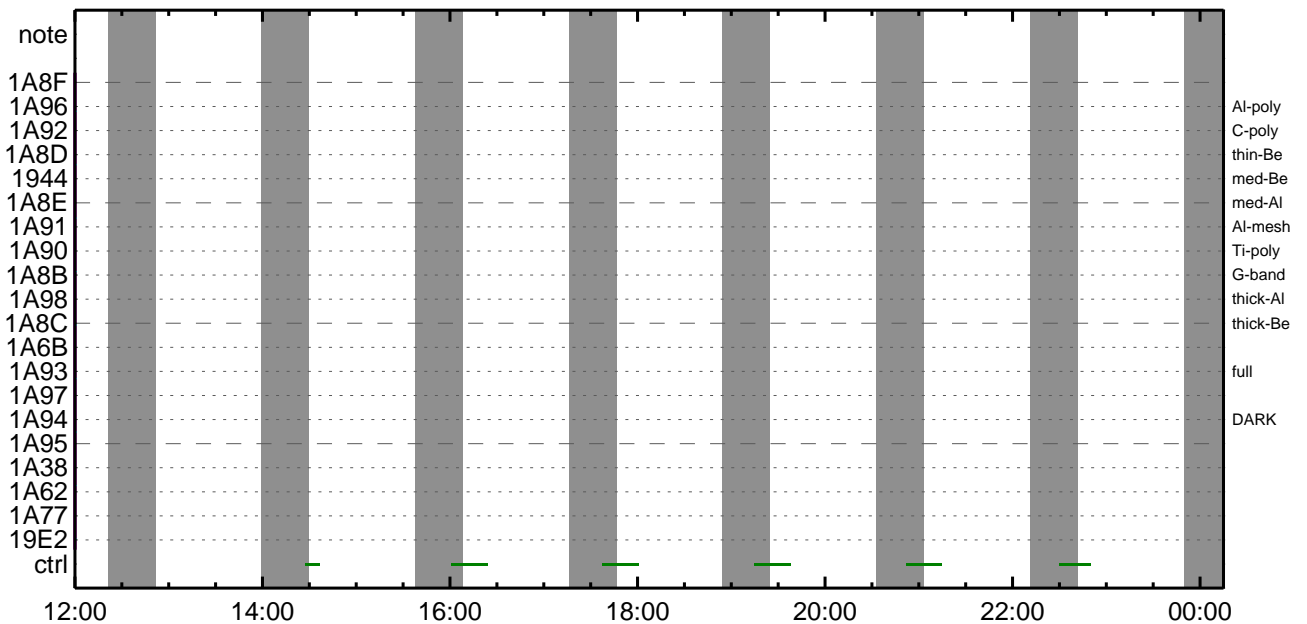
CMDI #0210 2015/06/27



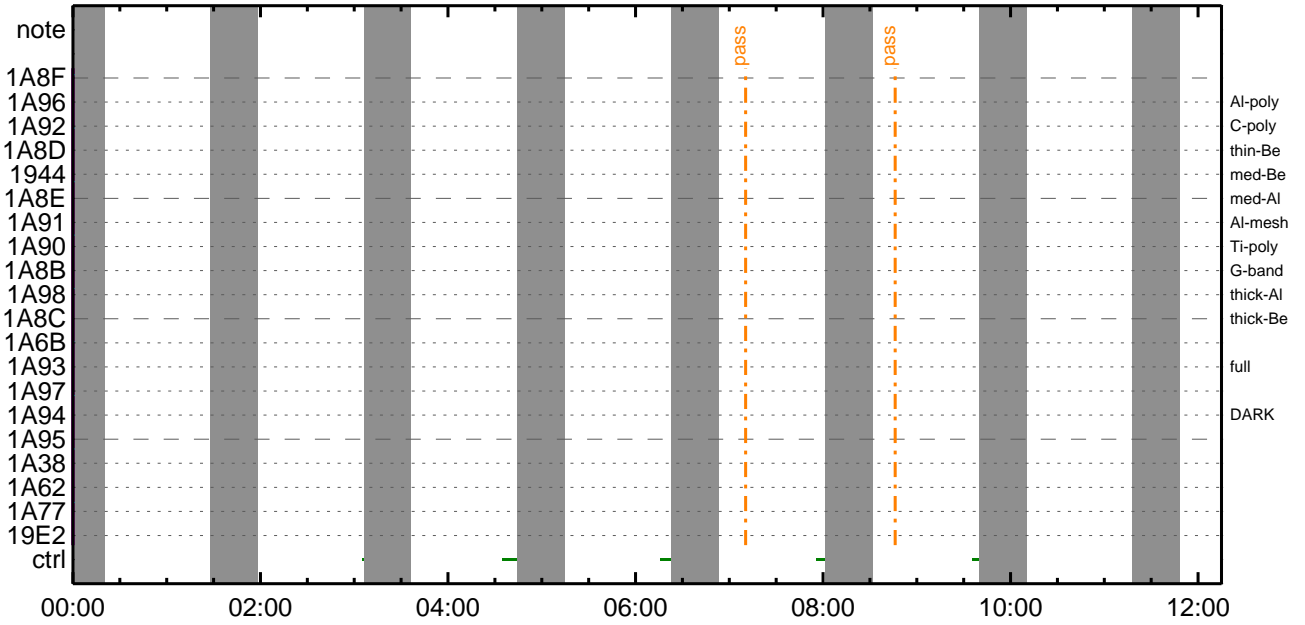
CMDI #0210 2015/06/28



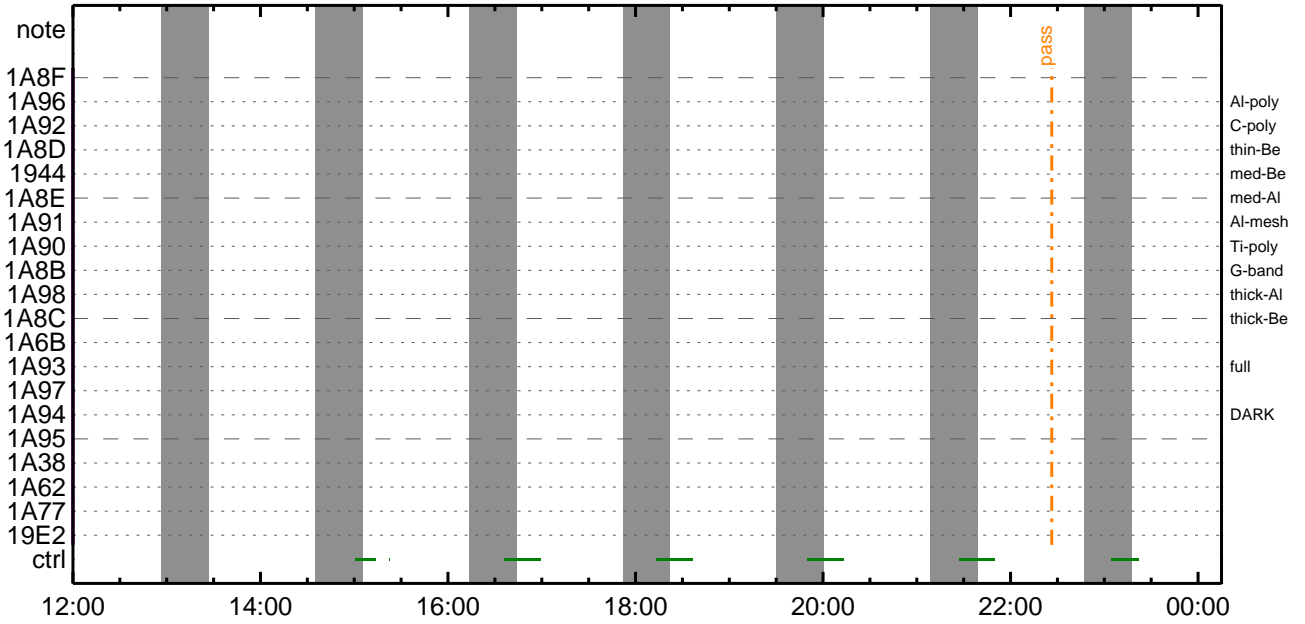
CMDI #0210 2015/06/28



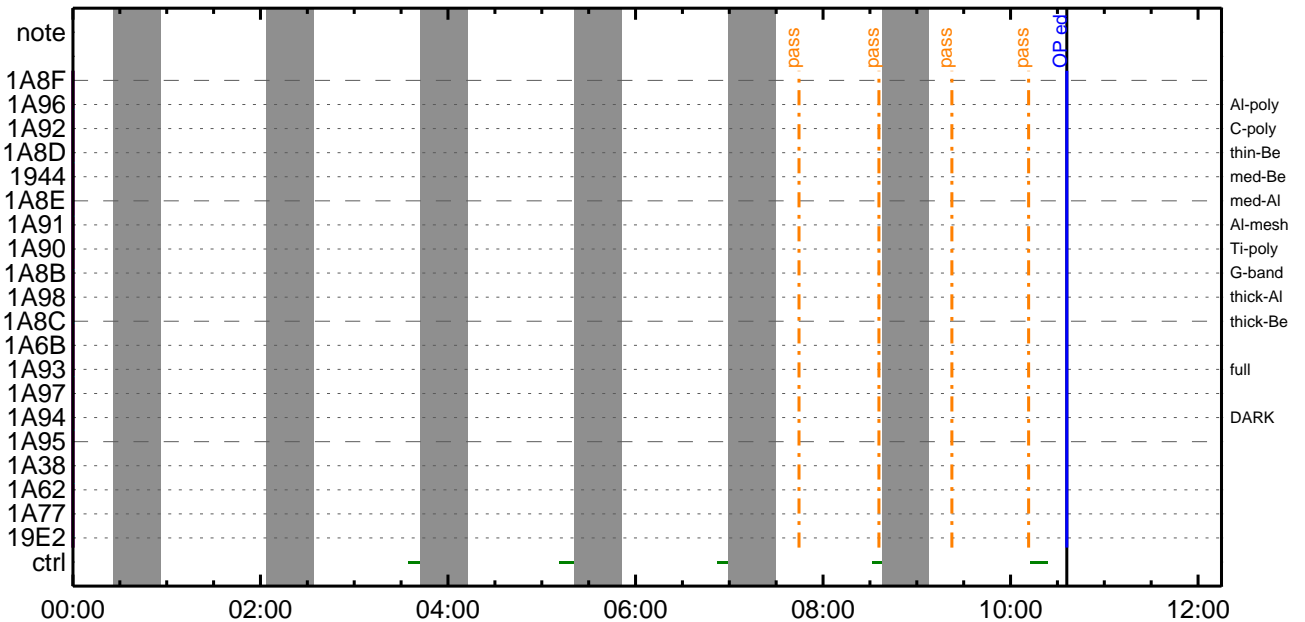
CMDI #0210 2015/06/29



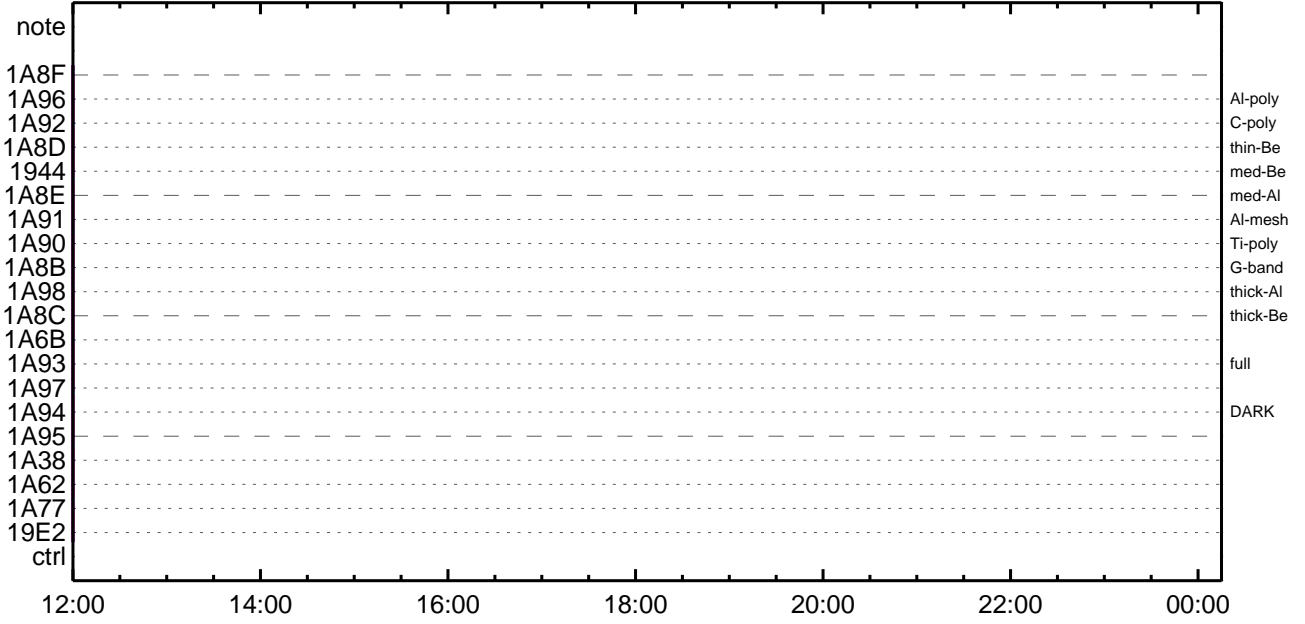
CMDI #0210 2015/06/29



CMDI #0210 2015/06/30



CMDI #0210 2015/06/30



(a) Spacecraft Operation Procedure (real-commands)

```
main-210 2015-06-20 14:24:35 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Ä»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFÿ6YÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ðÁð•µ°Æ»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈè¿µ•ííÈ;ÈèÈ¼°ÇÓð•ð¿¼í¹ç¿Í;çÀ®, ùñ¹ðèððçÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÀY6Y×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-210:OP
0020 ()
0021 . S. OG og-210:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁY6Y×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀY6Y×¾ª î»ðð³ îÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðí¼È¹ç•è² ÌOKðð³ îÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀY6Y×¾ª î»ðð³ îÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðí¼È¹ç•è² ÌOKðð³ îÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀY6Y×¾ª î»ðð³ îÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPðí¼È¹ç•è² ÌOKðð³ îÇ§
0081 C.
0082 . C. ***** òÈ²¼òí¼Ä´ ¶¹¼ðÈÈ-ò¼Ä+¿® (¼áµ-YÀY6Y×¾ª¿çððÄÓÃæç¾ª ð¼ð¿¼í¹ç¿çðâ) *****
0083 C. DHUYâ;¼YÉ;È¼Y¾; Yí;¼YÉ;ÈððÍáð¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADð-Ä+¿¿®NGðí¼È¹ç; ç°È²¼ðÍTI-CMDÄ+¿¿®ðí¼È¹Ôðððð³ðÈ;f
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0096 C.                                ðþð¿;çSETðÈDUMPaİÆ±ºìÏÑŸ¹ðÇ¹Ôð|ð³ðÈ;£
0097 C.
0098 . C. TIŸ³ŸFŸÓŸÉðððÀDİ¿ (UT)
0099 +. TI 2015-06-20 10:00:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2015-06-20 10:00:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2015-06-20 10:00:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2015-06-20 10:04:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                                °È²¼ðİÄê%îİÑðİŸÁŸ§ŸÁŸ-¹àİŰ
0116 C.                                çç[HK1_TI_CMD_ENA/DIS]           EQ        ENA
0117 C.                                çç[HK1_TI_CMD_NUM]              EQ        4
0118 C.                                çç[HK1_NEXT_EXEC_PIM]           EQ        DHU
0119 C.                                çç[HK1_NEXT_EXEC_DC]           EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİî°èŸÁŸÓŸ×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                                çç[HK1_DMP_TOP_ADRS_1]           EQ        07
0129 C.                                çç[HK1_DMP_TOP_ADRS_0]           EQ        2B
0130 C.                                çç[HK1_DMP_BLOCK_NUM]           EQ        3
0131 C.                                çç[HK1_DMP_REPEAT_NUM]          EQ        0
0132 C.                                çç[HK1_DMA_DMP_PIM]            EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                                çç[HK1_PKT_FORM_NO]             EQ        7
0136 C.                                çç[HK1_PKT_GEN_TIME]           EQ        0.25 s
0137 C.                                çç[HK1_S_TLM_BIT_RATE]          EQ        32k
0138 C.                                çç[HK1_X_TLM_BIT_RATE]          EQ        4M
0139 C.                                çç[HK1_DMP_CHK_FLG]            EQ        EXEC
0140 C.
0141 . C. ŸÁŸÓŸ×¼ªªİ»ðð³İÇ§
0142 C.                                çç[HK1_DMP_CHK_FLG]            EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBLðİŸÈ¹Ç•è²İOKðð³İÇ§
0145 C.
0146 . C. DHUŸâ;¼ŸÉ;È¼Ÿ½,Ÿì;¼ŸÈ;Èððİáð¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                                çç[HK1_PKT_FORM_NO]             EQ        2
0150 C.                                çç[HK1_PKT_GEN_TIME]           EQ        0.5S
0151 C.                                çç[HK1_S_TLM_BIT_RATE]          EQ        32K
0152 C.                                çç[HK1_X_TLM_BIT_RATE]          EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2015-06-20 10:04:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2015-06-20 10:04:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2015-06-20 10:04:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2015-06-20 10:04:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ´ŰĂİðİ»öŸòÈÄð¹ðÈDCBC•×²è *****
0192 C. (ŸáºİŸÓŸÁŸÉŸPŸEŸÁŸÇŸÈòÈŸ¼ð¼Ä»Űð¹è)
0193 . S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 1m36s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2015-06-20 10:04:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'úÃîâî»ö¼ÝðĒĀðñ¹ēDCBC•x²è *****
0156 C. (¼ā°îÿÔÿĀÿĒÿPÿÿĒÿĀÿçÿĒēñĒ¼ā¼Ā»Ûñ¹ē)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¼Dÿ¹•Ī Daily±çĪÑñĒ'Øñ¹ēDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOSÿĀÿSÿÿĀÿ-¼Ā»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.
```



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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 85 83 08 08)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 54 8f 08 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 95 51 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0b 80 80 20 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 0c 80 80 08 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0f 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 10 80 80 08 08)
0110 + DC 07-F0 MDP_XRT_FLD_ENA
0111 BC (d8)
0112 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0113 BC (c8)
0114 + DC 07-F0 MDP_XRT_AEC_RESET
0115 BC (d0)
0116 + DC 07-F0 MDP_XRT_ARS_DIS
0117 BC (d5)
0118 + DC 07-F0 MDP_XRT_FLD_RESET
0119 BC (da)
0120 + DC 07-F0 MDP_XRT_QT_PROG_SET
0121 BC (c4 0f)
0122 + DC 07-F0 MDP_XRT_FL_PROG_SET
0123 BC (c5 02)
0124 . C. ----- Success Verify ? OK / NG ____
0125 C.
0126 C.
0127 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0128 C.
0129 + DC 07-F0 MDP_XRT_MODE_OBSV
0130 BC (c2)
0131 + TI 2015-06-20 10:04:02.0
0132 DC 07-F0 MDP_XRT_MODE_OBSV
0133 BC (c2)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C. ***** XRT END *****
0137 C.
0138 . C. ***** MDP `úÃîñï»ö¼ÝñÊÃÐñ¹ñèDCBC•x²è *****
0139 C. (%á°îÿÓÿÃÿÈÿPÿËÿáÿçÿèñÊ¼ññ¼Ã»Ûñ¹ñè)
0140 . S. DC-BC dcbc-402:DCBC
0141 (MDP_known_event)
0142 C.
0143 C.
0144 . C. ***** ÿDÿ¹•Ï Daily±¿ÍññÈ´Øñ¹ñèDCBC•x²è *****
0145 . S. DC-BC dcbc-153:DCBC
0146 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0147 C.
0148 C.
0149 . C. ;ãLOSÿÁÿ§ÿËÿ-¼Ã»Û;ã
0150 C.
0151 . C. ***** LOS *****
0152 C.
```

*** OP Sequence for XRT ***

2015/06/20	10:14:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	10:14:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	10:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/06/20	10:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2015/06/20	10:15:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/06/20	10:15:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/06/20	10:15:22.5	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/06/20	10:15:24.5	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/06/20	10:15:26.5	XRT_FLD_RESET_449_OG [0x1c1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/20	10:17:56.5	XRT_QT_PROG_SET_406_OG [0x196]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2015/06/20	10:17:58.5	XRT_FL_PROG_SET_413_OG [0x19d]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/06/20	10:18:00.5	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/20	10:50:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	10:50:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	10:50:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/20	10:50:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/20	10:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/20	11:21:30.0	XRT_Custom_430_OG [0x1ae]					
2015/06/20	11:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/20	12:29:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	12:29:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	12:29:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/20	12:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/20	12:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/20	13:00:00.0	XRT_Custom_430_OG [0x1ae]					
2015/06/20	13:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/20	14:08:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	14:08:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	14:08:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/20	14:08:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/20	14:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/20	14:45:30.0	XRT_Custom_430_OG [0x1ae]					
2015/06/20	14:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/20	15:46:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	15:46:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	15:46:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/20	15:46:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/20	15:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/20	16:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2015/06/20	17:21:30.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/20	17:21:32.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/06/20	17:21:52.0	XRT_FLD_DIS_409_OG [0x199]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/06/20	17:23:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/06/20	17:23:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/06/20	17:23:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2015/06/20	17:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/20	18:11:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2015/06/20	18:11:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/06/20	18:12:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/20	18:14:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/20	18:14:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/20	18:14:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/06/20	18:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/20	19:00:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/20	19:00:02.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/06/20	19:00:22.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/20	19:02:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/20	19:02:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/20	19:02:28.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2015/06/20	19:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/20	19:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/20	19:14:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/20	19:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/06/20	19:15:00.0	AOCs_Or-e-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2015/06/20	19:15:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/20	19:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/20	19:15:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/20	19:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/20	19:15:26.0	XRT_FLD_RESET_449_OG [0x1c1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/20	19:17:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2015/06/20	19:17:58.0	XRT_FL_PROG_SET_413_OG [0x19d]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/06/20	19:46:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/20	19:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/20	20:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/20	20:41:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/20	20:41:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/20	20:41:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/20	20:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/20	21:23:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/20	21:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/20	22:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/20	22:20:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/20	22:20:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/20	22:20:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/20	22:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/20	22:58:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/20	22:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/20	23:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/20	23:58:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/20	23:58:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/20	23:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/21	00:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/21	00:28:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/21	00:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/21	01:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	01:36:32.0	XRT_CTRL_MANU_402_OG [0x192]							

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2015/06/21	01:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	01:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/21	01:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/21	02:07:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/21	02:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/06/21	03:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/21	03:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	03:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	03:13:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/21	03:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/21	03:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/21	03:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/06/21	04:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/21	04:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	04:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	04:44:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/21	04:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/21	05:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/21	05:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/06/21	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/21	05:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	05:56:00.5	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/06/21	05:56:16.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/06/21	05:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/06/21	05:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/06/21	05:58:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/21	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2015/06/21	06:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/21	06:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	06:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	06:06:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/06/21	06:06:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/06/21	06:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/06/21	06:06:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/06/21	06:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/06/21	06:06:26.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/21	06:08:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/21	06:08:58.0	XRT_FL_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2015/06/21	06:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2015/06/21	06:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/21	06:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	06:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21	06:24:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/21	06:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/21	07:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/21	07:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/06/21	08:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/21	08:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/21			MDP_XRT_CTRL_MANU	1	07-F0	c1	

2015/06/21	08:04:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/21	08:04:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/21	08:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/21	08:40:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/21	08:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/21	09:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	09:44:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	09:44:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/21	09:44:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/21	09:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/21	10:19:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/21	10:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/21	11:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	11:27:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	11:27:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/21	11:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/21	11:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/21	11:57:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/21	11:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/21	13:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	13:05:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	13:05:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/21	13:05:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/21	13:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/21	13:36:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/21	13:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/21	14:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	14:43:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	14:43:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/21	14:43:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/21	14:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/21	15:31:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/21	15:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/21	16:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	16:22:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	16:22:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/21	16:22:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/21	16:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/21	17:08:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/21	17:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/21	17:45:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	17:45:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/06/21	17:45:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/06/21	17:45:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/21	17:48:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/21	17:48:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/21	17:48:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2015/06/21	17:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/21	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/21	17:55:26.0	XRT_CTRL_MANU_402_OG [0x192]							

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2015/06/21	17:55:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2015/06/21	17:55:30.0	AOCS_OrE-point_Start_1_OG [0x097]				
		AOCU_NM		5	02-76	03 00 00 00 00
2015/06/21	17:55:48.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA		1	07-F0	d8
2015/06/21	17:55:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8
2015/06/21	17:55:52.0	XRT_AEC_RESET_441_OG [0x1b9]				
		MDP_XRT_AEC_RESET		1	07-F0	d0
2015/06/21	17:55:54.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS		1	07-F0	d5
2015/06/21	17:55:56.0	XRT_FLD_RESET_449_OG [0x1c1]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/06/21	17:58:26.0	XRT_QT_PROG_SET_406_OG [0x196]				
		MDP_XRT_QT_PROG_SET		2	07-F0	c4 05
2015/06/21	17:58:28.0	XRT_FL_PROG_SET_413_OG [0x19d]				
		MDP_XRT_FL_PROG_SET		2	07-F0	c5 13
2015/06/21	18:00:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/21	18:00:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/21	18:00:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/06/21	18:00:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/06/21	18:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9
2015/06/21	18:44:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/21	18:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/06/21	19:39:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/21	19:39:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/21	19:39:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/06/21	19:39:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/06/21	19:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9
2015/06/21	20:22:00.0	XRT_Custom_430_OG [0x1ae]				
2015/06/21	20:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/06/21	21:17:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/21	21:17:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/21	21:17:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/06/21	21:17:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/06/21	21:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9
2015/06/21	21:58:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/21	21:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/06/21	22:55:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/21	22:55:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/21	22:55:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/06/21	22:55:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/06/21	22:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9
2015/06/21	23:28:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/21	23:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/06/22	00:34:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/22	00:34:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/22	00:34:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/06/22	00:34:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/06/22	00:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9
2015/06/22	01:04:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/22	01:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0
2015/06/22	02:12:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/22	02:12:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU		1	07-F0	c1
2015/06/22	02:12:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET		1	07-F0	da
2015/06/22	02:12:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8
2015/06/22	02:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				

2015/06/22	02:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/22	02:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/06/22	03:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/22	03:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	03:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	03:40:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/22	03:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/22	04:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/22	04:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/06/22	05:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/22	05:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	05:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	05:20:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/22	05:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/22	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/22	06:05:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	06:05:30.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/06/22	06:05:46.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/06/22	06:08:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/06/22	06:08:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/06/22	06:08:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/22	06:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2015/06/22	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/22	06:15:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	06:15:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	06:15:30.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/06/22	06:15:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/06/22	06:15:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/06/22	06:15:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/06/22	06:15:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/06/22	06:15:56.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/06/22	06:18:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/22	06:18:28.0	XRT_FL_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2015/06/22	06:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2015/06/22	07:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/22	07:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	07:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	07:00:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/22	07:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/22	07:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/22	07:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2015/06/22	08:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/06/22	08:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	08:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/06/22	08:40:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/06/22	08:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/06/22	09:16:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/06/22	09:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2015/06/22	10:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	10:22:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	10:22:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/22	10:22:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/22	10:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/22	10:55:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/22	10:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/22	12:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	12:02:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	12:02:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/22	12:02:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/22	12:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/22	12:33:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/22	12:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/22	13:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	13:41:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	13:41:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/22	13:41:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/22	13:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/22	14:16:30.5	XRT_Custom_430_OG [0x1ae]							
2015/06/22	14:17:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/22	15:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	15:19:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	15:19:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/22	15:19:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/22	15:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/22	16:06:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/22	16:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/22	16:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	16:58:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	16:58:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/22	16:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/22	17:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/22	17:43:30.5	XRT_Custom_430_OG [0x1ae]							
2015/06/22	17:44:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/22	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/06/22	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/06/22	18:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/22	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/22	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/22	18:02:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2015/06/22	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/22	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/22	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/06/22	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/06/22	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/22	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				

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2015/06/22	18:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/06/22	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/22	18:10:26.0	XRT_FLD_RESET_449_OG [0x1c1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/22	18:12:56.0	XRT_QT_PROG_SET_406_OG [0x196]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/06/22	18:12:58.0	XRT_FL_PROG_SET_413_OG [0x19d]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/06/22	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/22	18:36:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/22	18:36:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/22	18:36:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/22	18:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/22	18:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/22	19:20:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/22	19:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/22	20:14:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/22	20:14:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/22	20:14:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/22	20:14:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/22	20:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/22	20:57:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/22	20:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/22	21:53:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/22	21:53:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/22	21:53:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/22	21:53:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/22	21:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/22	22:33:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/22	22:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/22	23:31:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/22	23:31:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/22	23:31:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/22	23:31:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/22	23:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	00:02:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/23	00:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	01:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	01:10:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	01:10:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	01:10:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	01:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	01:40:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/23	01:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	02:47:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	02:47:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	02:47:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	02:47:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	02:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	03:18:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/23	03:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	04:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	04:16:32.0	XRT_CTRL_MANU_402_OG [0x192]			

2015/06/23	04:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/23	04:16:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/23	04:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/23	04:57:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/23	04:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2015/06/23	05:29:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/23	05:29:26.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/23	05:29:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_RECAL	2	07-F8	78 00				
2015/06/23	05:33:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/06/23	05:33:46.0	XRT_FLD_DIS_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/06/23	05:34:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/23	05:34:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/23	05:34:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/23	05:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2015/06/23	05:39:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/23	05:39:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/23	05:39:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/23	05:39:30.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/06/23	05:39:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00				
2015/06/23	05:39:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/23	05:39:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/23	05:39:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/23	05:39:56.0	XRT_FLD_RESET_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/23	05:42:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/23	05:42:28.0	XRT_FL_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/06/23	05:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/06/23	05:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/23	05:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/23	05:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/23	05:57:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/23	06:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/23	06:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/23	06:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2015/06/23	07:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/23	07:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/23	07:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/23	07:37:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/23	07:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/23	08:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/23	08:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2015/06/23	08:35:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/23	10:24:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
		AOCU_NM		5	02-76	00 00 00 00 00				