

XRT Timeline to be uploaded on 2015/06/23

Period: 2015/06/23 10:14:00 - 2015/07/01 09:01:00

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Normal mode

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XOB #1A95: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (2ms)

Term	Pointing (x, y)	Comment
06/23 10:27:00 - 06/23 17:33:30	Track (247.9, 118.2) ^{Ⓜ 06/23 10:24:00}	# OP start + 10min, track AR 12371
06/23 18:31:30 - 06/24 06:03:24	Track (317.5, 118.3) ^{Ⓜ 06/23 18:28:30}	# track AR 12371
06/24 06:16:30 - 06/24 17:54:24	Track (415.1, 118.9) ^{Ⓜ 06/24 06:13:30}	# track AR 12371
06/24 18:54:30 - 06/25 05:29:30	Track (508.0, 120.0) ^{Ⓜ 06/24 18:04:30}	# track AR 12371
06/25 06:27:30 - 06/25 17:07:00	Track (597.6, 121.6) ^{Ⓜ 06/25 06:24:30}	# track AR 12371
06/25 18:07:00 - 06/26 05:38:24	Track (674.4, 123.7) ^{Ⓜ 06/25 18:04:00}	# track AR 12371

PROG= 05 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 30	2-time(s)	2.0sec
Open/G-band	Open/G-band close	Safe Norm 2ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2	2-time(s)	2.0sec
Seqn= 69	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 2ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 42	4-time(s)	2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 62	8-time(s)	160.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A94: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi

Term	Pointing (x, y)	Comment
06/23 18:21:30 - 06/23 18:28:24	Fixed (0.0, 0.0)	synoptic, shifted 18.5 min
06/24 06:06:30 - 06/24 06:13:24	Fixed (0.0, 0.0)	synoptic, shifted 3.5 min
06/24 17:57:30 - 06/24 18:04:24	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min
06/25 06:17:30 - 06/25 06:24:24	Fixed (0.0, 0.0)	synoptic, shifted 14.5 min
06/25 17:57:00 - 06/25 18:03:54	Fixed (0.0, 0.0)	synoptic, shifted -6.0 min
06/26 05:41:30 - 06/26 05:55:00	Fixed (0.0, 0.0)	synoptic, shifted -21.5 min

PROG= 06 1-time(s)

Subr= 1	1-time(s)	12.0sec
Seqn= 33	1-time(s)	4.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 5	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 40	1-time(s)	4.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 39	1-time(s)	2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 91	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 2ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 2ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1A96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
06/23 10:27:00 - 06/23 17:33:30	Track (247.9, 118.2) ^{Ⓜ 06/23 10:24:00}	# OP start + 10min, track AR 12371
06/23 18:31:30 - 06/24 06:03:24	Track (317.5, 118.3) ^{Ⓜ 06/23 18:28:30}	# track AR 12371
06/24 06:16:30 - 06/24 17:54:24	Track (415.1, 118.9) ^{Ⓜ 06/24 06:13:30}	# track AR 12371
06/24 18:54:30 - 06/25 05:29:30	Track (508.0, 120.0) ^{Ⓜ 06/24 18:04:30}	# track AR 12371
06/25 06:27:30 - 06/25 17:07:00	Track (597.6, 121.6) ^{Ⓜ 06/25 06:24:30}	# track AR 12371
06/25 18:07:00 - 06/26 05:38:24	Track (674.4, 123.7) ^{Ⓜ 06/25 18:04:00}	# track AR 12371

PROG= 19 30-time(s)													
Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 80 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	2ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

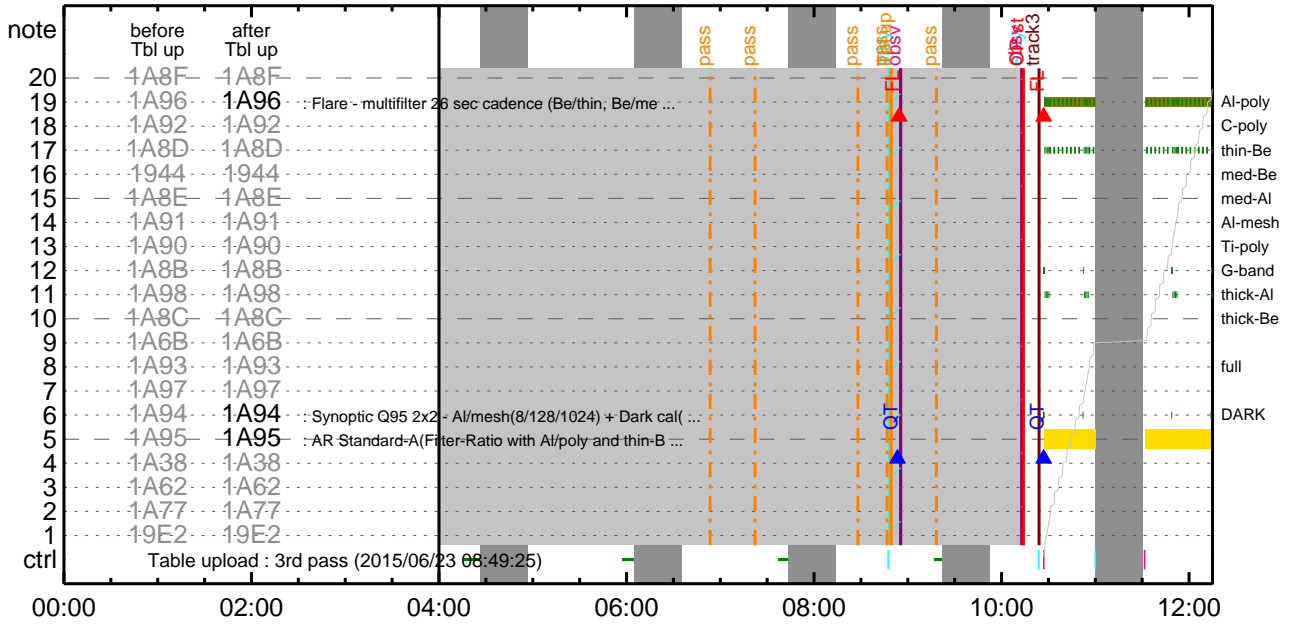
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Flare Detection

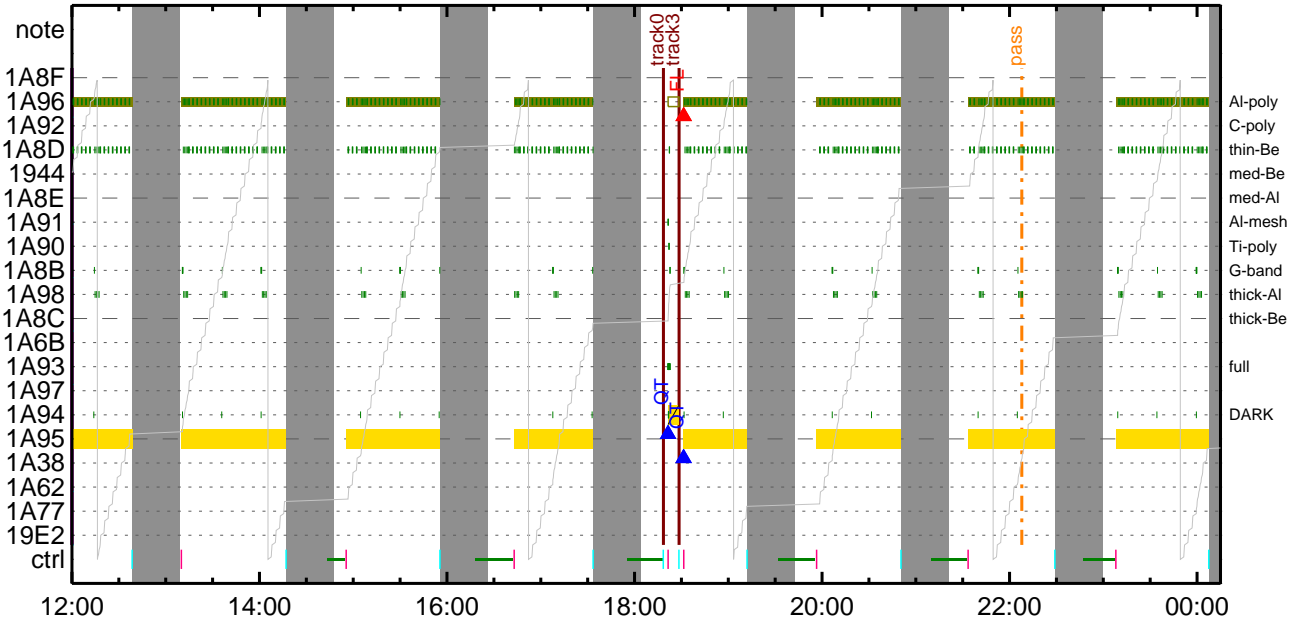
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FLD Patrol												
Term		Pointing (x, y)						Comment				
06/23 18:28:46 - 06/24 06:03:46	Track (317.5, 118.3)	@ 06/23 18:28:30	# track AR 12371									
06/24 06:13:46 - 06/24 17:54:46	Track (415.1, 118.9)	@ 06/24 06:13:30	# track AR 12371									
06/24 18:04:46 - 06/25 06:14:46	Track (508.0, 120.0)	@ 06/24 18:04:30	# track AR 12371									
06/25 06:24:46 - 06/25 17:54:16	Track (597.6, 121.6)	@ 06/25 06:24:30	# track AR 12371									
06/25 18:04:16 - 06/26 05:38:46	Track (674.4, 123.7)	@ 06/25 18:04:00	# track AR 12371									
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

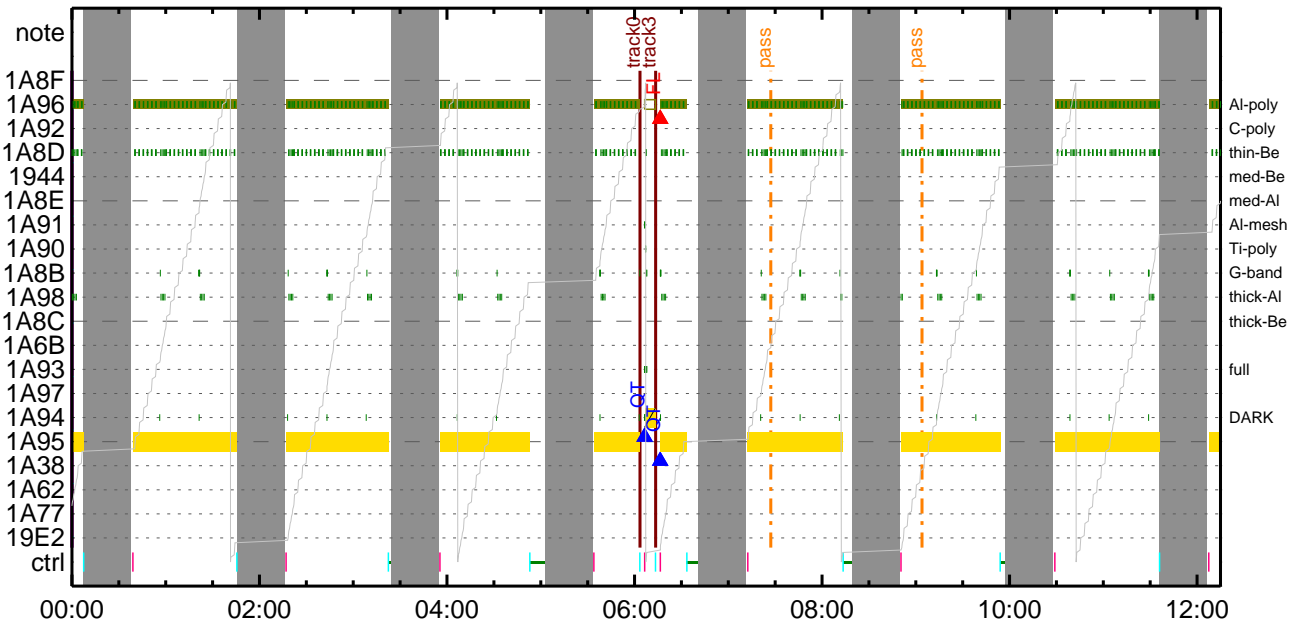
CMDI #0216 2015/06/23



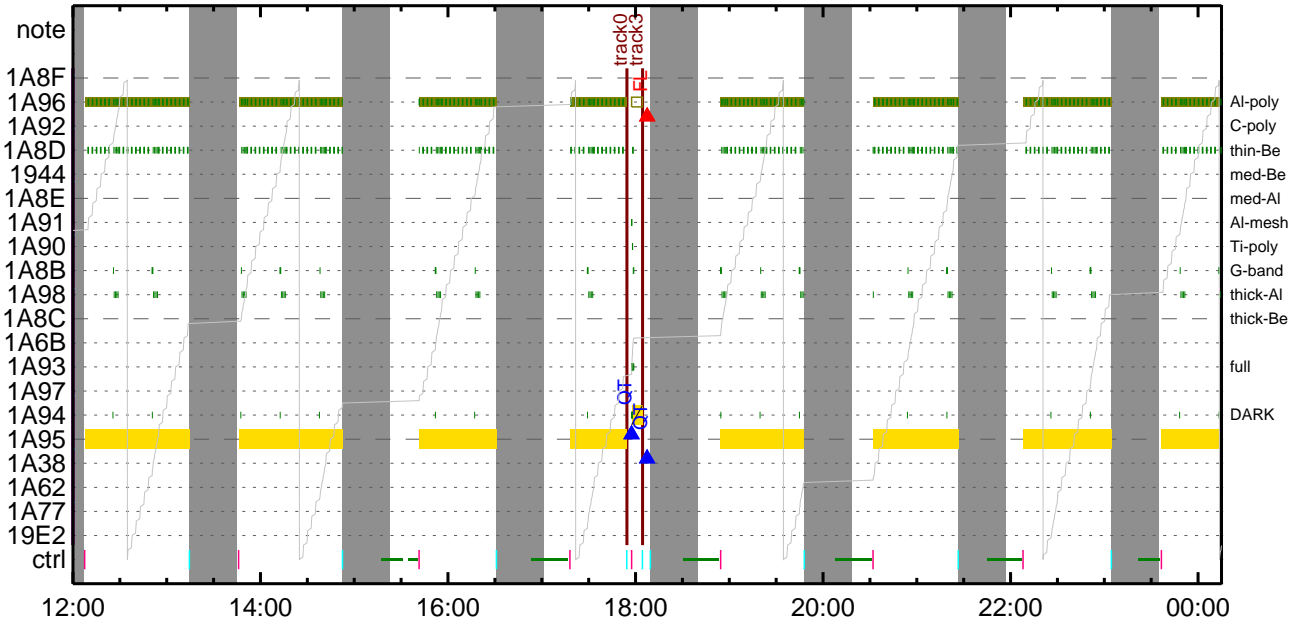
CMDI #0216 2015/06/23



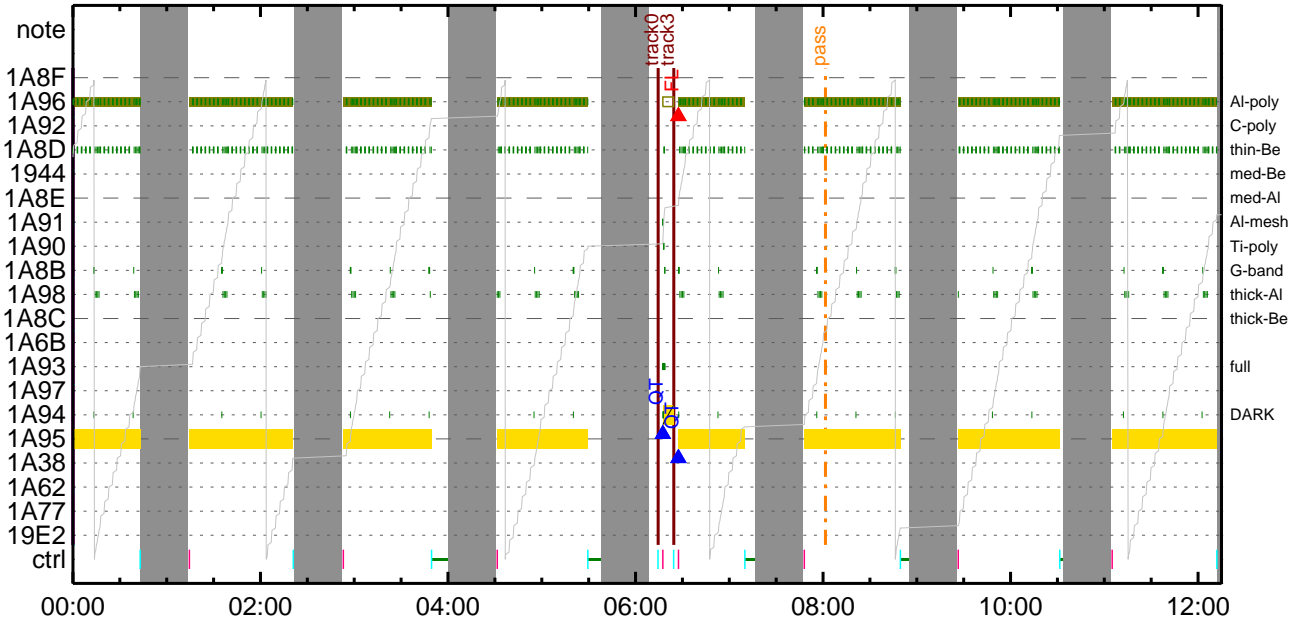
CMDI #0216 2015/06/24



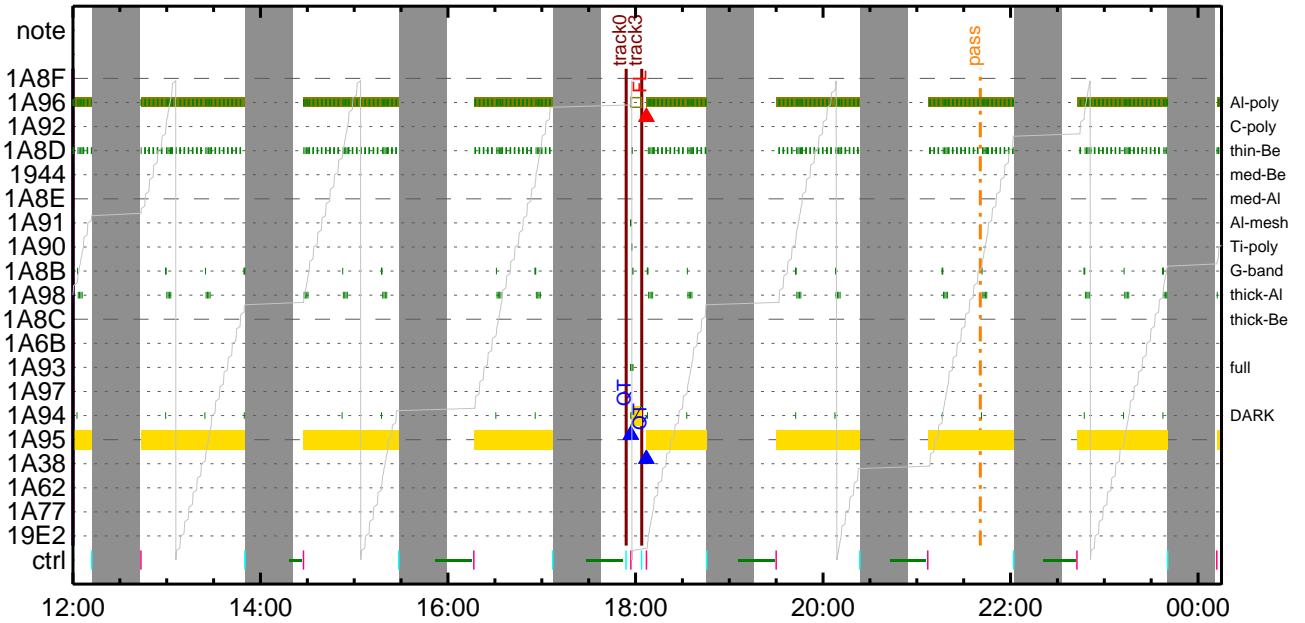
CMDI #0216 2015/06/24



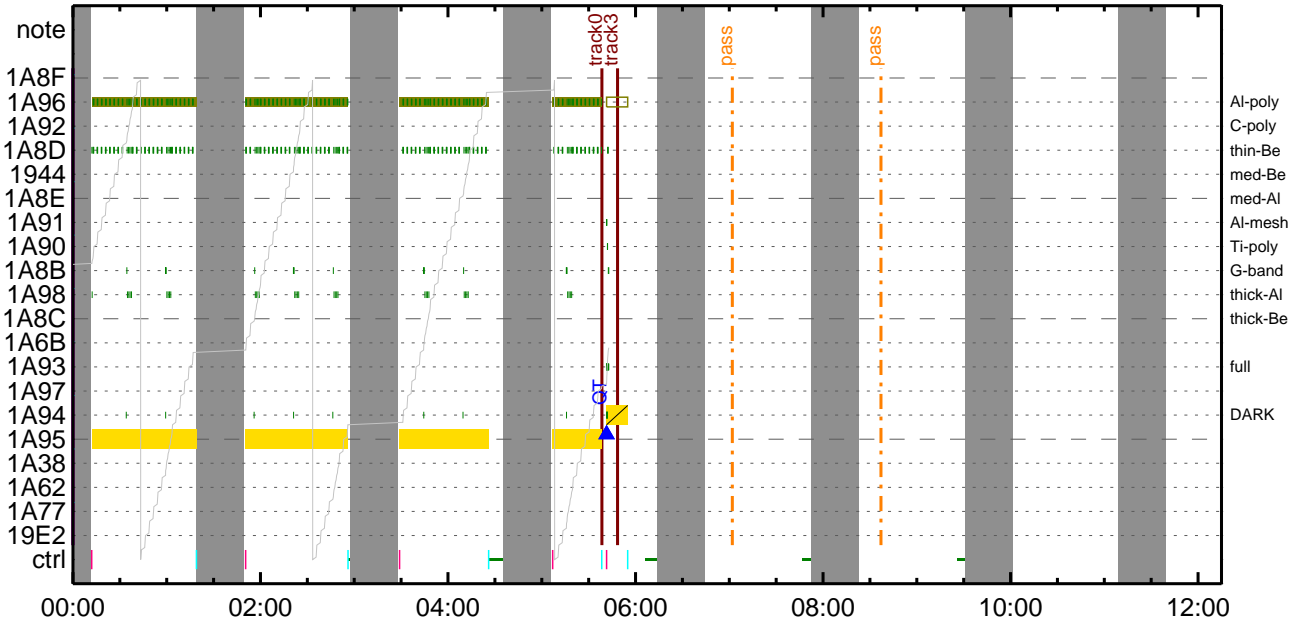
CMDI #0216 2015/06/25



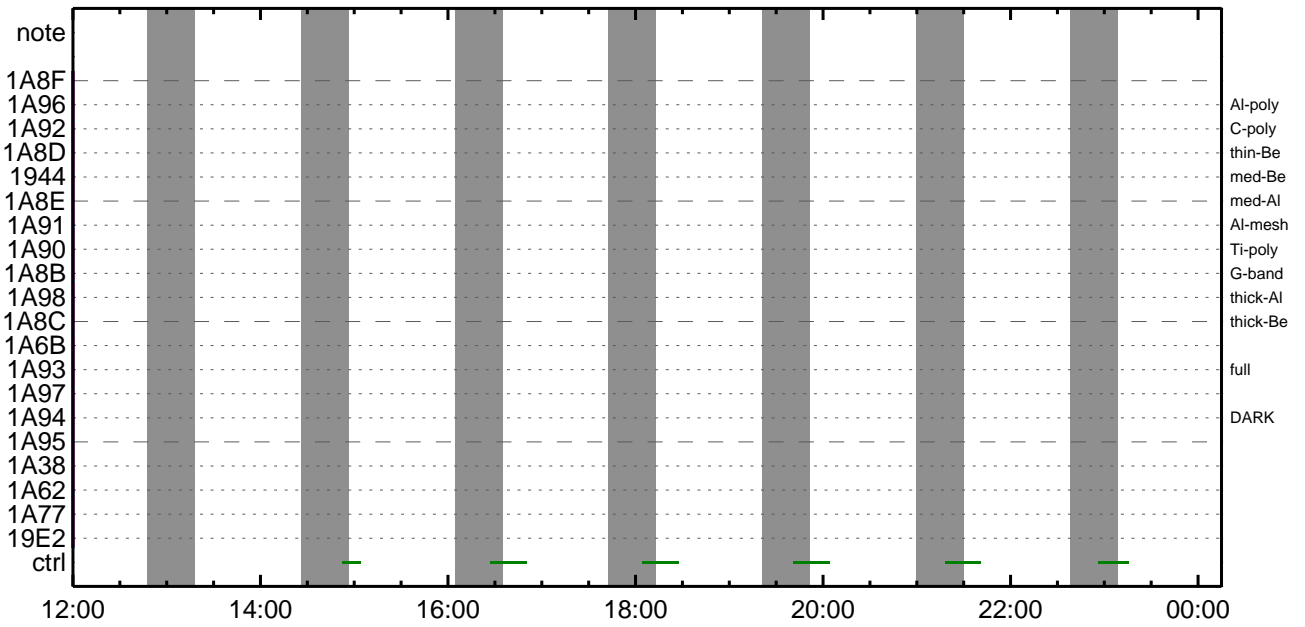
CMDI #0216 2015/06/25



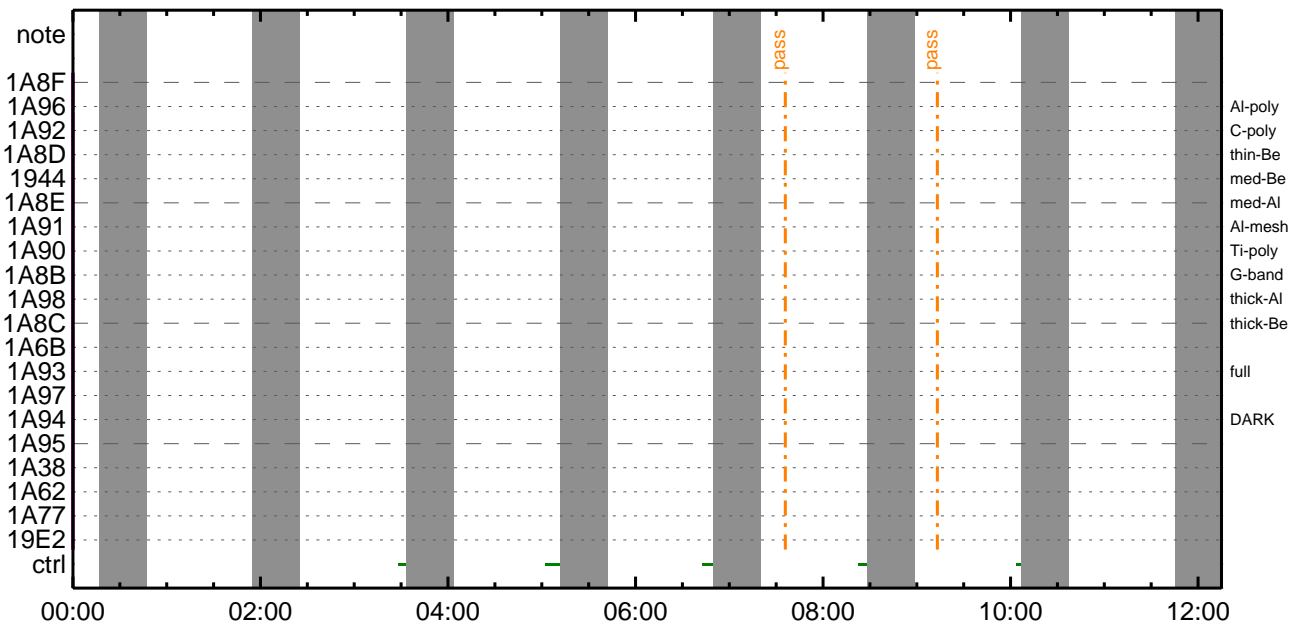
CMDI #0216 2015/06/26



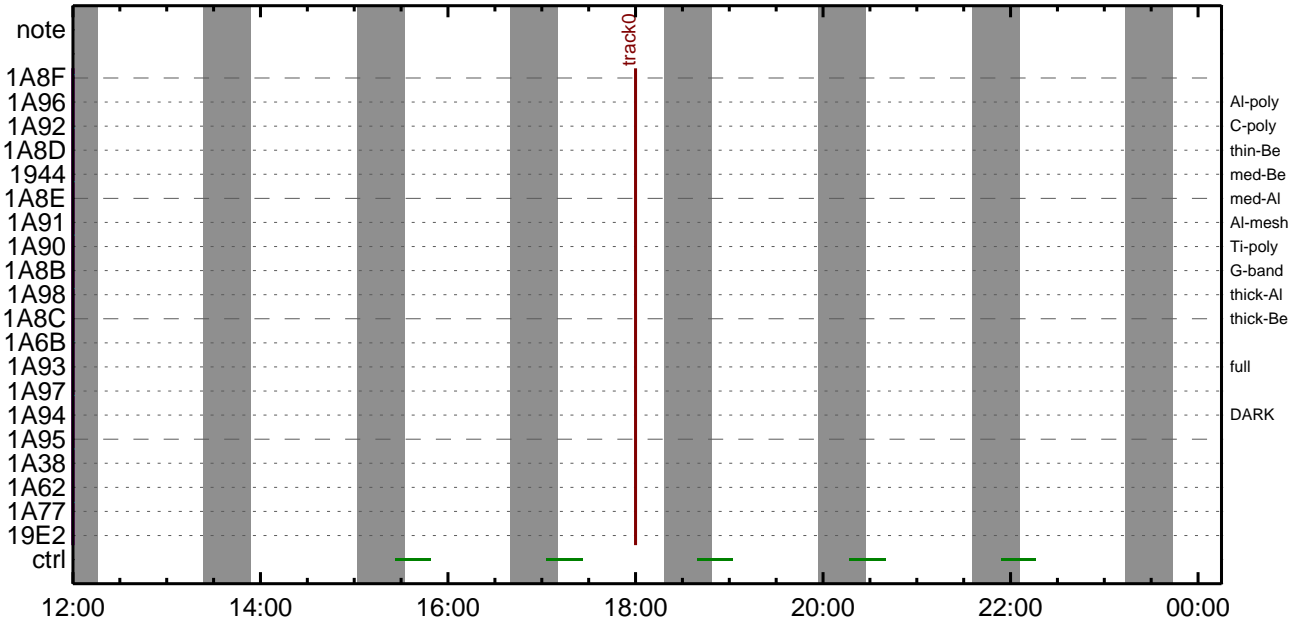
CMDI #0216 2015/06/26



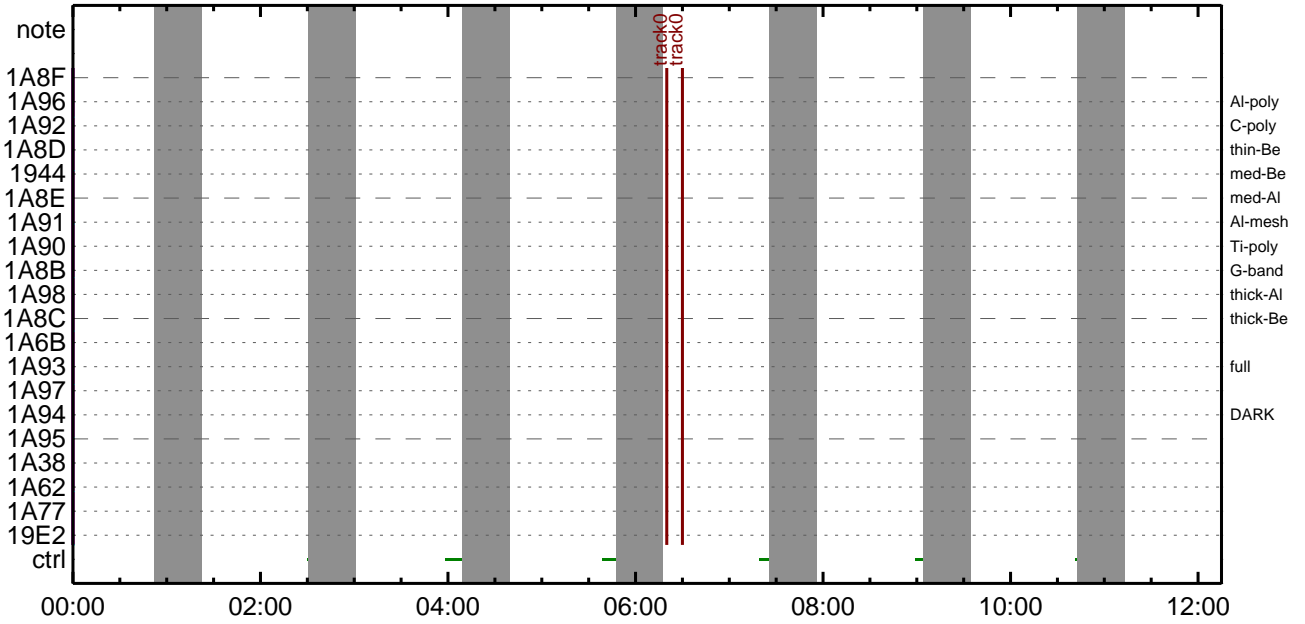
CMDI #0216 2015/06/27



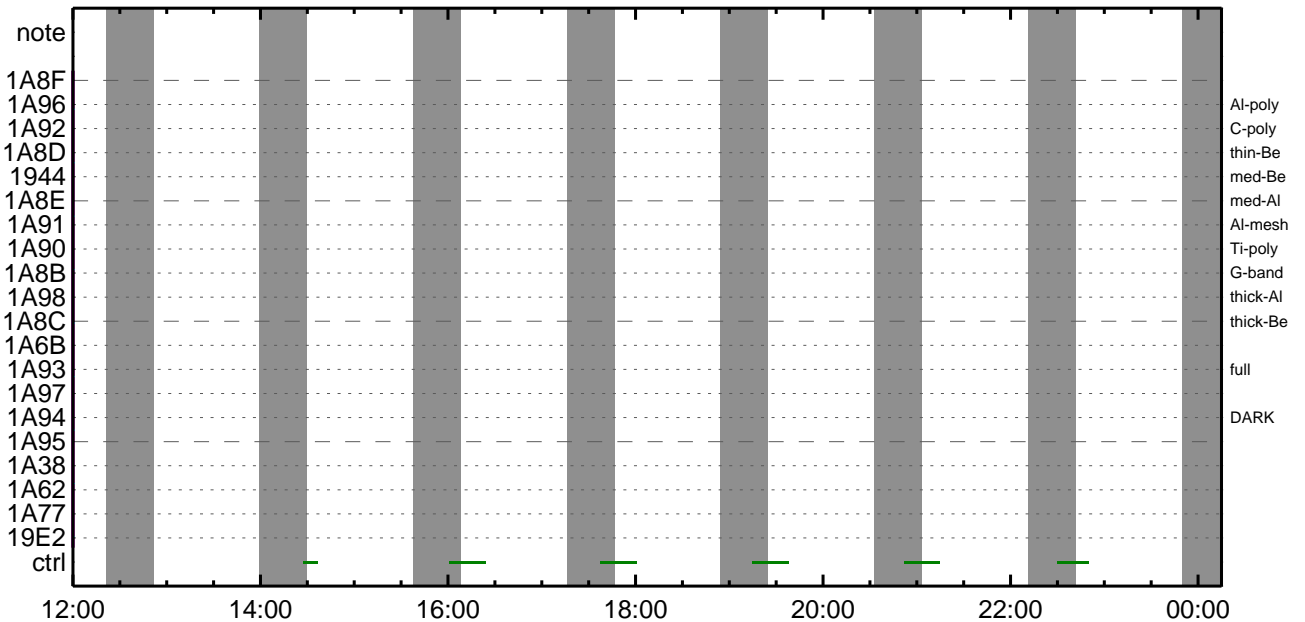
CMDI #0216 2015/06/27



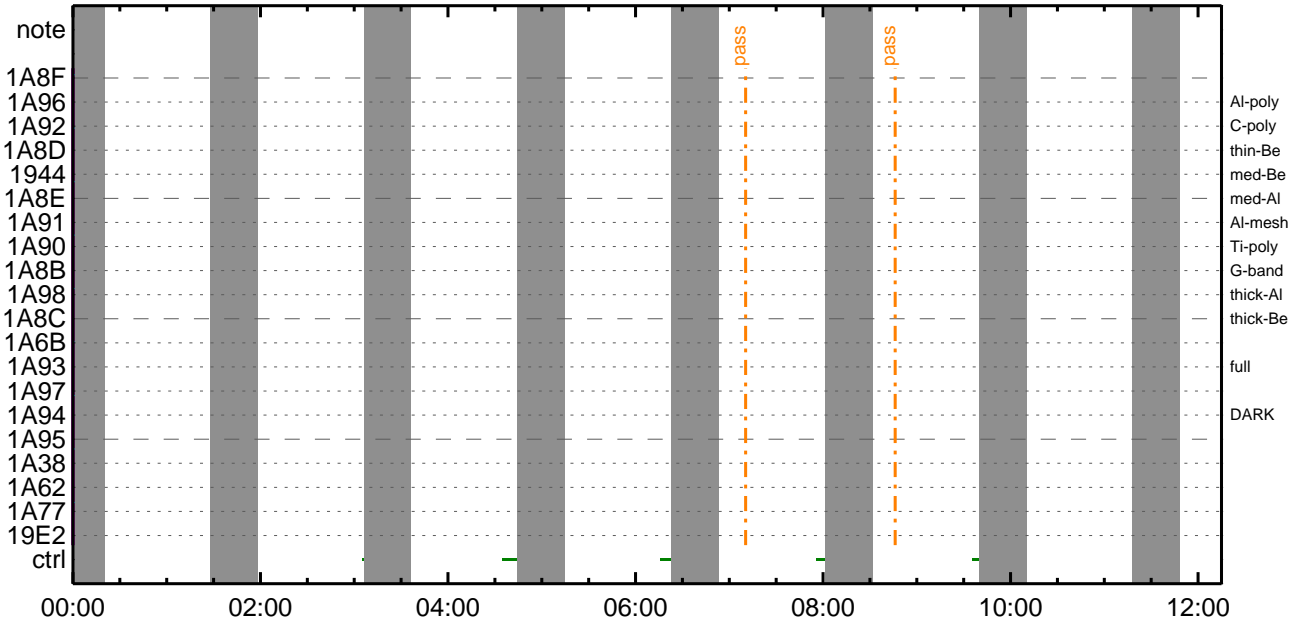
CMDI #0216 2015/06/28



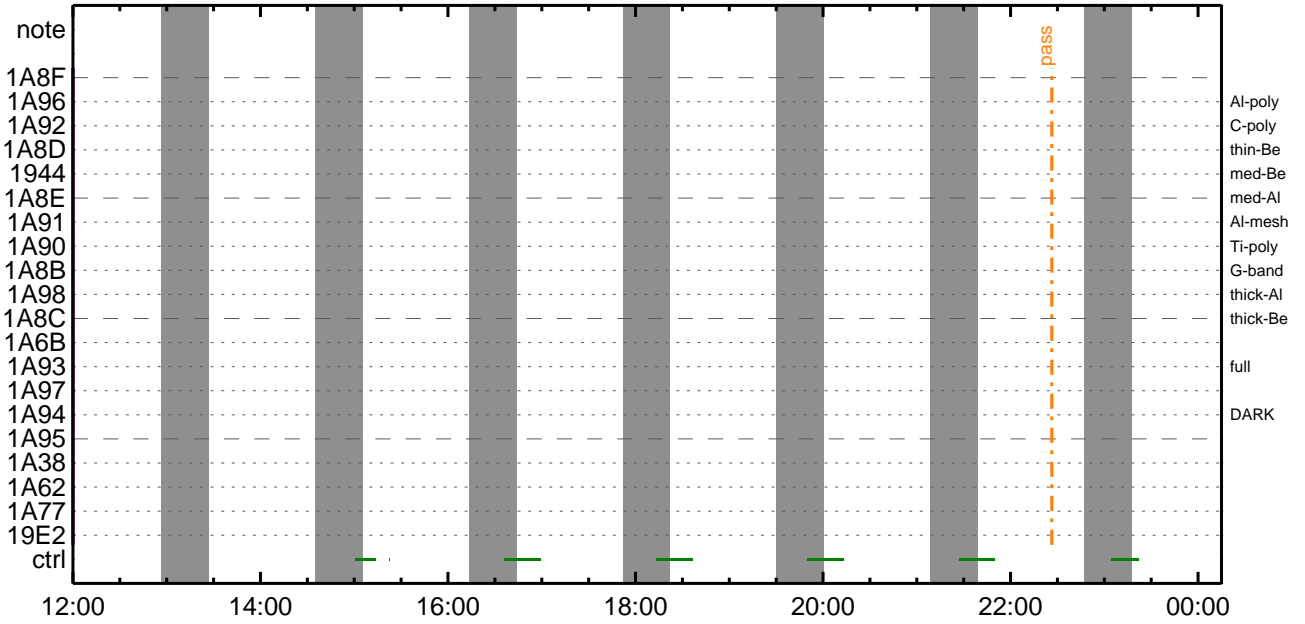
CMDI #0216 2015/06/28



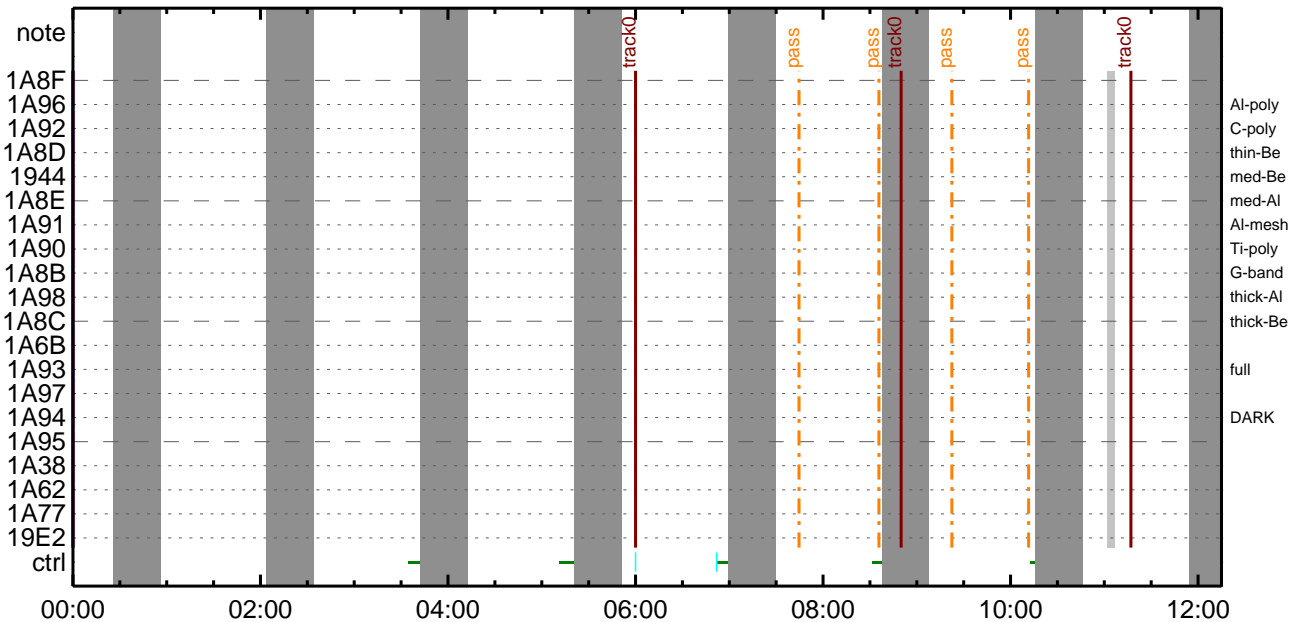
CMDI #0216 2015/06/29



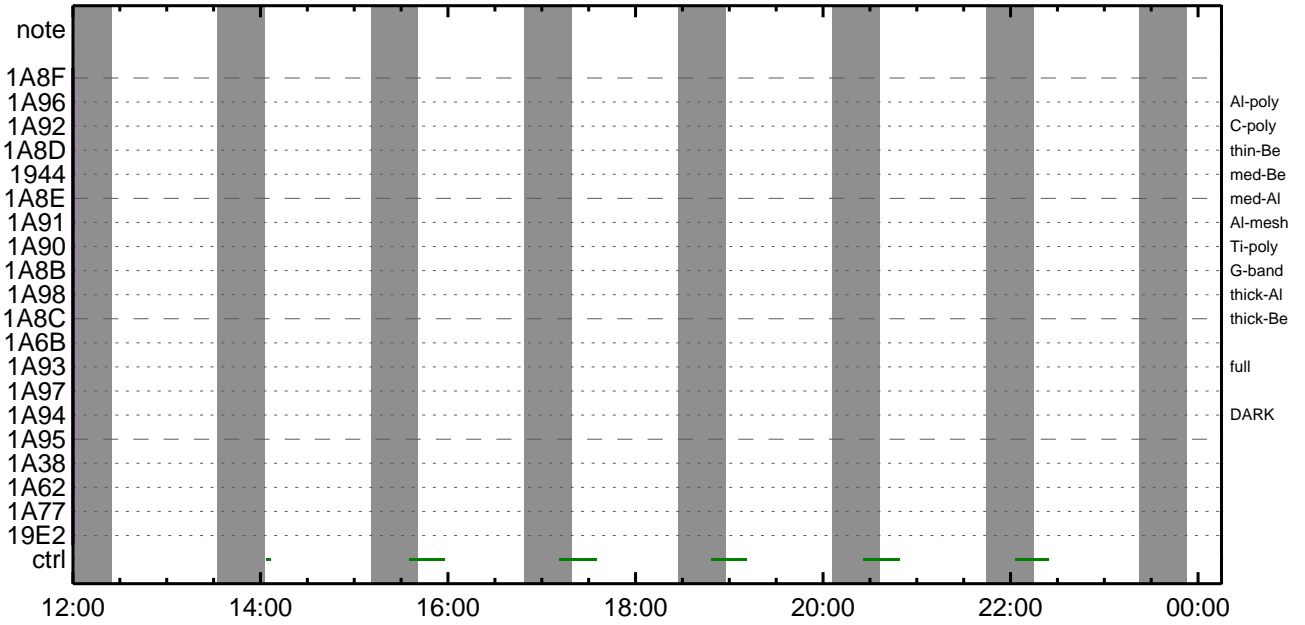
CMDI #0216 2015/06/29



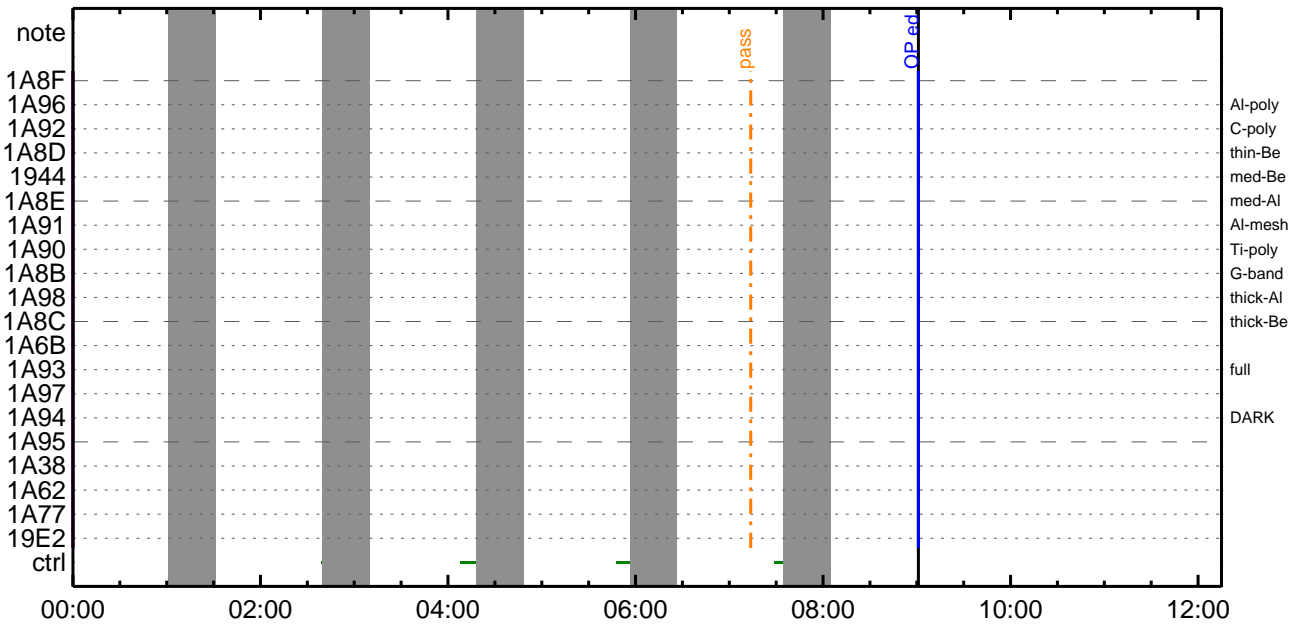
CMDI #0216 2015/06/30



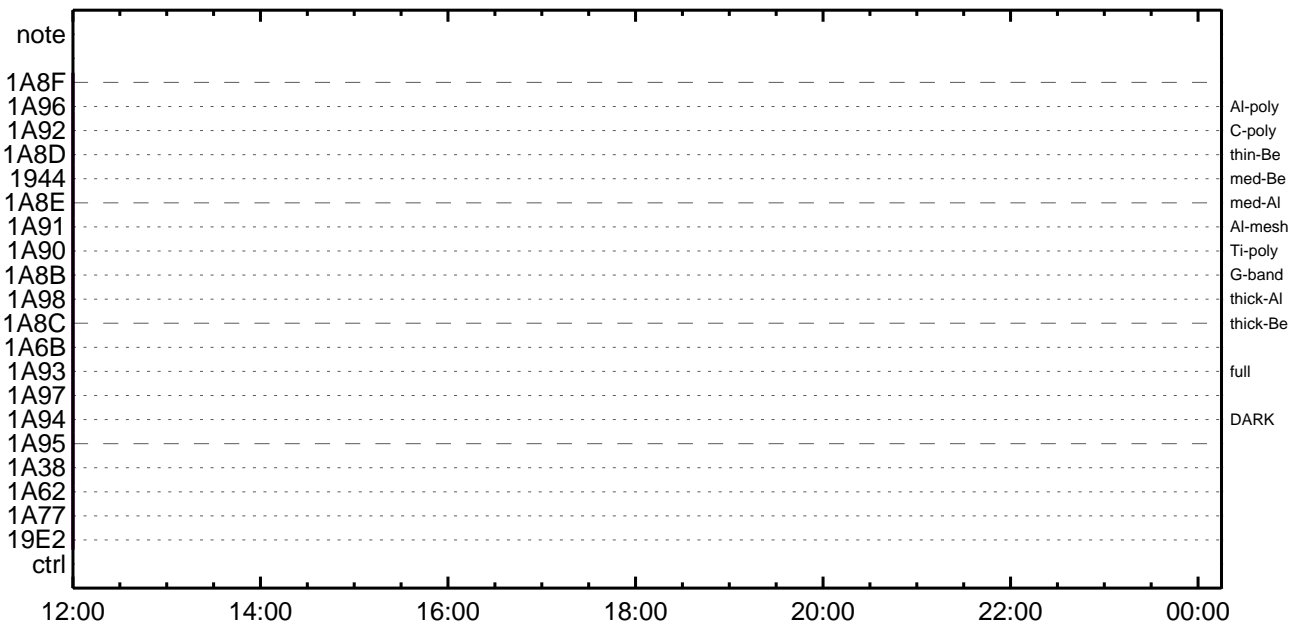
CMDI #0216 2015/06/30



CMDI #0216 2015/07/01



CMDI #0216 2015/07/01




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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C. XRT Obs. Table Upload
0098 . S. RAM ram-291:MDP_OBS_X
0099 ( )
0100 C.
0101 +. DC 07-F0 MDP_DUMP_XRTTBL
0102 BC (84 07 00 00 00 3a d4)
0103 . C. ----- Comparison Check ? OK / ERR ____
0104 C.
0105 C.
0106 +. DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 01 b1 b1 04 04)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 02 b1 b1 08 08)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 03 b1 b1 08 08)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 04 b1 b1 06 06)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 05 85 83 06 06)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 06 85 83 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 07 85 83 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 08 80 80 20 20)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 09 80 80 20 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 0a 80 80 08 20)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 0f 80 80 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 10 80 80 08 08)
0130 + DC 07-F0 MDP_XRT_FLD_ENA
0131 BC (d8)
0132 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0133 BC (c8)
0134 + DC 07-F0 MDP_XRT_AEC_RESET
0135 BC (d0)
0136 + DC 07-F0 MDP_XRT_ARS_DIS
0137 BC (d5)
0138 + DC 07-F0 MDP_XRT_FLD_RESET
0139 BC (da)
0140 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0141 BC (c4 05)
0142 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0143 BC (c5 13)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C.
0147 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0148 C.
0149 +. DC 07-F0 MDP_XRT_MODE_OBSV
0150 BC (c2)
0151 +. TI 2015-06-23 10:13:02.0
0152 DC 07-F0 MDP_XRT_MODE_OBSV
0153 BC (c2)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C. ***** XRT END *****
0157 C.
0158 . C. ***** MDP (ũãîñî»ö¼ÿñÊÄðñ¹ñèDCBC•x²è *****
0159 C. (¼ã°îÿÓÿÄÿËÿÏÿËÿÄÿÇÿËÿÈÿ¼ñ¼Ä»Ûñ¹ñè)
0160 . S. DC-BC dcbc-402:DCBC
0161 (MDP_known_event)
0162 C.
0163 C.
0164 . C. ***** ÿDÿ¹•Ï Daily±¿ÎññÉ´Øñ¹ñèDCBC•x²è *****
0165 . S. DC-BC dcbc-153:DCBC
0166 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0167 C.
0168 C.
0169 . C. ;ãLOSÿÁÿÏÿÄÿÿ-¼Ä»Û;ã
0170 C.
0171 . C. ***** LOS *****
0172 C.

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Jun 23, 15 13:11

XRT_OGLIST_0216.chk

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*** OP Sequence for XRT ***

2015/06/23	10:23:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	10:23:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/06/23	10:24:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2015/06/23	10:24:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/06/23	10:24:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/06/23	10:24:20.5	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/06/23	10:24:22.5	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/23	10:24:24.5	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	10:26:56.5	XRT_QT_PROG_SET_406_OG [0x196]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/06/23	10:26:58.5	XRT_FL_PROG_SET_413_OG [0x19d]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/06/23	10:27:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	11:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	11:00:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	11:00:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	11:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	11:30:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/23	11:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	12:38:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	12:38:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	12:38:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	12:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	13:09:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/23	13:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	14:17:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	14:17:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	14:17:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	14:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	14:54:30.5	XRT_Custom_430_OG [0x1ae]			
2015/06/23	14:55:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	15:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	15:55:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	15:55:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	15:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	16:42:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/23	16:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	17:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	17:33:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	17:33:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	17:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	18:18:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	18:18:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/06/23	18:18:30.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/06/23	18:18:46.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/06/23	18:21:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/06/23	18:21:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/23	18:21:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/06/23	18:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	18:28:24.0	XRT_CTRL_MANU_402_OG [0x192]			

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2015/06/23	18:28:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2015/06/23	18:28:30.0	AOCS_OrE-point_Start_1_OG [0x097]		5	02-76	03 00 00 00 00
2015/06/23	18:28:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/06/23	18:28:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/06/23	18:28:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/06/23	18:28:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/23	18:28:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	18:31:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/06/23	18:31:28.0	XRT_FL_PROG_SET_413_OG [0x19d]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/06/23	18:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	19:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	19:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	19:12:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	19:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	19:55:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/23	19:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	20:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	20:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	20:50:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	20:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	21:32:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/23	21:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/23	22:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/23	22:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/23	22:29:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/23	22:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/23	23:07:00.0	XRT_Custom_430_OG [0x1ae]				
2015/06/23	23:08:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	00:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	00:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	00:07:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/24	00:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/24	00:38:00.0	XRT_Custom_430_OG [0x1ae]				
2015/06/24	00:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	01:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	01:45:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	01:45:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/24	01:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/24	02:16:00.0	XRT_Custom_430_OG [0x1ae]				
2015/06/24	02:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	03:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	03:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	03:22:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/24	03:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/24	03:54:30.0	XRT_Custom_430_OG [0x1ae]				
2015/06/24	03:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	04:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	04:53:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	04:53:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2015/06/24	04:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/24	05:33:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/24	05:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	06:03:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/06/24	06:03:30.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/06/24	06:03:46.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/06/24	06:06:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/06/24	06:06:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/24	06:06:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/06/24	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	06:13:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	06:13:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/06/24	06:13:30.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2015/06/24	06:13:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/06/24	06:13:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/06/24	06:13:50.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/06/24	06:13:52.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/24	06:13:54.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	06:16:26.0	XRT_QT_PROG_SET_406_OG [0x196]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/06/24	06:16:28.0	XRT_FL_PROG_SET_413_OG [0x19d]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/06/24	06:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	06:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	06:33:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	06:33:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/24	06:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/24	07:11:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/24	07:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	08:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	08:13:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	08:13:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/24	08:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/24	08:49:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/24	08:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	09:54:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	09:54:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	09:54:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/24	09:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/24	10:28:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/24	10:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	11:36:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	11:36:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	11:36:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/24	11:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/24	12:06:30.0	XRT_Custom_430_OG [0x1ae]			
2015/06/24	12:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/24	13:14:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/24	13:14:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/24	13:14:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2015/06/24	13:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/24	13:45:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/24	13:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/24	14:52:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/24	14:52:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/24	14:52:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/24	14:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/24	15:40:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/24	15:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/24	16:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/24	16:31:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/24	16:31:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/24	16:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/24	17:17:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/24	17:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/24	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/24	17:54:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/06/24	17:54:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/06/24	17:54:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/24	17:57:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/24	17:57:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/24	17:57:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2015/06/24	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/24	18:04:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/24	18:04:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/06/24	18:04:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/06/24	18:04:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/06/24	18:04:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/06/24	18:04:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/06/24	18:04:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/24	18:04:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/24	18:07:26.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/06/24	18:07:28.0	XRT_FL_PROG_SET_413_OG [0x19d]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/06/24	18:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/24	18:09:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/24	18:09:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/24	18:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/24	18:53:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/24	18:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/24	19:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/24	19:48:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/24	19:48:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/24	19:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/24	20:31:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/24	20:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/24	21:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/24	21:26:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/24	21:26:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/24	21:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2015/06/24	22:07:00.0	XRT_Custom_430_OG [0x1ae]					
2015/06/24	22:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/24	23:04:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/24	23:04:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/24	23:04:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/24	23:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/24	23:35:30.0	XRT_Custom_430_OG [0x1ae]					
2015/06/24	23:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/25	00:43:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/25	00:43:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/25	00:43:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/25	00:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/25	01:13:30.0	XRT_Custom_430_OG [0x1ae]					
2015/06/25	01:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/25	02:21:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/25	02:21:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/25	02:21:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/25	02:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/25	02:52:00.0	XRT_Custom_430_OG [0x1ae]					
2015/06/25	02:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/25	03:49:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/25	03:49:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/25	03:49:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/25	03:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/25	04:30:30.0	XRT_Custom_430_OG [0x1ae]					
2015/06/25	04:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/25	05:29:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/25	05:29:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/25	05:29:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/06/25	05:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/06/25	06:14:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/25	06:14:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/06/25	06:14:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2015/06/25	06:14:46.0	XRT_FLD_DIS_418_OG [0x1a2]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/06/25	06:17:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/06/25	06:17:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/06/25	06:17:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2015/06/25	06:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/06/25	06:24:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/06/25	06:24:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/06/25	06:24:30.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2015/06/25	06:24:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/06/25	06:24:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/06/25	06:24:50.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/06/25	06:24:52.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/06/25	06:24:54.0	XRT_FLD_RESET_443_OG [0x1bb]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/06/25	06:27:26.0	XRT_QT_PROG_SET_406_OG [0x196]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2015/06/25	06:27:28.0	XRT_FL_PROG_SET_413_OG [0x19d]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2015/06/25	06:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2015/06/25	07:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/25	07:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/25	07:10:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/25	07:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/25	07:47:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/25	07:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/25	08:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/25	08:49:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/25	08:49:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/25	08:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/25	09:25:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/25	09:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/25	10:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/25	10:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/25	10:31:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/25	10:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/25	11:04:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/25	11:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/25	12:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/25	12:12:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/25	12:12:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/25	12:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/25	12:42:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/25	12:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/25	13:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/25	13:50:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/25	13:50:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/25	13:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/25	14:26:30.5	XRT_Custom_430_OG [0x1ae]							
2015/06/25	14:27:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/25	15:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/25	15:28:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/25	15:28:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/25	15:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/25	16:15:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/25	16:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/25	17:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/25	17:07:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/06/25	17:07:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/06/25	17:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/06/25	17:53:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/25	17:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/06/25	17:54:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/06/25	17:54:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/06/25	17:56:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/06/25	17:56:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/06/25	17:56:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2015/06/25	17:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/06/25	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/06/25	18:03:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							

2015/06/25	18:04:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	03	00	00	00
2015/06/25	18:04:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/06/25	18:04:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/06/25	18:04:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/06/25	18:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/06/25	18:04:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/25	18:06:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2015/06/25	18:06:58.0	XRT_FL_PROG_SET_413_OG [0x19d]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2015/06/25	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/25	18:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/25	18:45:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/25	18:45:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/25	18:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/25	19:29:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/25	19:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/25	20:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/25	20:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/25	20:23:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/25	20:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/25	21:06:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/25	21:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/25	22:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/25	22:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/25	22:02:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/25	22:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/25	22:41:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/25	22:42:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/25	23:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/25	23:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/25	23:40:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/25	23:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/26	00:11:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/26	00:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/26	01:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/26	01:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/26	01:19:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/26	01:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/26	01:49:30.0	XRT_Custom_430_OG [0x1ae]							
2015/06/26	01:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/26	02:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/26	02:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/26	02:56:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/26	02:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/06/26	03:28:00.0	XRT_Custom_430_OG [0x1ae]							
2015/06/26	03:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/06/26	04:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/06/26	04:26:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/06/26	04:26:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/06/26	04:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2015/06/26	05:06:00.0	XRT_Custom_430_OG [0x1ae]			
2015/06/26	05:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/26	05:38:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/26	05:38:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/06/26	05:38:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/06/26	05:38:46.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/06/26	05:41:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/06/26	05:41:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/06/26	05:41:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/06/26	05:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/06/26	05:48:30.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2015/06/26	05:55:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/26	06:00:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_432_OG [0x1b0]			
		TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2015/06/27	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 f4 19 b0 cd
2015/06/28	06:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/06/28	06:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 f4 19 b0 cd
2015/06/30	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/06/30	06:00:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/30	06:00:02.5	XRT_TCIB_XRT_S_HTR_A_DIS_447_OG [0x1bf]			
		TCIB_XRT_S_HTR_A_DIS	0	04-C0	
2015/06/30	06:52:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/06/30	06:52:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/06/30	06:52:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/06/30	06:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/06/30	08:50:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 f4 19 b0 cd
2015/06/30	11:17:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00