

# XRT Timeline to be uploaded on 2015/07/07

Period: 2015/07/07 09:52:00 - 2015/07/16 10:19:00

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Normal mode

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## XOB #1AAE: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)

Term	Pointing (x, y)	Comment
07/07 10:05:00 - 07/07 17:43:00	Track ( -396.6, 134.8) <sup>Ⓜ 07/07 10:02:00</sup>	# OP start + 10min - AR 12381
07/07 18:41:00 - 07/08 05:58:54	Track ( -325.8, 132.4) <sup>Ⓜ 07/07 18:38:00</sup>	# AR 12381
07/08 06:12:00 - 07/08 17:59:54	Track ( -227.5, 129.7) <sup>Ⓜ 07/08 06:09:00</sup>	# AR 12381
07/08 18:13:00 - 07/09 06:22:54	Track ( -121.9, 127.6) <sup>Ⓜ 07/08 18:10:00</sup>	# AR 12381
07/09 06:36:00 - 07/09 17:16:00	Track ( -11.3, 126.3) <sup>Ⓜ 07/09 06:33:00</sup>	# AR 12381
07/09 18:14:30 - 07/10 05:47:54	Track ( 92.8, 125.7) <sup>Ⓜ 07/09 18:11:30</sup>	# AR 12381
07/10 06:01:00 - 07/10 17:37:24	Track ( 196.8, 126.0) <sup>Ⓜ 07/10 05:58:00</sup>	# AR 12381
07/10 18:37:30 - 07/11 05:59:54	Track ( 298.8, 127.1) <sup>Ⓜ 07/10 17:47:30</sup>	# AR 12381
07/11 06:13:00 - 07/11 08:32:00	Track ( 401.2, 129.1) <sup>Ⓜ 07/11 06:10:00</sup>	# AR 12381
07/11 15:03:00 - 07/11 18:06:24	Track ( 470.9, 131.1) <sup>Ⓜ 07/11 15:00:00</sup>	# AR 12381
07/11 18:19:30 - 07/12 06:31:54	Track ( 495.9, 131.9) <sup>Ⓜ 07/11 18:16:30</sup>	# AR 12381
07/12 06:45:00 - 07/12 08:59:54	Track ( 586.0, 135.7) <sup>Ⓜ 07/12 06:42:00</sup>	# AR 12381
07/12 15:03:00 - 07/12 17:25:00	Track ( 641.5, 138.7) <sup>Ⓜ 07/12 15:00:00</sup>	# AR 12381

**PROG= 02 Inf.-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 9</b>	<b>2-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
<b>Seqn= 92</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 42</b>	<b>4-time(s)</b>	<b>2.0sec</b>										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 62</b>	<b>20-time(s)</b>	<b>90.0sec</b>										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

## XOB #1A99: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Thi

Term	Pointing (x, y)	Comment
07/07 18:31:00 - 07/07 18:37:54	Fixed ( 0.0, 0.0)	synoptic, shifted 28.0 min
07/08 06:02:00 - 07/08 06:08:54	Fixed ( 0.0, 0.0)	synoptic, shifted -1.0 min
07/08 18:03:00 - 07/08 18:09:54	Fixed ( 0.0, 0.0)	synoptic
07/09 06:26:00 - 07/09 06:32:54	Fixed ( 0.0, 0.0)	synoptic, shifted 23.0 min
07/09 18:04:30 - 07/09 18:11:24	Fixed ( 0.0, 0.0)	synoptic, shifted 1.5 min
07/10 05:51:00 - 07/10 05:57:54	Fixed ( 0.0, 0.0)	synoptic, shifted -12.0 min
07/10 17:40:30 - 07/10 17:47:24	Fixed ( 0.0, 0.0)	synoptic, shifted -22.5 min
07/11 06:03:00 - 07/11 06:09:54	Fixed ( 0.0, 0.0)	synoptic
07/11 18:09:30 - 07/11 18:16:24	Fixed ( 0.0, 0.0)	synoptic, shifted 6.5 min
07/12 06:35:00 - 07/12 06:41:54	Fixed ( 0.0, 0.0)	synoptic, shifted 32.0 min

**PROG= 04 1-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>12.0sec</b>										
<b>Seqn= 33</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 61</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 39</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 54</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms												
Term		Pointing (x, y)					Comment					
07/11 09:08:30 - 07/11 14:59:54		Fixed ( 0.0, -920.0)					# HOP 81 - South Pole					
07/12 09:44:00 - 07/12 14:59:54		Fixed ( 0.0, 900.0)					# HOP 81 - North Pole					
<b>PROG= 06 Inf.-time(s)</b>												
Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 9 2-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 7 1-time(s) 30.0sec												
└─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Subr= 3 30-time(s) 2.0sec												
└─ Seqn= 57 1-time(s) 60.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AAA: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/1024), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45												
Term		Pointing (x, y)					Comment					
07/12 18:15:00 - 07/12 18:21:54		Fixed ( 0.0, 0.0)					synoptic, sfhited +12.0 min					
<b>PROG= 07 1-time(s)</b>												
Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 33 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 61 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 77 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Subr= 2 1-time(s) 12.0sec												
└─ Seqn= 46 2-time(s) 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 25 1-time(s) 4.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 21 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 37 1-time(s) 2.0sec												
└─ med-Al/Open med-Al/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ med-Al/Open med-Al/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 25 1-time(s) 4.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 21 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 37 1-time(s) 2.0sec												
└─ med-Al/Open med-Al/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ med-Al/Open med-Al/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 46 1-time(s) 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

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Flare mode

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XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512												
Term		Pointing (x, y)					Comment					
07/07 10:05:00 - 07/07 17:43:00		Track ( -396.6, 134.8) <sup>Ⓢ 07/07 10:02:00</sup>					# OP start + 10min - AR 12381					
07/07 18:41:00 - 07/08 05:58:54		Track ( -325.8, 132.4) <sup>Ⓢ 07/07 18:38:00</sup>					# AR 12381					
07/08 06:12:00 - 07/08 17:59:54		Track ( -227.5, 129.7) <sup>Ⓢ 07/08 06:09:00</sup>					# AR 12381					

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07/08 18:13:00 - 07/09 06:22:54 Track ( -121.9, 127.6) @ 07/08 18:10:00 # AR 12381
07/09 06:36:00 - 07/09 17:16:00 Track ( -11.3, 126.3) @ 07/09 06:33:00 # AR 12381
07/09 18:14:30 - 07/10 05:47:54 Track ( 92.8, 125.7) @ 07/09 18:11:30 # AR 12381
07/10 06:01:00 - 07/10 17:37:24 Track ( 196.8, 126.0) @ 07/10 05:58:00 # AR 12381
07/10 18:37:30 - 07/11 05:59:54 Track ( 298.8, 127.1) @ 07/10 17:47:30 # AR 12381
07/11 06:13:00 - 07/11 08:32:00 Track ( 401.2, 129.1) @ 07/11 06:10:00 # AR 12381
07/11 09:08:30 - 07/11 14:59:54 Fixed ( 0.0, -920.0) # HOP 81 - South Pole
07/11 15:03:00 - 07/11 18:06:24 Track ( 470.9, 131.1) @ 07/11 15:00:00 # AR 12381
07/11 18:19:30 - 07/12 06:31:54 Track ( 495.9, 131.9) @ 07/11 18:16:30 # AR 12381
07/12 06:45:00 - 07/12 08:59:54 Track ( 586.0, 135.7) @ 07/12 06:42:00 # AR 12381
07/12 09:44:00 - 07/12 14:59:54 Fixed ( 0.0, 900.0) # HOP 81 - North Pole
07/12 15:03:00 - 07/12 17:25:00 Track ( 641.5, 138.7) @ 07/12 15:00:00 # AR 12381

```

**PROG= 03 30-time(s)**

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Subr= 1 20-time(s) 2.0sec
├─ Seqn= 11 1-time(s) 2.0sec
│   └─ Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
├─ Seqn=100 1-time(s) 10.0sec
│   ├── thin-Be/Open med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
│   ├── med-Be/Open Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
│   └─ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec
├─ Seqn= 10 1-time(s) 2.0sec
│   ├── med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
│   └─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
├─ Seqn= 11 1-time(s) 2.0sec
│   └─ Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Seqn= 53 1-time(s) 2.0sec
    ├── Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    ├── Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
    └─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec

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Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

\* \* \* \* \*

NOT USED

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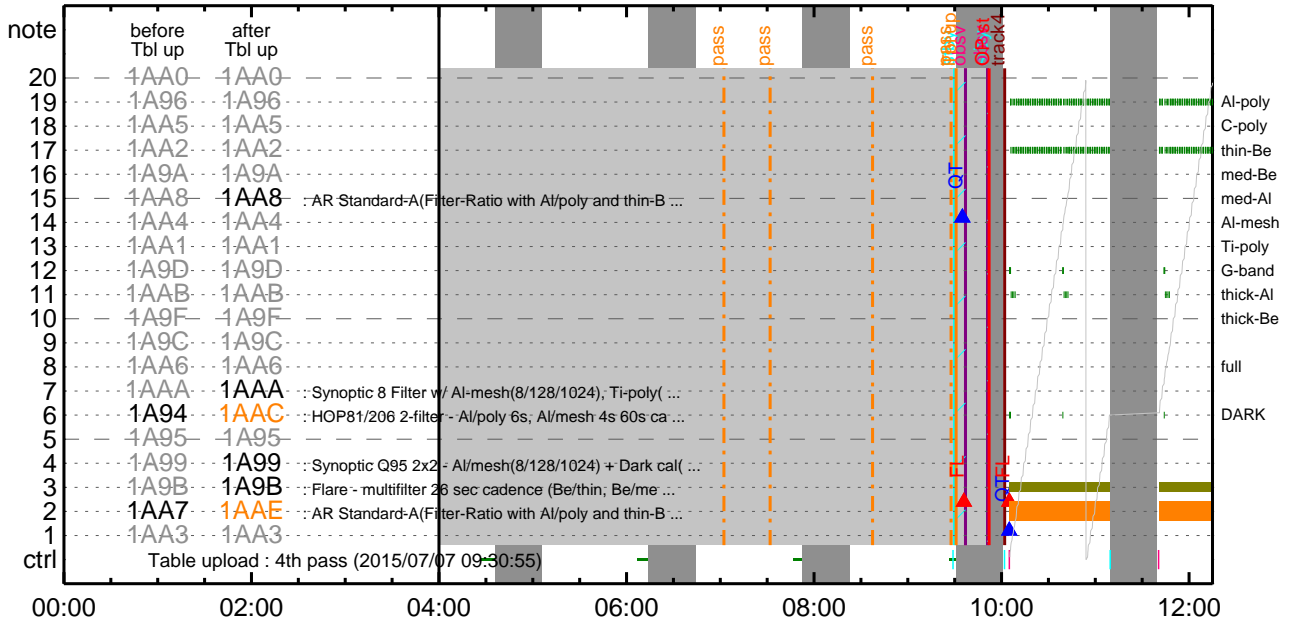
**Flare Detection**

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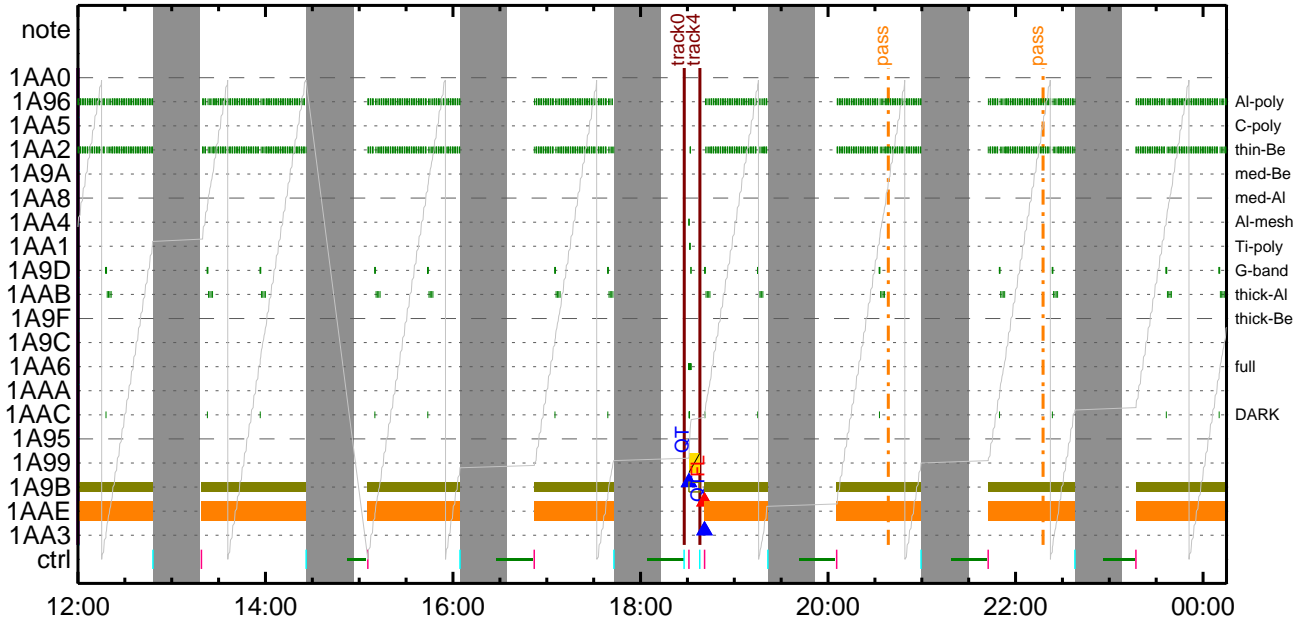
**FLD Patrol**

Term	Pointing (x, y)	Comment
07/07 18:38:16 - 07/08 05:59:16	Track ( -325.8, 132.4) @ 07/07 18:38:00	# AR 12381
07/08 06:09:16 - 07/08 18:00:16	Track ( -227.5, 129.7) @ 07/08 06:09:00	# AR 12381
07/08 18:10:16 - 07/09 06:23:16	Track ( -121.9, 127.6) @ 07/08 18:10:00	# AR 12381
07/09 06:33:16 - 07/09 18:01:46	Track ( -11.3, 126.3) @ 07/09 06:33:00	# AR 12381
07/09 18:11:46 - 07/10 05:48:16	Track ( 92.8, 125.7) @ 07/09 18:11:30	# AR 12381
07/10 05:58:16 - 07/10 17:37:46	Track ( 196.8, 126.0) @ 07/10 05:58:00	# AR 12381
07/10 17:47:46 - 07/11 06:00:16	Track ( 298.8, 127.1) @ 07/10 17:47:30	# AR 12381
07/11 06:10:16 - 07/11 18:06:46	Track ( 401.2, 129.1) @ 07/11 06:10:00	# AR 12381
07/11 18:16:46 - 07/12 06:32:16	Track ( 495.9, 131.9) @ 07/11 18:16:30	# AR 12381
07/12 06:42:16 - 07/12 18:12:16	Track ( 586.0, 135.7) @ 07/12 06:42:00	# AR 12381
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 30sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

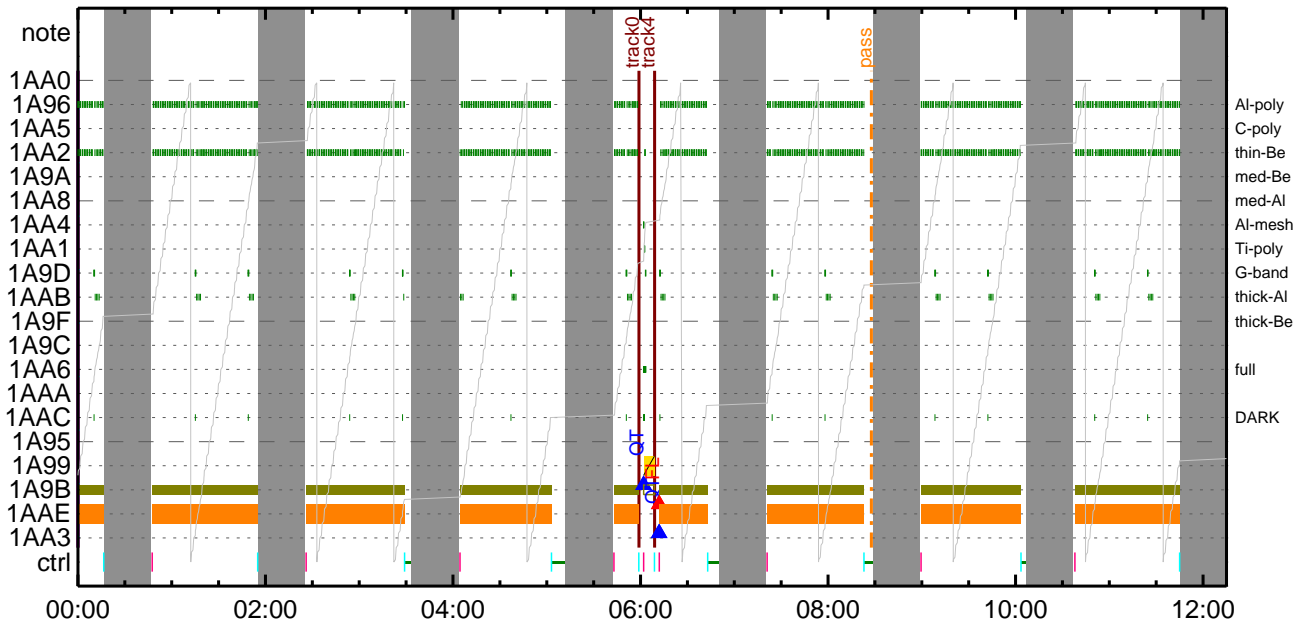
### CMDI #0251 2015/07/07



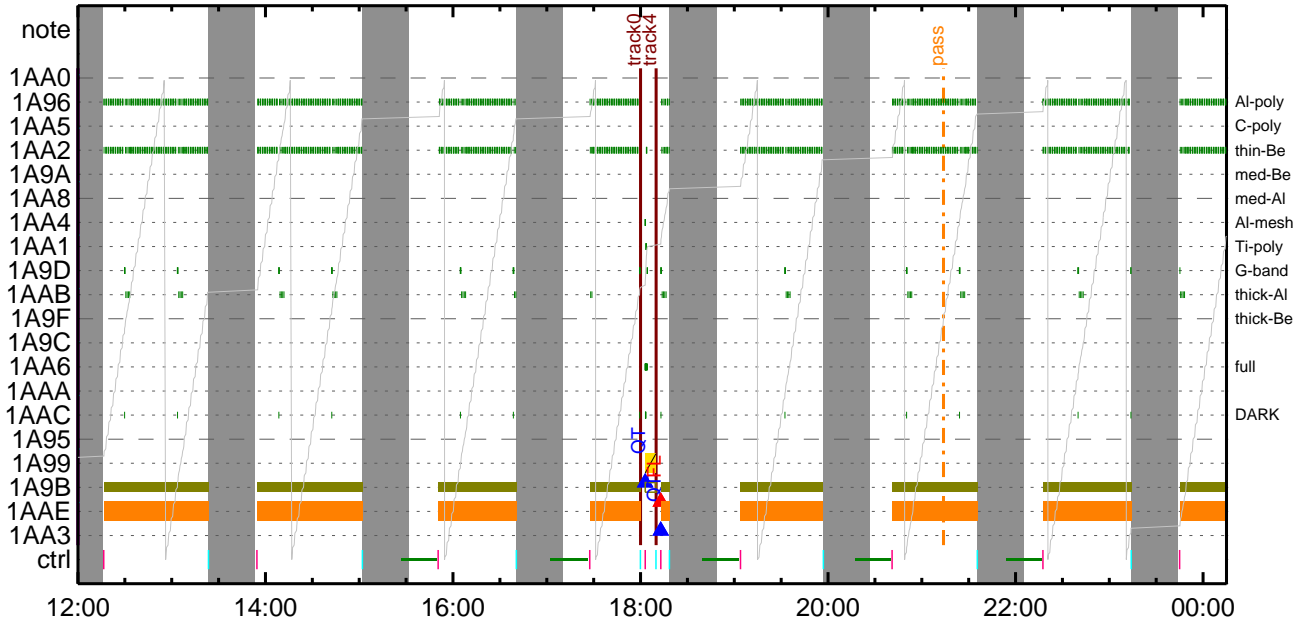
### CMDI #0251 2015/07/07



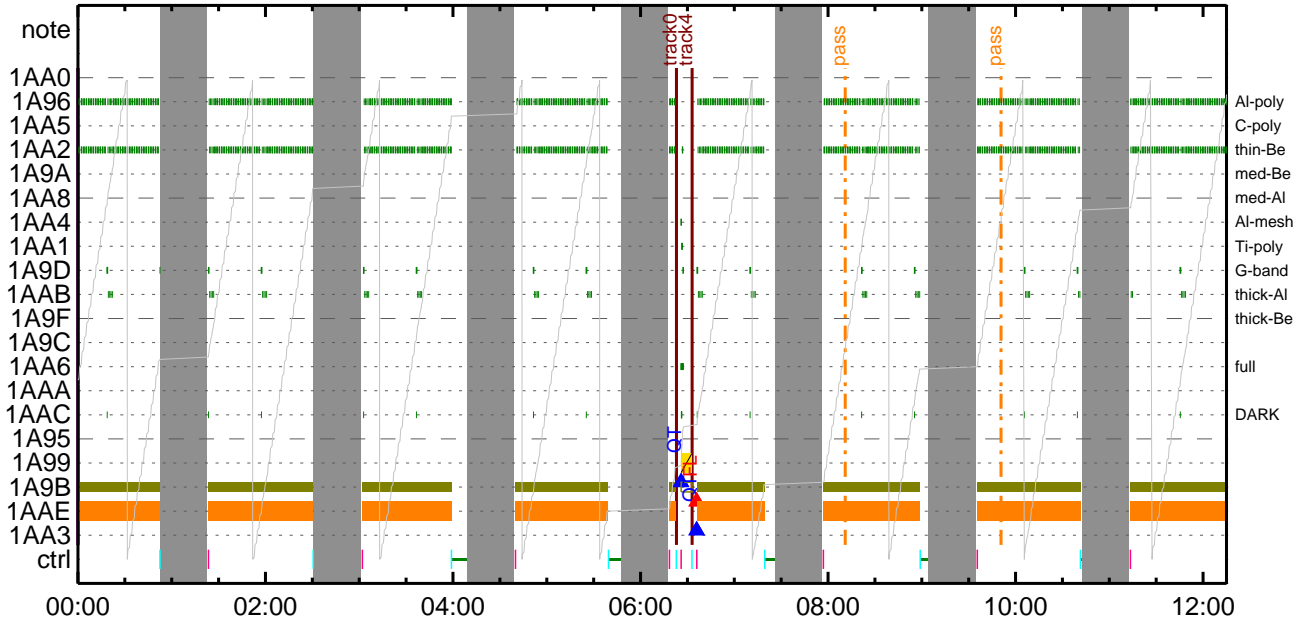
### CMDI #0251 2015/07/08



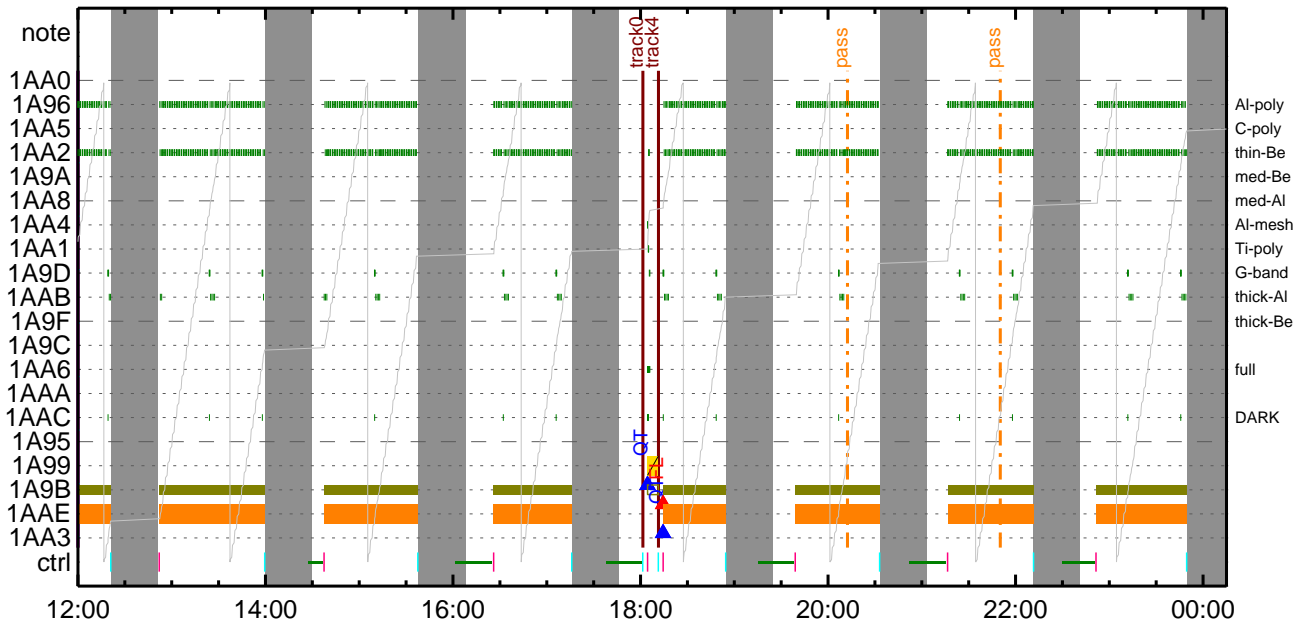
CMDI #0251 2015/07/08



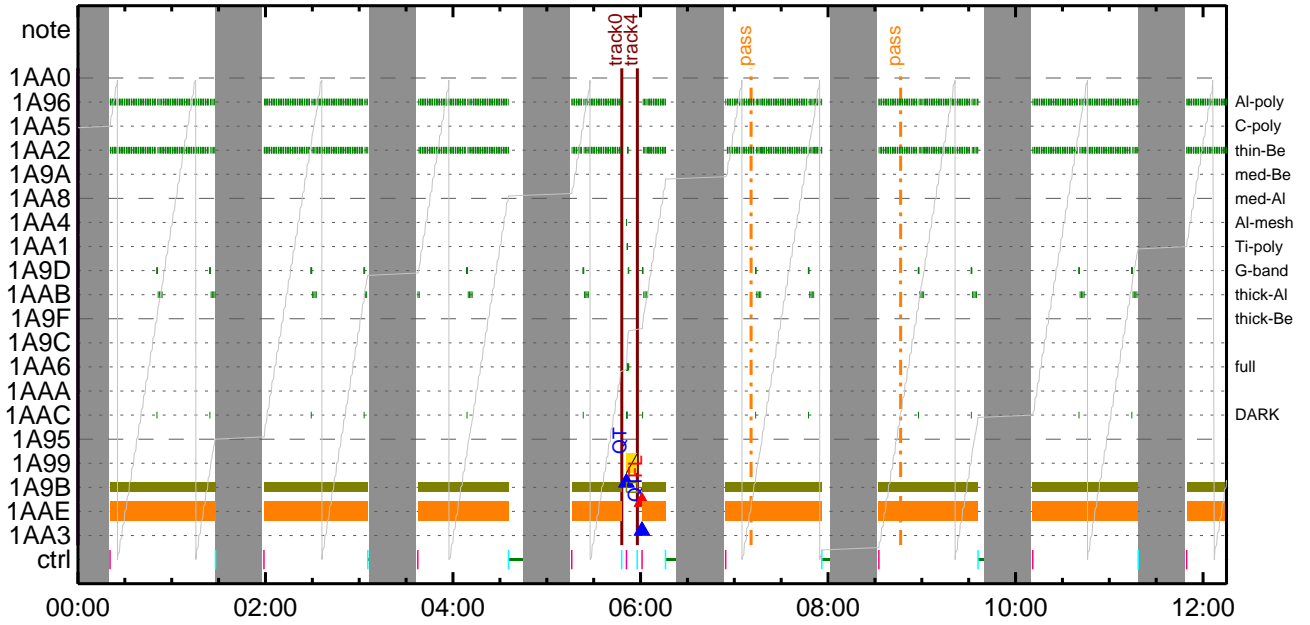
CMDI #0251 2015/07/09



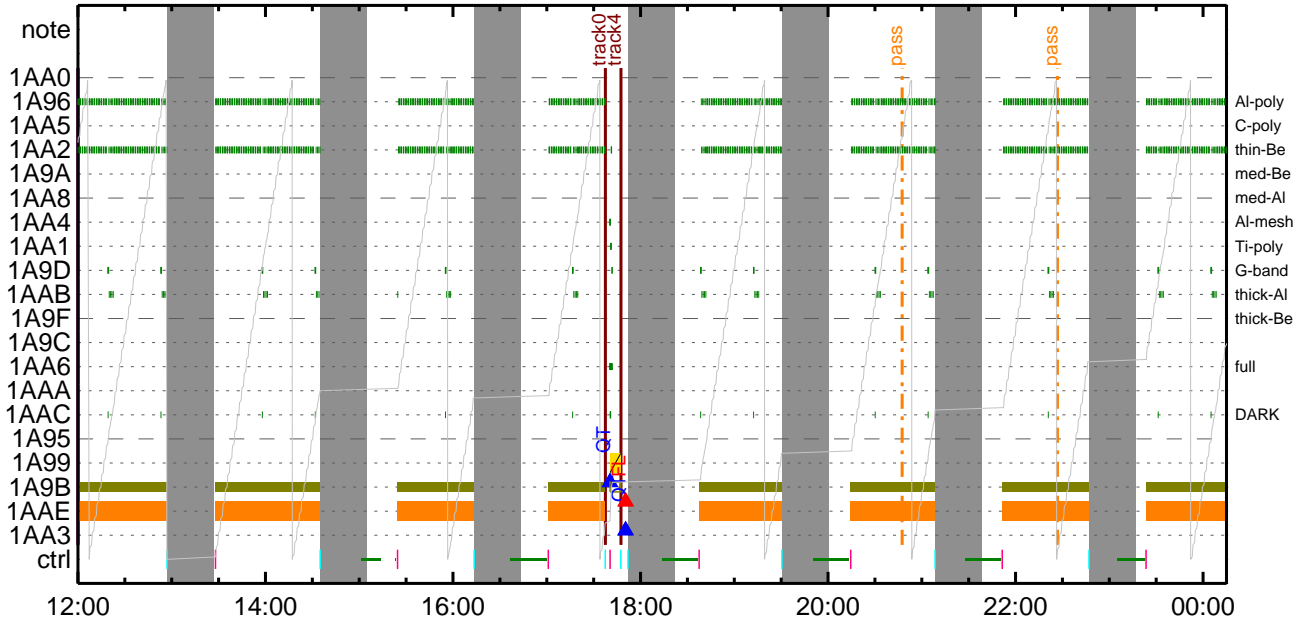
CMDI #0251 2015/07/09



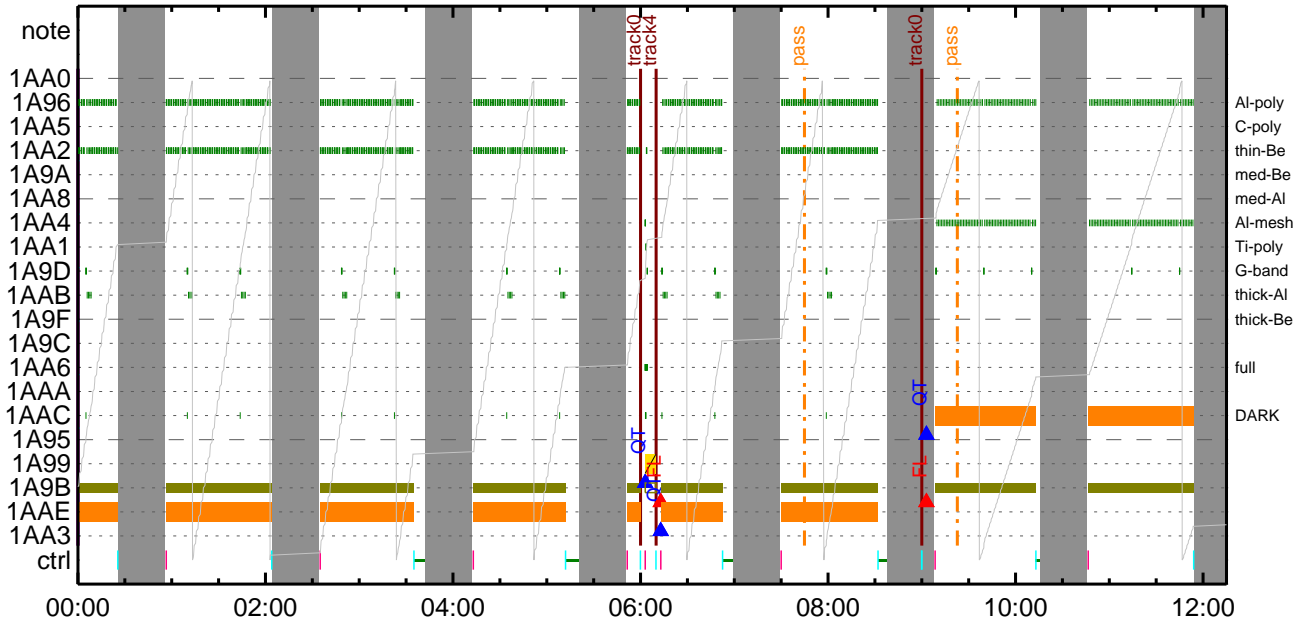
CMDI #0251 2015/07/10



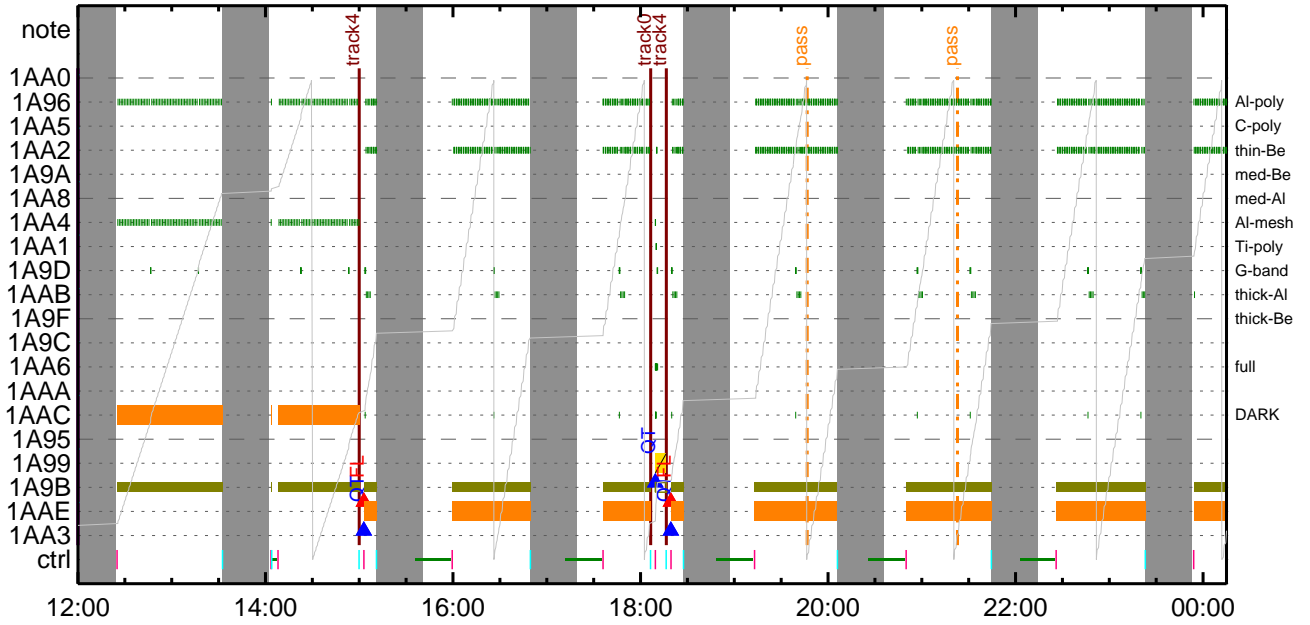
CMDI #0251 2015/07/10



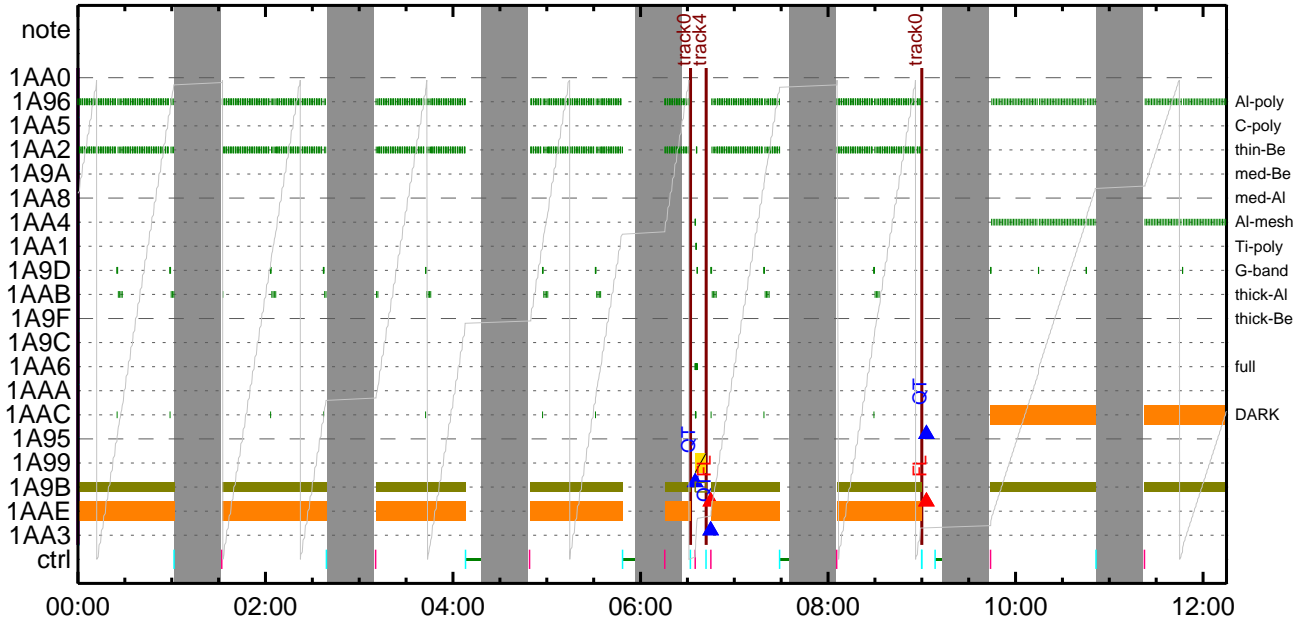
CMDI #0251 2015/07/11



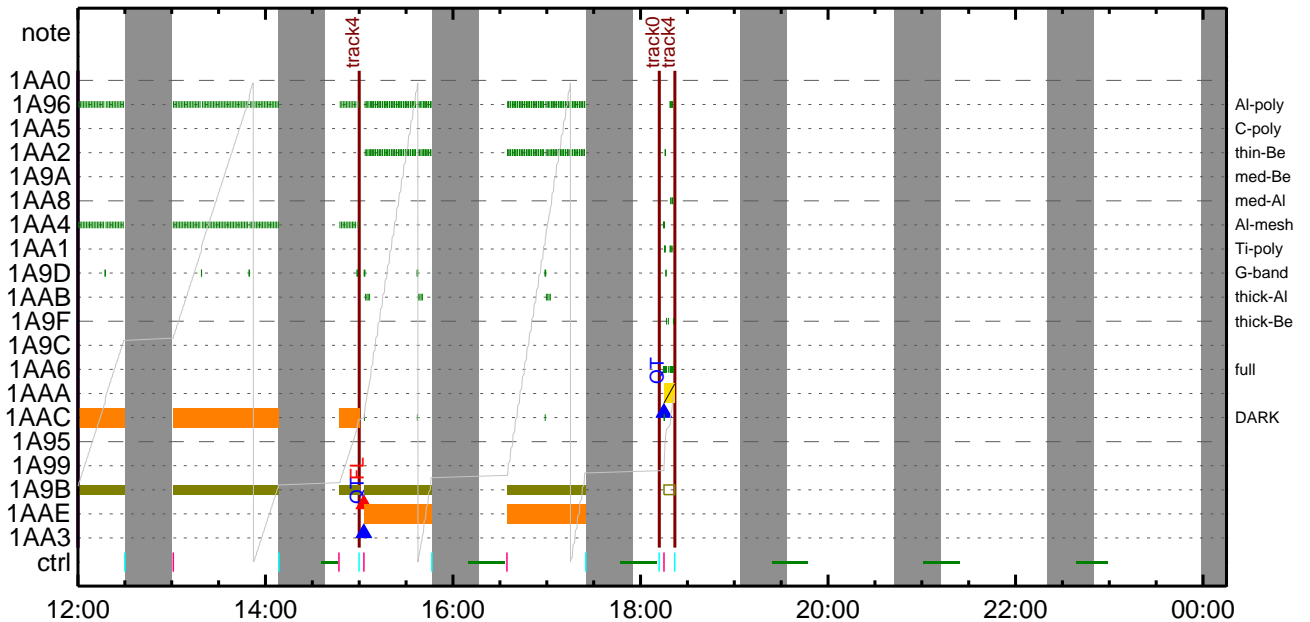
CMDI #0251 2015/07/11



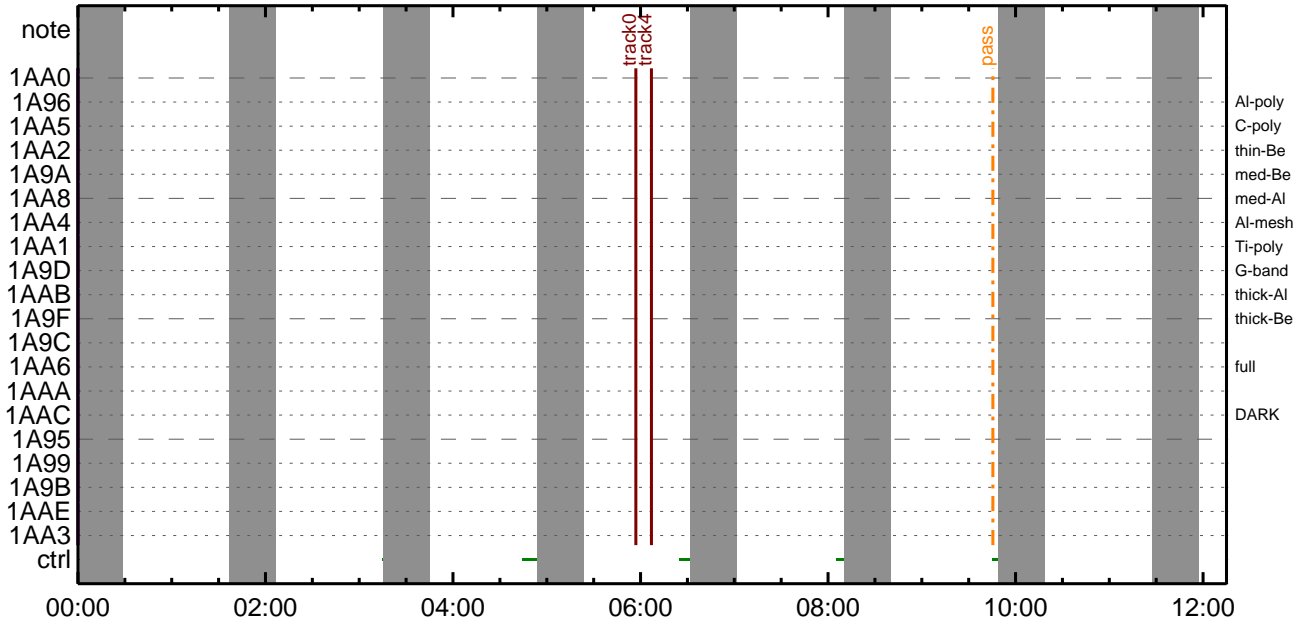
CMDI #0251 2015/07/12



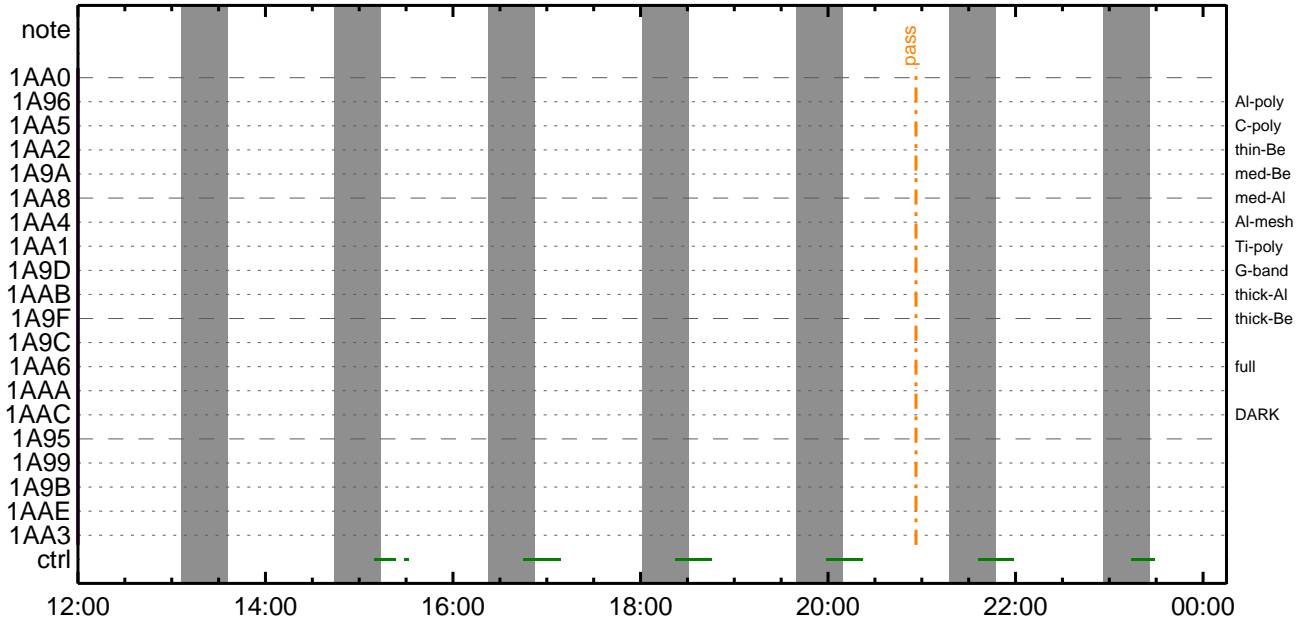
CMDI #0251 2015/07/12



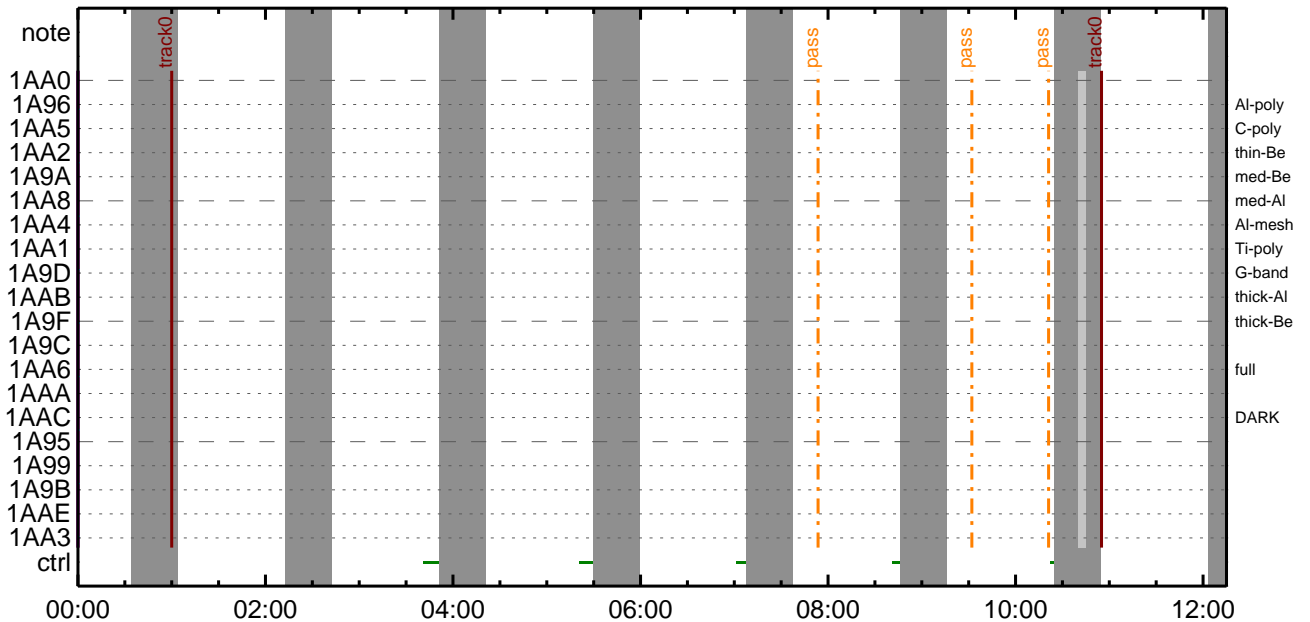
CMDI #0251 2015/07/13



CMDI #0251 2015/07/13

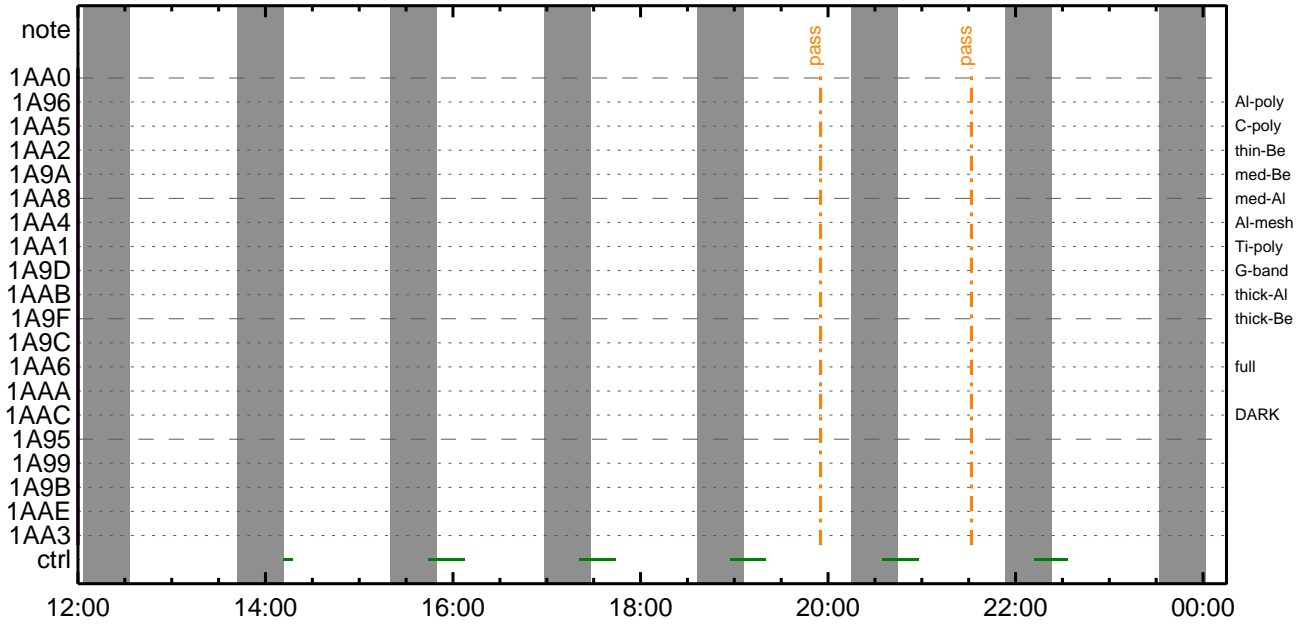


CMDI #0251 2015/07/14

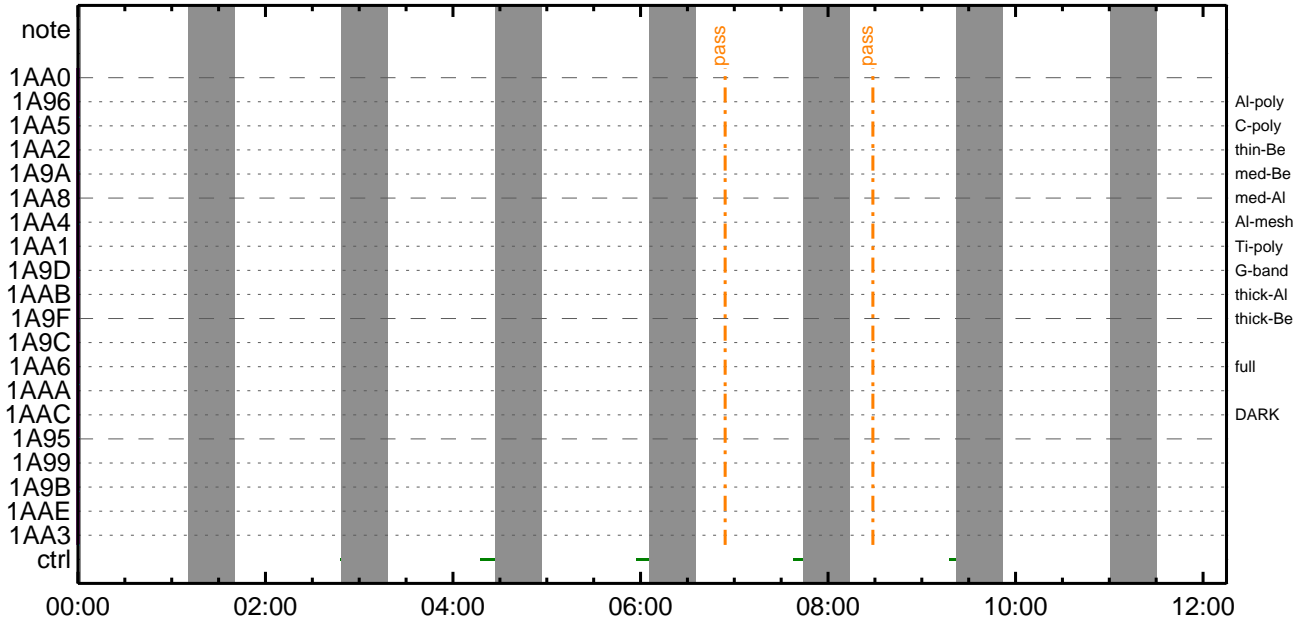




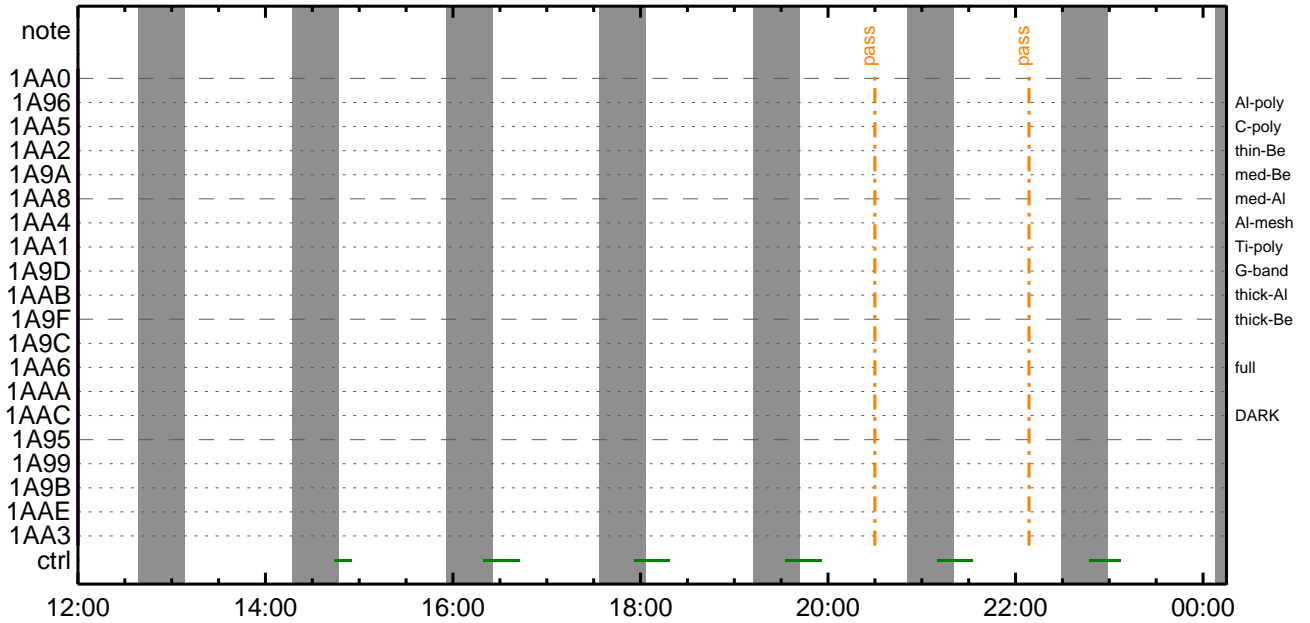
CMDI #0251 2015/07/14



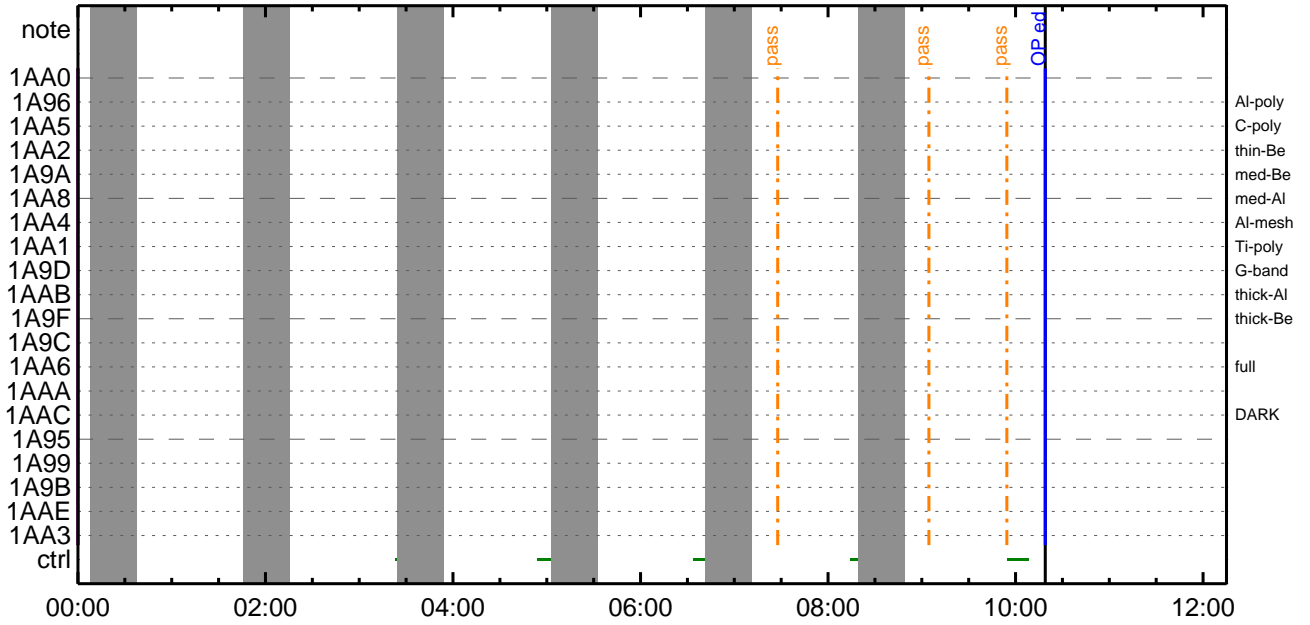
CMDI #0251 2015/07/15



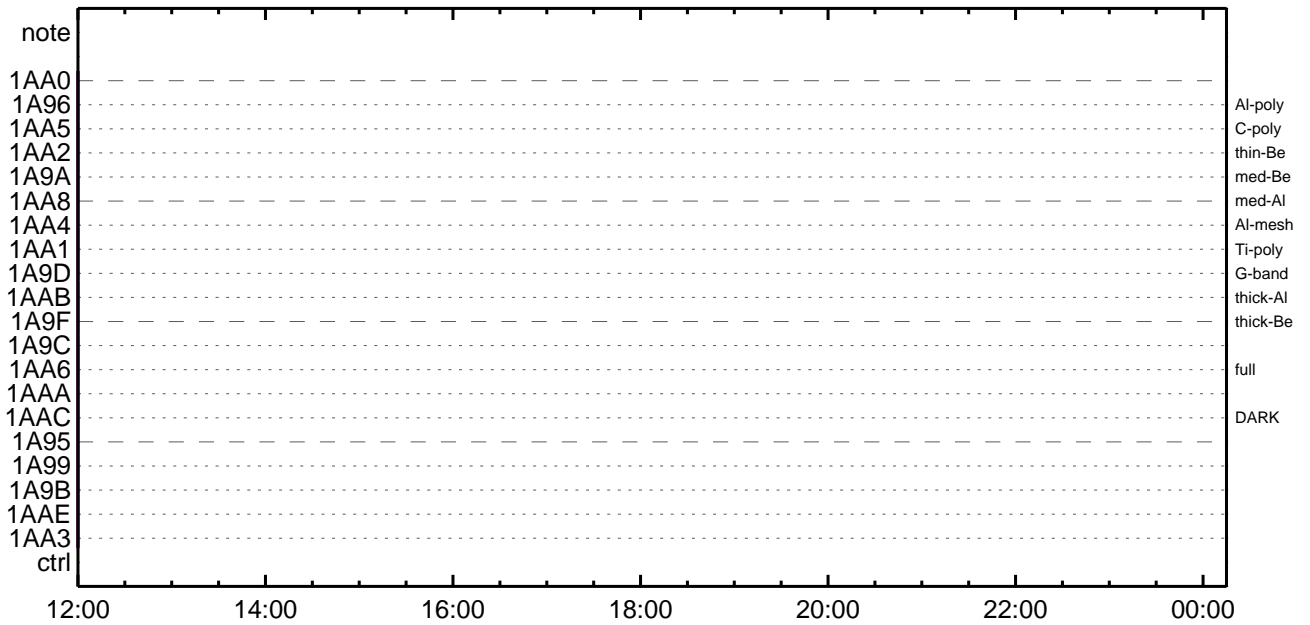
CMDI #0251 2015/07/15



CMDI #0251 2015/07/16



CMDI #0251 2015/07/16





```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-244:OP
0104 ( )
0105 S. OG og-244:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î¼E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î¼E¹ç•è²¼²îTI-CMDÁ÷ç²î¼E¹Ô²•²E²³²E;f
0180 C. ²²²ç;çSET²E²DUMP²îE²±²îY²¹²ç¹Ô²|²³²E;f
0181 C.
0182 C. TIY³Y²YôYE²ò²ÁDîç(UT)
0183 +. TI 2015-07-07 09:47:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-07-07 09:47:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-07-07 09:47:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```











(a) Spacecraft Operation Procedure (real-commands)

```
main-247 2015-07-07 13:42:51 154 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁã•µ°Æ»Í×ÁÇãÍYçYÁY×Yí;¼YÉ;ÈÈè¼µ•ííÈ;ÈãÈ¼°ÇÔã•ã¿¼í¹çãí;çÁ®, ùã¹ãèãããçÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-261:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉãîÁ°ãÈSTS_CHKãðOFFãÈã¹ãè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2015-07-07 09:51:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR ____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 85 83 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 20)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 20 08)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 80 80 08 20)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0f 80 80 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 10 80 80 08 08)
0112 + DC 07-F0 MDP_XRT_FLD_ENA
0113 BC (d8)
0114 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0115 BC (c8)
0116 + DC 07-F0 MDP_XRT_AEC_RESET
0117 BC (d0)
0118 + DC 07-F0 MDP_XRT_ARS_DIS
0119 BC (d5)
0120 + DC 07-F0 MDP_XRT_FLD_RESET
0121 BC (da)
0122 + DC 07-F0 MDP_XRT_QT_PROG_SET
0123 BC (c4 0f)
0124 + DC 07-F0 MDP_XRT_FL_PROG_SET
0125 BC (c5 03)
0126 . C. ----- Success Verify ? OK / NG ____
0127 C.
0128 C.
0129 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0130 C.
0131 + DC 07-F0 MDP_XRT_MODE_OBSV
0132 BC (c2)
0133 + TI 2015-07-07 09:51:02.0
0134 DC 07-F0 MDP_XRT_MODE_OBSV
0135 BC (c2)
0136 . C. ----- Success Verify ? OK / NG ____
0137 C.
0138 C. ***** XRT END *****
0139 C.
0140 . C. ***** MDP 'ûÃîñî»ö¼ýñÊÄð¹ñèDCBC•x²è *****
0141 C. (%á°îÿÓÿÄÿËÿÏÿËÿáÿçÿèñ¼ññ¼Ä»Û¹ñè)
0142 . S. DC-BC dcbc-402:DCBC
0143 (MDP_known_event)
0144 C.
0145 C.
0146 . C. ***** ÿDÿ¹•î Daily±¿îÑñÊ´Ø¹ñèDCBC•x²è *****
0147 . S. DC-BC dcbc-153:DCBC
0148 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0149 C.
0150 C.
0151 . C. ;ãLOSÿÄÿÏÿËÿËÿ¹¼Ä»Û;ä
0152 C.
0153 . C. ***** LOS *****
0154 C.
```

Jul 07, 15 13:43

## XRT\_OGLIST\_0251.chk

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\*\*\* OP Sequence for XRT \*\*\*

2015/07/07	10:01:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	10:01:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/07	10:02:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/07/07	10:02:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/07	10:02:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/07	10:02:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/07	10:02:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/07	10:02:24.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	10:04:56.0	XRT_QT_PROG_SET_413_OG [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/07/07	10:04:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/07	10:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/07	11:09:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	11:09:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	11:09:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/07	11:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/07	11:39:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/07	11:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/07	12:48:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	12:48:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	12:48:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/07	12:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/07	13:18:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/07	13:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/07	14:26:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	14:26:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	14:26:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/07	14:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/07	15:04:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/07	15:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/07	16:04:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	16:04:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	16:04:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/07	16:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/07	16:51:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/07	16:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/07	17:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	17:43:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	17:43:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/07	17:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/07	18:27:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	18:27:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/07	18:28:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/07/07	18:28:16.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/07	18:30:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/07	18:30:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/07	18:30:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/07/07	18:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/07	18:37:54.0	XRT_CTRL_MANU_402_OG [0x192]			

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2015/07/07	18:37:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2015/07/07	18:38:00.0	AOCS_OrE-point_Start_1_OG [0x097]		5	02-76	04 00 00 00 00
2015/07/07	18:38:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	d8
2015/07/07	18:38:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/07	18:38:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/07	18:38:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/07	18:38:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	18:40:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/07/07	18:40:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/07	18:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/07	19:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	19:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	19:21:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/07	19:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/07	20:04:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/07	20:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/07	20:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	20:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	20:59:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/07	21:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/07	21:41:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/07	21:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/07	22:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/07	22:38:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/07	22:38:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/07	22:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/07	23:16:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/07	23:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	00:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	00:16:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	00:16:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/08	00:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/08	00:46:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/08	00:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	01:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	01:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	01:55:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/08	01:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/08	02:25:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/08	02:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	03:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	03:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	03:29:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/08	03:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/08	04:03:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/08	04:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	05:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	05:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	05:03:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2015/07/08	05:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/08	05:42:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/08	05:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	05:58:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	05:58:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/08	05:59:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/07/08	05:59:16.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/08	06:01:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/08	06:01:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/08	06:01:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/07/08	06:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	06:08:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	06:08:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/08	06:09:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/07/08	06:09:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/08	06:09:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/08	06:09:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/08	06:09:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/08	06:09:24.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	06:11:56.0	XRT_QT_PROG_SET_413_OG [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/07/08	06:11:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/08	06:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	06:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	06:43:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	06:43:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/08	06:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/08	07:20:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/08	07:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	08:23:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	08:23:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	08:23:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/08	08:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/08	08:58:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/08	08:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	10:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	10:03:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	10:03:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/08	10:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/08	10:37:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/08	10:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	11:45:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	11:45:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	11:45:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/08	11:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/08	12:15:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/08	12:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/08	13:23:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/08	13:23:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/08	13:23:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

2015/07/08	13:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/08	13:53:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/08	13:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/08	15:02:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/08	15:02:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/08	15:02:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/08	15:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/08	15:49:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/08	15:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/08	16:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/08	16:40:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/08	16:40:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/08	16:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/08	17:26:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/08	17:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/08	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/08	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/08	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/08	18:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/08	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/08	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/08	18:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2015/07/08	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/08	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/08	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/07/08	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/08	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/08	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/08	18:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/08	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/08	18:10:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/08	18:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2015/07/08	18:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/07/08	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/08	18:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/08	18:18:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/08	18:18:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/08	18:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/08	19:03:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/08	19:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/08	19:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/08	19:57:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/08	19:57:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/08	20:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/08	20:40:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/08	20:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/08	21:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/08	21:35:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/08	21:35:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

2015/07/08	21:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/07/08	22:16:30.0	XRT_Custom_430_OG [0x1ae]						
2015/07/08	22:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/08	23:14:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/08	23:14:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/08	23:14:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/07/08	23:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/07/08	23:44:00.0	XRT_Custom_430_OG [0x1ae]						
2015/07/08	23:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/09	00:52:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/09	00:52:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/09	00:52:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/07/09	00:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/07/09	01:22:30.0	XRT_Custom_430_OG [0x1ae]						
2015/07/09	01:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/09	02:30:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/09	02:30:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/09	02:30:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/07/09	02:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/07/09	03:01:00.0	XRT_Custom_430_OG [0x1ae]						
2015/07/09	03:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/09	03:59:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/09	03:59:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/09	03:59:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/07/09	04:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/07/09	04:39:00.0	XRT_Custom_430_OG [0x1ae]						
2015/07/09	04:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/09	05:39:30.5	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/09	05:39:32.5	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/09	05:39:34.5	XRT_PREFLR_STRT_444_OG [0x1bc]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/07/09	05:42:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/07/09	06:17:30.0	XRT_Custom_430_OG [0x1ae]						
2015/07/09	06:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/09	06:22:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/09	06:22:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2015/07/09	06:23:00.0	AOCS_OrE-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2015/07/09	06:23:16.0	XRT_FLD_DIS_418_OG [0x1a2]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/07/09	06:25:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/07/09	06:25:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/07/09	06:25:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2015/07/09	06:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/09	06:32:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/09	06:32:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/07/09	06:33:00.0	AOCS_OrE-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	04 00 00 00 00			
2015/07/09	06:33:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/07/09	06:33:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/07/09	06:33:20.0	XRT_AEC_RESET_441_OG [0x1b9]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/07/09	06:33:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/07/09	06:33:24.0	XRT_FLD_RESET_443_OG [0x1bb]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/09	06:35:56.0	XRT_QT_PROG_SET_413_OG [0x19d]						

2015/07/09	06:35:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/07/09	06:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/09	07:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/09	07:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/09	07:19:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/09	07:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/09	07:56:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/09	07:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/09	08:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/09	08:59:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/09	08:59:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/09	09:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/09	09:34:30.5	XRT_Custom_430_OG [0x1ae]					
2015/07/09	09:35:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/09	10:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/09	10:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/09	10:41:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/09	10:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/09	11:12:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/09	11:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/09	12:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/09	12:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/09	12:21:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/09	12:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/09	12:51:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/09	12:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/09	13:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/09	13:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/09	13:59:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/09	14:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/09	14:36:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/09	14:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/09	15:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/09	15:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/09	15:37:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/09	15:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/09	16:25:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/09	16:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/09	17:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/09	17:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/09	17:16:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/09	17:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/09	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/09	18:01:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2015/07/09	18:01:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2015/07/09	18:01:46.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/07/09	18:04:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/07/09	18:04:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/09	18:04:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04



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2015/07/09	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/09	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/09	18:11:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/09	18:11:30.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/07/09	18:11:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/09	18:11:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/09	18:11:50.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/09	18:11:52.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/09	18:11:54.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/09	18:14:26.0	XRT_QT_PROG_SET_413_OG [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/07/09	18:14:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/09	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/09	18:54:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/09	18:54:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/09	18:54:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/09	18:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/09	19:38:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/09	19:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/09	20:33:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/09	20:33:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/09	20:33:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/09	20:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/09	21:15:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/09	21:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/09	22:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/09	22:11:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/09	22:11:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/09	22:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/09	22:50:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/09	22:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/09	23:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/09	23:49:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/09	23:49:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/09	23:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	00:19:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/10	00:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	01:28:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	01:28:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	01:28:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	01:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	01:58:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/10	01:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	03:05:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	03:05:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	03:05:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	03:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	03:36:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/10	03:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	04:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	04:35:32.0	XRT_FLD_RESET_415_OG [0x19f]			

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2015/07/10	04:35:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	04:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	05:15:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	05:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	05:47:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	05:47:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/10	05:48:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/07/10	05:48:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/10	05:50:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/10	05:50:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/10	05:50:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/07/10	05:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	05:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	05:57:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/10	05:58:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2015/07/10	05:58:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/10	05:58:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/10	05:58:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/10	05:58:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/10	05:58:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	06:00:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/07/10	06:00:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/10	06:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	06:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	06:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	06:16:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	06:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	06:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	06:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	07:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	07:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	07:56:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	07:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	08:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	08:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	09:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	09:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	09:36:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	09:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	10:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	10:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	11:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	11:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	11:18:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	11:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	11:48:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	11:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	12:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	12:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2015/07/10	12:57:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	13:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	13:27:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	13:28:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	14:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	14:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	14:35:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	14:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	15:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	15:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	16:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	16:13:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	16:13:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	16:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	17:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	17:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	17:37:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	17:37:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/10	17:37:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/07/10	17:37:46.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/10	17:40:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/10	17:40:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/10	17:40:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/07/10	17:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	17:47:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	17:47:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/10	17:47:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2015/07/10	17:47:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/10	17:47:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/10	17:47:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/10	17:47:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/10	17:47:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	17:50:26.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/07/10	17:50:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/10	17:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	17:52:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	17:52:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	17:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	18:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	18:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	19:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	19:30:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	19:30:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/10	19:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/10	20:13:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/10	20:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	21:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/10	21:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/10	21:08:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2015/07/10	21:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/10	21:50:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/10	21:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/10	22:47:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/10	22:47:02.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/10	22:47:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/10	22:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/10	23:22:30.5	XRT_Custom_430_OG [0x1ae]								
2015/07/10	23:23:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	00:25:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	00:25:32.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	00:25:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	00:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	00:55:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/11	00:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	02:04:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	02:04:02.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	02:04:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	02:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	02:34:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/11	02:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	03:35:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	03:35:02.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	03:35:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	03:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	04:12:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/11	04:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	05:12:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	05:12:02.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	05:12:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	05:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	05:50:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/11	05:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/11	06:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/11	06:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/11	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/11	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/11	06:02:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2015/07/11	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/07/11	06:10:00.0	AOCs_OrE-point_Start_1_OG [0x097]								
			AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/11	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/11	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/11	06:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/11	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/11	06:10:24.0	XRT_FLD_RESET_443_OG [0x1bb]								
			MDP_XRT_FLD_RESET	1	07-F0	da				

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2015/07/11	06:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2015/07/11	06:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/11	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	06:52:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	06:52:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	06:52:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	06:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	07:29:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/11	07:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	08:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	08:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	08:32:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	08:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	08:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	08:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/07/11	09:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	51	ca	00	00
2015/07/11	09:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/11	09:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/11	09:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/11	09:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/11	09:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	09:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/07/11	09:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/11	09:07:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/11	09:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	10:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	10:13:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	10:13:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	10:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	10:45:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/11	10:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	11:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	11:54:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	11:54:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	11:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	12:24:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/11	12:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	13:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	13:32:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	13:32:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	13:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	14:02:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/11	14:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	14:04:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	14:04:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	14:04:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	14:07:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/11	14:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	14:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

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2015/07/11	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	14:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	15:00:00.0	AOCS_Orе-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/07/11	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04	00	00	00	00
2015/07/11	15:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/11	15:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/11	15:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/11	15:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/11	15:02:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	15:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2015/07/11	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/11	15:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	15:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	15:11:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	15:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	15:58:31.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	15:59:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2015/07/11	16:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	16:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	16:49:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	16:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	17:35:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	17:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2015/07/11	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	18:06:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	18:06:30.0	AOCS_Orе-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/07/11	18:06:46.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00	00	00	00	00
2015/07/11	18:09:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/11	18:09:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/11	18:09:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/11	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2015/07/11	18:16:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	18:16:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	18:16:30.0	AOCS_Orе-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/07/11	18:16:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04	00	00	00	00
2015/07/11	18:16:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/11	18:16:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/11	18:16:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/11	18:16:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/11	18:19:26.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	18:19:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2015/07/11	18:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/11	18:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/11	18:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/11	18:27:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/11	18:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/11	19:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/11	19:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							

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2015/07/11	20:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/11	20:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/11	20:06:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/11	20:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/11	20:49:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/11	20:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/11	21:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/11	21:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/11	21:44:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/11	21:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/11	22:25:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/11	22:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/11	23:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/11	23:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/11	23:23:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/11	23:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/11	23:53:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/11	23:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	01:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	01:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	01:01:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/12	01:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/12	01:31:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/12	01:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	02:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	02:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	02:39:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/12	02:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/12	03:09:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/12	03:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	04:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	04:08:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	04:08:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/12	04:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/12	04:48:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/12	04:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	05:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	05:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	05:48:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/12	05:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/12	06:14:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/12	06:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	06:31:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	06:31:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/12	06:32:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/07/12	06:32:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/12	06:34:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/12	06:34:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/12	06:34:58.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/07/12	06:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]				

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2015/07/12	06:41:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	06:41:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	06:42:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/12	06:42:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00
2015/07/12	06:42:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/12	06:42:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/12	06:42:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/12	06:42:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/12	06:44:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	06:44:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/07/12	06:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/12	07:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	07:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	07:29:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	07:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/12	08:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/12	08:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/07/12	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	08:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	08:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	09:00:00.5	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/12	09:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 b0 00 00 00
2015/07/12	09:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/12	09:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/12	09:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/12	09:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/12	09:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	09:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/07/12	09:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/12	09:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	09:08:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	09:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/12	09:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/12	09:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/07/12	10:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	10:51:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	10:51:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	10:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/12	11:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/12	11:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/07/12	12:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	12:30:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12	12:30:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/12	12:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/12	13:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/12	13:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/07/12	14:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/12	14:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/12			MDP_XRT_FLD_RESET	1	07-F0	da



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2015/07/12	14:08:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/12	14:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/12	14:46:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/12	14:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/12	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/12	14:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/07/12	15:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/12	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/12	15:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/12	15:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/12	15:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/12	15:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/12	15:02:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2015/07/12	15:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/07/12	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/12	15:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/12	15:46:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/12	15:46:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/12	15:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/12	16:33:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/12	16:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/12	17:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/12	17:25:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/12	17:25:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/12	17:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/12	18:11:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/12	18:11:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/12	18:12:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/12	18:12:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/12	18:14:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/12	18:14:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/12	18:14:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2015/07/12	18:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/12	18:21:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/12	18:22:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/12	18:27:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_404_OG [0x194]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2015/07/13	05:57:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/13	06:07:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/14	01:00:00.5	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/14	10:55:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				