

# XRT Timeline to be uploaded on 2015/07/16

Period: 2015/07/16 10:19:00 - 2015/07/21 10:01:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #1AAB: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Ti/Poly(1024ms) - w leak image-3ms												
Term		Pointing (x, y)					Comment					
07/17 00:03:00 - 07/17 00:09:54		Fixed ( -528.4, -528.4)					4 quadrant obs #1					
<b>PROG= 11 1-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 86 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 34 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1A9F: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1024ms) - w leak image-3 ms												
Term		Pointing (x, y)					Comment					
07/17 00:13:00 - 07/17 00:19:54		Fixed ( 528.4, -528.4)					4 quadrant obs #2					
<b>PROG= 10 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 15 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 34 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AA0: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1024ms) - w leak image-3 ms												
Term		Pointing (x, y)					Comment					
07/17 00:23:00 - 07/17 00:29:54		Fixed ( 528.4, 528.4)					4 quadrant obs #3					
<b>PROG= 20 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 35 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 34 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AA1: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1024ms) - w leak image-3 ms												
Term		Pointing (x, y)					Comment					
07/17 00:33:00 - 07/17 00:43:30		Fixed ( -528.4, 528.4)					4 quadrant obs #4					
<b>PROG= 13 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 3 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												

┌	<b>Seqn= 34</b>	<b>2-time(s)</b>	<b>2.0sec</b>																		
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
└	<b>Subr= 3</b>	<b>2-time(s)</b>	<b>2.0sec</b>																		
┌	<b>Seqn= 54</b>	<b>1-time(s)</b>	<b>2.0sec</b>																		
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec							
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

**XOB #1AB1: CME watch - 4x4 - AEC 2 - Al-poly - G-band (2x2,1ms) - Leak (2x2,1ms) - 60s cad**

Term	Pointing (x, y)	Comment	
07/17 01:20:02 - 07/17 01:28:54	Fixed ( 936.0, 0.0)	HOP 130, 1 of 15	
07/17 01:32:02 - 07/17 01:44:54	Fixed ( 468.0, 0.0)	HOP 130, 2 of 15	
07/17 02:04:02 - 07/17 02:22:00	Fixed ( -468.0, 0.0)	HOP 130, 4 of 15	
07/17 02:54:32 - 07/17 03:07:24	Fixed ( -936.0, 0.0)	HOP 130, 5 of 15	
07/17 03:10:32 - 07/17 03:17:24	Fixed ( 815.0, 470.0)	HOP 130, 6 of 15	
07/17 03:20:32 - 07/17 03:33:24	Fixed ( 411.0, 847.0)	HOP 130, 7 of 15	
07/17 03:36:32 - 07/17 03:50:00	Fixed ( 0.0, 941.0)	HOP 130, 8 of 15	
07/17 04:33:02 - 07/17 04:45:54	Fixed ( -411.0, 847.0)	HOP 130, 9 of 15	
07/17 04:49:02 - 07/17 04:55:54	Fixed ( -815.0, 470.0)	HOP 130, 10 of 15	
07/17 04:59:02 - 07/17 05:05:54	Fixed ( 815.0, -470.0)	HOP 130, 11 of 15	
07/17 05:09:02 - 07/17 05:30:30	Fixed ( 411.0, -847.0)	HOP 130, 12 of 15	
07/17 06:11:32 - 07/17 06:24:24	Fixed ( 0.0, -941.0)	HOP 130, 13 of 15	
07/17 06:27:32 - 07/17 06:40:24	Fixed ( -411.0, -847.0)	HOP 130, 14 of 15	
07/17 06:43:32 - 07/17 06:50:24	Fixed ( -815.0, -471.0)	HOP 130, 15 of 15	

**PROG= 17 Inf.-time(s)**

┌	<b>Subr= 1</b>	<b>30-time(s)</b>	<b>60.0sec</b>											
	<b>Seqn= 78</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec
└	<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
┌	<b>Seqn= 26</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

**XOB #1AAF: HOP130-Synoptic 3 Filter- 2x2 Q95 (Almesh 12/128/723ms; Tipoly 24/362/1443ms; Althick 65536ms) with G-band (3ms)**

Term	Pointing (x, y)	Comment	
07/17 01:48:02 - 07/17 02:00:54	Fixed ( 0.0, 0.0)	HOP 130, 3 of 15	

**PROG= 05 2-time(s)**

┌	<b>Subr= 1</b>	<b>1-time(s)</b>	<b>180.0sec</b>											
	<b>Seqn= 68</b>	<b>1-time(s)</b>	<b>25.0sec</b>											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 94</b>	<b>1-time(s)</b>	<b>25.0sec</b>											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 58</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	<b>Seqn= 54</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

**XOB #1AAA: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/1024), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45**

Term	Pointing (x, y)	Comment	
07/17 06:53:32 - 07/17 07:00:24	Fixed ( 0.0, 0.0)	synoptic, shifted manually	
07/17 17:58:02 - 07/17 18:04:54	Fixed ( 0.0, 0.0)	synoptic, shifted -5.0 min	
07/18 05:42:32 - 07/18 05:49:24	Fixed ( 0.0, 0.0)	synoptic, shifted -20.5 min	

**PROG= 07 1-time(s)**

┌	<b>Subr= 1</b>	<b>1-time(s)</b>	<b>12.0sec</b>											
	<b>Seqn= 33</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
	<b>Seqn= 61</b>	<b>1-time(s)</b>	<b>4.0sec</b>											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec

Seqn	1-time(s)	2.0sec																
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec					
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec					
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec					
<b>Seqn= 54 1-time(s) 2.0sec</b>																		
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec					
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec					
<b>Subr= 2 1-time(s) 12.0sec</b>																		
<b>Seqn= 46 2-time(s) 2.0sec</b>																		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
<b>Seqn= 25 1-time(s) 4.0sec</b>																		
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
<b>Seqn= 21 1-time(s) 4.0sec</b>																		
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
<b>Seqn= 37 1-time(s) 2.0sec</b>																		
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
<b>Seqn= 25 1-time(s) 4.0sec</b>																		
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
<b>Seqn= 21 1-time(s) 4.0sec</b>																		
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
<b>Seqn= 37 1-time(s) 2.0sec</b>																		
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
<b>Seqn= 46 1-time(s) 2.0sec</b>																		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec					
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval							

**XOB #1AB0: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)**

Term	Pointing (x, y)	Comment
07/17 07:03:32 - 07/17 17:54:54	Track ( -381.4, 67.2) <sup>@ 07/17 07:00:30</sup>	# track AR 12386
07/17 18:08:02 - 07/18 05:39:24	Track ( -287.7, 63.7) <sup>@ 07/17 18:05:00</sup>	# track AR 12386
07/18 05:52:32 - 07/18 09:27:00	Track ( -184.7, 60.8) <sup>@ 07/18 05:49:30</sup>	# track AR 12386

**PROG= 18 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>																		
<b>Seqn= 9 2-time(s) 2.0sec</b>																		
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec					
<b>Subr= 2 1-time(s) 2.0sec</b>																		
<b>Seqn= 92 1-time(s) 2.0sec</b>																		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec					
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec					
<b>Seqn= 42 4-time(s) 2.0sec</b>																		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec					
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec					
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec					
<b>Seqn= 22 20-time(s) 45.0sec</b>																		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec					
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec					
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec					
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec					
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	2.0sec					
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	2.0sec					
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval							

\* \* \* \* \*

**Flare mode**

\* \* \* \* \*

**XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
07/17 01:20:02 - 07/17 01:28:54	Fixed ( 936.0, 0.0)	HOP 130, 1 of 15
07/17 01:32:02 - 07/17 01:44:54	Fixed ( 468.0, 0.0)	HOP 130, 2 of 15
07/17 02:04:02 - 07/17 02:22:00	Fixed ( -468.0, 0.0)	HOP 130, 4 of 15
07/17 02:54:32 - 07/17 03:07:24	Fixed ( -936.0, 0.0)	HOP 130, 5 of 15
07/17 03:10:32 - 07/17 03:17:24	Fixed ( 815.0, 470.0)	HOP 130, 6 of 15
07/17 03:20:32 - 07/17 03:33:24	Fixed ( 411.0, 847.0)	HOP 130, 7 of 15
07/17 03:36:32 - 07/17 03:50:00	Fixed ( 0.0, 941.0)	HOP 130, 8 of 15
07/17 04:33:02 - 07/17 04:45:54	Fixed ( -411.0, 847.0)	HOP 130, 9 of 15
07/17 04:49:02 - 07/17 04:55:54	Fixed ( -815.0, 470.0)	HOP 130, 10 of 15
07/17 04:59:02 - 07/17 05:05:54	Fixed ( 815.0, -470.0)	HOP 130, 11 of 15
07/17 05:09:02 - 07/17 05:30:30	Fixed ( 411.0, -847.0)	HOP 130, 12 of 15
07/17 06:11:32 - 07/17 06:24:24	Fixed ( 0.0, -941.0)	HOP 130, 13 of 15
07/17 06:27:32 - 07/17 06:40:24	Fixed ( -411.0, -847.0)	HOP 130, 14 of 15
07/17 06:43:32 - 07/17 06:50:24	Fixed ( -815.0, -471.0)	HOP 130, 15 of 15
07/17 07:03:32 - 07/17 17:54:54	Track ( -381.4, 67.2) <sup>@ 07/17 07:00:30</sup>	# track AR 12386
07/17 18:08:02 - 07/18 05:39:24	Track ( -287.7, 63.7) <sup>@ 07/17 18:05:00</sup>	# track AR 12386

PROG= 03 30-time(s)												
Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 53 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

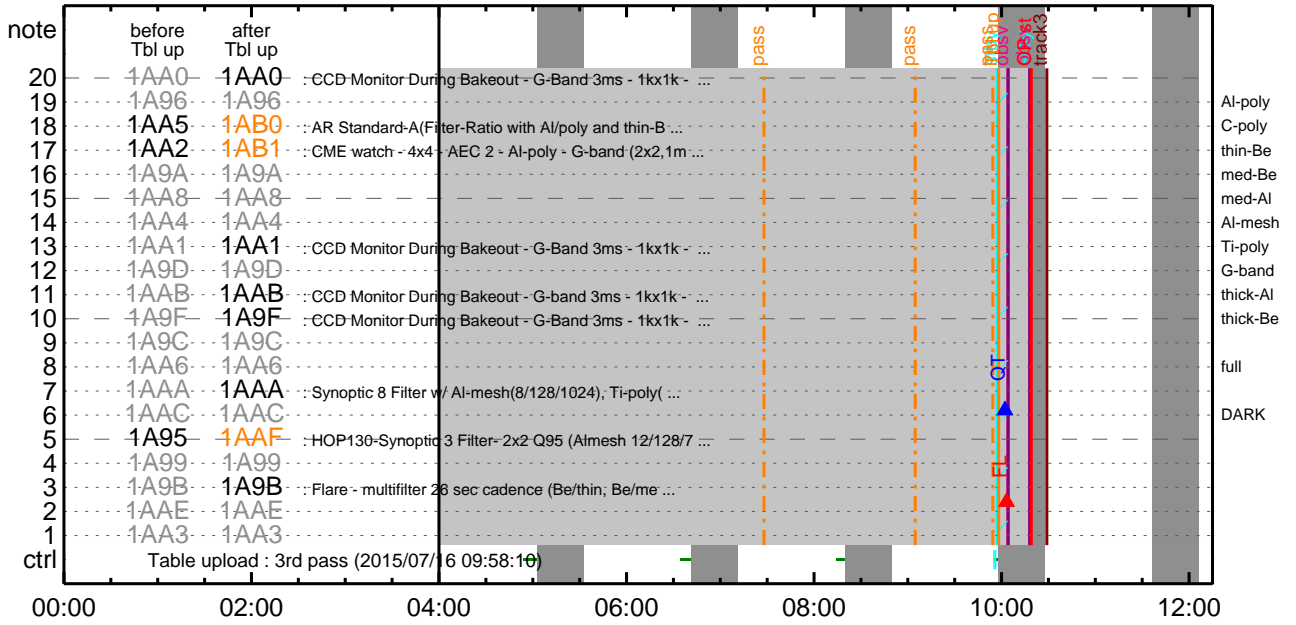
\* \* \* \* \*

### Flare Detection

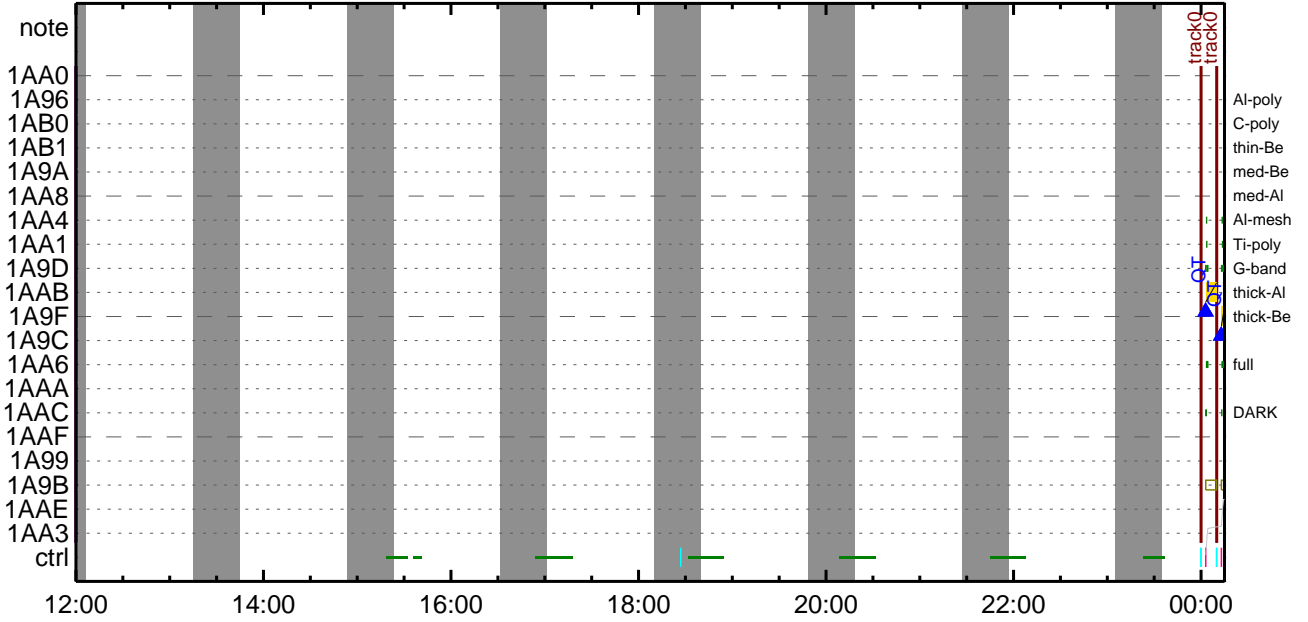
\* \* \* \* \*

FLD Patrol												
Term		Pointing (x, y)		Comment								
07/17 01:17:18 - 07/17 01:45:18		Fixed ( 936.0, 0.0)		HOP 130, 1 of 15								
07/17 02:01:18 - 07/17 06:50:48		Fixed ( -468.0, 0.0)		HOP 130, 4 of 15								
07/17 07:00:48 - 07/17 17:55:18		Track ( -381.4, 67.2) @ 07/17 07:00:30		# track AR 12386								
07/17 18:05:18 - 07/18 05:39:48		Track ( -287.7, 63.7) @ 07/17 18:05:00		# track AR 12386								
07/18 05:49:48 - 07/21 10:01:00		Track ( -184.7, 60.8) @ 07/18 05:49:30		# track AR 12386								
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

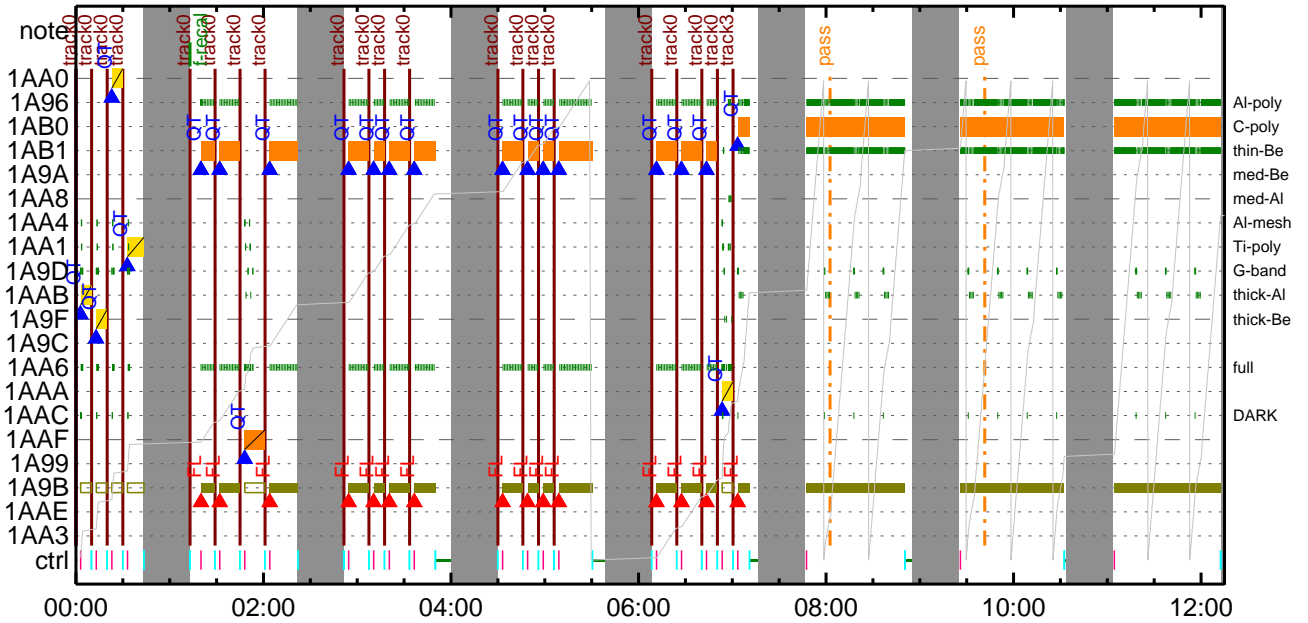
### CMDI #0270 2015/07/16



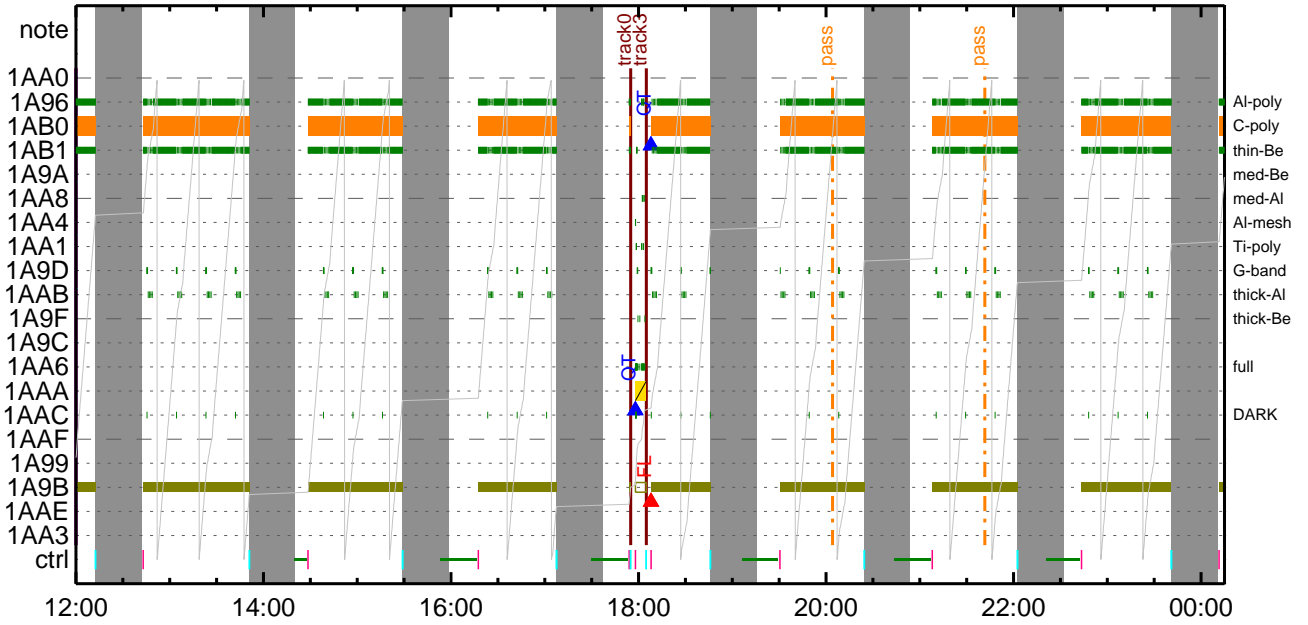
### CMDI #0270 2015/07/16



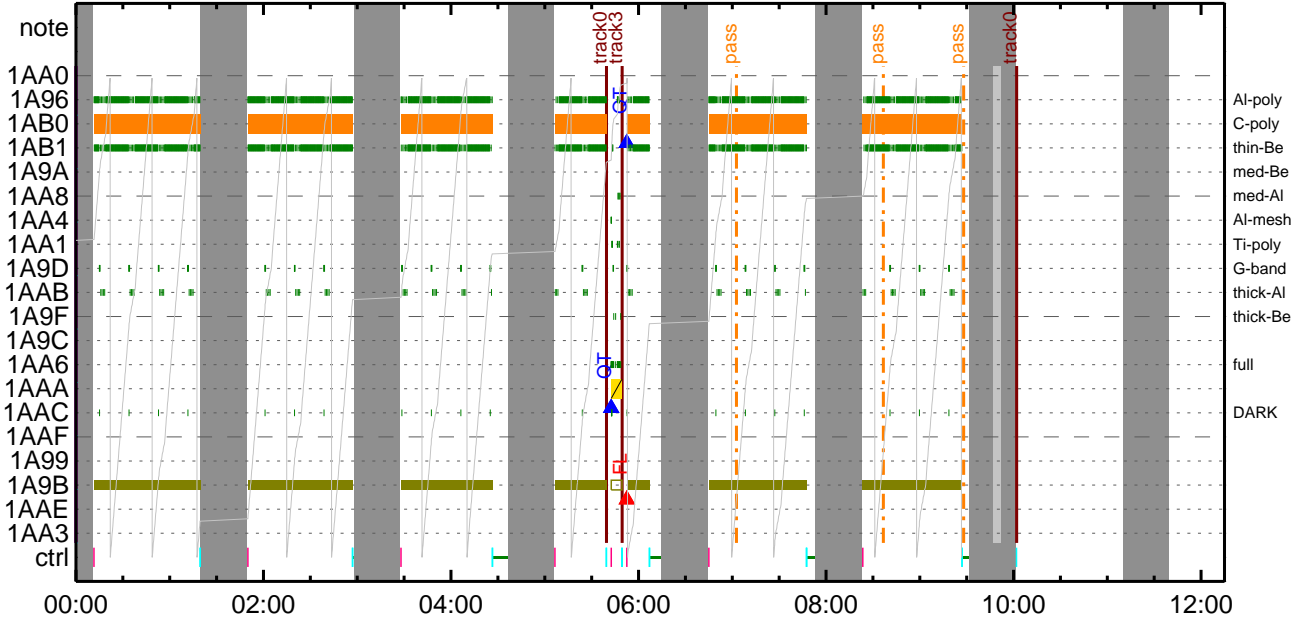
### CMDI #0270 2015/07/17



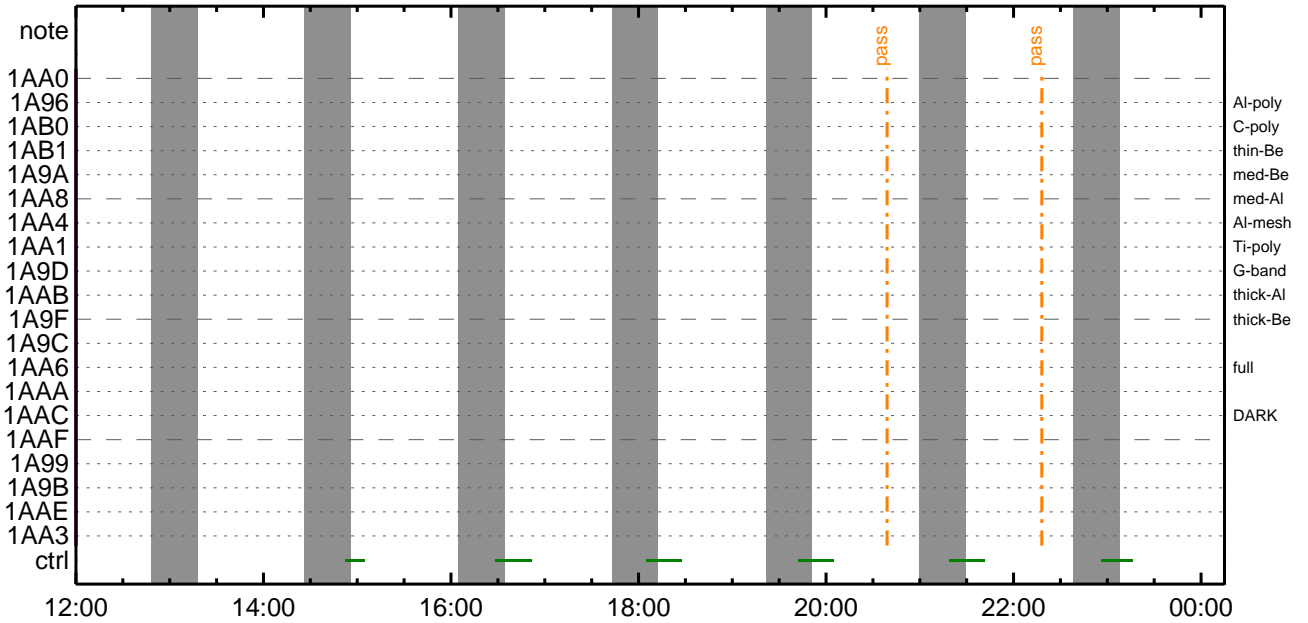
### CMDI #0270 2015/07/17



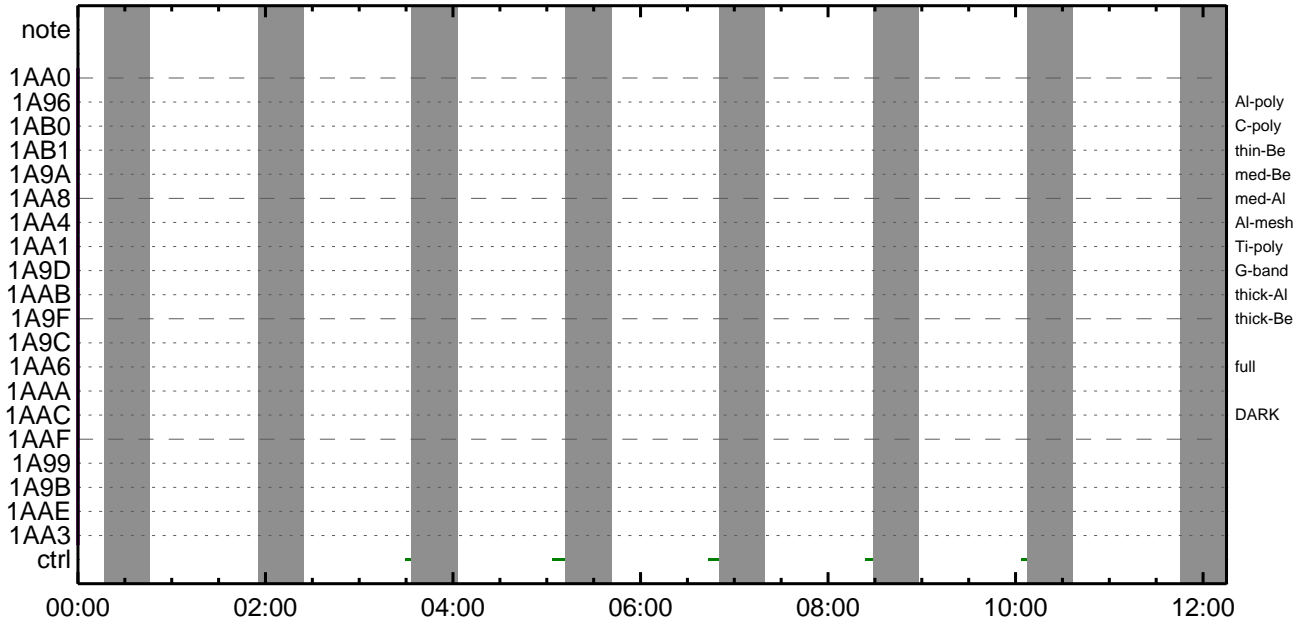
### CMDI #0270 2015/07/18



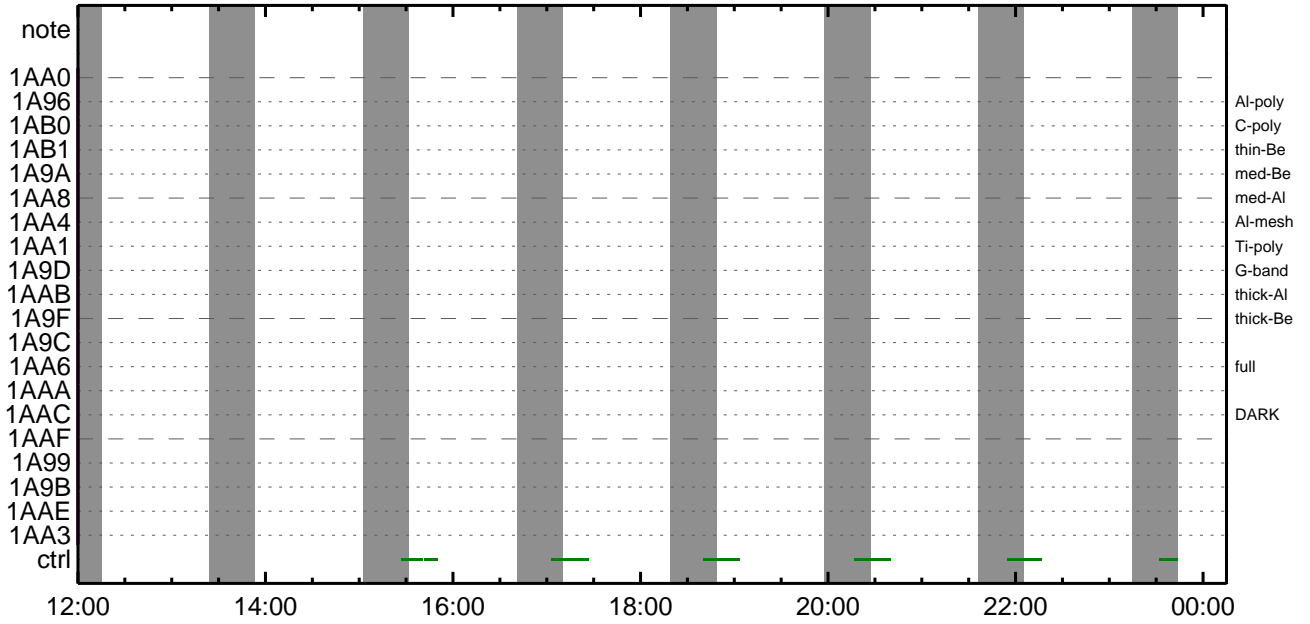
### CMDI #0270 2015/07/18



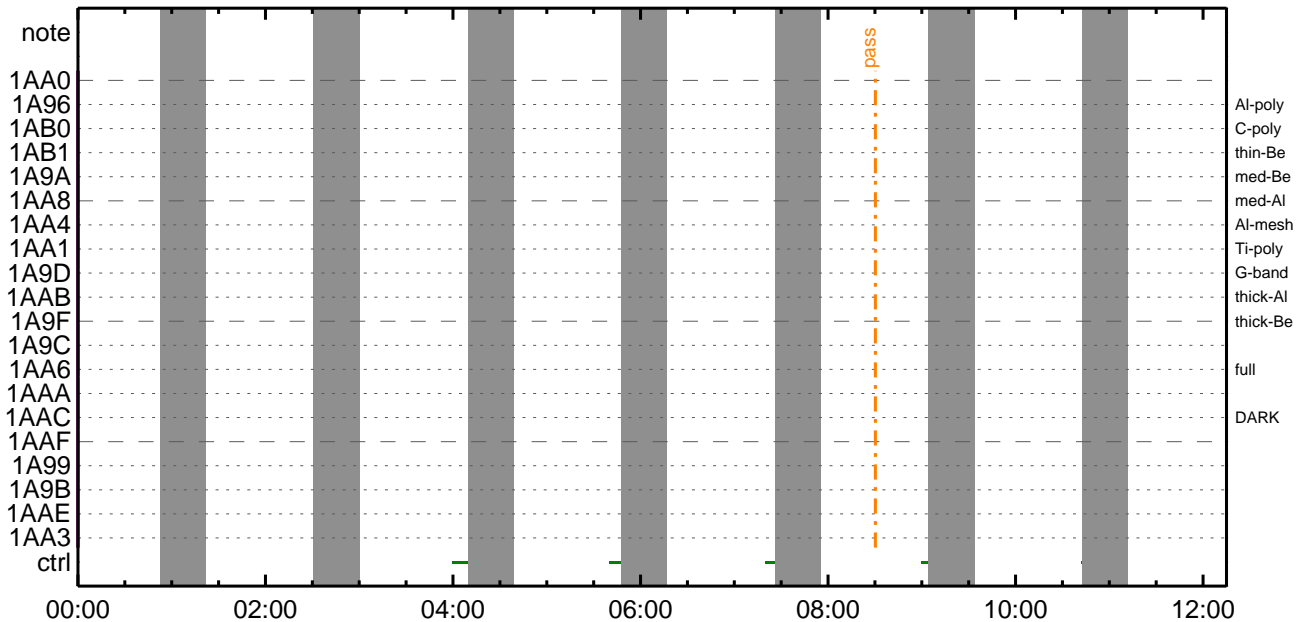
### CMDI #0270 2015/07/19



### CMDI #0270 2015/07/19



### CMDI #0270 2015/07/20









```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP      op-261:OP
0104 . ( )
0105 . S. OG      og-261:OG
0106 . ( )
0107 C.
0108 . C. ;ãNMOG&OPîî°ëYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 C.      çç[HK1_DMP_TOP_ADRS_1]          EQ      40
0113 C.      çç[HK1_DMP_TOP_ADRS_0]          EQ      0
0114 C.      çç[HK1_DMP_BLOCK_NUM]          EQ     127
0115 C.      çç[HK1_DMP_REPEAT_NUM]         EQ      0
0116 C.      çç[HK1_DMA_DMP_PIM]            EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 C.      çç[HK1_PKT_FORM_NO]             EQ      7
0120 C.      çç[HK1_PKT_GEN_TIME]           EQ     0.25 s
0121 C.      çç[HK1_S_TLM_BIT_RATE]         EQ     32k
0122 C.      çç[HK1_X_TLM_BIT_RATE]         EQ     4M
0123 C.      çç[HK1_DMP_CHK_FLG]           EQ     EXEC
0124 . C.  YAYôYx½ªî»ð³îÇ§
0125 C.      çç[HK1_DMP_CHK_FLG]             EQ     NON
0126 . C. RAM ID=NMOGñî¼Ê¹ç•ê²îOKð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 C.      çç[HK1_DMP_TOP_ADRS_1]          EQ     41
0132 C.      çç[HK1_DMP_TOP_ADRS_0]          EQ      0
0133 C.      çç[HK1_DMP_BLOCK_NUM]          EQ     127
0134 C.      çç[HK1_DMP_REPEAT_NUM]         EQ      0
0135 C.      çç[HK1_DMA_DMP_PIM]            EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 C.      çç[HK1_PKT_FORM_NO]             EQ      7
0139 C.      çç[HK1_PKT_GEN_TIME]           EQ     0.25 s
0140 C.      çç[HK1_S_TLM_BIT_RATE]         EQ     32k
0141 C.      çç[HK1_X_TLM_BIT_RATE]         EQ     4M
0142 C.      çç[HK1_DMP_CHK_FLG]           EQ     EXEC
0143 . C.  YAYôYx½ªî»ð³îÇ§
0144 C.      çç[HK1_DMP_CHK_FLG]             EQ     NON
0145 . C. RAM ID=NMOGñî¼Ê¹ç•ê²îOKð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 C.      çç[HK1_DMP_TOP_ADRS_1]          EQ     42
0151 C.      çç[HK1_DMP_TOP_ADRS_0]          EQ      0
0152 C.      çç[HK1_DMP_BLOCK_NUM]          EQ     65
0153 C.      çç[HK1_DMP_REPEAT_NUM]         EQ      0
0154 C.      çç[HK1_DMA_DMP_PIM]            EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 C.      çç[HK1_PKT_FORM_NO]             EQ      7
0158 C.      çç[HK1_PKT_GEN_TIME]           EQ     0.25 s
0159 C.      çç[HK1_S_TLM_BIT_RATE]         EQ     32k
0160 C.      çç[HK1_X_TLM_BIT_RATE]         EQ     4M
0161 C.      çç[HK1_DMP_CHK_FLG]           EQ     EXEC
0162 . C.  YAYôYx½ªî»ð³îÇ§
0163 C.      çç[HK1_DMP_CHK_FLG]             EQ     NON
0164 . C. RAM ID=NMOG,RAM ID=OPñî¼Ê¹ç•ê²îOKð³îÇ§
0165 C.
0166 . C. ***** òÊ²¼òî¼Ä´¶Ã°òÊÊ¬ò°À÷¿® (¼åµ-YAYôYx½ªê¼çòðÀÔÃæç¼ª°"òÊ¼¹¹çòçðâ) *****
0167 C. DHUYâ;½YE;Ê¼Y½;Yî;½YE;Êðîïã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 C.      çç[HK1_PKT_FORM_NO]             EQ      2
0171 C.      çç[HK1_PKT_GEN_TIME]           EQ     0.5S
0172 C.      çç[HK1_S_TLM_BIT_RATE]         EQ     32K
0173 C.      çç[HK1_X_TLM_BIT_RATE]         EQ     4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOADñ-Á÷¿®NGñî¼î¹ç;ç°Ê²¼òîïTI-CMDÁ÷¿®ñî¼¹Ôñ•ñÊñññ³ñÊ;ç
0180 C.      òÊ¼;çSETòEDUMPIÊ±°îYÑY¹ñÇ¹Ôñ|ñ³ñÊ;ç
0181 C.
0182 . C. TIY³Y½YôYÊðððÀDî¿(UT)
0183 +. TI 2015-07-16 10:14:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.      çç[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186 C.
0187 +. TI 2015-07-16 10:14:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.      çç[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190 C.
0191 +. TI 2015-07-16 10:14:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.      çç[HK1_TI_CMD_NUM]              EQ     1COUNTUP

```

```
0194 C.
0195 +. TI 2015-07-16 10:18:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼□İÄē%İİñ□İŷÄŷ§ŷÄŷ¹âİÜ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°ēŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½¹İ»□ò³İÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL□İ½È¹ç•ē²İOK□ò³İÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼. ŷİ;¼ŷÈ;È□òİâ□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2015-07-16 10:18:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2015-07-16 10:18:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2015-07-16 10:18:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2015-07-16 10:18:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÄİ□İ»ö¼ŷ□ÈÄ□¹□ēDCBC•x²è *****
0276 C. (¼â°İŷÖŷÄŷÈŷŷŷÄŷçŷè□È¼□□¼Ä»Û□¹□è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹.İ Daily±çİñ□È´Ø□¹□ēDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. İ;ãLOSŷÄŷ§ŷÄŷ¹¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. *****
0130 C. FPP CCD Heaters ENA
0131 C.
0132 C.
0133 C.
0134 C.
0135 C. *****
0136 C.
0137 . C. < FG Decontam Level 1 -45 to -35, Duty=15% >
0138 C. ( Set SP heater duty = 0 % (for safety))
0139 +. DC 07-F2 FPP_FG_OPHTR_DUTY
0140 BC (54 03 0f)
0141 + DC 07-F2 FPP_FG_OPHTR_BAND
0142 BC (56 06 fe a2 fe 3e)
0143 + DC 07-F2 FPP_SP_OPHTR_DUTY
0144 BC (4c 03 00)
0145 . C. -----
0146 C. FPP_FG_CCDHTR_DC = 15 [ ]
0147 C. FPP_FG_CCDHTR_DB_HI = -35 [ ]
0148 C. FPP_FG_CCDHTR_DB_LO = -45 [ ]
0149 C. FPP_SP_CCDHTR_DC = 00 [ ]
0150 C. -----
0151 C.
0152 . C. Pause XRT obs by XRT_CTRL_MANU
0153 C.
0154 . C. < FPP Decontam Mode Enable >
0155 +. DC 07-F2 FPP_PWR_ON_ENA
0156 BC (12 02)
0157 +. DC 07-F2 FPP_DE_CONTAM_MODE
0158 BC (24 03 01)
0159 . C. -----
0160 C. FPP_DE_CONTAM_MODE = ON [ ]
0161 C. -----
0162 C.
0163 . C. Resume XRT obs by XRT_CTRL_AUTO
0164 C.
0165 . C. < Disable SP Heater >
0166 C. (Note: Both SP/FG Decontam heaters come on
0167 C. when Decontam mode is entered)
0168 +. DC 07-F2 FPP_SP_OPHTR_DIS
0169 BC (4a 02)
0170 . C. -----
0171 C. FPP_SPHTR_STAT = 01 (OFF) [ ]
0172 C. -----
0173 C.
0174 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0175 +. DC 07-FC EIS_MODE_CHG_ENA
0176 BC (20)
0177 . C. Verify EIS_MODE_CHG_FLG is ENA
0178 +. DC 07-FC EIS_MODE_MANU
0179 BC (21 02)
0180 . C. Verify EIS in MANUAL mode
0181 . C. Estimated OBSTBL upload time is 1m6s
0182 C. *****
0183 C. EIS START OBSTBL LOAD
0184 C. *****
0185 . S. RAM ram-820:EIS_OBSTBL
0186 ( )
0187 +. DC 07-FC EIS_DUMP_OBSTBL
0188 BC (07 07 07 00 00 70 00)
0189 C.
0190 C. Execute, after the success of OBSTBL upload.
0191 C. Set EIS TI-commands
0192 +. TI 2015-07-16 10:18:50.0
0193 DC 07-FC EIS_MODE_CHG_ENA

```

```

0194 BC (20)
0195 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0196 C. *****
0197 C. EIS END OBSTBL LOAD
0198 C. *****
0199 C.
0200 . C. ***** MDP 'ûÃîñî»ò¼ŷñÊÂĐñ¹ñèDCBC•x²è *****
0201 C. (¼á°îŷÓŷÃŷÈŷŦŷËŷáŷçŷèñÊ¼ñ¼¼Â»Ûñ¹ñè)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ŷĐŷ¹•İ Daily±¿İññÊ´Øñ¹ñèDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ;ãLOSŷÁŷŦŷËŷ¹¼Â»Û¿ä
0212 C.
0213 . C. ***** LOS *****
0214 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-263 2015-07-16 11:51:36 140 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èð¿αΑβ•μ°Ε»Í×ÁÇαÍYçYÁY×Yí;¼YÉ;ÈÈè%μ•íÍÉ;ÈβÈ¼°ÇÔα•α¿¼í¹çαÍ;çÀ®, ùα¹αèαβαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-262:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2015-07-16 10:18:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG_____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 80 80 20 20)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 80 80 20 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 80 80 08 20)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 c0 c0 10 10)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0a 40 c0 10 10)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0b 40 40 10 10)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0c c0 40 10 10)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0d 85 83 06 06)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 0e 85 83 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 0f 80 80 06 06)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 10 80 80 08 08)
0098 + DC 07-F0 MDP_XRT_FLD_ENA
0099 BC (d8)
0100 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0101 BC (c8)
0102 + DC 07-F0 MDP_XRT_AEC_RESET
0103 BC (d0)
0104 + DC 07-F0 MDP_XRT_ARS_DIS
0105 BC (d5)
0106 + DC 07-F0 MDP_XRT_FLD_RESET
0107 BC (da)
0108 + DC 07-F0 MDP_XRT_QT_PROG_SET
0109 BC (c4 07)
0110 + DC 07-F0 MDP_XRT_FL_PROG_SET
0111 BC (c5 03)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C.
0115 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0116 C.
0117 + DC 07-F0 MDP_XRT_MODE_OBSV
0118 BC (c2)
0119 + TI 2015-07-16 10:18:02.0
0120 DC 07-F0 MDP_XRT_MODE_OBSV
0121 BC (c2)
0122 . C. ----- Success Verify ? OK / NG ____
0123 C.
0124 C. ***** XRT END *****
0125 C.
0126 . C. ***** MDP `úÃîñî»ö%ÝñÊÃðñ¹ñèDCBC•x²è *****
0127 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝÁÝÇÝèñÊ%¼ñ¼Ã»Ûñ¹ñè)
0128 . S. DC-BC dcbc-402:DCBC
0129 (MDP_known_event)
0130 C.
0131 C.
0132 . C. ***** ¥ÐÝ¹•Ï Daily±¿ÍÑñÊ´Øñ¹ñèDCBC•x²è *****
0133 . S. DC-BC dcbc-153:DCBC
0134 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0135 C.
0136 C.
0137 . C. ¡ãLOS¥Á¥$¥Ã¥¬¼Ã»Ûñ¹ñè
0138 C.
0139 . C. ***** LOS *****
0140 C.
```



Jul 16, 15 11:51

## XRT\_OGLIST\_0270.chk

Page 1/9

\*\*\* OP Sequence for XRT \*\*\*

2015/07/16	10:29:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00	00	00	00
2015/07/16	18:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/16	18:27:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_406_OG [0x196]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2015/07/16	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/16	23:59:56.0	XRT_CTRL_MANU_407_OG [0x197]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	00:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2015/07/17	00:02:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/07/17	00:02:52.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2015/07/17	00:02:54.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/17	00:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/17	00:02:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/17	00:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/17	00:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	00:09:56.0	XRT_CTRL_MANU_407_OG [0x197]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	00:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2015/07/17	00:12:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/07/17	00:12:52.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2015/07/17	00:12:54.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/17	00:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/17	00:12:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/17	00:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/17	00:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	00:19:56.0	XRT_CTRL_MANU_407_OG [0x197]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	00:20:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2015/07/17	00:22:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/07/17	00:22:52.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2015/07/17	00:22:54.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/17	00:22:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/17	00:22:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/17	00:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/17	00:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	00:29:56.0	XRT_CTRL_MANU_407_OG [0x197]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	00:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2015/07/17	00:32:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/07/17	00:32:52.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2015/07/17	00:32:54.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/17	00:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/17	00:32:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/17	00:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/17	00:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	00:43:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	00:43:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/17	00:43:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/17	00:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/17	01:12:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/17	01:12:56.0	XRT_CTRL_MANU_402_OG [0x192]							

2015/07/17	01:12:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			XRT_FOCUS_RECAL	2	07-F8	78	00
2015/07/17	01:13:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 00 00	ac cd
2015/07/17	01:16:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2015/07/17	01:17:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/17	01:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/17	01:17:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/17	01:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/17	01:17:26.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/17	01:19:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2015/07/17	01:20:00.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/07/17	01:20:02.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/17	01:28:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	01:28:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	01:28:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2015/07/17	01:29:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00	d6 67
2015/07/17	01:29:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/17	01:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/17	01:29:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/17	01:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/17	01:29:26.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/17	01:31:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2015/07/17	01:32:00.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/07/17	01:32:02.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/17	01:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	01:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	01:44:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00
2015/07/17	01:45:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 00 00	00 00
2015/07/17	01:45:18.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/07/17	01:47:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/07/17	01:47:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/17	01:48:00.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2015/07/17	01:48:02.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/17	02:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	02:00:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	02:00:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2015/07/17	02:01:00.0	AOCS_Ore-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 00 00	29 99
2015/07/17	02:01:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/17	02:01:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/17	02:01:22.5	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/17	02:01:24.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/17	02:01:26.5	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/17	02:03:58.5	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2015/07/17	02:04:00.5	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/07/17	02:04:02.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/17	02:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	02:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

Jul 16, 15 11:51

## XRT\_OGLIST\_0270.chk

Page 3/9

2015/07/17	02:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	02:22:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/17	02:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/17	02:51:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	02:51:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	02:51:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/17	02:51:30.0	AOCS_OrE-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 00 00 53 33
2015/07/17	02:51:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/17	02:51:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/17	02:51:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/17	02:51:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/17	02:51:56.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	02:54:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2015/07/17	02:54:30.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/17	02:54:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/17	03:07:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	03:07:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	03:07:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/17	03:07:30.0	AOCS_OrE-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 d6 36 b7 8e
2015/07/17	03:07:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/17	03:07:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/17	03:07:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/17	03:07:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/17	03:07:56.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	03:10:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2015/07/17	03:10:30.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/17	03:10:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/17	03:17:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	03:17:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	03:17:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/17	03:17:30.0	AOCS_OrE-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 b4 b5 db 75
2015/07/17	03:17:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/17	03:17:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/17	03:17:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/17	03:17:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/17	03:17:56.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	03:20:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2015/07/17	03:20:30.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/17	03:20:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/17	03:33:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	03:33:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	03:33:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/17	03:33:30.0	AOCS_OrE-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 ac 5b 00 00
2015/07/17	03:33:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/17	03:33:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/17	03:33:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/17	03:33:54.0	XRT_ARS_DIS_423_OG [0x1a7]				

Jul 16, 15 11:51

## XRT\_OGLIST\_0270.chk

Page 4/9

2015/07/17	03:33:56.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/17	03:36:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2015/07/17	03:36:30.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/07/17	03:36:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/17	03:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/17	03:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/17	03:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/17	03:50:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/07/17	03:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/07/17	04:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/17	04:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/17	04:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/07/17	04:30:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	b4	b5	24 8b
2015/07/17	04:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/07/17	04:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/07/17	04:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/07/17	04:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/07/17	04:30:26.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/17	04:32:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2015/07/17	04:33:00.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/07/17	04:33:02.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/17	04:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/17	04:45:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/17	04:45:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/07/17	04:46:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	d6	36	48 72
2015/07/17	04:46:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/07/17	04:46:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/07/17	04:46:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/07/17	04:46:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/07/17	04:46:26.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/17	04:48:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2015/07/17	04:49:00.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/07/17	04:49:02.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/17	04:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/17	04:55:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/07/17	04:55:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/07/17	04:56:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00	29	ca	b7 8e
2015/07/17	04:56:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/07/17	04:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/07/17	04:56:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/07/17	04:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/07/17	04:56:26.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/07/17	04:58:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2015/07/17	04:59:00.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/07/17	04:59:02.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/07/17	05:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

Jul 16, 15 11:51

## XRT\_OGLIST\_0270.chk

Page 5/9

2015/07/17	05:05:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	05:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/17	05:06:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	00 4b 4b db 75
2015/07/17	05:06:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/17	05:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/17	05:06:22.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/17	05:06:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/17	05:06:26.0	XRT_FLD_RESET_443_OG [0x1bb] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	05:08:58.0	XRT_QT_PROG_SET_421_OG [0x1a5] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2015/07/17	05:09:00.0	XRT_FL_PROG_SET_428_OG [0x1ac] MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/17	05:09:02.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/17	05:30:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	05:30:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	05:30:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	05:30:36.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/17	05:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/17	06:08:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	06:08:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	06:08:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/17	06:08:30.0	AOCS_ORe-point_Start_18_OG [0x0a8] AOCU_NM	5	02-76	00 53 a5 00 00
2015/07/17	06:08:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/17	06:08:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/17	06:08:52.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/17	06:08:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/17	06:08:56.0	XRT_FLD_RESET_443_OG [0x1bb] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	06:11:28.0	XRT_QT_PROG_SET_421_OG [0x1a5] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2015/07/17	06:11:30.0	XRT_FL_PROG_SET_428_OG [0x1ac] MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/17	06:11:32.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/17	06:24:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	06:24:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	06:24:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/17	06:24:30.0	AOCS_ORe-point_Start_19_OG [0x0a9] AOCU_NM	5	02-76	00 4b 4b 24 8b
2015/07/17	06:24:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/17	06:24:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/17	06:24:52.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/17	06:24:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/17	06:24:56.0	XRT_FLD_RESET_443_OG [0x1bb] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	06:27:28.0	XRT_QT_PROG_SET_421_OG [0x1a5] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2015/07/17	06:27:30.0	XRT_FL_PROG_SET_428_OG [0x1ac] MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/17	06:27:32.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/17	06:40:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	06:40:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	06:40:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/17	06:40:30.0	AOCS_ORe-point_Start_20_OG [0x0aa] AOCU_NM	5	02-76	00 29 db 48 72
2015/07/17	06:40:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/17	06:40:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/17	06:40:52.0	XRT_AEC_RESET_441_OG [0x1b9]			

2015/07/17	06:40:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/17	06:40:56.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/17	06:43:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/17	06:43:30.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2015/07/17	06:43:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2015/07/17	06:50:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/17	06:50:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	06:50:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	06:50:30.0	AOCS_Ore-point_Start_8_OG [0x09e]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2015/07/17	06:50:48.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00	00 00 00 00	
2015/07/17	06:53:26.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/07/17	06:53:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/07/17	06:53:30.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/17	06:53:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07	
2015/07/17	07:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/17	07:00:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	07:00:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	07:00:30.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2015/07/17	07:00:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	00 00 00 00	
2015/07/17	07:00:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/17	07:00:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/17	07:00:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/17	07:00:56.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/17	07:03:28.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/17	07:03:30.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2015/07/17	07:03:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2015/07/17	07:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/17	07:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	07:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	07:11:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/17	07:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/17	07:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/17	07:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2015/07/17	08:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/17	08:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	08:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	08:50:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/17	08:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/17	09:25:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/17	09:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2015/07/17	10:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/17	10:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	10:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/17	10:32:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/17	10:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/17	11:03:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/17	11:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2015/07/17	12:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

2015/07/17	12:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	12:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	12:12:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/17	12:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/17	12:42:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/17	12:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/17	13:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	13:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	13:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	13:51:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/17	13:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/17	14:27:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/17	14:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/17	15:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	15:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	15:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	15:29:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/17	15:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/17	16:16:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/17	16:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/17	17:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	17:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	17:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	17:07:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/17	17:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/17	17:53:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/17	17:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/17	17:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	17:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	17:54:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	17:55:00.0	AOCS_Orе-point_Start_8_OG [0x09e]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/17	17:55:18.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/07/17	17:57:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/07/17	17:57:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/07/17	17:58:00.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/17	17:58:02.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2015/07/17	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/17	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	18:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/17	18:05:00.0	AOCS_Orе-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/07/17	18:05:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/07/17	18:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/17	18:05:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/17	18:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/17	18:05:26.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/17	18:07:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/17	18:08:00.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2015/07/17	18:08:02.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/07/17			MDP_XRT_CTRL_AUTO	1	07-F0	c0	

Jul 16, 15 11:51

## XRT\_OGLIST\_0270.chk

Page 8/9

2015/07/17	18:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	18:46:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	18:46:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	18:46:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/17	18:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/17	19:29:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/17	19:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/17	20:24:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	20:24:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	20:24:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	20:24:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/17	20:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/17	21:07:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/17	21:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/17	22:02:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	22:02:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	22:02:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	22:02:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/17	22:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/17	22:42:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/17	22:43:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/17	23:41:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	23:41:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/17	23:41:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/17	23:41:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/17	23:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/18	00:10:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/18	00:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/18	01:19:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	01:19:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	01:19:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/18	01:19:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/18	01:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/18	01:49:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/18	01:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/18	02:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	02:57:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	02:57:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/18	02:57:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/18	03:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/18	03:27:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/18	03:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/18	04:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	04:26:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	04:26:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/18	04:26:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/18	04:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/18	05:05:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/18	05:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/18	05:39:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1



Jul 16, 15 11:51

## XRT\_OGLIST\_0270.chk

Page 9/9

2015/07/18	05:39:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	05:39:28.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/18	05:39:30.0	AOCS_ORe-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00 00 00 00 00
2015/07/18	05:39:48.0	XRT_FLD_DIS_418_OG [0x1a2] MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/18	05:42:26.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/18	05:42:28.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/18	05:42:30.0	XRT_QT_PROG_SET_432_OG [0x1b0] MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2015/07/18	05:42:32.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/18	05:49:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	05:49:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	05:49:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/18	05:49:30.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	03 00 00 00 00
2015/07/18	05:49:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/18	05:49:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/18	05:49:52.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/18	05:49:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/18	05:49:56.0	XRT_FLD_RESET_443_OG [0x1bb] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/18	05:52:28.0	XRT_QT_PROG_SET_422_OG [0x1a6] MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2015/07/18	05:52:30.0	XRT_FL_PROG_SET_428_OG [0x1ac] MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/18	05:52:32.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/18	06:07:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	06:07:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	06:07:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/18	06:07:06.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/18	06:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/18	06:44:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/18	06:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/18	07:47:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	07:47:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	07:47:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/18	07:47:36.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/18	07:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/18	08:22:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/18	08:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/18	09:27:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	09:27:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	09:27:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/18	09:27:06.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/18	09:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/18	10:01:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/18	10:02:00.0	AOCS_ORe-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00 00 00 00 00