

# XRT Timeline to be uploaded on 2015/07/18

Period: 2015/07/18 09:52:00 - 2015/07/28 10:54:00

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Normal mode

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## XOB #1AB5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)

Term	Pointing (x, y)	Comment
07/18 10:05:00 - 07/18 15:49:54	Track ( -166.4, 129.1) <sup>@ 07/18 10:02:00</sup>	# OP start + 10min AR 12386
07/20 23:15:00 - 07/21 05:48:24	Track ( -122.6, 139.2) <sup>@ 07/20 23:12:00</sup>	AR 12387 obs.
07/21 06:01:30 - 07/21 09:36:30	Track ( -62.8, 138.3) <sup>@ 07/21 05:58:30</sup>	AR 12387 obs.

**PROG= 06 1-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 9 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 92 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn= 42 4-time(s) 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
  - Seqn= 17 8-time(s) 360.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 110.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 110.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 110.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-

Term	Pointing (x, y)	Comment
07/18 15:53:00 - 07/18 16:02:00	Fixed ( 0.0, 0.0)	synoptic with stray light study
07/19 18:08:00 - 07/19 18:16:00	Fixed ( 0.0, 0.0)	synoptic and stray light study
07/20 06:25:00 - 07/20 06:31:54	Fixed ( 0.0, 0.0)	synoptic, shifted 22.0 min
07/20 18:05:00 - 07/20 18:11:54	Fixed ( 0.0, 0.0)	synoptic, shifted 2.0 min
07/21 05:52:56 - 07/21 05:58:24	Fixed ( 0.0, 0.0)	synoptic, shifted -11.5 min

**PROG= 14 1-time(s)**

- Subr= 1 1-time(s) 12.0sec
  - Seqn= 33 1-time(s) 4.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 61 1-time(s) 4.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 49 1-time(s) 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 39 1-time(s) 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 54 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1AB4: Stray light 2015-19 ; Ti-poly Al-mesh 2x2 full FOV(1min-cad) and 2x2 256 on North-DC(10sec-cad)

Term	Pointing (x, y)	Comment
07/18 16:04:30 - 07/18 16:52:24	Fixed ( 0.0, 0.0)	synoptic with stray light study
07/19 18:18:30 - 07/19 18:44:54	Fixed ( 0.0, 0.0)	synoptic and stray light study

**PROG= 04 1-time(s)**

- Subr= 2 16-time(s) 2.0sec
  - Seqn= 27 1-time(s) 10.0sec
    - Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 2x2 512x512 (1024, 1200) Q=95 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 1ms Obs 2x2 512x512 (1024, 1200) Q=95 0 0 2.0sec

Subr= 3	7-time(s)	2.0sec											
Seqn= 4	1-time(s)	2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	DPCM	0	0	2.0sec
Seqn= 27	3-time(s)	10.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	512x512	(1024, 1200)	Q=95	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	512x512	(1024, 1200)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1AA2: CME watch - 4x4 - AEC 2 - Al-poly - G-band (2x2,1ms) - Leak (2x2,1ms) - 120s cad													
Term	Pointing (x, y)		Comment										
07/18 16:55:30 - 07/18 17:43:30	Fixed (	-10.0, -994.0)	HOP 79 N-S 1/20										
07/18 18:32:00 - 07/18 19:21:30	Fixed (	-10.0, -894.0)	HOP 79 N-S 2/20										
07/18 20:09:00 - 07/18 21:00:00	Fixed (	-10.0, -794.0)	HOP 79 N-S 3/20										
07/18 21:46:00 - 07/18 22:38:30	Fixed (	-10.0, -694.0)	HOP 79 N-S 4/20										
07/18 23:20:30 - 07/19 00:17:00	Fixed (	-10.0, -594.0)	HOP 79 N-S 5/20										
07/19 00:50:00 - 07/19 01:16:54	Fixed (	-10.0, -494.0)	HOP 79 N-S 6/20										
07/19 01:20:00 - 07/19 01:55:30	Fixed (	-10.0, -394.0)	HOP 79 N-S 7/20										
07/19 02:28:30 - 07/19 02:55:24	Fixed (	-10.0, -294.0)	HOP 79 N-S 8/20										
07/19 02:58:30 - 07/19 03:29:30	Fixed (	-10.0, -194.0)	HOP 79 N-S 9/20										
07/19 04:07:00 - 07/19 05:03:00	Fixed (	-10.0, -94.0)	HOP 79 N-S 10/20										
07/19 05:55:38 - 07/19 06:43:30	Fixed (	-10.0, -4.0)	HOP 79 N-S 11/20										
07/19 07:23:30 - 07/19 07:50:24	Fixed (	-10.0, 96.0)	HOP 79 N-S 12/20										
07/19 07:53:30 - 07/19 08:23:30	Fixed (	-10.0, 196.0)	HOP 79 N-S 13/20										
07/19 09:02:00 - 07/19 09:28:54	Fixed (	-10.0, 296.0)	HOP 79 N-S 14/20										
07/19 09:32:00 - 07/19 10:04:00	Fixed (	-10.0, 396.0)	HOP 79 N-S 15/20										
07/19 10:40:30 - 07/19 11:07:24	Fixed (	-10.0, 496.0)	HOP 79 N-S 16/20										
07/19 11:10:30 - 07/19 11:45:30	Fixed (	-10.0, 596.0)	HOP 79 N-S 17/20										
07/19 12:19:00 - 07/19 12:45:54	Fixed (	-10.0, 696.0)	HOP 79 N-S 18/20										
07/19 12:49:00 - 07/19 13:24:00	Fixed (	-10.0, 796.0)	HOP 79 N-S 19/20										
07/19 13:57:00 - 07/19 14:33:54	Fixed (	-10.0, 896.0)	HOP 79 N-S 20/20										
07/19 14:37:00 - 07/19 16:41:00	Fixed (	750.0, 490.0)	EIS off-limb obs.										
07/20 18:15:00 - 07/20 23:11:54	Fixed (	0.0, -150.0)	SOT flat field										

PROG= 01	Inf.-time(s)												
Subr= 1	30-time(s)	120.0sec											
Seqn= 78	1-time(s)	4.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 26	1-time(s)	2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1AB3: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/1024), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45)													
Term	Pointing (x, y)		Comment										
07/19 05:45:30 - 07/19 05:55:00	Fixed (	-10.0, -4.0)	HOP 79 N-S 11/20										

PROG= 08	1-time(s)												
Subr= 1	1-time(s)	12.0sec											
Seqn= 33	1-time(s)	4.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5	1-time(s)	2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
Seqn= 61	1-time(s)	4.0sec											
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77	1-time(s)	2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	12.0sec											
Seqn= 46	2-time(s)	2.0sec											
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 25	2-time(s)	4.0sec											
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 21	2-time(s)	4.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 37 2-time(s) 2.0sec</b>												
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1AA8: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)**

Term	Pointing (x, y)	Comment
07/19 17:37:00 - 07/19 18:04:54	Track ( -374.6, 146.8) <sup>Ⓢ 07/19 17:34:00</sup>	AR 12387 obs.
07/19 22:48:00 - 07/20 05:39:30	Track ( -332.2, 145.1) <sup>Ⓢ 07/19 22:45:00</sup>	AR 12387 obs.
07/20 06:35:00 - 07/20 17:16:30	Track ( -267.0, 142.8) <sup>Ⓢ 07/20 06:32:00</sup>	AR 12387 obs.

**PROG= 15 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 9 2-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 92 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 42 4-time(s) 2.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 62 8-time(s) 240.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1A9C: G-Band Alignment with North Pole Q90 2x2(G-band and VLS=CLS) - 1msec - 5min cadence - Partial Sun-wNGT**

Term	Pointing (x, y)	Comment
07/19 19:00:00 - 07/19 20:44:54	Fixed ( 0.0, 900.0)	# Co-alignment North

**PROG= 09 1-time(s)**

<b>Subr= 1 24-time(s) 300.0sec</b>												
<b>Seqn= 98 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
<b>Seqn= 63 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1A9D: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec- 8 min cadence-wNGT**

Term	Pointing (x, y)	Comment
07/19 21:00:00 - 07/19 22:44:54	Fixed ( -945.0, 0.0)	Co-alignment East limb

**PROG= 12 1-time(s)**

<b>Subr= 1 15-time(s) 480.0sec</b>												
<b>Seqn= 19 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
<b>Seqn= 43 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

**Flare mode**

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**XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
07/18 10:05:00 - 07/18 15:49:54	Track ( -166.4, 129.1) <sup>Ⓢ 07/18 10:02:00</sup>	# OP start + 10min AR 12386
07/18 16:55:30 - 07/18 17:43:30	Fixed ( -10.0, -994.0)	HOP 79 N-S 1/20
07/18 18:32:00 - 07/18 19:21:30	Fixed ( -10.0, -894.0)	HOP 79 N-S 2/20
07/18 20:09:00 - 07/18 21:00:00	Fixed ( -10.0, -794.0)	HOP 79 N-S 3/20
07/18 21:46:00 - 07/18 22:38:30	Fixed ( -10.0, -694.0)	HOP 79 N-S 4/20
07/18 23:20:30 - 07/19 00:17:00	Fixed ( -10.0, -594.0)	HOP 79 N-S 5/20
07/19 00:50:00 - 07/19 01:16:54	Fixed ( -10.0, -494.0)	HOP 79 N-S 6/20
07/19 01:20:00 - 07/19 01:55:30	Fixed ( -10.0, -394.0)	HOP 79 N-S 7/20
07/19 02:28:30 - 07/19 02:55:24	Fixed ( -10.0, -294.0)	HOP 79 N-S 8/20
07/19 02:58:30 - 07/19 03:29:30	Fixed ( -10.0, -194.0)	HOP 79 N-S 9/20
07/19 04:07:00 - 07/19 05:03:00	Fixed ( -10.0, -94.0)	HOP 79 N-S 10/20
07/19 05:55:38 - 07/19 06:43:30	Fixed ( -10.0, -4.0)	HOP 79 N-S 11/20
07/19 07:23:30 - 07/19 07:50:24	Fixed ( -10.0, 96.0)	HOP 79 N-S 12/20
07/19 07:53:30 - 07/19 08:23:30	Fixed ( -10.0, 196.0)	HOP 79 N-S 13/20
07/19 09:02:00 - 07/19 09:28:54	Fixed ( -10.0, 296.0)	HOP 79 N-S 14/20
07/19 09:32:00 - 07/19 10:04:00	Fixed ( -10.0, 396.0)	HOP 79 N-S 15/20
07/19 10:40:30 - 07/19 11:07:24	Fixed ( -10.0, 496.0)	HOP 79 N-S 16/20
07/19 11:10:30 - 07/19 11:45:30	Fixed ( -10.0, 596.0)	HOP 79 N-S 17/20

07/19 12:19:00 - 07/19 12:45:54	Fixed ( -10.0, 696.0)	HOP 79 N-S 18/20
07/19 12:49:00 - 07/19 13:24:00	Fixed ( -10.0, 796.0)	HOP 79 N-S 19/20
07/19 13:57:00 - 07/19 14:33:54	Fixed ( -10.0, 896.0)	HOP 79 N-S 20/20
07/19 14:37:00 - 07/19 16:41:00	Fixed ( 750.0, 490.0)	EIS off-limb obs.
07/19 17:37:00 - 07/19 18:04:54	Track ( -374.6, 146.8) <sup>Ⓢ 07/19 17:34:00</sup>	AR 12387 obs.
07/19 22:48:00 - 07/20 05:39:30	Track ( -332.2, 145.1) <sup>Ⓢ 07/19 22:45:00</sup>	AR 12387 obs.
07/20 06:35:00 - 07/20 17:16:30	Track ( -267.0, 142.8) <sup>Ⓢ 07/20 06:32:00</sup>	AR 12387 obs.
07/20 18:15:00 - 07/20 23:11:54	Fixed ( 0.0, -150.0)	SOT flat field
07/20 23:15:00 - 07/21 05:48:24	Track ( -122.6, 139.2) <sup>Ⓢ 07/20 23:12:00</sup>	AR 12387 obs.
07/21 06:01:30 - 07/21 09:36:30	Track ( -62.8, 138.3) <sup>Ⓢ 07/21 05:58:30</sup>	AR 12387 obs.

**PROG= 03 30-time(s)**

<b>Subr= 1</b>	<b>20-time(s)</b>	<b>2.0sec</b>											
├─	<b>Seqn= 11</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
├─	Al-poly/Open	Al-poly/thick-Al close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
├─	<b>Seqn=100</b>	<b>1-time(s)</b>	<b>10.0sec</b>										
├─	thin-Be/Open	med-Be/Open close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec	
├─	med-Be/Open	Open/thick-Al close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
├─	Open/thick-Al	Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
<b>Subr= 2</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
├─	<b>Seqn= 10</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
├─	med-Al/Open	med-Al/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
├─	Open/thick-Be	Open/thick-Be close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
├─	<b>Seqn= 11</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
├─	Al-poly/Open	Al-poly/thick-Al close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
├─	<b>Seqn= 53</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
├─	Open/G-band	Open/G-band open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
├─	Open/thick-Al	Open/thick-Al close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
├─	Open/thick-Al	Open/thick-Al close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

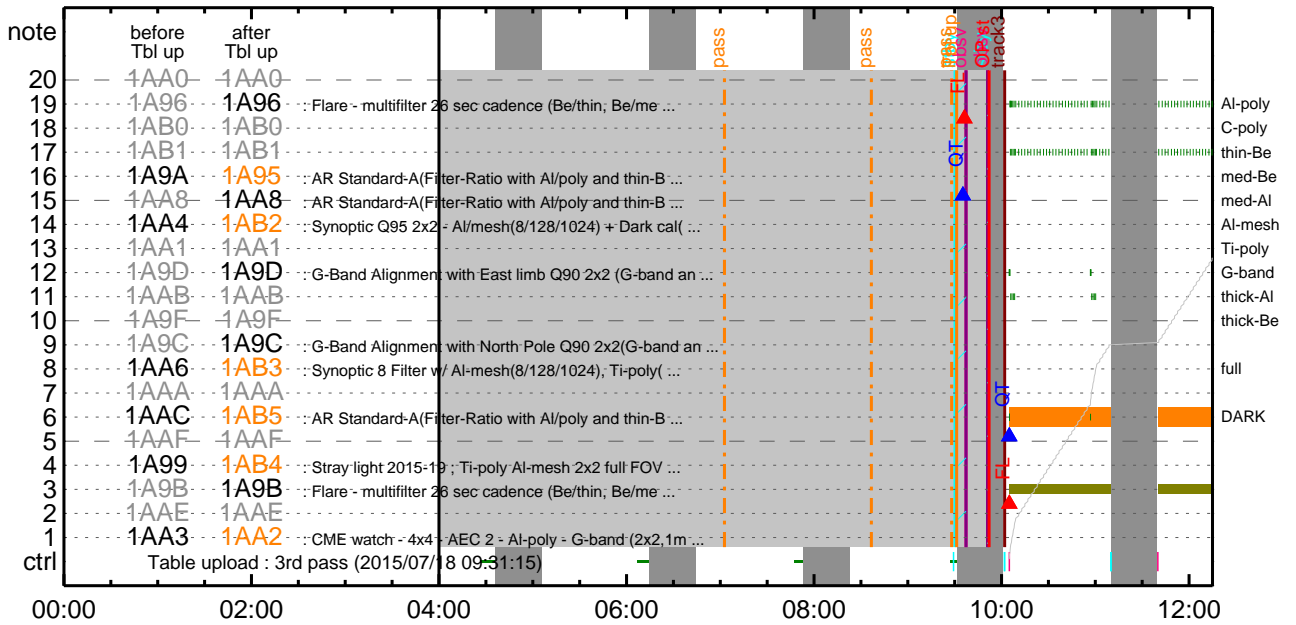
**Flare Detection**

\* \* \* \* \*

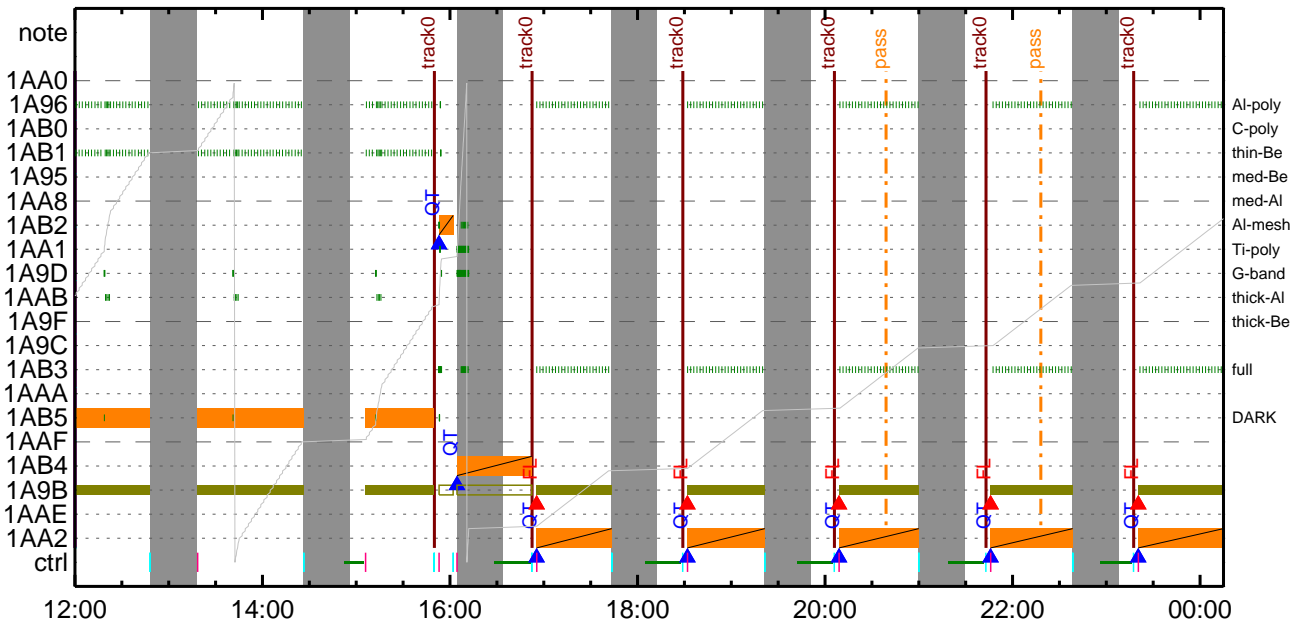
**FLD Patrol**

Term	Pointing (x, y)	Comment
07/18 16:52:48 - 07/19 05:42:46	Fixed ( -10.0, -994.0)	HOP 79 N-S 1/20
07/19 05:55:24 - 07/19 18:05:18	Fixed ( -10.0, -4.0)	HOP 79 N-S 11/20
07/19 22:45:18 - 07/20 06:22:16	Track ( -332.2, 145.1) <sup>Ⓢ 07/19 22:45:00</sup>	AR 12387 obs.
07/20 06:32:18 - 07/20 18:02:16	Track ( -267.0, 142.8) <sup>Ⓢ 07/20 06:32:00</sup>	AR 12387 obs.
07/20 18:12:18 - 07/21 05:52:48	Fixed ( 0.0, -150.0)	SOT flat field
07/21 05:58:48 - 07/28 10:54:00	Track ( -62.8, 138.3) <sup>Ⓢ 07/21 05:58:30</sup>	AR 12387 obs.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

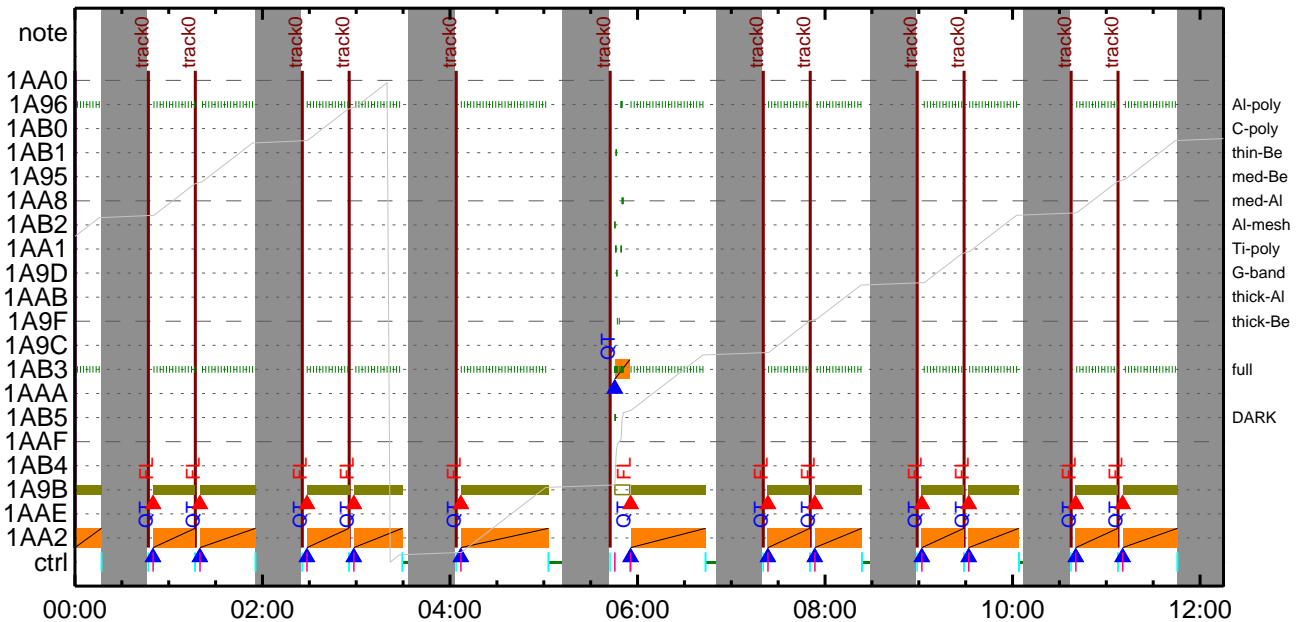
### CMDI #0275 2015/07/18



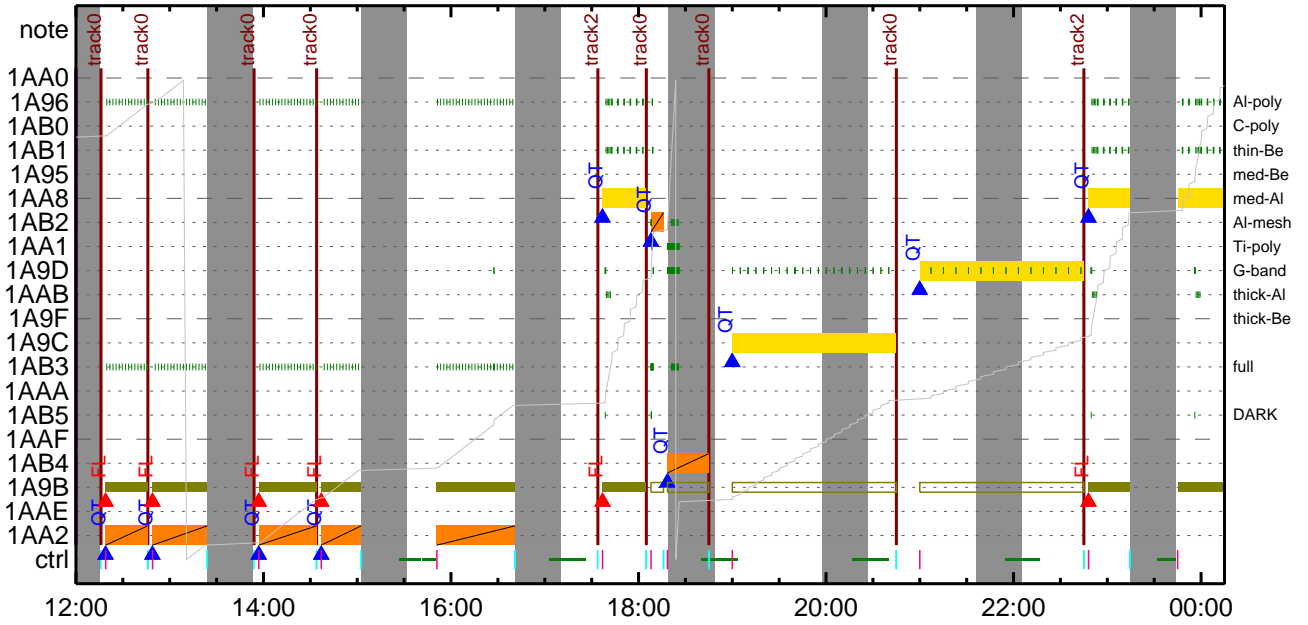
### CMDI #0275 2015/07/18



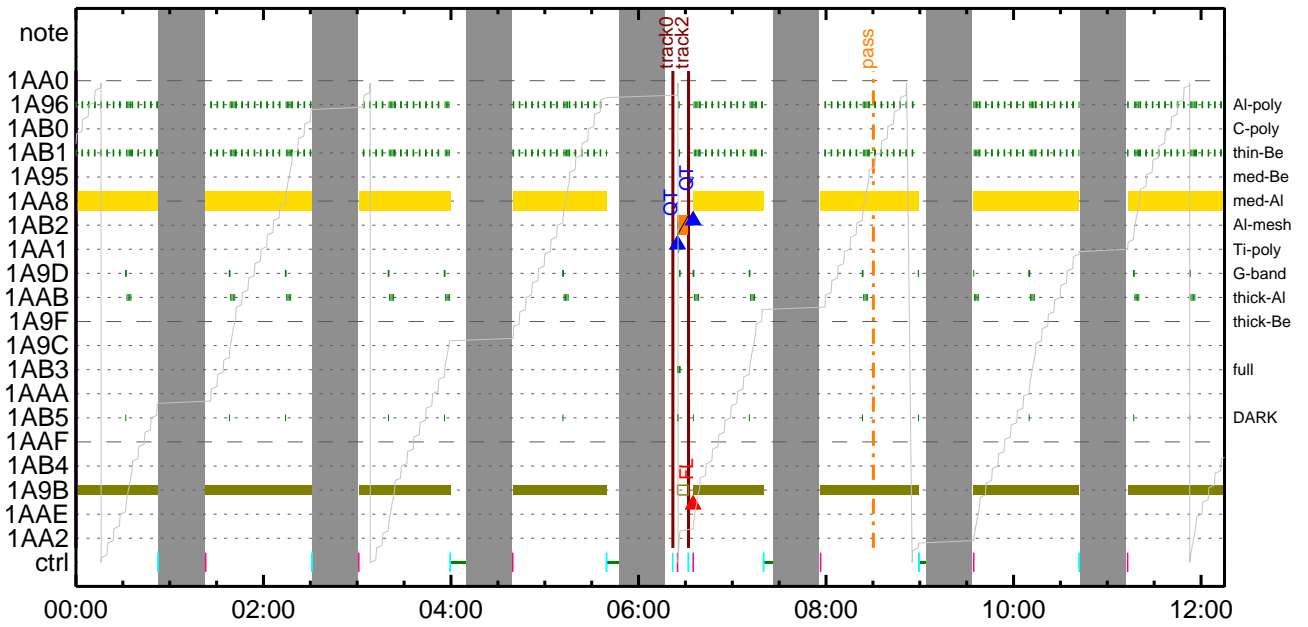
### CMDI #0275 2015/07/19



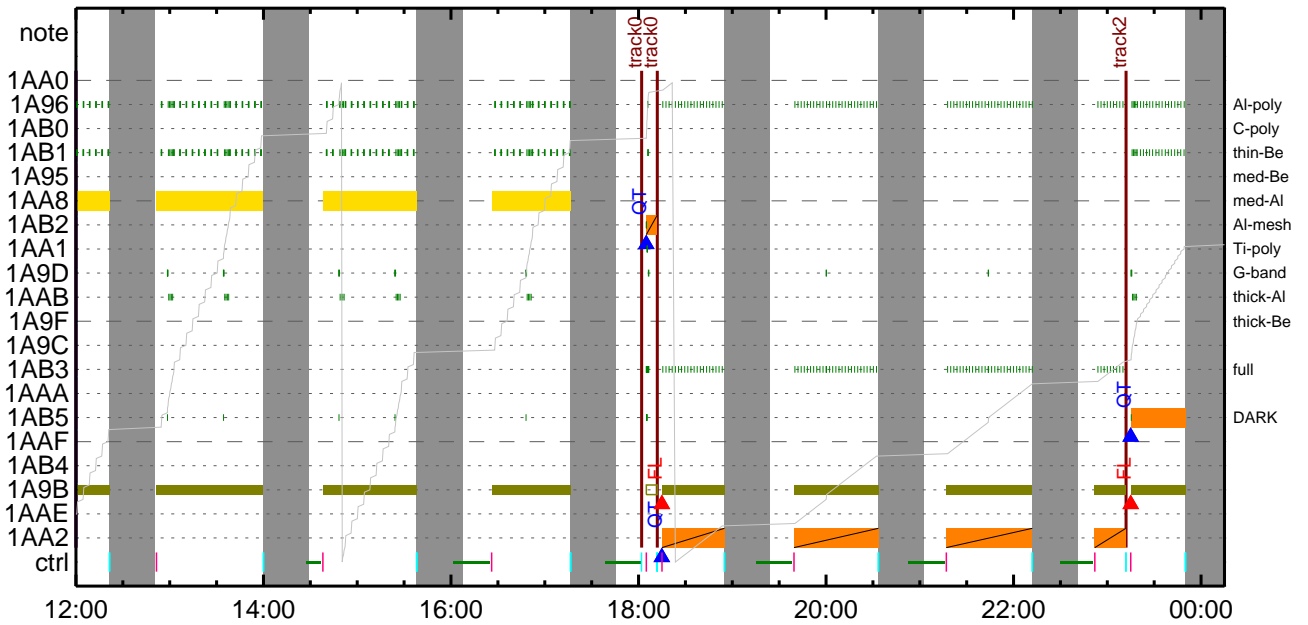
CMDI #0275 2015/07/19



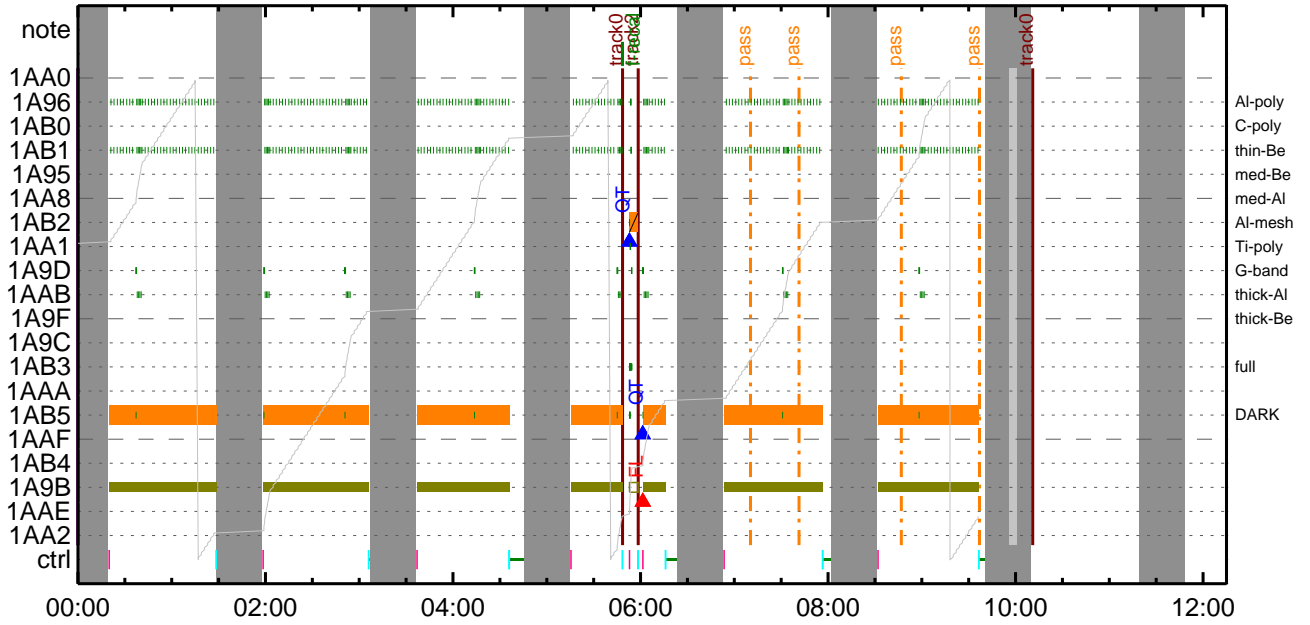
CMDI #0275 2015/07/20



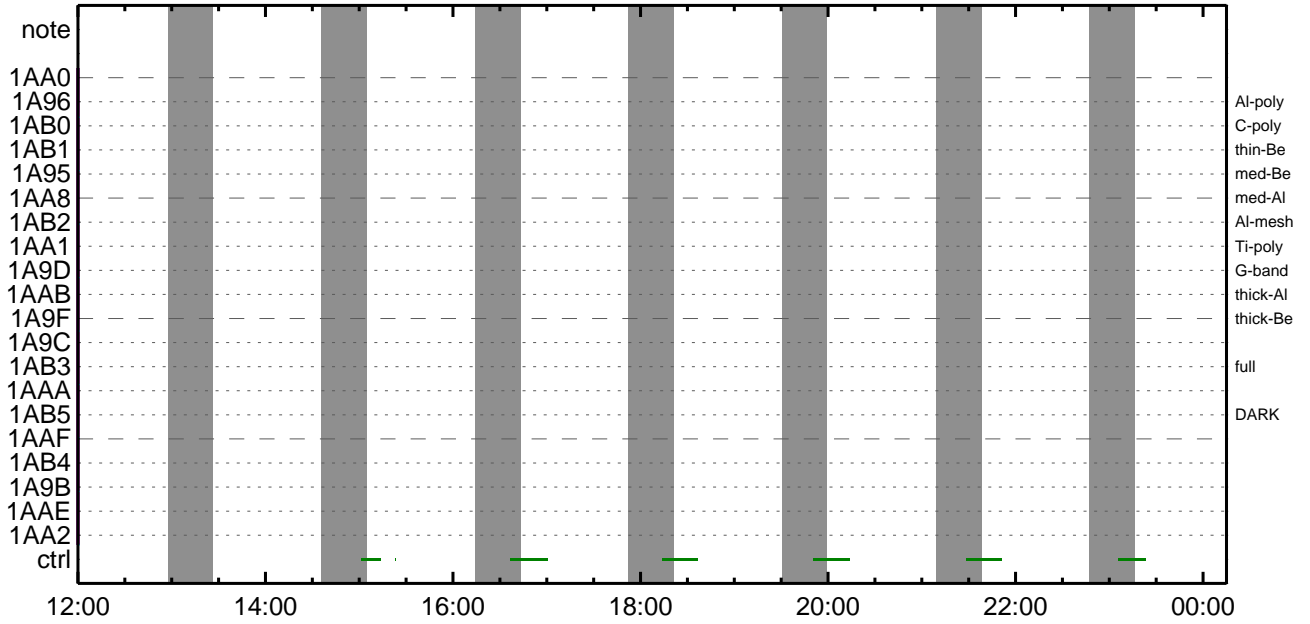
CMDI #0275 2015/07/20



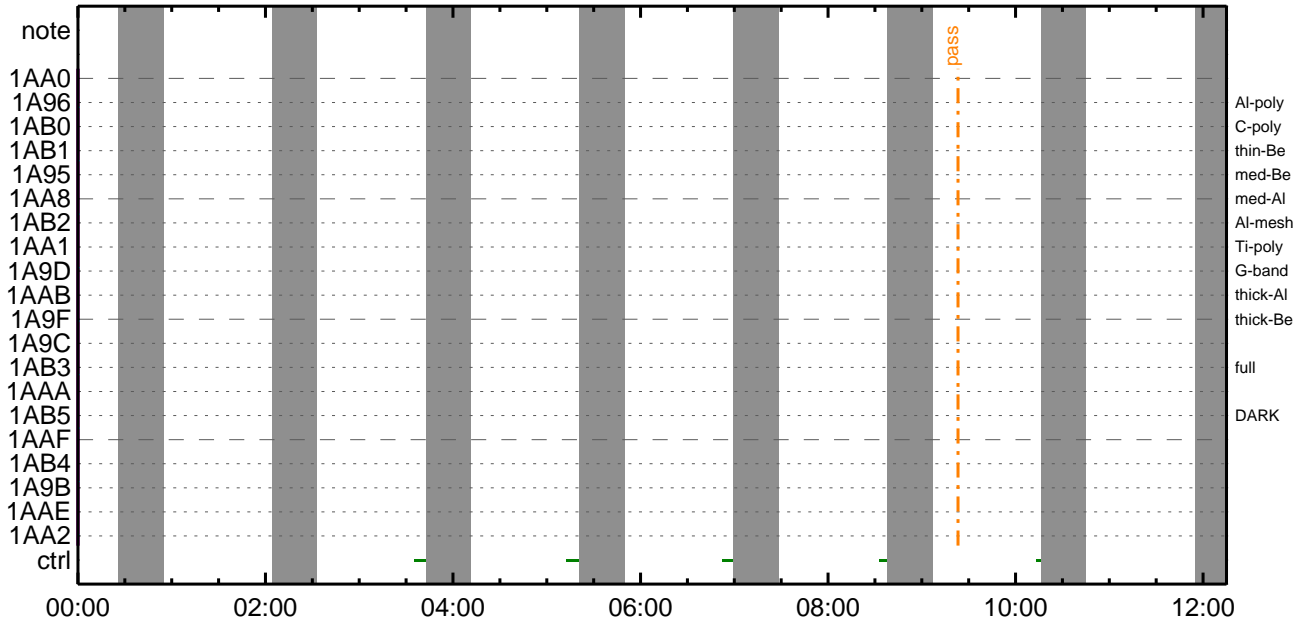
CMDI #0275 2015/07/21



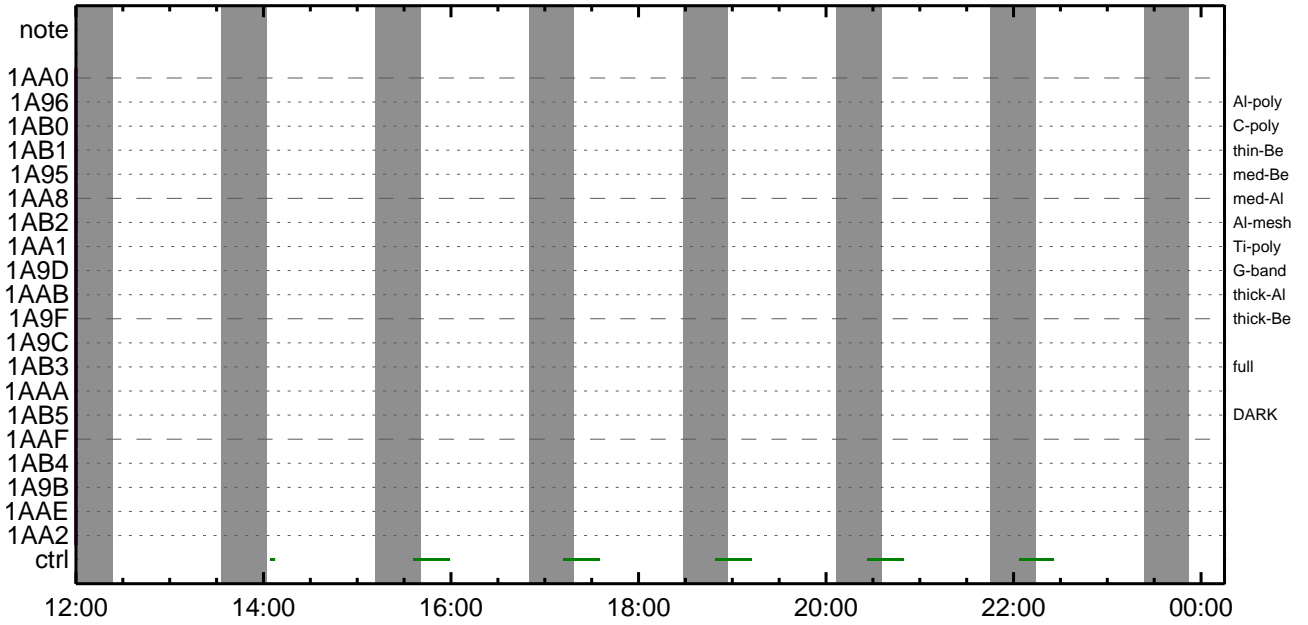
CMDI #0275 2015/07/21



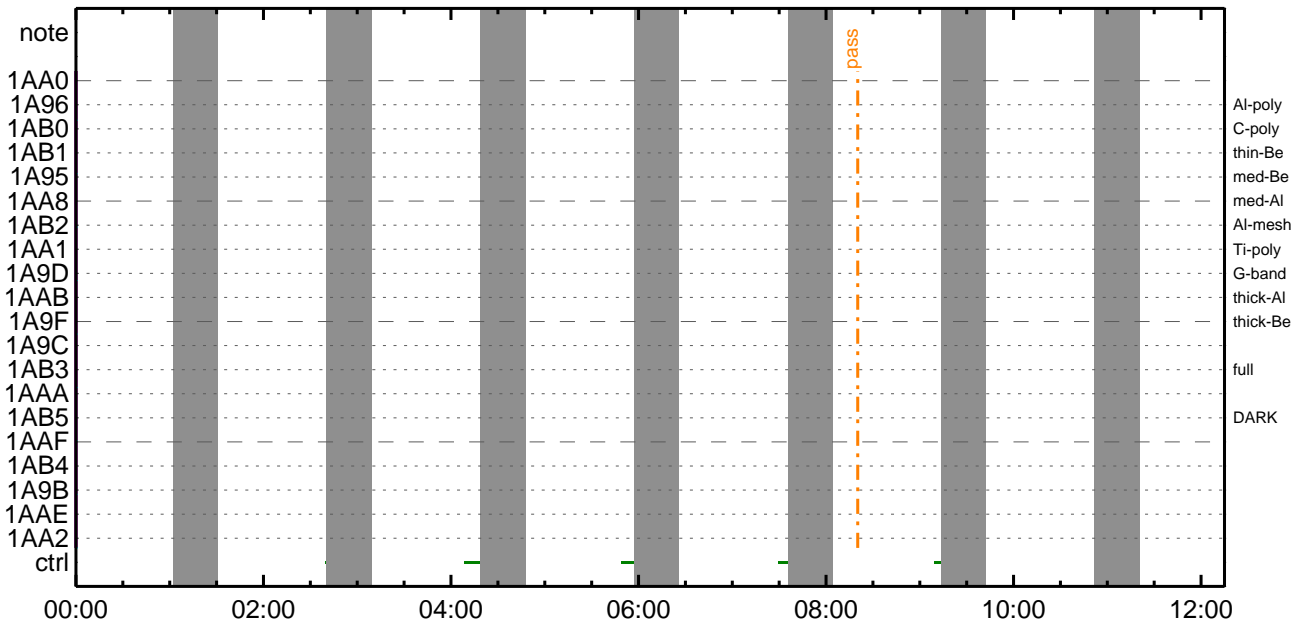
CMDI #0275 2015/07/22



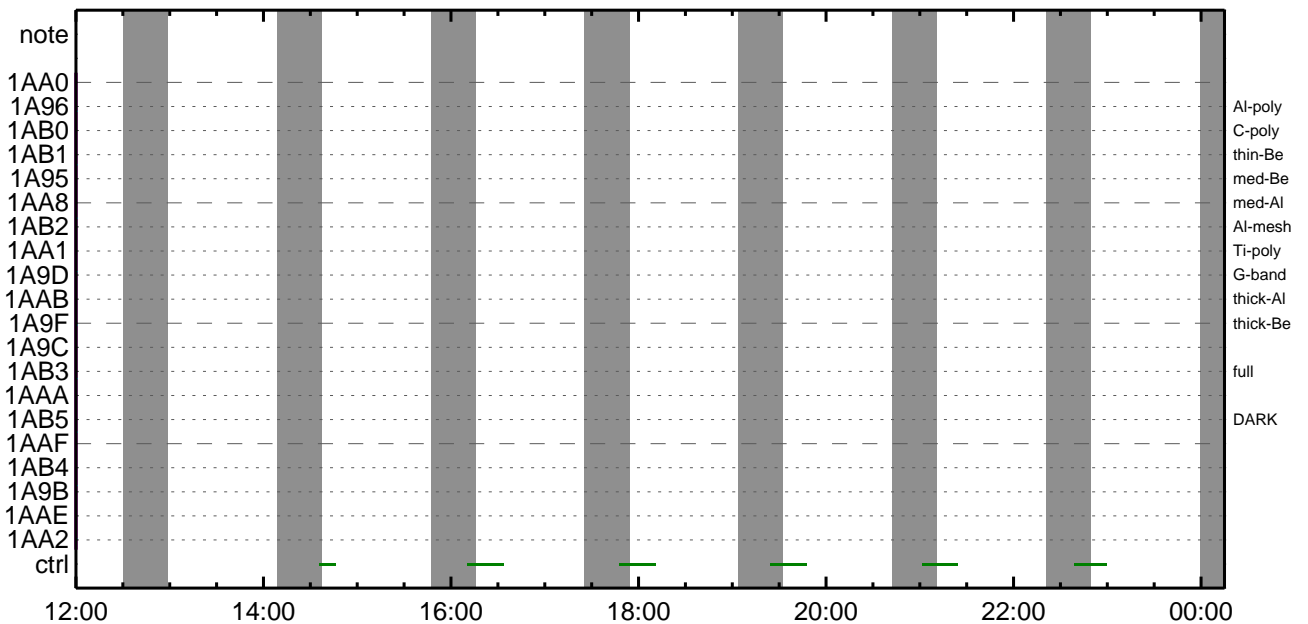
CMDI #0275 2015/07/22



CMDI #0275 2015/07/23

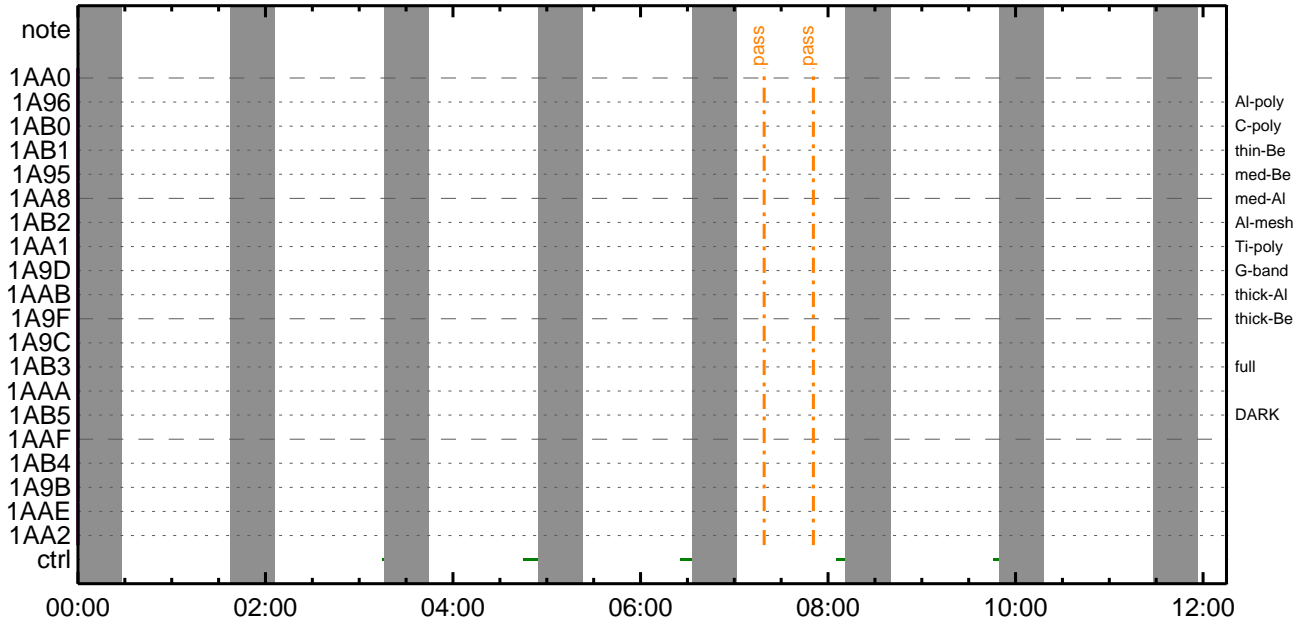


CMDI #0275 2015/07/23

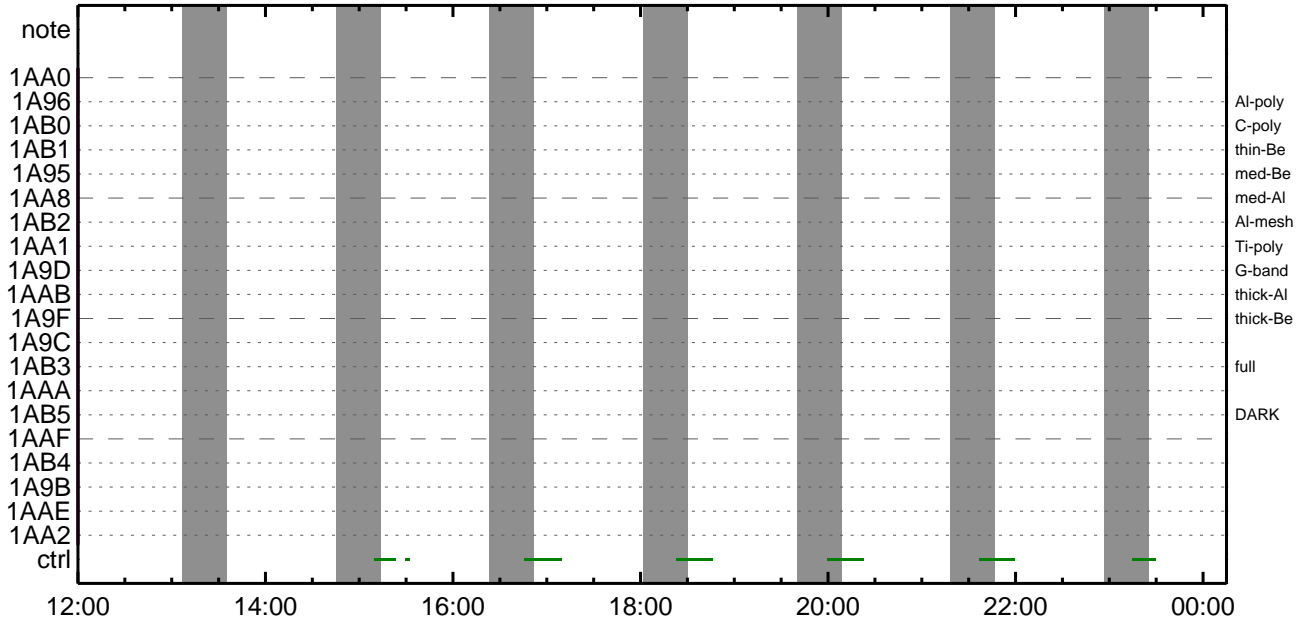




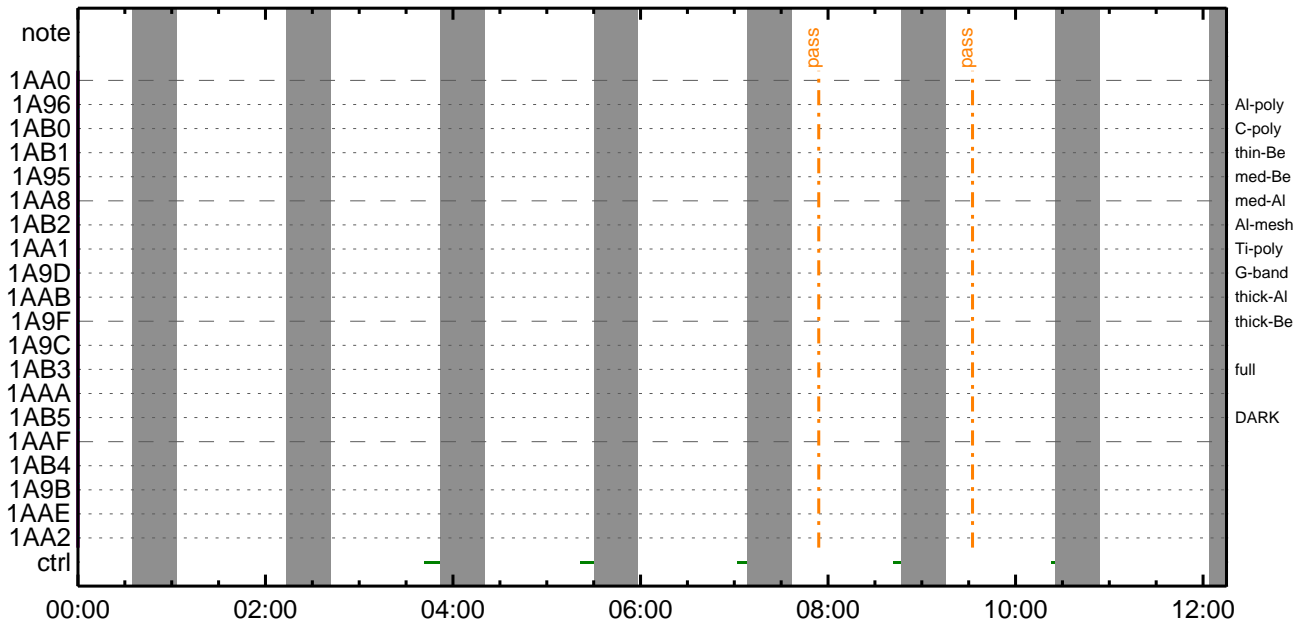
CMDI #0275 2015/07/24



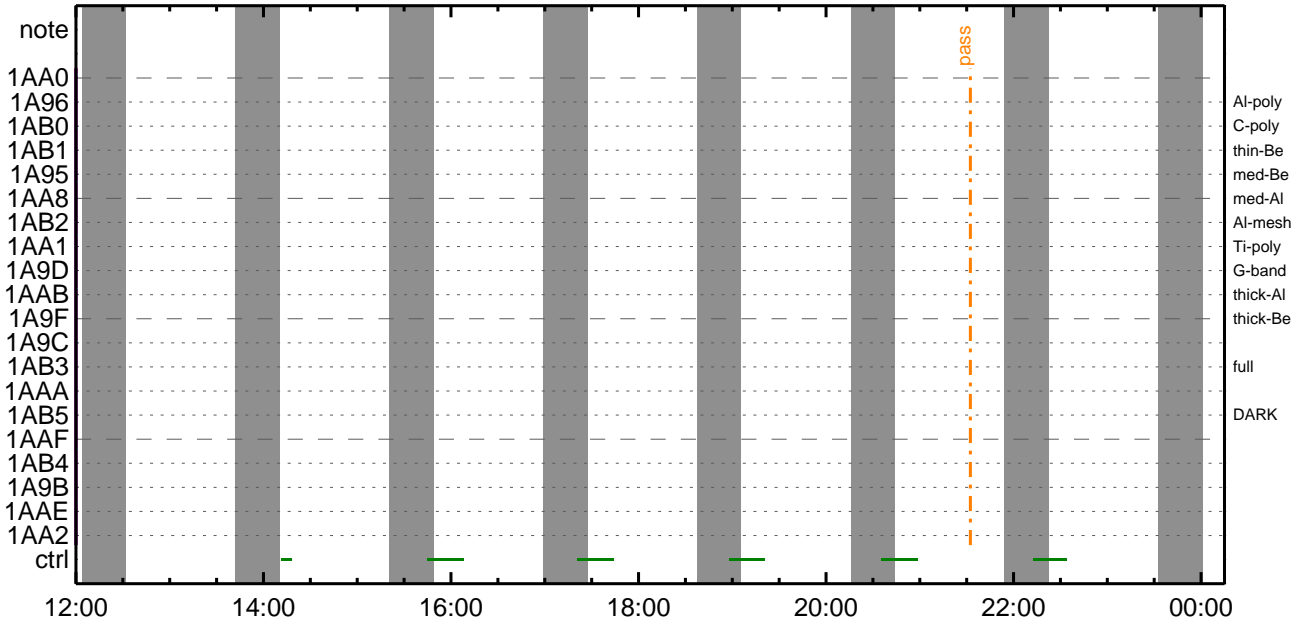
CMDI #0275 2015/07/24



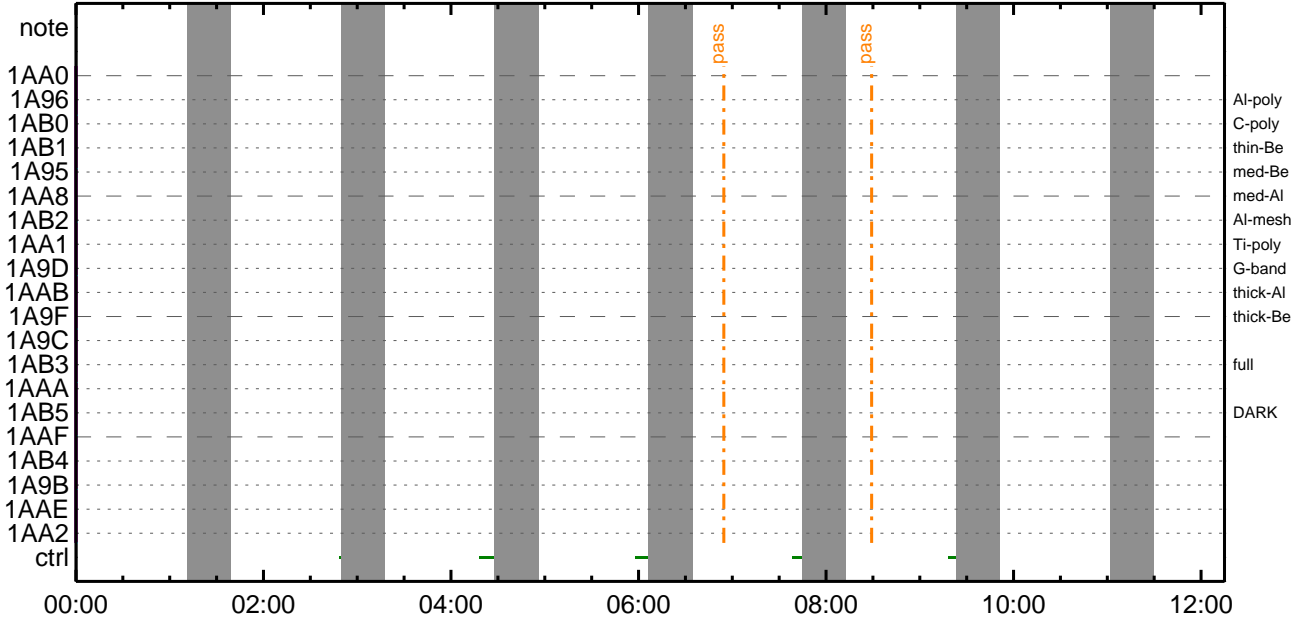
CMDI #0275 2015/07/25



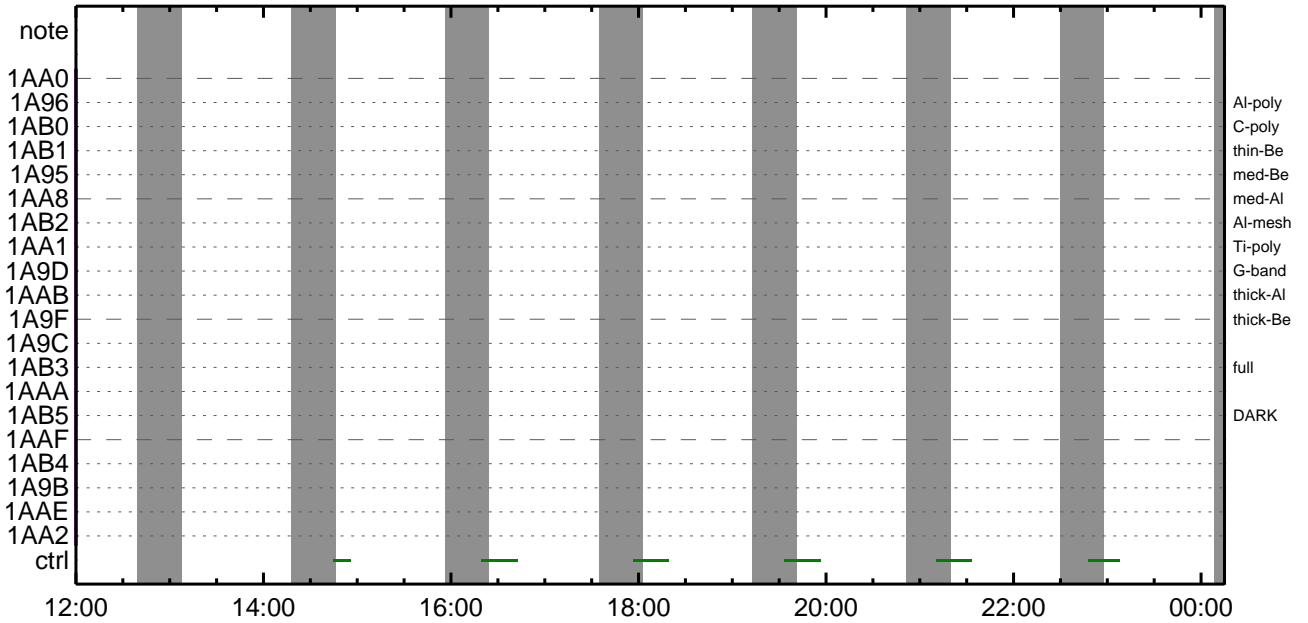
### CMDI #0275 2015/07/25



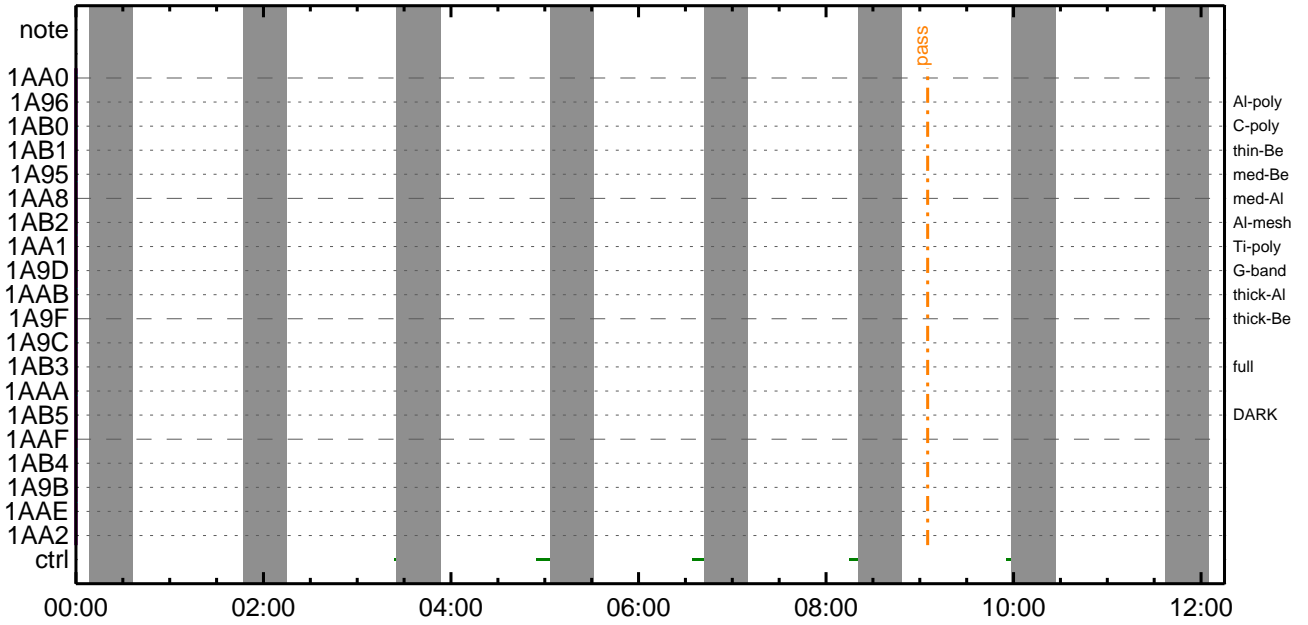
### CMDI #0275 2015/07/26



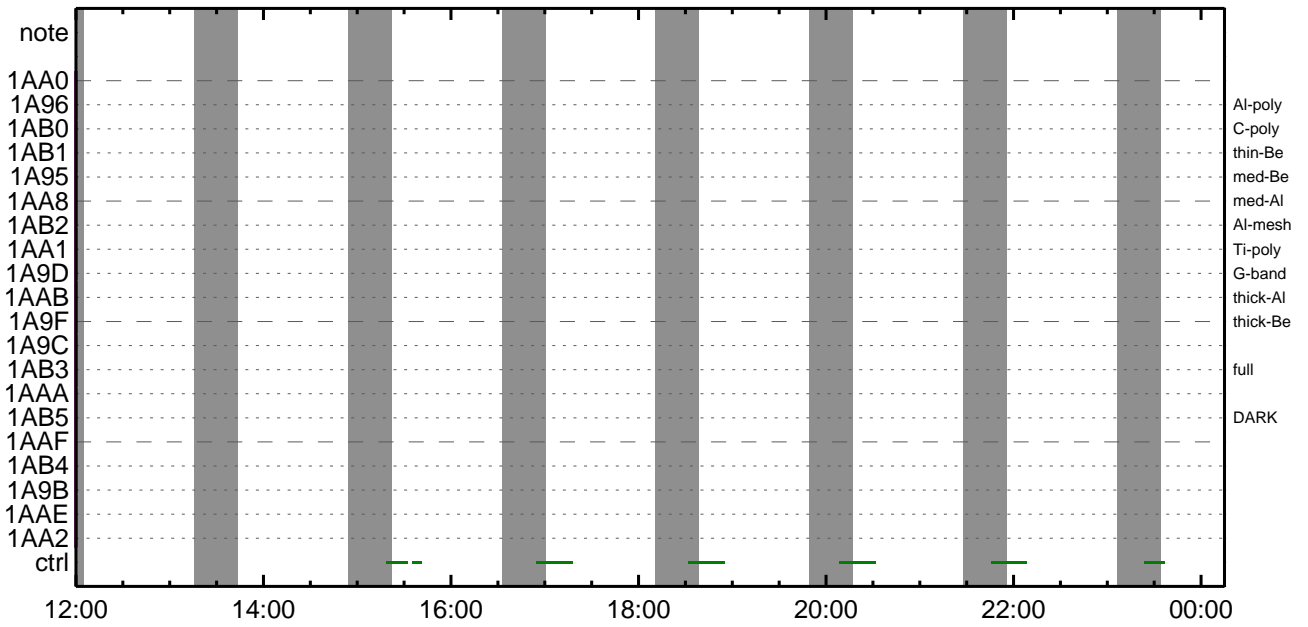
### CMDI #0275 2015/07/26



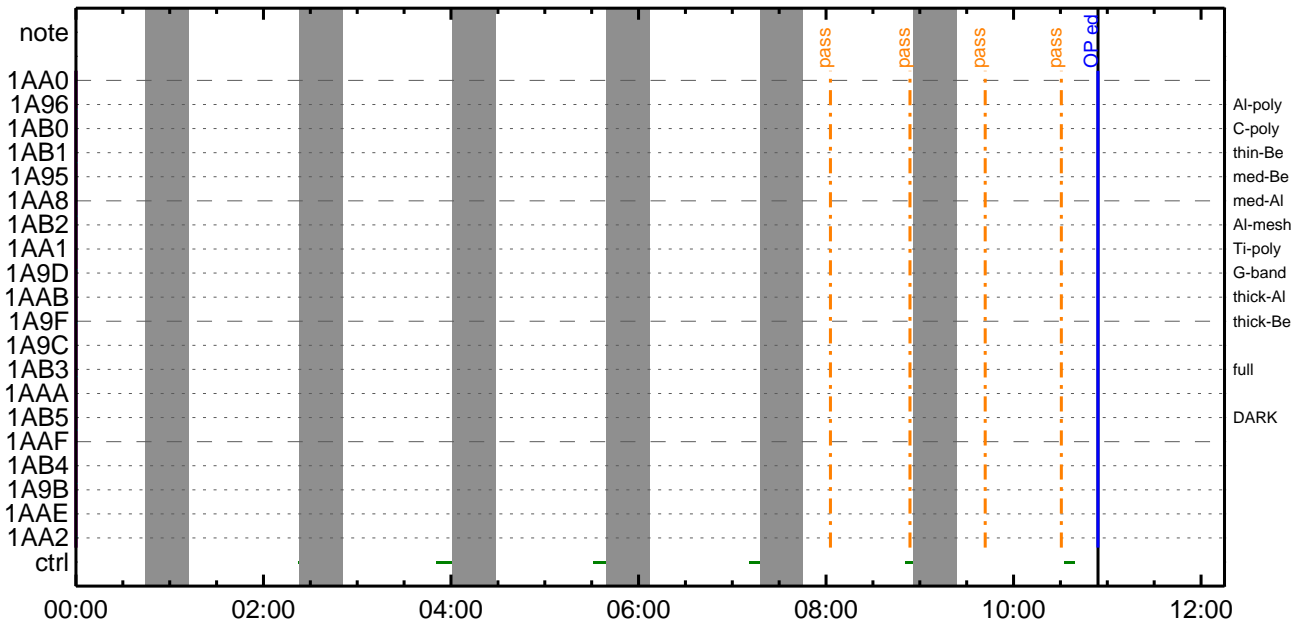
CMDI #0275 2015/07/27



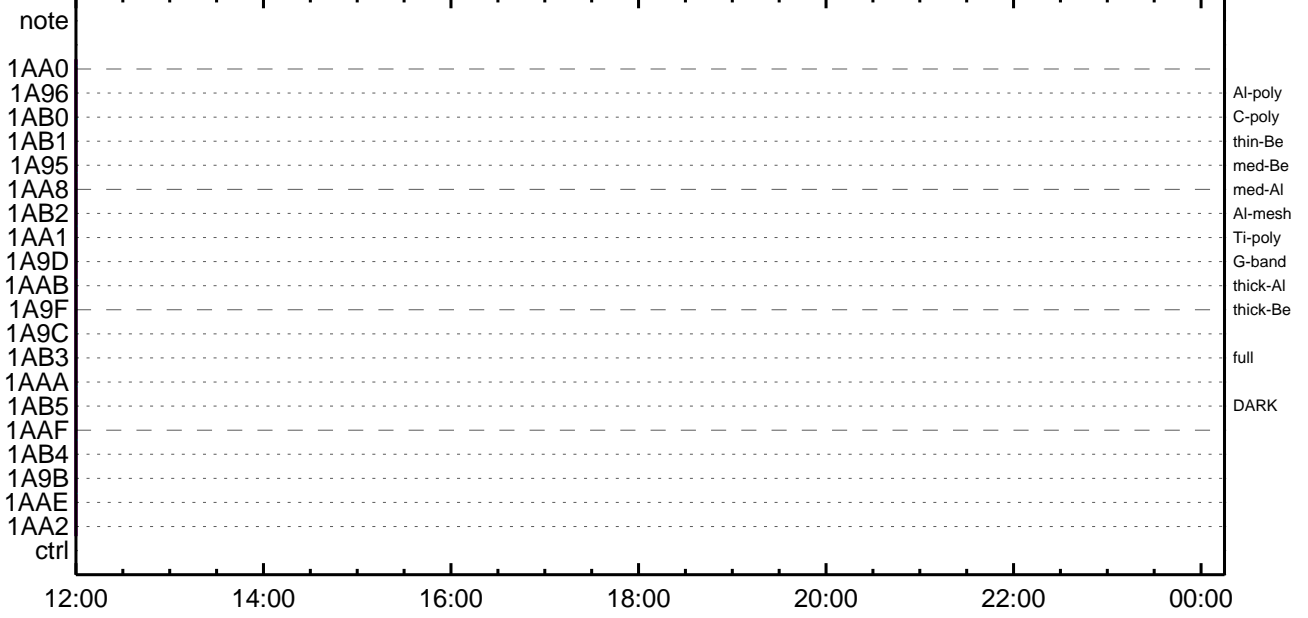
CMDI #0275 2015/07/27



CMDI #0275 2015/07/28



CMDI #0275 2015/07/28





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-266:OP
0104 ( )
0105 S. OG og-266:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°è¥ÅYó¥x;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥ÅYó¥x½ªî»òð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½Ê¹ç•è²îOKòð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥ÅYó¥x½ªî»òð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½Ê¹ç•è²îOKòð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥ÅYó¥x½ªî»òð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½Ê¹ç•è²îOKòð³îÇ§
0165 C.
0166 C. ***** °Ê²¼òî½Ã´¶Á°òÊÊ¬ò°Á÷¿@ (¼åµ-¥ÅYó¥x½ªî»òð³îÇ§²¼òî½Ã´¶Á°òÊÊ¬ò°Á÷¿@) *****
0167 C. DHUYâ;¼YE;Ê½Y½;¥i;¼YE;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½Ê¹ç;ç°Ê²¼òî½TI-CMDÁ÷¿@²¼òî½Ê¹ç°²¼òî½Ê¹ç;
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³¥P¥ó¥EòðÁDî¿(UT)
0183 +. TI 2015-07-18 09:47:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-07-18 09:47:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-07-18 09:47:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.  
0195 +. TI 2015-07-18 09:51:59.5  
0196 DC 01-B2 DHU\_OP\_START  
0197 C.                    çç[HK1\_TI\_CMD\_NUM]                    EQ            1COUNTUP  
0198 C.  
0199 C. °Ê²¼ñîÄê%îíññîî¥Á¥§¥Á¥-¹àîÛ  
0200 C.                    çç[HK1\_TI\_CMD\_ENA/DIS]                   EQ            ENA  
0201 C.                    çç[HK1\_TI\_CMD\_NUM]                    EQ            4  
0202 C.                    çç[HK1\_NEXT\_EXEC\_PIM]                   EQ            DHU  
0203 C.                    çç[HK1\_NEXT\_EXEC\_DC]                    EQ            0xB3  
0204 C.  
0205 . C. \*\*\*\*\*  
0206 C. TIîî°ê¥Á¥ô¥×  
0207 C. \*\*\*\*\*  
0208 C.  
0209 C. TI\_TBL(0x03AB00-0x03AEFF;§ 1024byte)  
0210 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0211 BC                    (03 ab 03 01 02)  
0212 C.                    çç[HK1\_DMP\_TOP\_ADRS\_1]                   EQ            07  
0213 C.                    çç[HK1\_DMP\_TOP\_ADRS\_0]                   EQ            2B  
0214 C.                    çç[HK1\_DMP\_BLOCK\_NUM]                    EQ            3  
0215 C.                    çç[HK1\_DMP\_REPEAT\_NUM]                   EQ            0  
0216 C.                    çç[HK1\_DMA\_DMP\_PIM]                    EQ            DHU  
0217 +. DC 01-22 DHU\_MODE\_CHNG  
0218 BC                    (07 0b f8)  
0219 C.                    çç[HK1\_PKT\_FORM\_NO]                    EQ            7  
0220 C.                    çç[HK1\_PKT\_GEN\_TIME]                    EQ            0.25 s  
0221 C.                    çç[HK1\_S\_TLM\_BIT\_RATE]                   EQ            32k  
0222 C.                    çç[HK1\_X\_TLM\_BIT\_RATE]                   EQ            4M  
0223 C.                    çç[HK1\_DMP\_CHK\_FLG]                    EQ            EXEC  
0224 C.  
0225 . C. ¥Á¥ô¥×½ªî»ò³îÇ§  
0226 C.                    çç[HK1\_DMP\_CHK\_FLG]                    EQ            NON  
0227 C.  
0228 . C. RAM ID=TI\_TBLñî¼Ê¹ç•ë²îOKñò³îÇ§  
0229 C.  
0230 . C. DHU¥â;¼¥Ê;Ê¼¥½. ¥î;¼¥Ê;Êñòîáñ¹  
0231 +. DC 01-22 DHU\_MODE\_CHNG  
0232 BC                    (02 0a f8)  
0233 C.                    çç[HK1\_PKT\_FORM\_NO]                    EQ            2  
0234 C.                    çç[HK1\_PKT\_GEN\_TIME]                    EQ            0.5S  
0235 C.                    çç[HK1\_S\_TLM\_BIT\_RATE]                   EQ            32K  
0236 C.                    çç[HK1\_X\_TLM\_BIT\_RATE]                   EQ            4M  
0237 C.  
0238 . C. Stop EIS observation and temporarily disable EIS mode changes  
0239 C.  
0240 C.  
0241 C. \*\*\*\*\* Start EIS operation (TI set) \*\*\*\*\*  
0242 C. Execute, after the success of OP upload.  
0243 C. Set EIS TI-commands  
0244 +. TI 2015-07-18 09:51:30.0  
0245 DC 07-FC EIS\_MODE\_MANU  
0246 BC                    (21 02)  
0247 +. TI 2015-07-18 09:51:40.0  
0248 DC 07-FC EIS\_MODE\_CHG\_DIS  
0249 BC                    (22)  
0250 . C.                    [        ] [HK1\_TI\_CMD\_NUM]                   EQ            2 COUNTUP  
0251 C. \*\*\*\*\* End EIS operation (TI set) \*\*\*\*\*  
0252 C.  
0253 C.  
0254 C. \*\*\*\*\*  
0255 C. SOT TI command set  
0256 C. \*\*\*\*\*  
0257 C. Execute, after the success of OP upload.  
0258 +. TI 2015-07-18 09:51:16.0  
0259 DC 07-F0 MDP\_SOT\_MODE\_STBY  
0260 BC                    (41)  
0261 . C. -----  
0262 C.                    HK1\_TI\_CMD\_NUM                    = 1 CNTUP [    ]  
0263 C. -----  
0264 C. \*\*\*\*\* SOT END \*\*\*\*\*  
0265 C.  
0266 C. \*\*\*\*\* XRT START \*\*\*\*\*  
0267 C. Execute, after the success of OP upload.  
0268 +. TI 2015-07-18 09:51:00.0  
0269 DC 07-F0 MDP\_XRT\_MODE\_STBY  
0270 BC                    (c3)  
0271 . C.                    [        ] [HK1\_TI\_CMD\_NUM]                   EQ            1COUNTUP  
0272 C.  
0273 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0274 C.  
0275 . C. \*\*\*\*\* MDP ´ûÃîñî»ö¼ÝññÊÄñ¹ñèDCBC•x²è \*\*\*\*\*  
0276 C. (%á°î¥Á¥Ê¥¥¥¥¥Á¥ç¥èñ¼ññ¼Á»Ûñ¹ñè)  
0277 . S. DC-BC dcbc-402:DCBC  
0278 (MDP\_known\_event)  
0279 C.  
0280 C.  
0281 . C. \*\*\*\*\* ¥Ð¥¹•î Daily±çîññè´òñ¹ñèDCBC•x²è \*\*\*\*\*  
0282 . S. DC-BC dcbc-153:DCBC  
0283 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0284 C.  
0285 C.  
0286 . C. ¦ãLOS¥Á¥§¥Á¥-¼Á»Û;ã  
0287 C.  
0288 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0289 C.





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0096 C. EIS START OBSTBL LOAD
0097 C. *****
0098 . S. RAM ram-820:EIS_OBSTBL
0099 ( )
0100 +. DC 07-FC EIS_DUMP_OBSTBL
0101 BC (07 07 07 00 00 70 00)
0102 C.
0103 C. Execute, after the success of OBSTBL upload.
0104 C. Set EIS TI-commands
0105 +. TI 2015-07-18 09:51:50.0
0106 DC 07-FC EIS_MODE_CHG_ENA
0107 BC (20)
0108 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0109 C. *****
0110 C. EIS END OBSTBL LOAD
0111 C. *****
0112 C.
0113 . C. ***** MDP 'uÃîî»ô¼ÝðÊÄð¹ððDCBC•x²è *****
0114 C. (¼å°ïŸÓŸÄŸÈŸŸÈŸáŸŸçŸŸèåÈ¼ð¼Ä»Û¹åè)
0115 . S. DC-BC dcbc-402:DCBC
0116 (MDP_known_event)
0117 C.
0118 C.
0119 . C. ***** ŸÐŸ¹•ï Daily±;ĩÑå'Ø¹åèDCBC•x²è *****
0120 . S. DC-BC dcbc-153:DCBC
0121 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0122 C.
0123 C.
0124 . C. ;ãLOSŸÁŸŸŸÄŸŸ⁻¼Ä»Û;ã
0125 C.
0126 . C. ***** LOS *****
0127 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-268 2015-07-18 12:39:58 136 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÁY-¼Ä»Ü;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíÈq¿qAb•µ°EÍxÁÇqÍYçYÁY×Yí;¼YÉjÈÈ%µ•ííÉjÈqE¼°ÇQq•q¿¼l¹çqÍ;çÀ®,ùn¹qèqBqÇÁ+¿®q•qÈqmq³qÈj;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG_____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR _____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 96 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 60 20 18)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d a0 80 18 20)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0f 80 80 06 06)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 10 80 80 08 08)
0062 + DC 07-F0 MDP_XRT_FLD_ENA
0063 BC (d8)
0064 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0065 BC (c8)
0066 + DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 + DC 07-F0 MDP_XRT_ARS_DIS
0069 BC (d5)
0070 + DC 07-F0 MDP_XRT_FLD_RESET
0071 BC (da)
0072 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0073 BC (c4 10)
0074 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0075 BC (c5 13)
0076 . C. ----- Success Verify ? OK / NG _____
0077 C.
0078 C.
0079 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0080 C.
0081 +. DC 07-F0 MDP_XRT_MODE_OBSV
0082 BC (c2)
0083 +. TI 2015-07-18 09:51:02.0
0084 DC 07-F0 MDP_XRT_MODE_OBSV
0085 BC (c2)
0086 . C. ----- Success Verify ? OK / NG _____
0087 C.
0088 C. ***** XRT END *****
0089 . C. *****
0090 C. SOT table upload
0091 C. *****
0092 . C. < Stop FG table >
0093 +. DC 07-F0 MDP_FG_CTRL_MANU
0094 BC (51)
0095 . C. -----
```

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0096 C. MDP_FG_CTRL_MODE = MANU [ ]
0097 C. -----
0098 C.
0099 . C. <Upload FG Observation Table>
0100 . S. RAM ram-263:MDP_OBS_F
0101 ( )
0102 C.
0103 . C. < Dump RAMID=MDP_OBS_F >
0104 +. DC 07-F0 MDP_DUMP_FGTBL
0105 BC (82 07 00 00 00 38 b8)
0106 C. -----
0107 C. MDP_OBS_F verify = OK/NG [ ]
0108 C. -----
0109 C.
0110 C. *****
0111 C. SOT TI command set
0112 C. *****
0113 C. Execute, after the success of TBL upload.
0114 +. TI 2015-07-18 09:51:18.0
0115 DC 07-F0 MDP_SOT_MODE_OBSV
0116 BC (40)
0117 . C. -----
0118 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0119 C. -----
0120 C.
0121 C.
0122 . C. ***** MDP `úÃîñÏ»ö¼ÝðËÂÐñ¹æDCBC•x²è *****
0123 C. (%ãºîÿÓÿÄÿÈÿÏÿÑÿäÿçÿèº¼ñ¼Ã»Ûñ¹æè)
0124 . S. DC-BC dcbc-402:DCBC
0125 (MDP_known_event)
0126 C.
0127 C.
0128 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑñË´Øñ¹æDCBC•x²è *****
0129 . S. DC-BC dcbc-153:DCBC
0130 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0131 C.
0132 C.
0133 . C. ;ãLOSÿÄÿÏÿÛÿ-¼Ã»Û;ä
0134 C.
0135 . C. ***** LOS *****
0136 C.

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\*\*\* OP Sequence for XRT \*\*\*

2015/07/18	10:01:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	10:01:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	10:01:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/07/18	10:02:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2015/07/18	10:02:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/18	10:02:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/18	10:02:22.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/18	10:02:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/18	10:02:26.0	XRT_FLD_RESET_447_OG [0x1bf]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/18	10:04:56.0	XRT_QT_PROG_SET_401_OG [0x191]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2015/07/18	10:04:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2015/07/18	10:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/18	11:10:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	11:10:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	11:10:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/18	11:10:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/18	11:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/18	11:39:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/18	11:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/18	12:48:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	12:48:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	12:48:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/18	12:48:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/18	12:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/18	13:17:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/18	13:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/18	14:26:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	14:26:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	14:26:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/18	14:26:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/18	14:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/18	15:05:00.5	XRT_Custom_430_OG [0x1ae]					
2015/07/18	15:06:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/18	15:49:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	15:49:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	15:49:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/07/18	15:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2015/07/18	15:50:18.0	XRT_FLD_DIS_426_OG [0x1aa]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/07/18	15:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/07/18	15:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/18	15:52:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2015/07/18	15:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/18	16:02:00.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	16:02:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/18	16:02:04.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/07/18	16:02:24.0	XRT_FLD_DIS_429_OG [0x1ad]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/07/18	16:04:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		

2015/07/18	16:04:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/18	16:04:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2015/07/18	16:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/18	16:52:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	16:52:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	16:52:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2015/07/18	16:52:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	58 5a 00 e5
2015/07/18	16:52:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/18	16:52:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/18	16:52:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/18	16:52:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/18	16:52:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/18	16:55:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2015/07/18	16:55:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/07/18	16:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/18	17:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	17:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	17:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/18	17:43:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/18	17:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/18	18:28:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	18:28:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2015/07/18	18:29:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	4f 74 00 e5
2015/07/18	18:29:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/18	18:29:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/18	18:29:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/18	18:29:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/18	18:29:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/18	18:31:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2015/07/18	18:31:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/07/18	18:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/18	19:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	19:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	19:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/18	19:21:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/18	19:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/18	20:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	20:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2015/07/18	20:06:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	46 97 00 e5
2015/07/18	20:06:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/18	20:06:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/18	20:06:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/18	20:06:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/18	20:06:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/18	20:08:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2015/07/18	20:08:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/07/18	20:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]					

2015/07/18	21:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	21:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	21:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/18	21:00:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/18	21:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/18	21:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	21:42:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/18	21:43:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 3d b2 00 e5	
2015/07/18	21:43:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/18	21:43:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/18	21:43:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/18	21:43:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/18	21:43:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/18	21:45:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2015/07/18	21:45:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/07/18	21:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/18	22:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	22:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	22:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/18	22:38:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/18	22:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/18	23:17:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/18	23:17:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/18	23:17:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 34 cc 00 e5	
2015/07/18	23:17:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/18	23:17:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/18	23:17:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/18	23:17:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/18	23:17:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/18	23:20:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2015/07/18	23:20:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/07/18	23:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/19	00:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/19	00:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/19	00:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/19	00:17:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/19	00:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/19	00:46:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/19	00:46:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/19	00:47:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 2b e7 00 e5	
2015/07/19	00:47:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/19	00:47:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/19	00:47:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/19	00:47:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/19	00:47:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/19	00:49:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	

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2015/07/19	00:49:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	00:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	01:16:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	01:16:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	01:16:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	01:17:00.0	AOCS_OrE-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 23 02 00 e5
2015/07/19	01:17:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	01:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	01:17:22.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	01:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	01:17:26.0	XRT_FLD_RESET_447_OG [0x1bf]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	01:19:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/07/19	01:19:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	01:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	01:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	01:55:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	01:55:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	01:55:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/19	01:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/19	02:25:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	02:25:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	02:25:30.0	AOCS_OrE-point_Start_10_OG [0x0a0]			
		AOCU_NM	5	02-76	00 1a 24 00 e5
2015/07/19	02:25:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	02:25:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	02:25:50.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	02:25:52.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	02:25:54.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	02:28:26.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/07/19	02:28:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	02:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	02:55:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	02:55:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	02:55:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	02:55:30.0	AOCS_OrE-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76	00 11 3f 00 e5
2015/07/19	02:55:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	02:55:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	02:55:52.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	02:55:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	02:55:56.0	XRT_FLD_RESET_447_OG [0x1bf]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	02:58:26.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/07/19	02:58:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	02:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	03:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	03:29:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	03:29:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	03:29:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/19	03:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			

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2015/07/19	04:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/19	04:03:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	04:04:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	04:04:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 08 5a 00 e5
2015/07/19	04:04:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	04:04:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	04:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	04:04:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	04:06:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	04:06:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/07/19	04:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	05:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	05:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	05:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	05:03:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	05:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/19	05:42:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/19	05:42:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	05:42:30.0	AOCS_OrE-point_Start_13_OG [0x0a3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	05:42:46.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00 00 5a 00 e5
2015/07/19	05:45:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/19	05:45:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/19	05:45:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	05:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2015/07/19	05:55:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	05:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	05:55:04.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	05:55:24.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	05:55:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	05:55:28.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	05:55:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	05:55:32.0	XRT_FLD_RESET_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	05:55:34.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	05:55:36.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/07/19	05:55:38.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	06:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	06:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	06:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	06:43:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	06:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/19	07:20:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/19	07:20:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	07:20:30.0	AOCS_OrE-point_Start_14_OG [0x0a4]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	07:20:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 f7 75 00 e5
2015/07/19	07:20:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	07:20:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0



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2015/07/19	07:20:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	07:20:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	07:23:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/07/19	07:23:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	07:23:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	07:50:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	07:50:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	07:50:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	07:50:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 ee 98 00 e5
2015/07/19	07:50:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	07:50:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	07:50:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	07:50:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	07:50:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	07:53:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/07/19	07:53:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	07:53:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	08:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	08:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	08:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	08:23:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/19	08:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/19	08:58:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	08:58:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	08:59:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 e5 b3 00 e5
2015/07/19	08:59:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	08:59:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	08:59:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	08:59:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	08:59:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	09:01:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/07/19	09:01:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	09:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	09:28:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	09:28:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	09:28:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	09:29:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 dc cd 00 e5
2015/07/19	09:29:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	09:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	09:29:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	09:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	09:29:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	09:31:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2015/07/19	09:31:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	09:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	10:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	10:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2015/07/19	10:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	10:04:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/19	10:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/19	10:37:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	10:37:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/07/19	10:37:30.0	AOCS_Orе-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 d3 e8 00 e5		
2015/07/19	10:37:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/19	10:37:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/19	10:37:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/19	10:37:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/19	10:37:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	10:40:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2015/07/19	10:40:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2015/07/19	10:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/19	11:07:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	11:07:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	11:07:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/07/19	11:07:30.0	AOCS_Orе-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00 cb 03 00 e5		
2015/07/19	11:07:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/19	11:07:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/19	11:07:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/19	11:07:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/19	11:07:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	11:10:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2015/07/19	11:10:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2015/07/19	11:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/19	11:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	11:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	11:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	11:45:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/19	11:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/19	12:15:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	12:15:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/07/19	12:16:00.0	AOCS_Orе-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00 c2 25 00 e5		
2015/07/19	12:16:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/19	12:16:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/19	12:16:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/19	12:16:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/19	12:16:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	12:18:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2015/07/19	12:18:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2015/07/19	12:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/19	12:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	12:45:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	12:45:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/07/19	12:46:00.0	AOCS_Orе-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 b9 40 00 e5		

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2015/07/19	12:46:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/19	12:46:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/19	12:46:22.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/19	12:46:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/19	12:46:26.0	XRT_FLD_RESET_447_OG [0x1bf]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	12:48:56.0	XRT_QT_PROG_SET_431_OG [0x1af]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01	
2015/07/19	12:48:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2015/07/19	12:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/19	13:24:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	13:24:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	13:24:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	13:24:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/19	13:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/19	13:53:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	13:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa
2015/07/19	13:54:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]					
		AOCU_NM	5	02-76	00	b0	5b
						00	e5
2015/07/19	13:54:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/19	13:54:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/19	13:54:20.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/19	13:54:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/19	13:54:24.0	XRT_FLD_RESET_443_OG [0x1bb]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	13:56:56.0	XRT_QT_PROG_SET_431_OG [0x1af]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01	
2015/07/19	13:56:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2015/07/19	13:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/19	14:33:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	14:33:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	14:33:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa
2015/07/19	14:34:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]					
		AOCU_NM	5	02-76	00	d4	73
						bd	59
2015/07/19	14:34:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/19	14:34:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/19	14:34:22.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/19	14:34:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/19	14:34:26.0	XRT_FLD_RESET_447_OG [0x1bf]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	14:36:56.0	XRT_QT_PROG_SET_431_OG [0x1af]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01	
2015/07/19	14:36:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2015/07/19	14:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/19	15:02:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	15:02:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	15:02:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	15:02:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/19	15:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/19	15:50:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/19	15:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/19	16:41:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	16:41:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/19	16:41:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/19	16:41:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		

2015/07/19	16:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/19	17:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	17:33:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/19	17:34:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	02 00 00 00 00
2015/07/19	17:34:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	17:34:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	17:34:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	17:34:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	17:34:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	17:36:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/07/19	17:36:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	17:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	18:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	18:05:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/07/19	18:05:18.0	XRT_FLD_DIS_426_OG [0x1aa]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/19	18:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/19	18:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	18:07:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/07/19	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	18:16:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	18:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	18:16:04.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/19	18:16:24.0	XRT_FLD_DIS_429_OG [0x1ad]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/19	18:18:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/19	18:18:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	18:18:28.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/07/19	18:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	18:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	18:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	18:44:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/19	18:45:00.0	AOCS_ORe-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00 b0 00 00 00
2015/07/19	18:45:18.0	XRT_FLD_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/19	18:45:20.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/19	18:45:22.0	XRT_ARS_DIS_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	18:59:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2015/07/19	19:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	20:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	20:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	20:44:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/19	20:45:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	00 00 00 54 00
2015/07/19	20:45:18.0	XRT_FLD_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/19	20:45:20.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/19	20:45:22.0	XRT_ARS_DIS_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	20:59:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2015/07/19	21:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]				

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2015/07/19	22:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	22:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	22:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/19	22:45:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	02 00 00 00 00
2015/07/19	22:45:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/19	22:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/19	22:45:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/19	22:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/19	22:45:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	22:47:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/07/19	22:47:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/19	22:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/19	23:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	23:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/19	23:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/19	23:14:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/19	23:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/19	23:44:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/19	23:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	00:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	00:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	00:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	00:52:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/20	00:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/20	01:22:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/20	01:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	02:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	02:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	02:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	02:31:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/20	02:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/20	03:00:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/20	03:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	03:59:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	03:59:33.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	03:59:35.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	03:59:37.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/20	04:02:45.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/20	04:38:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/20	04:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	05:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	05:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	05:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	05:39:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/20	05:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/20	06:21:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	06:21:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/20	06:22:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00

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2015/07/20	06:22:16.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/20	06:24:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/20	06:24:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/20	06:24:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/07/20	06:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	06:31:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	06:31:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	06:31:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/20	06:32:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]			
		AOCU_NM	5	02-76	02 00 00 00 00
2015/07/20	06:32:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/20	06:32:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/20	06:32:22.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/20	06:32:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/20	06:32:26.0	XRT_FLD_RESET_447_OG [0x1bf]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	06:34:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/07/20	06:34:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/20	06:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	07:20:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	07:20:03.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	07:20:05.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	07:20:07.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/20	07:23:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/20	07:55:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/20	07:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	08:59:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	08:59:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	08:59:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	08:59:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/20	09:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/20	09:33:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/20	09:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	10:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	10:42:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	10:42:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	10:42:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/20	10:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/20	11:12:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/20	11:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	12:21:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	12:21:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	12:21:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	12:21:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/20	12:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/20	12:50:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/20	12:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/20	14:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	14:00:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/20	14:00:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/20	14:00:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			

2015/07/20	14:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/20	14:37:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/20	14:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/20	15:38:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	15:38:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	15:38:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/20	15:38:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/20	15:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/20	16:25:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/20	16:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/20	17:16:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	17:16:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	17:16:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/20	17:16:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/20	17:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/20	18:01:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	18:01:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/20	18:02:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2015/07/20	18:02:16.0	XRT_FLD_DIS_418_OG [0x1a2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/07/20	18:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/07/20	18:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/20	18:04:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2015/07/20	18:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/20	18:11:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	18:11:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	18:11:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/20	18:12:00.0	AOCS_ORe-point_Start_27_OG [0x0b1]					
			AOCU_NM	5	02-76	00 0d 58 00 00	
2015/07/20	18:12:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/20	18:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/20	18:12:22.0	XRT_AEC_RESET_441_OG [0x1b9]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/20	18:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/20	18:12:26.0	XRT_FLD_RESET_447_OG [0x1bf]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/20	18:14:56.0	XRT_QT_PROG_SET_431_OG [0x1af]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2015/07/20	18:14:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/07/20	18:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/20	18:55:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	18:55:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	18:55:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/20	18:55:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/20	18:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/20	19:38:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/20	19:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/20	20:33:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	20:33:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/20	20:33:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/20	20:33:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/20	20:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/20	21:16:00.0	XRT_Custom_430_OG [0x1ae]					

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2015/07/20	21:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/20	22:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/20	22:12:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/20	22:12:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/20	22:12:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/20	22:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/20	22:51:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/20	22:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/20	23:11:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/20	23:11:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/20	23:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/07/20	23:12:00.0	AOCS_OrE-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/07/20	23:12:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/20	23:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/20	23:12:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/20	23:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/20	23:12:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/20	23:14:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2015/07/20	23:14:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/07/20	23:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/20	23:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/20	23:50:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/20	23:50:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/20	23:50:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/20	23:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/21	00:19:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/21	00:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/21	01:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/21	01:28:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/21	01:28:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/21	01:28:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/21	01:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/21	01:57:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/21	01:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/21	03:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/21	03:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/21	03:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/21	03:06:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/21	03:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/21	03:36:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/21	03:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/21	04:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/21	04:36:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/21	04:36:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/21	04:36:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/21	04:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/21	05:14:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/21	05:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/21	05:48:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				



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2015/07/21	05:48:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	05:48:28.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd] XRT_FOCUS_RECAL	2	07-F8	78 00
2015/07/21	05:48:30.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2015/07/21	05:52:28.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/21	05:52:48.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/21	05:52:50.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/21	05:52:52.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/21	05:52:54.0	XRT_QT_PROG_SET_427_OG [0x1ab] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/07/21	05:52:56.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/21	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	05:58:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	05:58:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/21	05:58:30.0	AOCS_ORe-point_Start_24_OG [0x0ae] AOCU_NM	5	02-76	02 00 00 00 00
2015/07/21	05:58:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/21	05:58:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/21	05:58:52.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/21	05:58:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/21	05:58:56.0	XRT_FLD_RESET_447_OG [0x1bf] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/21	06:01:26.0	XRT_QT_PROG_SET_401_OG [0x191] MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/07/21	06:01:28.0	XRT_FL_PROG_SET_428_OG [0x1ac] MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/21	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/21	06:16:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	06:16:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	06:16:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/21	06:16:06.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/21	06:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/21	06:52:30.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/21	06:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/21	07:56:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	07:56:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	07:56:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/21	07:56:36.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/21	07:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/21	08:31:00.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/21	08:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/21	09:36:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	09:36:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	09:36:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/21	09:36:36.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/21	09:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/21	10:11:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00