

# XRT Timeline to be uploaded on 2015/07/21

Period: 2015/07/21 10:01:00 - 2015/07/29 10:55:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #1AB7: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 40s cad												
Term	Pointing (x, y)		Comment									
07/21 10:14:00 - 07/21 17:38:24	Track ( 715.9, -463.1) <sup>Ⓜ 07/21 10:11:00</sup>		# OP start + 10min Filament Obs.									
07/27 06:18:00 - 07/27 11:14:54	Fixed ( 0.0, 0.0)		#SOT Post bakeout flat fields									
<b>PROG= 10 Inf.-time(s)</b>												
└─ Subr= 1 90-time(s) 40.0sec												
└─ Seqn= 29 1-time(s) 4.0sec												
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 26 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-												
Term	Pointing (x, y)		Comment									
07/21 17:41:30 - 07/21 17:48:24	Fixed ( 0.0, 0.0)		synoptic, shifted -21.5 min									
07/22 06:03:00 - 07/22 06:09:54	Fixed ( 0.0, 0.0)		synoptic									
07/22 17:56:00 - 07/22 18:02:54	Fixed ( 0.0, 0.0)		synoptic, shifted -7.0 min									
07/23 06:33:30 - 07/23 06:40:24	Fixed ( 0.0, 0.0)		synoptic, shifted 30.5 min									
07/23 18:14:00 - 07/23 18:20:54	Fixed ( 0.0, 0.0)		synoptic, shifted 11.0 min									
07/24 06:00:30 - 07/24 06:07:24	Fixed ( 0.0, 0.0)		synoptic, shifted -2.5 min									
07/25 06:06:30 - 07/25 06:13:24	Fixed ( 0.0, 0.0)		synoptic, shifted 3.5 min									
07/25 18:03:30 - 07/25 18:10:24	Fixed ( 0.0, 0.0)		synoptic, shifted 0.5 min									
07/26 05:33:30 - 07/26 05:40:24	Fixed ( 0.0, 0.0)		synoptic, shifted -29.5 min									
07/26 18:22:30 - 07/26 18:29:24	Fixed ( 0.0, 0.0)		synoptic, shifted 19.5 min									
07/27 06:09:24 - 07/27 06:14:54	Fixed ( 0.0, 0.0)		synoptic, shifted 5.0 min									
07/27 18:00:30 - 07/27 18:07:24	Fixed ( 0.0, 0.0)		synoptic, shifted -2.5 min									
07/28 06:14:30 - 07/28 06:21:24	Fixed ( 0.0, 0.0)		synoptic, shifted 11.5 min									

PROG= 14 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 33 1-time(s) 4.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0 0 2.0sec
└─ Seqn= 61 1-time(s) 4.0sec												
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 49 1-time(s) 2.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 39 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AB5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms												
Term	Pointing (x, y)		Comment									
07/21 18:38:00 - 07/22 05:12:30	Track ( 42.1, 137.5) <sup>Ⓜ 07/21 17:48:30</sup>		#AR 12387									
07/22 06:13:00 - 07/22 17:52:54	Track ( 151.2, 137.7) <sup>Ⓜ 07/22 06:10:00</sup>		#12387									
07/22 18:06:00 - 07/23 05:49:00	Track ( 254.0, 139.0) <sup>Ⓜ 07/22 18:03:00</sup>		#AR 12387									
07/23 06:43:30 - 07/23 17:25:30	Track ( 359.5, 141.6) <sup>Ⓜ 07/23 06:40:30</sup>		#AR 12387									
07/23 18:24:00 - 07/24 05:57:24	Track ( 452.4, 144.9) <sup>Ⓜ 07/23 18:21:00</sup>		#AR 12387									
07/24 06:10:30 - 07/24 17:47:54	Track ( 540.2, 149.3) <sup>Ⓜ 07/24 06:07:30</sup>		#AR 12387									
07/24 18:47:00 - 07/25 05:21:30	Track ( 621.5, 154.7) <sup>Ⓜ 07/24 17:58:00</sup>		#AR 12387									
07/25 06:16:30 - 07/25 18:00:24	Track ( 697.1, 161.2) <sup>Ⓜ 07/25 06:13:30</sup>		#AR 12387									
07/25 18:13:30 - 07/26 05:30:24	Track ( 761.3, 168.4) <sup>Ⓜ 07/25 18:10:30</sup>		#AR 12387									
07/26 10:43:30 - 07/26 17:35:00	Track ( 833.3, 179.5) <sup>Ⓜ 07/26 10:40:30</sup>		AR 12387									
07/26 23:09:00 - 07/27 03:23:30	Track ( 872.1, 188.2) <sup>Ⓜ 07/26 22:29:30</sup>		AR 12387									

07/27 04:03:00 - 07/27 06:04:54 Fixed ( 886.5, 192.4) AR 12387 by fixed point  
 07/27 11:18:00 - 07/27 17:57:24 Fixed ( 886.5, 192.4) AR 12387  
 07/27 18:56:00 - 07/28 05:31:00 Fixed ( 886.5, 192.4) #AR 12387  
 07/28 06:24:30 - 07/28 10:32:30 Fixed ( 886.5, 192.4) #AR 12387

**PROG= 06 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>													
<b>Seqn= 9 2-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>													
<b>Seqn= 92 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 42 4-time(s) 2.0sec</b>													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 17 8-time(s) 360.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	110.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	110.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	110.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1AB3: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/1024), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45**

Term	Pointing (x, y)	Comment
07/24 17:51:00 - 07/24 17:57:54	Fixed ( 0.0, 0.0)	synoptic, shifted -12.0 min

**PROG= 08 1-time(s)**

<b>Subr= 1 1-time(s) 12.0sec</b>													
<b>Seqn= 33 1-time(s) 4.0sec</b>													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 5 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 61 1-time(s) 4.0sec</b>													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 77 1-time(s) 2.0sec</b>													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 54 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 1-time(s) 12.0sec</b>													
<b>Seqn= 46 2-time(s) 2.0sec</b>													
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 25 2-time(s) 4.0sec</b>													
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 21 2-time(s) 4.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 37 2-time(s) 2.0sec</b>													
	med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1AB6: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 30s cad**

Term	Pointing (x, y)	Comment
07/26 05:43:30 - 07/26 10:40:24	Track ( -22.9, 0.1) @ 07/26 05:40:30	#SOT Post bakeout flat field and EIS Sensitivity monitoring

**PROG= 02 Inf.-time(s)**

<b>Subr= 1 120-time(s) 30.0sec</b>													
<b>Seqn= 29 1-time(s) 4.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>													
<b>Seqn= 26 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A9C: G-Band Alignment with North Pole Q90 2x2(G-band and VLS=CLS) - 1msec - 5min cadence - Partial Sun-wNGT													
Term		Pointing (x, y)					Comment						
07/26 18:44:30 - 07/26 20:29:24		Fixed ( 0.0, 930.0)					Co alignment at N-pole						
<b>PROG= 09 1-time(s)</b>													
└─ Subr= 1 24-time(s) 300.0sec													
└─ Seqn= 98 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
└─ Seqn= 63 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A9D: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec- 8 min cadence-wNGT													
Term		Pointing (x, y)					Comment						
07/26 20:44:30 - 07/26 22:29:24		Fixed ( -945.0, 0.0)					Co alignment at E-limb						
<b>PROG= 12 1-time(s)</b>													
└─ Subr= 1 15-time(s) 480.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
└─ Seqn= 43 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \* **Flare mode** \* \* \* \* \*

XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)													
Term		Pointing (x, y)					Comment						
07/21 10:14:00 - 07/21 17:38:24		Track ( 715.9, -463.1) @ 07/21 10:11:00					# OP start + 10min Filament Obs.						
07/21 18:38:00 - 07/22 05:12:30		Track ( 42.1, 137.5) @ 07/21 17:48:30					#AR 12387						
07/22 06:13:00 - 07/22 17:52:54		Track ( 151.2, 137.7) @ 07/22 06:10:00					#12387						
07/22 18:06:00 - 07/23 05:49:00		Track ( 254.0, 139.0) @ 07/22 18:03:00					#AR 12387						
07/23 06:43:30 - 07/23 17:25:30		Track ( 359.5, 141.6) @ 07/23 06:40:30					#AR 12387						
07/23 18:24:00 - 07/24 05:57:24		Track ( 452.4, 144.9) @ 07/23 18:21:00					#AR 12387						
07/24 06:10:30 - 07/24 17:47:54		Track ( 540.2, 149.3) @ 07/24 06:07:30					#AR 12387						
07/24 18:47:00 - 07/25 05:21:30		Track ( 621.5, 154.7) @ 07/24 17:58:00					#AR 12387						
07/25 06:16:30 - 07/25 18:00:24		Track ( 697.1, 161.2) @ 07/25 06:13:30					#AR 12387						
07/25 18:13:30 - 07/26 05:30:24		Track ( 761.3, 168.4) @ 07/25 18:10:30					#AR 12387						
07/26 05:43:30 - 07/26 10:40:24		Track ( -22.9, 0.1) @ 07/26 05:40:30					#SOT Post bakeout flat field and EIS Sensitivity monitoring						
07/26 10:43:30 - 07/26 17:35:00		Track ( 833.3, 179.5) @ 07/26 10:40:30					AR 12387						
07/26 23:09:00 - 07/27 03:23:30		Track ( 872.1, 188.2) @ 07/26 22:29:30					AR 12387						
07/27 04:03:00 - 07/27 06:04:54		Fixed ( 886.5, 192.4)					AR 12387 by fixed point						
07/27 06:18:00 - 07/27 11:14:54		Fixed ( 0.0, 0.0)					#SOT Post bakeout flat fields						
07/27 11:18:00 - 07/27 17:57:24		Fixed ( 886.5, 192.4)					AR 12387						
07/27 18:56:00 - 07/28 05:31:00		Fixed ( 886.5, 192.4)					#AR 12387						
07/28 06:24:30 - 07/28 10:32:30		Fixed ( 886.5, 192.4)					#AR 12387						
<b>PROG= 03 30-time(s)</b>													
└─ Subr= 1 20-time(s) 2.0sec													
└─ Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn=100 1-time(s) 10.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 53 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \* **Active Region Search** \* \* \* \* \*

NOT USED

\* \* \* \* \* **Flare Detection** \* \* \* \* \*

FLD Patrol												
Term		Pointing (x, y)					Comment					
07/21 17:48:46 - 07/22 06:00:16		Track ( 42.1, 137.5) @ 07/21 17:48:30					#AR 12387					
07/22 06:10:16 - 07/22 17:53:16		Track ( 151.2, 137.7) @ 07/22 06:10:00					#12387					
07/22 18:03:16 - 07/23 06:30:46		Track ( 254.0, 139.0) @ 07/22 18:03:00					#AR 12387					

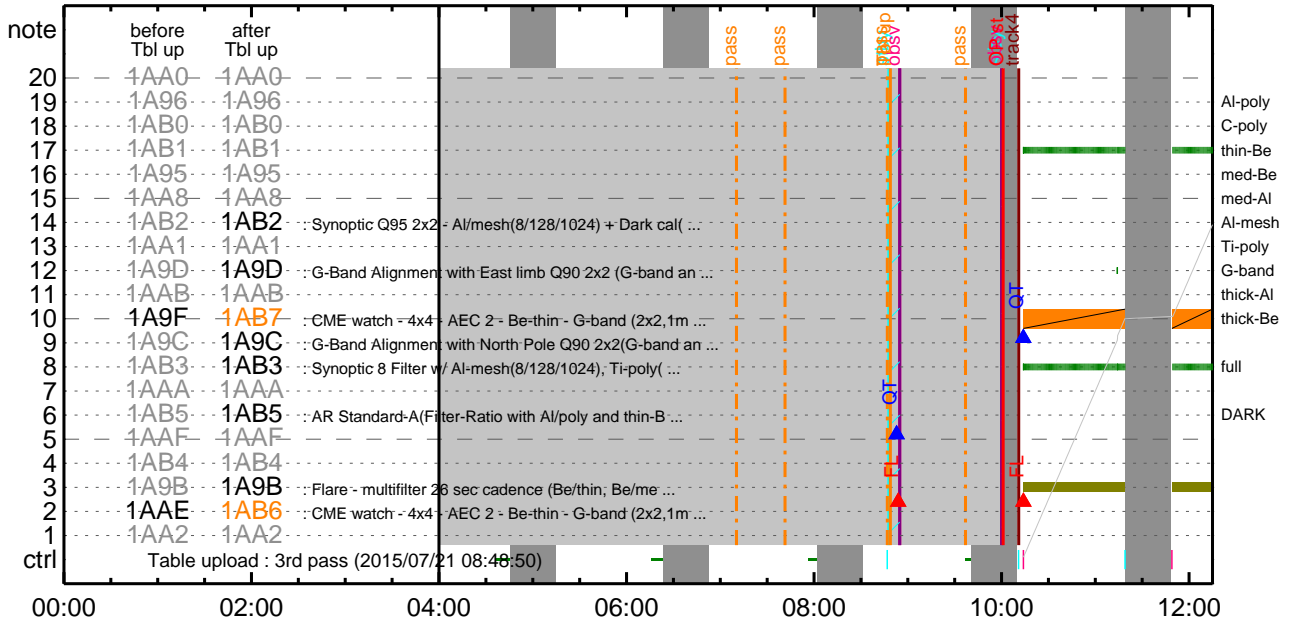
07/23 06:40:46 - 07/23 18:11:16	Track ( 359.5, 141.6)	@ 07/23 06:40:30	#AR 12387
07/23 18:21:16 - 07/24 05:57:46	Track ( 452.4, 144.9)	@ 07/23 18:21:00	#AR 12387
07/24 06:07:46 - 07/24 17:48:16	Track ( 540.2, 149.3)	@ 07/24 06:07:30	#AR 12387
07/24 17:58:16 - 07/25 06:03:46	Track ( 621.5, 154.7)	@ 07/24 17:58:00	#AR 12387
07/25 06:13:46 - 07/25 18:00:46	Track ( 697.1, 161.2)	@ 07/25 06:13:30	#AR 12387
07/25 18:10:46 - 07/26 05:30:46	Track ( 761.3, 168.4)	@ 07/25 18:10:30	#AR 12387
07/26 05:40:46 - 07/26 18:19:46	Track ( -22.9, 0.1)	@ 07/26 05:40:30	#SOT Post bakeout flat field and EIS Sensitivity monitoring
07/26 22:29:46 - 07/27 06:09:16	Track ( 872.1, 188.2)	@ 07/26 22:29:30	AR 12387
07/27 06:15:16 - 07/27 17:57:46	Fixed ( 0.0, 0.0)		#SOT Post bakeout flat fields
07/27 18:07:46 - 07/28 06:11:46	Fixed ( 886.5, 192.4)		#AR 12387
07/28 06:21:46 - 07/29 10:55:00	Fixed ( 886.5, 192.4)		#AR 12387

---

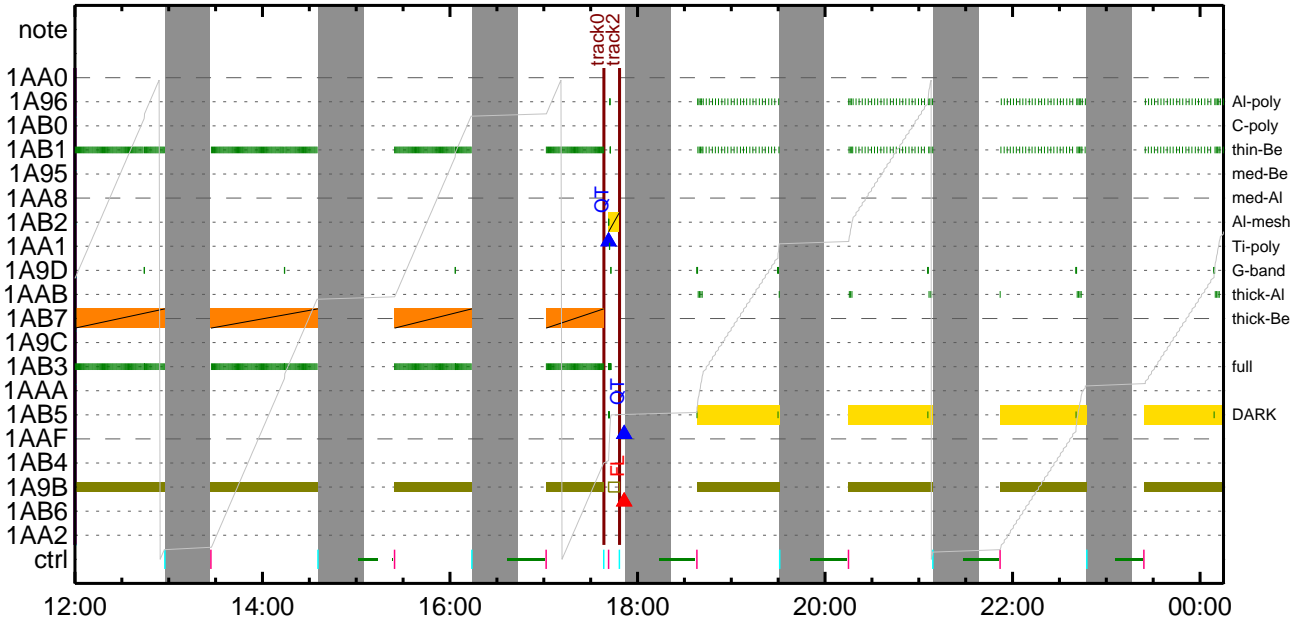
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

---

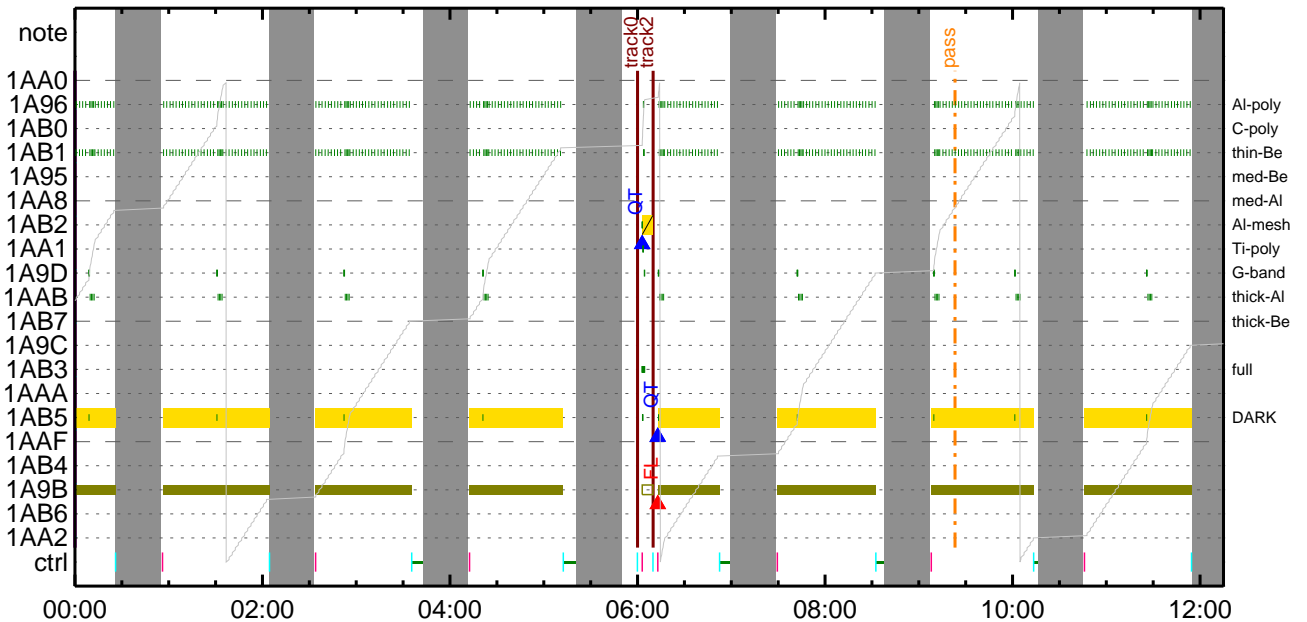
### CMDI #0278 2015/07/21



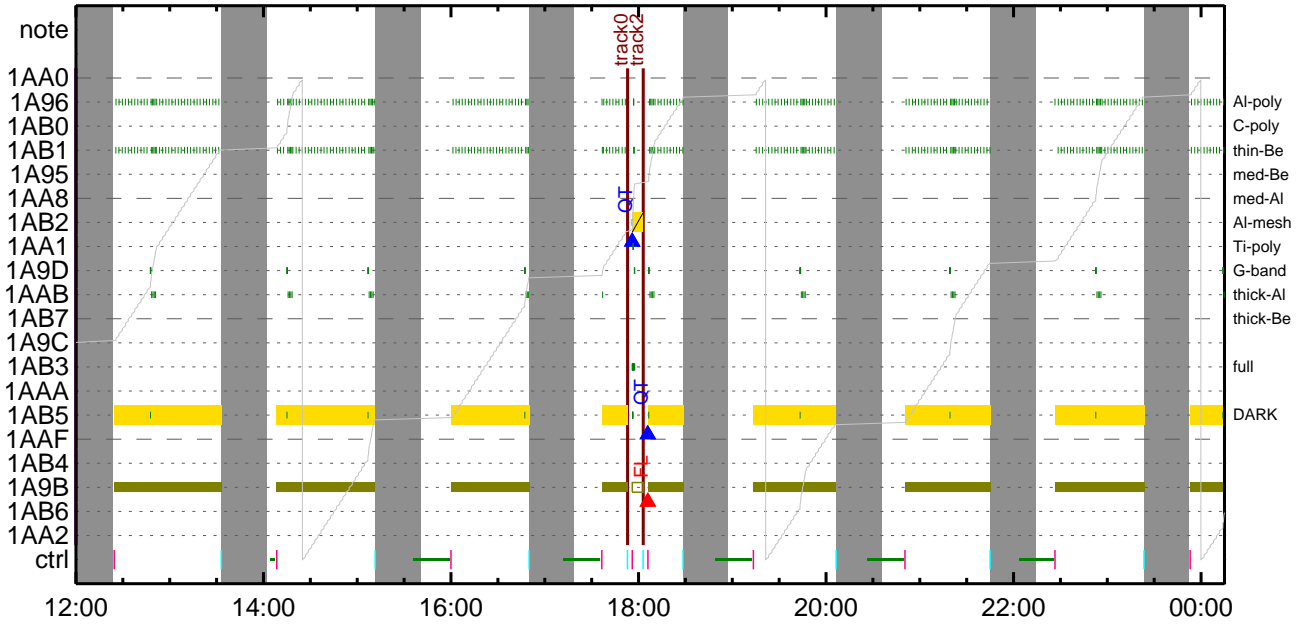
### CMDI #0278 2015/07/21



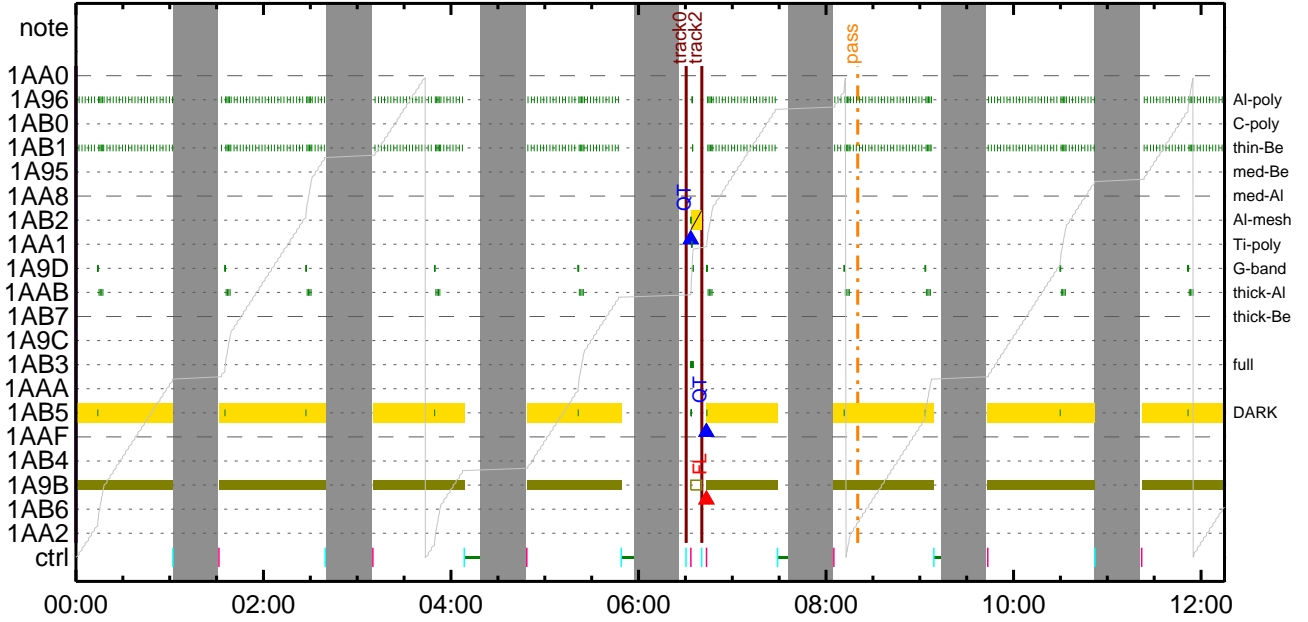
### CMDI #0278 2015/07/22



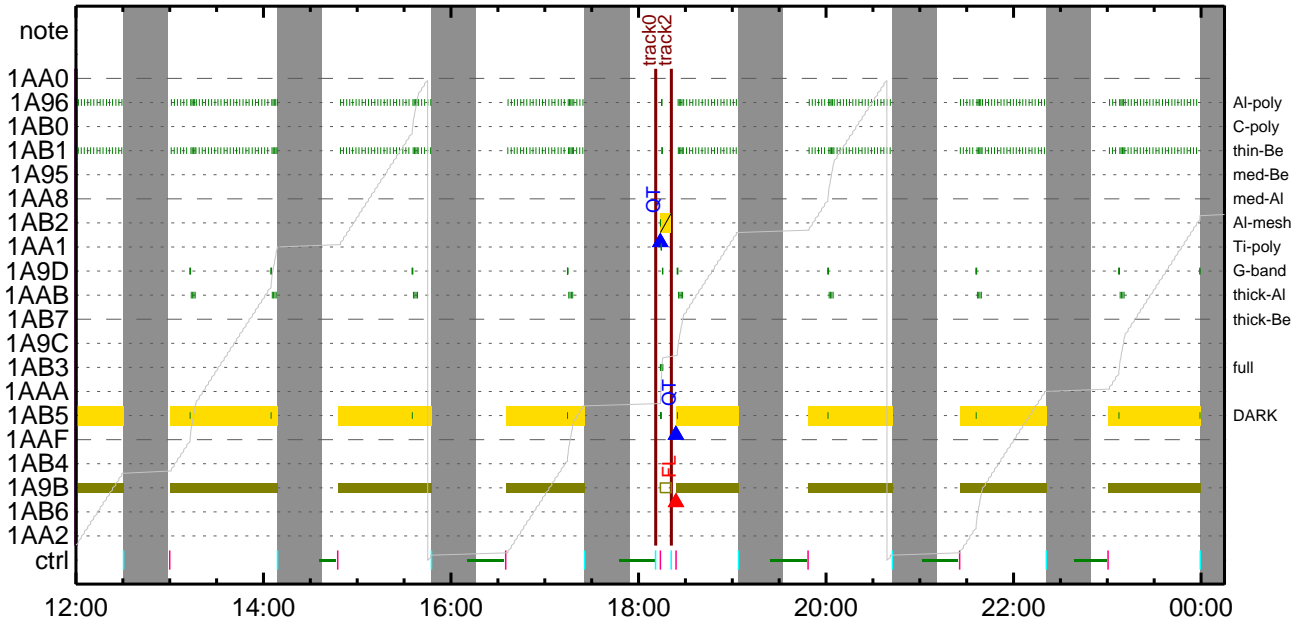
CMDI #0278 2015/07/22



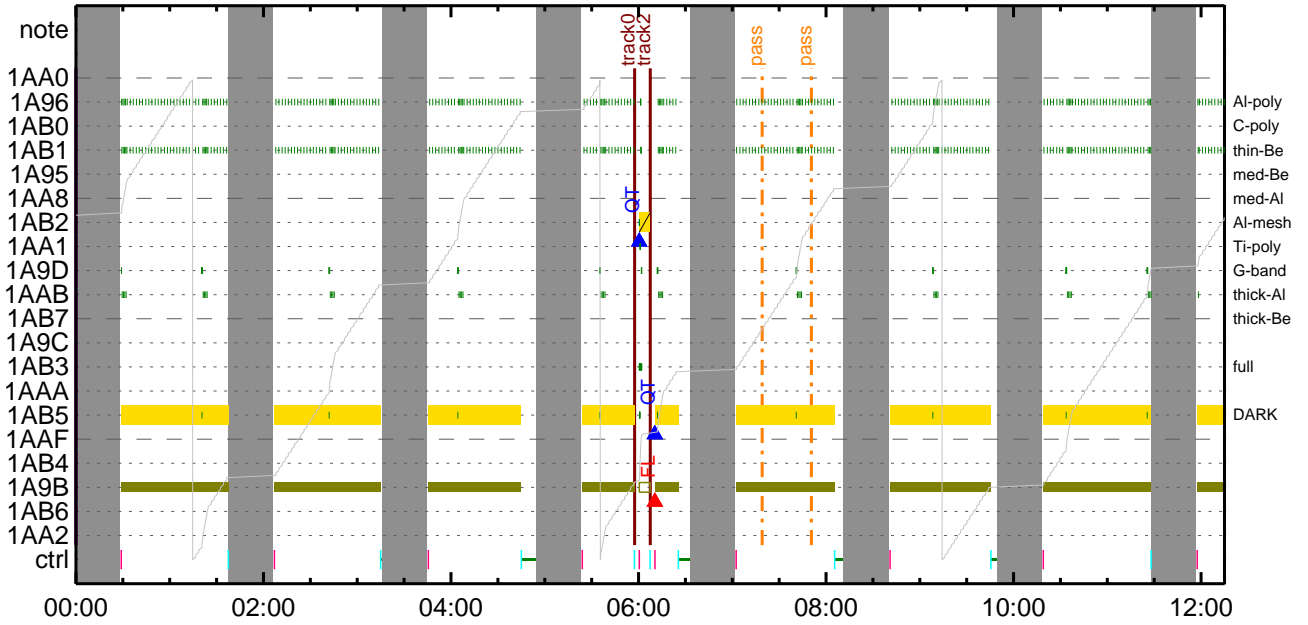
CMDI #0278 2015/07/23



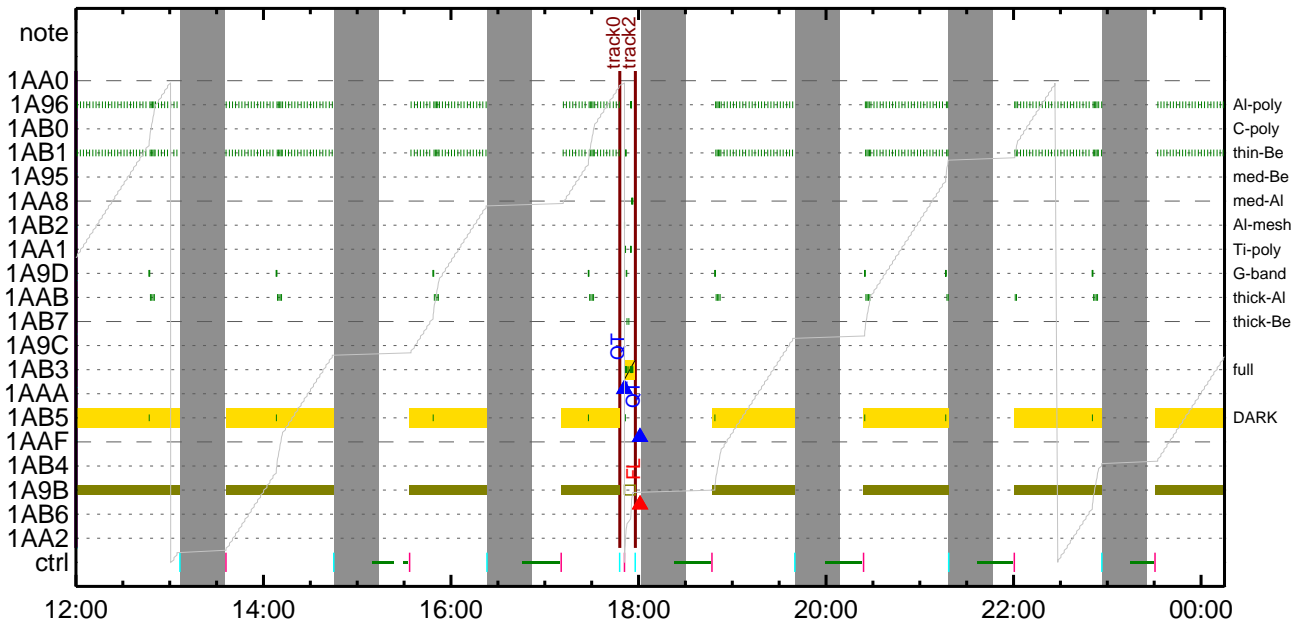
CMDI #0278 2015/07/23



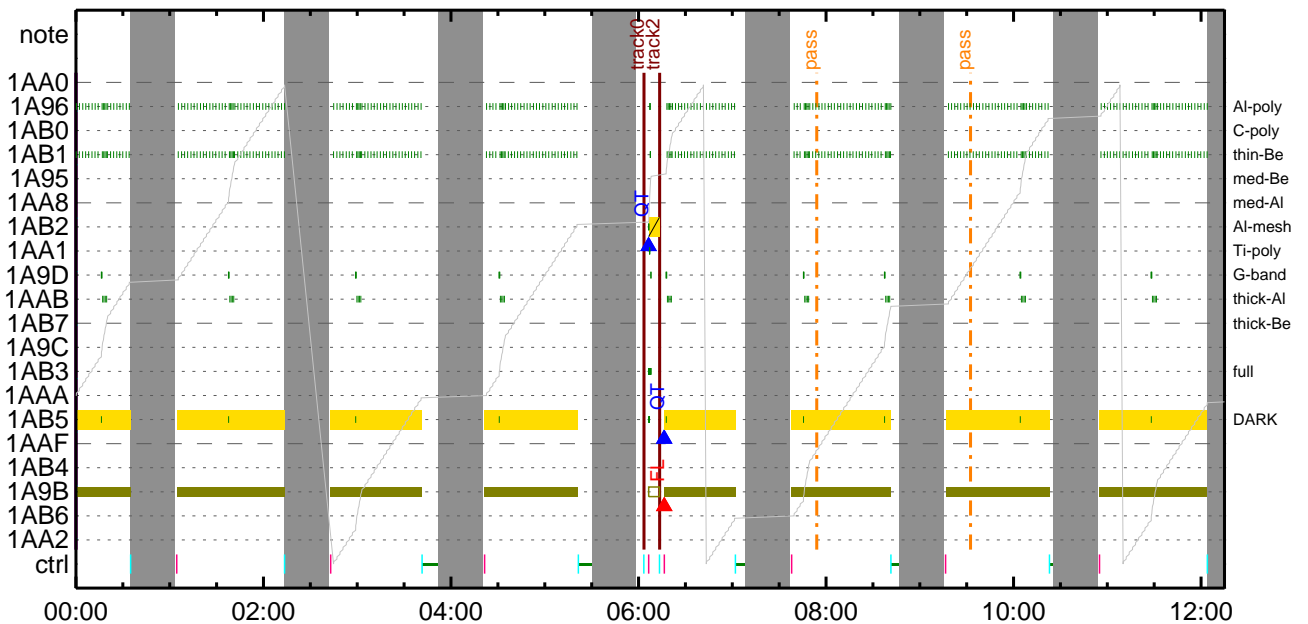
CMDI #0278 2015/07/24



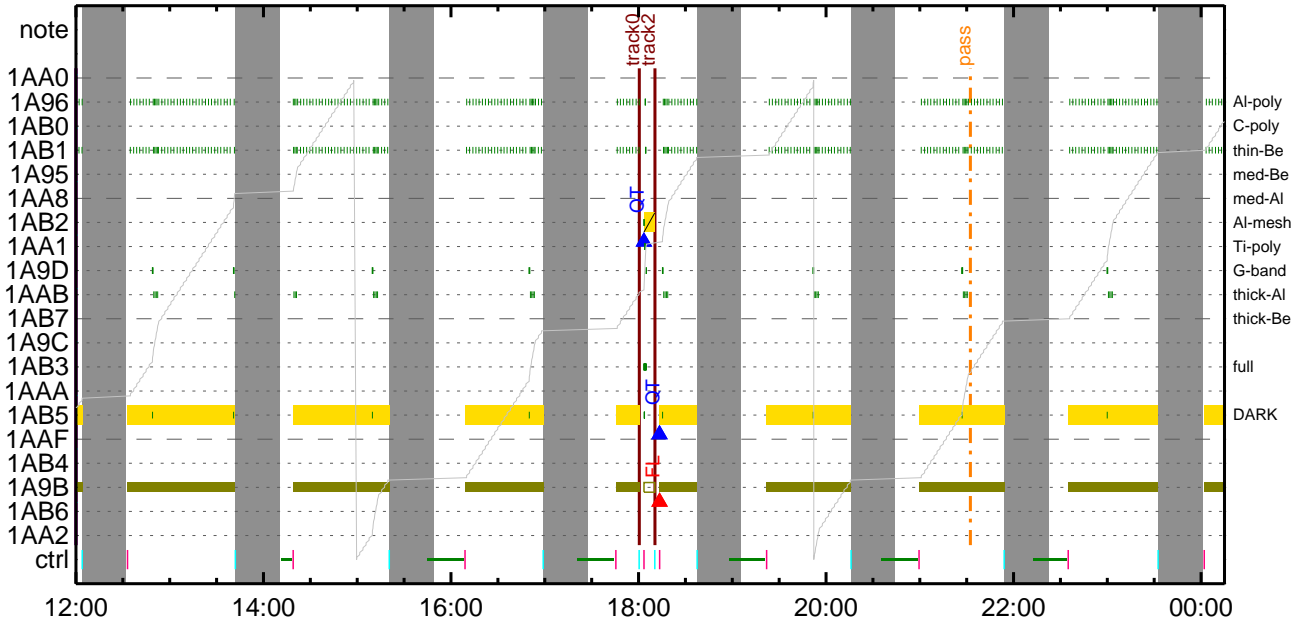
CMDI #0278 2015/07/24



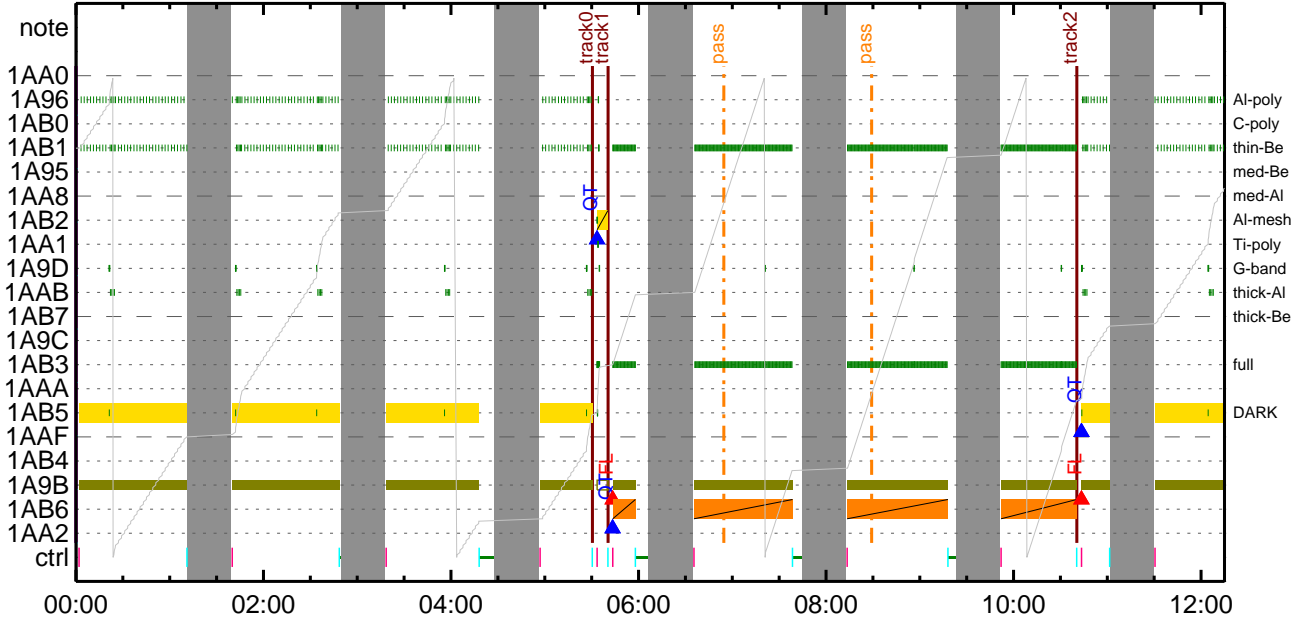
CMDI #0278 2015/07/25



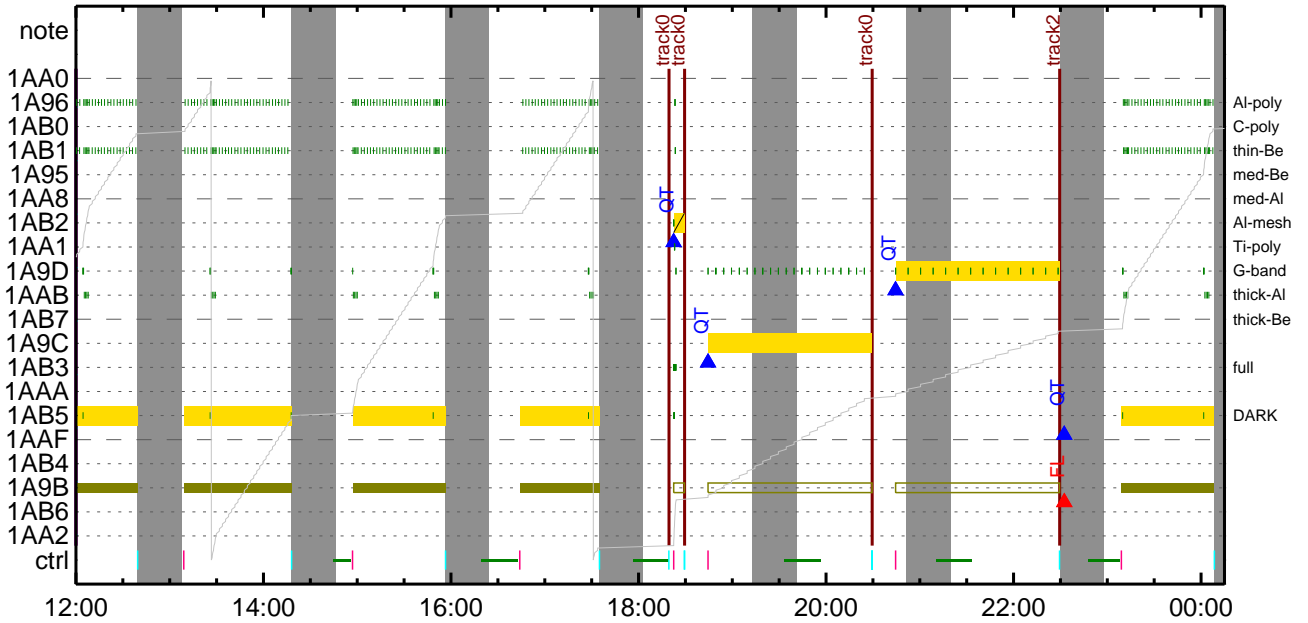
CMDI #0278 2015/07/25



CMDI #0278 2015/07/26

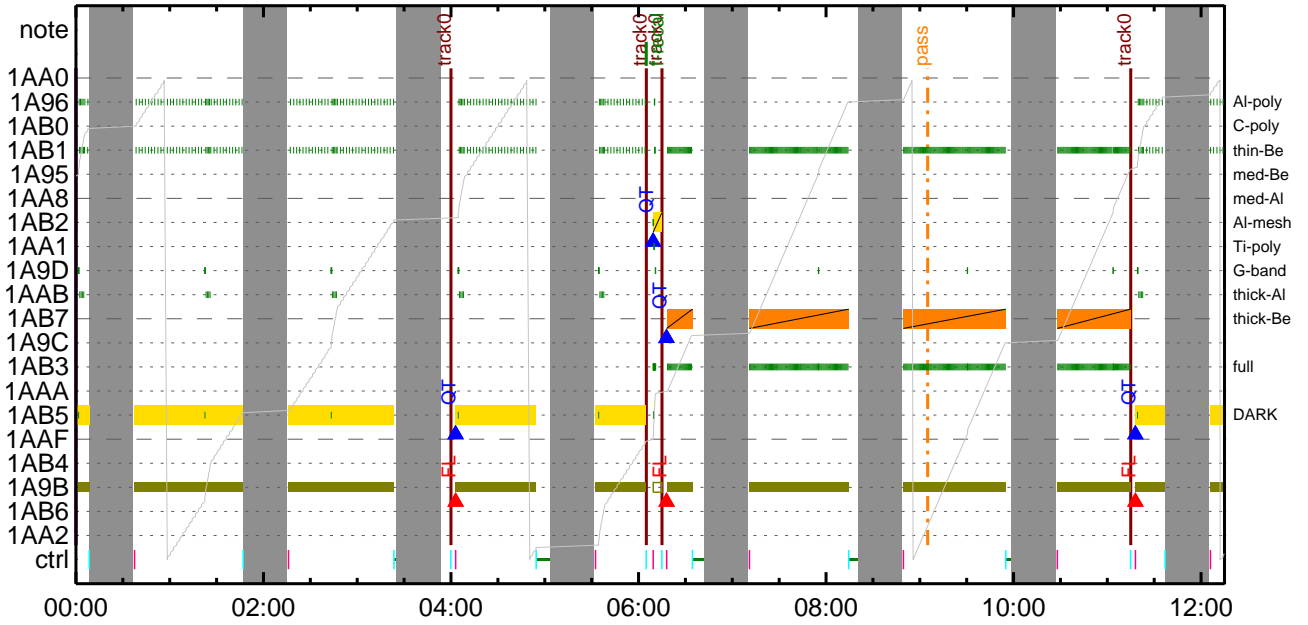


CMDI #0278 2015/07/26

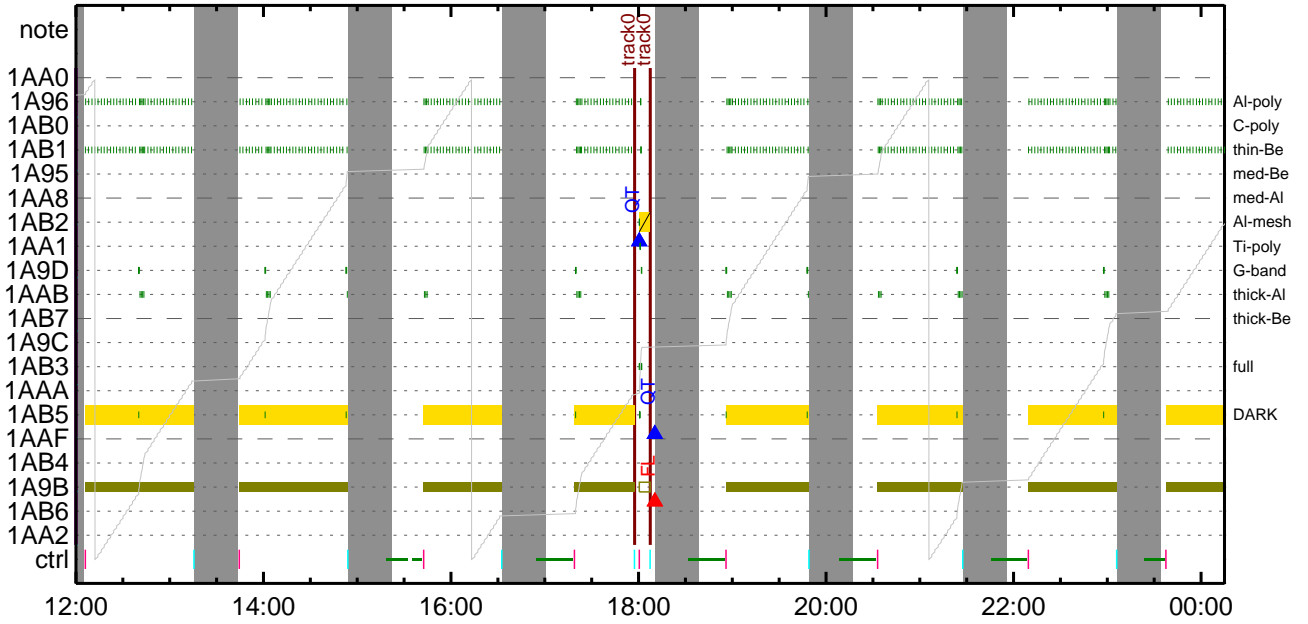




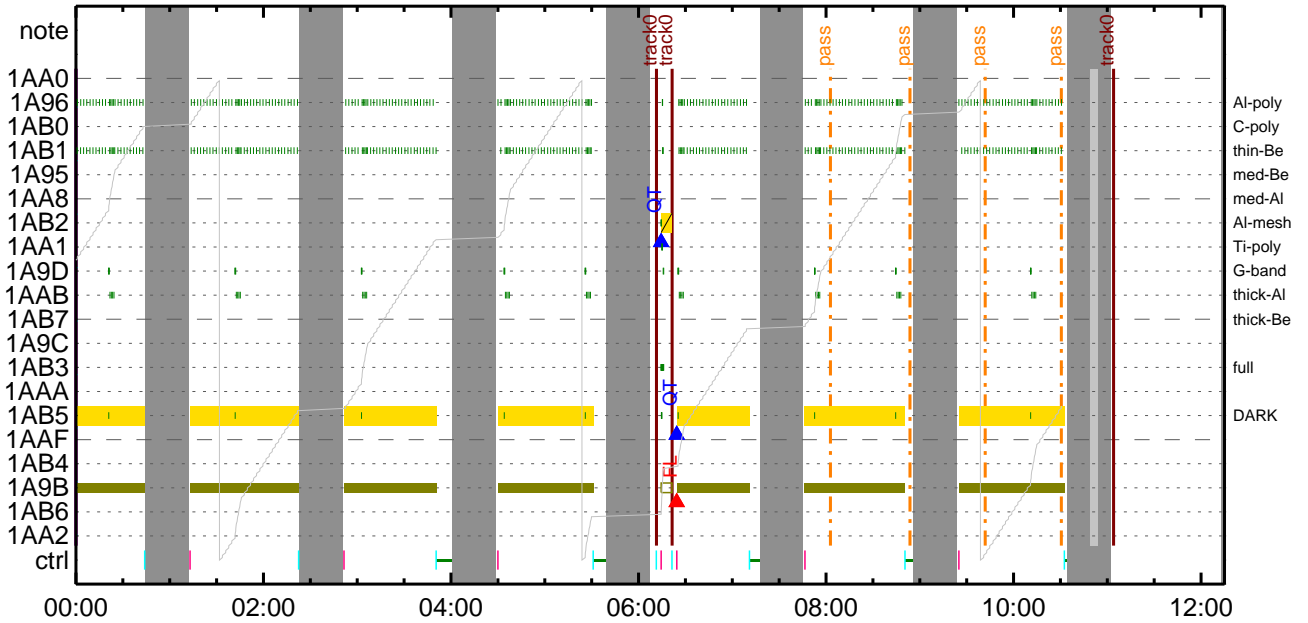
CMDI #0278 2015/07/27



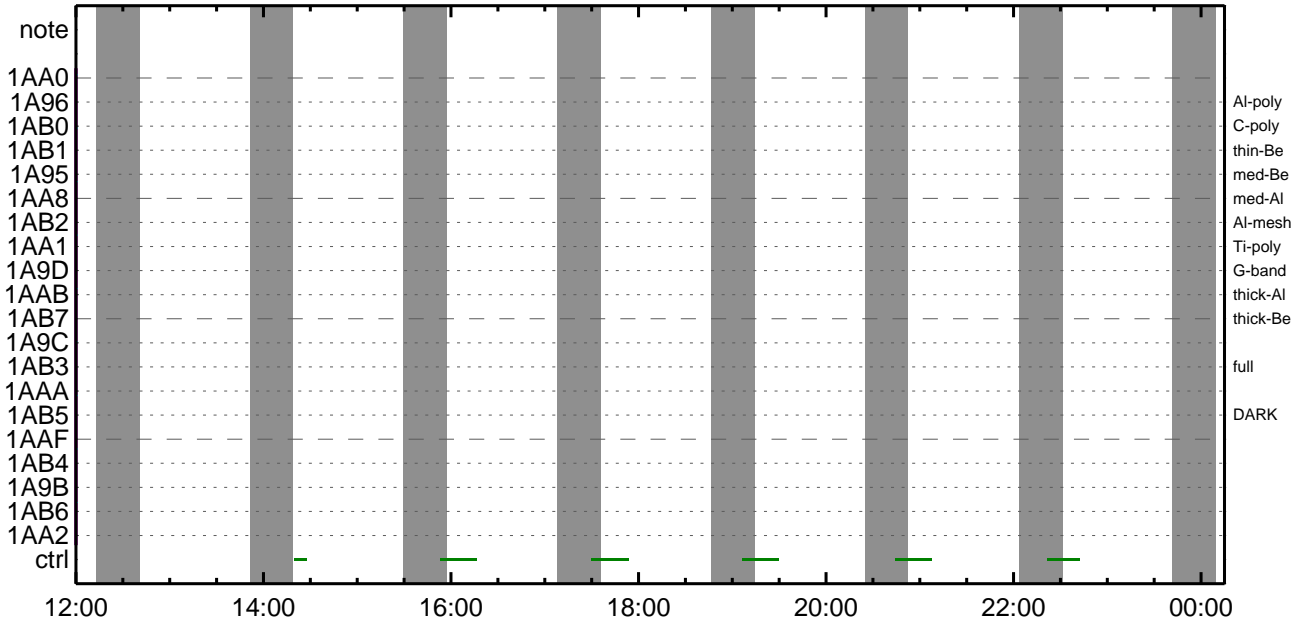
CMDI #0278 2015/07/27



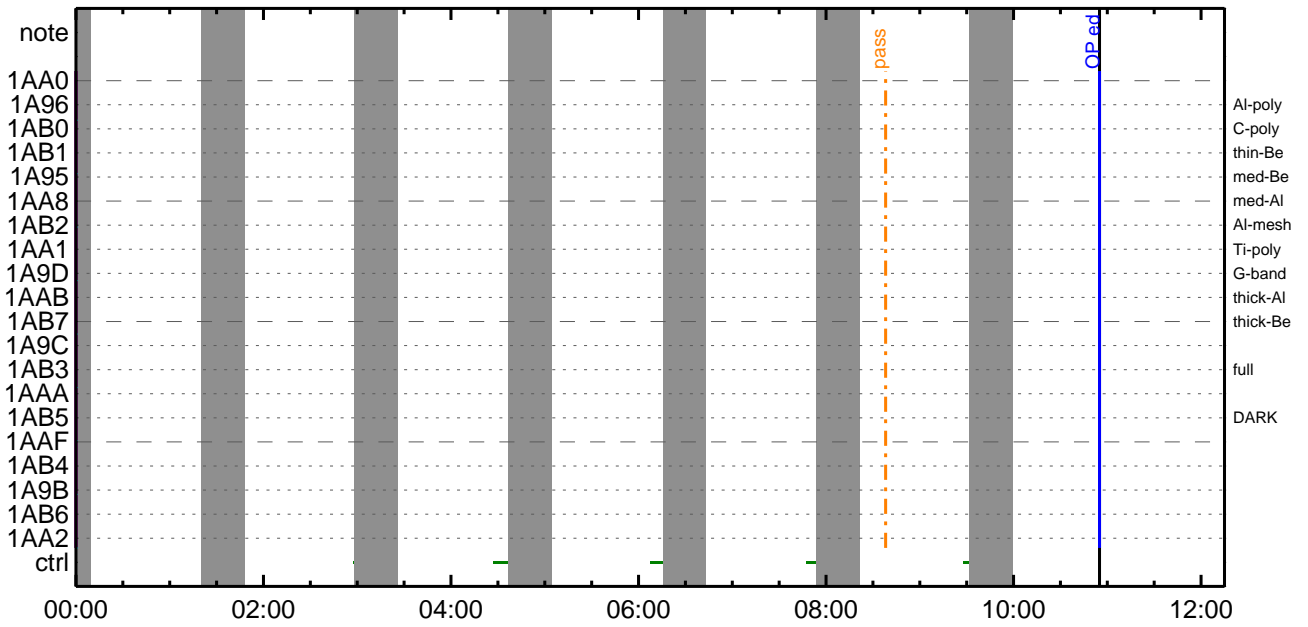
CMDI #0278 2015/07/28



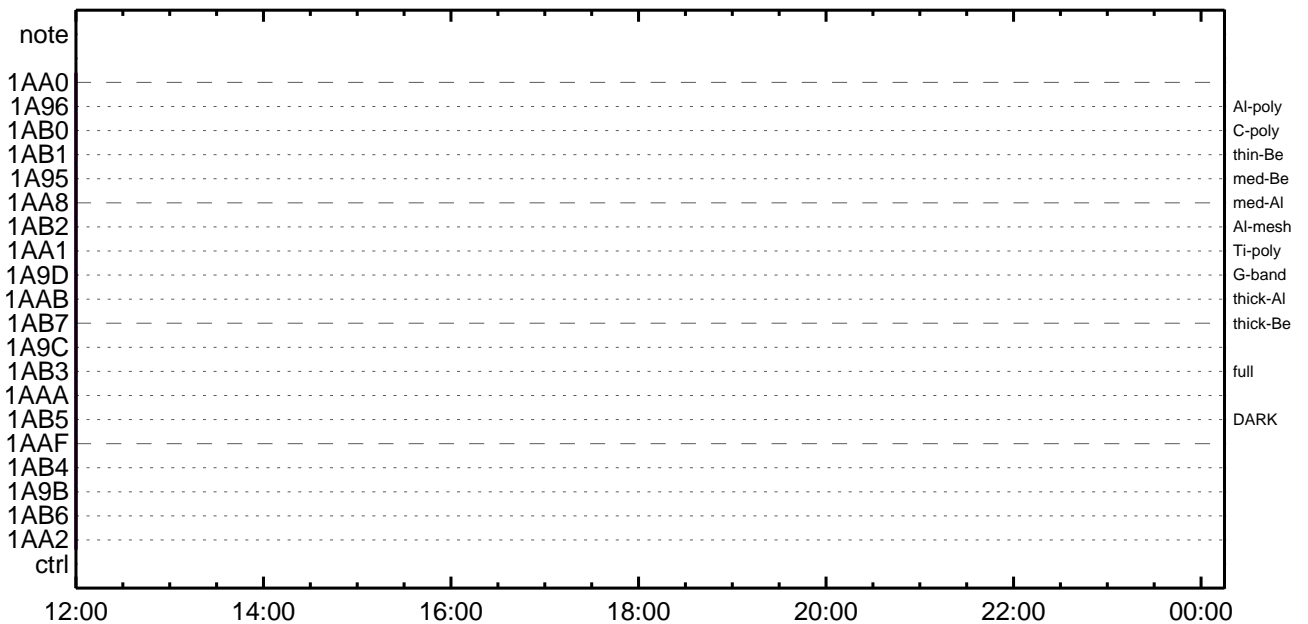
### CMDI #0278 2015/07/28



### CMDI #0278 2015/07/29



### CMDI #0278 2015/07/29







```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```





```

0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload FG Observation Table>
0109 . S. RAM ram-264:MDP_OBS_F
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. < Upload DPL table >
0120 C.
0121 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°ªÈSTS_CHKªðOFFªÈª¹ªë
0122 C.
0123 . S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHKªðONªÈª¹ªë
0134 C.
0135 C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700[ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2015-07-21 10:00:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 + DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 C. ----- Success Verify ? OK / NG_____
0162 C.
0163 C. XRT Obs. Table Upload
0164 . S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 C. ----- Comparison Check ? OK / ERR _____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 05 85 83 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 06 85 83 06 06)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 07 85 83 08 08)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 08 80 80 20 20)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 09 80 80 20 08)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0a 80 80 08 20)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0b 80 60 20 18)

```



```
0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 0c a0 80 18 20)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 0f 80 80 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 10 80 80 08 08)
0200 + DC 07-F0 MDP_XRT_FLD_ENA
0201 BC (d8)
0202 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0203 BC (c8)
0204 + DC 07-F0 MDP_XRT_AEC_RESET
0205 BC (d0)
0206 + DC 07-F0 MDP_XRT_ARS_DIS
0207 BC (d5)
0208 + DC 07-F0 MDP_XRT_FLD_RESET
0209 BC (da)
0210 + DC 07-F0 MDP_XRT_QT_PROG_SET
0211 BC (c4 06)
0212 + DC 07-F0 MDP_XRT_FL_PROG_SET
0213 BC (c5 03)
0214 . C. ----- Success Verify ? OK / NG ____
0215 C.
0216 C.
0217 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0218 C.
0219 + DC 07-F0 MDP_XRT_MODE_OBSV
0220 BC (c2)
0221 + TI 2015-07-21 10:00:02.0
0222 DC 07-F0 MDP_XRT_MODE_OBSV
0223 BC (c2)
0224 . C. ----- Success Verify ? OK / NG ____
0225 C.
0226 C. ***** XRT END *****
0227 C.
0228 . C. ***** MDP 'uAÎaî»ô%ÿaÊÂDâ¹aèDCBC•x²è *****
0229 C. (%â°îÿÓÿÃÿÈÿPÿËÿâÿçÿèaÈ%¼a¼Ã»Ûa¹aè)
0230 . S. DC-BC dcbc-402:DCBC
0231 (MDP_known_event)
0232 C.
0233 C.
0234 . C. ***** ÿDÿ¹.Ï Daily±;îÑaÈ´Øa¹aèDCBC•x²è *****
0235 . S. DC-BC dcbc-153:DCBC
0236 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0237 C.
0238 C.
0239 . C. ;ãLOSÿÃÿSÿËÿÿ¼Ã»Û;ã
0240 C.
0241 . C. ***** LOS *****
0242 C.
```



\*\*\* OP Sequence for XRT \*\*\*

```

2015/07/21 10:10:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/07/21 10:10:56.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2015/07/21 10:11:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 04 00 00 00 00
2015/07/21 10:11:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/07/21 10:11:18.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2015/07/21 10:11:20.0 XRT_AEC_RESET_441_OG [0x1b9]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2015/07/21 10:11:22.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2015/07/21 10:11:24.0 XRT_FLD_RESET_443_OG [0x1bb]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/07/21 10:13:56.0 XRT_QT_PROG_SET_448_OG [0x1c0]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2015/07/21 10:13:58.0 XRT_FL_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 03
2015/07/21 10:14:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/07/21 11:19:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/07/21 11:19:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/07/21 11:19:04.0 XRT_PREFLR_STRT_444_OG [0x1bc]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/07/21 11:22:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/07/21 11:48:00.0 XRT_Custom_430_OG [0x1ae]
2015/07/21 11:49:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/07/21 12:57:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/07/21 12:57:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/07/21 12:57:34.0 XRT_PREFLR_STRT_444_OG [0x1bc]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/07/21 13:00:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/07/21 13:26:00.0 XRT_Custom_430_OG [0x1ae]
2015/07/21 13:27:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/07/21 14:35:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/07/21 14:35:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/07/21 14:35:34.0 XRT_PREFLR_STRT_444_OG [0x1bc]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/07/21 14:38:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/07/21 15:23:30.0 XRT_Custom_430_OG [0x1ae]
2015/07/21 15:24:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/07/21 16:14:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/07/21 16:14:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/07/21 16:14:04.0 XRT_PREFLR_STRT_444_OG [0x1bc]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/07/21 16:17:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/07/21 17:00:30.0 XRT_Custom_430_OG [0x1ae]
2015/07/21 17:01:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/07/21 17:38:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/07/21 17:38:26.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2015/07/21 17:38:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2015/07/21 17:38:46.0 XRT_FLD_DIS_418_OG [0x1a2]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2015/07/21 17:41:24.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2015/07/21 17:41:26.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2015/07/21 17:41:28.0 XRT_QT_PROG_SET_427_OG [0x1ab]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0e
2015/07/21 17:41:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/07/21 17:48:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/07/21 17:48:26.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2015/07/21 17:48:30.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 02 00 00 00 00
2015/07/21 17:48:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/07/21 17:48:48.0 XRT_FLRCTRL_ENA_412_OG [0x19c]

```

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 2/19

2015/07/21	17:48:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/21	17:48:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/21	17:48:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/21	17:51:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/07/21	17:51:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/21	18:37:00.5	XRT_Custom_430_OG [0x1ae]				
2015/07/21	18:38:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/21	19:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	19:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/21	19:31:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/21	19:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/21	20:14:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/21	20:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/21	21:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	21:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/21	21:09:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/21	21:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/21	21:51:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/21	21:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/21	22:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/21	22:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/21	22:47:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/21	22:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/21	23:23:01.0	XRT_Custom_430_OG [0x1ae]				
2015/07/21	23:24:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/22	00:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/22	00:26:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/22	00:26:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/22	00:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/22	00:55:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/22	00:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/22	02:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/22	02:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/22	02:04:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/22	02:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/22	02:33:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/22	02:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/22	03:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/22	03:35:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/22	03:35:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/22	03:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/22	04:11:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/22	04:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/22	05:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/22	05:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/22	05:12:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/22	05:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/22	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/22	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/22	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]				

2015/07/22	06:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00	00	00	00	00
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/22	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/22	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/22	06:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]		2	07-F0	c4	0e			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/07/22	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/22	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/22	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/07/22	06:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]		5	02-76	02	00	00	00	00
			AOCU_NM	5	02-76	02	00	00	00	00
2015/07/22	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/22	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/22	06:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]		1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/22	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/22	06:10:24.0	XRT_FLD_RESET_443_OG [0x1bb]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/22	06:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]		2	07-F0	c4	06			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/07/22	06:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]		2	07-F0	c5	03			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/22	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/22	06:52:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/22	06:52:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/22	06:52:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/22	06:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/22	07:28:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/22	07:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/22	08:32:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/22	08:32:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/22	08:32:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/22	08:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/22	09:07:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/22	09:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/22	10:13:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/22	10:13:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/22	10:13:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/22	10:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/22	10:45:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/22	10:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/22	11:54:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/22	11:54:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/22	11:54:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/22	11:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/22	12:23:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/22	12:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/22	13:33:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/22	13:33:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/22	13:33:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/22	13:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/22	14:07:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/22	14:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/22	15:11:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/22	15:11:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/22	15:11:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]								

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 4/19

2015/07/22	15:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/22	15:59:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/22	16:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/22	16:50:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/22	16:50:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/22	16:50:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/22	16:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/22	17:35:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/22	17:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/22	17:52:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/22	17:52:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/22	17:53:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2015/07/22	17:53:16.0	XRT_FLD_DIS_418_OG [0x1a2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/07/22	17:55:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/07/22	17:55:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/22	17:55:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2015/07/22	17:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/22	18:02:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/22	18:02:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/07/22	18:03:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2015/07/22	18:03:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/22	18:03:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/22	18:03:20.0	XRT_AEC_RESET_441_OG [0x1b9]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/22	18:03:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/22	18:03:24.0	XRT_FLD_RESET_443_OG [0x1bb]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/22	18:05:56.0	XRT_QT_PROG_SET_401_OG [0x191]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2015/07/22	18:05:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/07/22	18:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/22	18:28:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/22	18:28:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/22	18:28:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/22	18:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/22	19:12:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/22	19:13:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/22	20:06:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/22	20:06:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/22	20:06:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/22	20:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/22	20:49:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/22	20:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/22	21:45:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/22	21:45:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/22	21:45:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/22	21:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/22	22:25:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/22	22:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/22	23:23:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/22	23:23:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/22	23:23:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					

2015/07/22	23:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/22	23:52:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/22	23:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/23	01:02:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/23	01:02:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/23	01:02:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/23	01:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/23	01:30:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/23	01:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/23	02:39:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/23	02:39:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/23	02:39:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/23	02:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/23	03:09:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/23	03:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/23	04:08:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/23	04:08:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/23	04:08:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/23	04:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/23	04:47:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/23	04:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/23	05:49:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/23	05:49:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/23	05:49:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/23	05:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/23	06:30:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/23	06:30:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/23	06:30:30.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2015/07/23	06:30:46.0	XRT_FLD_DIS_418_OG [0x1a2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/07/23	06:33:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/07/23	06:33:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/23	06:33:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2015/07/23	06:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/23	06:40:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/23	06:40:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/07/23	06:40:30.0	AOCS_OrE-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2015/07/23	06:40:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/23	06:40:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/23	06:40:50.0	XRT_AEC_RESET_441_OG [0x1b9]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/23	06:40:52.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/23	06:40:54.0	XRT_FLD_RESET_443_OG [0x1bb]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/23	06:43:26.0	XRT_QT_PROG_SET_401_OG [0x191]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2015/07/23	06:43:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/07/23	06:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/23	07:29:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/23	07:29:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/23	07:29:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/23	07:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 6/19

2015/07/23	08:04:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/23	08:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	09:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	09:09:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	09:09:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/23	09:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/23	09:42:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/23	09:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	10:52:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	10:52:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	10:52:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/23	10:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/23	11:21:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/23	11:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	12:30:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	12:30:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	12:30:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/23	12:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/23	12:59:00.5	XRT_Custom_430_OG [0x1ae]			
2015/07/23	13:00:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	14:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	14:09:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	14:09:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/23	14:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/23	14:46:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/23	14:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	15:47:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	15:47:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	15:47:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/23	15:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/23	16:34:00.5	XRT_Custom_430_OG [0x1ae]			
2015/07/23	16:35:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	17:25:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	17:25:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	17:25:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/23	17:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/23	18:10:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	18:10:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/23	18:11:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/07/23	18:11:16.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/23	18:13:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/23	18:13:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/23	18:13:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/07/23	18:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	18:20:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	18:20:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/23	18:21:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	02 00 00 00 00
2015/07/23	18:21:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/23	18:21:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/23	18:21:20.0	XRT_AEC_RESET_441_OG [0x1b9]			



Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 7/19

2015/07/23	18:21:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/23	18:21:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	18:23:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/07/23	18:23:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/23	18:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	19:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	19:04:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	19:04:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/23	19:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/23	19:47:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/23	19:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	20:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	20:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	20:42:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/23	20:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/23	21:24:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/23	21:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	22:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	22:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	22:21:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/23	22:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/23	22:59:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/23	23:00:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/23	23:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/23	23:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/23	23:59:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/24	00:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/24	00:28:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/24	00:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/24	01:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/24	01:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/24	01:37:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/24	01:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/24	02:06:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/24	02:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/24	03:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/24	03:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/24	03:15:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/24	03:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/24	03:44:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/24	03:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/24	04:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/24	04:45:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/24	04:45:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/24	04:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/24	05:23:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/24	05:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/24	05:57:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/24	05:57:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/24	05:57:30.0	AOCS_Ore-point_Start_2_OG [0x098]				

2015/07/24	05:57:46.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00	00	00	00	00
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/24	06:00:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/24	06:00:26.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/24	06:00:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]		2	07-F0	c4	0e			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/07/24	06:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/24	06:07:24.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/24	06:07:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/07/24	06:07:30.0	AOCS_Ore-point_Start_3_OG [0x099]		5	02-76	02	00	00	00	00
			AOCU_NM	5	02-76	02	00	00	00	00
2015/07/24	06:07:46.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/24	06:07:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/24	06:07:50.0	XRT_AEC_RESET_441_OG [0x1b9]		1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/24	06:07:52.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/24	06:07:54.0	XRT_FLD_RESET_443_OG [0x1bb]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/24	06:10:26.0	XRT_QT_PROG_SET_401_OG [0x191]		2	07-F0	c4	06			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/07/24	06:10:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]		2	07-F0	c5	03			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/24	06:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/24	06:25:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/24	06:25:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/24	06:25:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/24	06:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/24	07:01:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/24	07:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/24	08:05:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/24	08:05:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/24	08:05:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/24	08:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/24	08:40:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/24	08:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/24	09:45:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/24	09:45:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/24	09:45:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/24	09:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/24	10:18:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/24	10:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/24	11:28:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/24	11:28:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/24	11:28:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/24	11:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/24	11:56:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/24	11:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/24	13:06:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/24	13:06:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/24	13:06:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/24	13:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/24	13:35:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/24	13:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/24	14:45:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/24	14:45:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/24	14:45:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]		1	07-F0	e8				

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 9/19

2015/07/24	14:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/24	15:32:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/24	15:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/24	16:23:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/24	16:23:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/24	16:23:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/24	16:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/24	17:09:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/24	17:10:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/24	17:47:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/24	17:47:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/24	17:48:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2015/07/24	17:48:16.0	XRT_FLD_DIS_418_OG [0x1a2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/07/24	17:50:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/07/24	17:50:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/24	17:50:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2015/07/24	17:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/24	17:57:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/24	17:57:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/07/24	17:58:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2015/07/24	17:58:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/24	17:58:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/24	17:58:20.0	XRT_AEC_RESET_441_OG [0x1b9]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/24	17:58:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/24	17:58:24.0	XRT_FLD_RESET_443_OG [0x1bb]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/24	18:00:56.0	XRT_QT_PROG_SET_401_OG [0x191]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2015/07/24	18:00:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/07/24	18:46:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/24	18:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/24	19:40:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/24	19:40:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/24	19:40:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/24	19:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/24	20:23:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/24	20:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/24	21:18:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/24	21:18:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/24	21:18:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/24	21:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/24	21:59:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/24	22:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/24	22:56:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/24	22:56:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/24	22:56:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/24	22:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/24	23:29:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/24	23:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/25	00:35:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/25	00:35:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	

Tuesday July 21, 2015

9/19

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 10/19

2015/07/25	00:35:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	00:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/25	01:03:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/25	01:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	02:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	02:13:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	02:13:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	02:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/25	02:42:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/25	02:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	03:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	03:41:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	03:41:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	03:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/25	04:20:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/25	04:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	05:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	05:21:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	05:21:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	05:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/25	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	06:03:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/25	06:03:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/25	06:03:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/25	06:06:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/25	06:06:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/25	06:06:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/07/25	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	06:13:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	06:13:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/07/25	06:13:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/07/25	06:13:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/25	06:13:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/25	06:13:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/25	06:13:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/25	06:13:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	06:16:26.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2015/07/25	06:16:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/07/25	06:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	07:02:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	07:02:02.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	07:02:04.5	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	07:05:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/25	07:37:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/25	07:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	08:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	08:41:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	08:41:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	08:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 11/19

2015/07/25	09:15:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/25	09:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/07/25	10:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/25	10:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/25	10:23:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/25	10:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/25	10:54:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/25	10:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/07/25	12:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/25	12:04:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/25	12:04:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/25	12:04:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/25	12:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/25	12:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/25	12:32:00.0	XRT_Custom_430_OG [0x1ae]						
2015/07/25	12:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/07/25	13:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/25	13:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/25	13:42:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/25	13:42:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/25	13:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/25	13:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/25	14:18:00.0	XRT_Custom_430_OG [0x1ae]						
2015/07/25	14:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/07/25	15:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/25	15:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/25	15:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/25	15:20:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/25	15:20:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/25	15:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/25	15:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/25	16:08:00.0	XRT_Custom_430_OG [0x1ae]						
2015/07/25	16:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/07/25	16:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/25	16:59:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/25	16:59:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/25	16:59:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/25	16:59:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/25	17:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/25	17:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/25	17:44:30.0	XRT_Custom_430_OG [0x1ae]						
2015/07/25	17:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/07/25	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/25	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/25	18:00:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
2015/07/25	18:00:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/07/25	18:00:30.0	AOCS_Ore-point_Start_2_OG [0x098]						
2015/07/25	18:00:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/07/25	18:00:46.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/07/25	18:03:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
2015/07/25	18:03:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/07/25	18:03:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
2015/07/25	18:03:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/25	18:03:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]						
2015/07/25	18:03:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2015/07/25	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
2015/07/25	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/25	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]						
2015/07/25	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/25	18:10:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
2015/07/25	18:10:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/07/25	18:10:30.0	AOCS_Ore-point_Start_3_OG [0x099]						
2015/07/25	18:10:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00		
2015/07/25	18:10:46.0	XRT_FLD_ENA_411_OG [0x19b]						
2015/07/25	18:10:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/25	18:10:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
2015/07/25	18:10:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/25	18:10:50.0	XRT_AEC_RESET_441_OG [0x1b9]						
2015/07/25	18:10:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/25	18:10:52.0	XRT_ARS_DIS_423_OG [0x1a7]						
2015/07/25	18:10:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/25	18:10:54.0	XRT_FLD_RESET_443_OG [0x1bb]						
2015/07/25	18:10:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/25	18:13:26.0	XRT_QT_PROG_SET_401_OG [0x191]						
2015/07/25	18:13:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 12/19

2015/07/25	18:13:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/25	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	18:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	18:37:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	18:37:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	18:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/25	19:21:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/25	19:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	20:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	20:16:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	20:16:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	20:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/25	20:58:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/25	20:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	21:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	21:54:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	21:54:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	21:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/25	22:34:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/25	22:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/25	23:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/25	23:32:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/25	23:32:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/25	23:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	00:01:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/26	00:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	01:11:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	01:11:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	01:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	01:39:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/26	01:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	02:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	02:48:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	02:48:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	02:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	03:17:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/26	03:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	04:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	04:18:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	04:18:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	04:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	04:56:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/26	04:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	05:30:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	05:30:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/07/26	05:30:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/07/26	05:30:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/26	05:33:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/26	05:33:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 13/19

2015/07/26	05:33:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/07/26	05:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	05:40:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	05:40:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2015/07/26	05:40:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01	00 00 00 00			
2015/07/26	05:40:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/26	05:40:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/26	05:40:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/26	05:40:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/26	05:40:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	05:43:26.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2015/07/26	05:43:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/26	05:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	05:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	05:58:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	05:58:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	06:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	06:34:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/26	06:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	07:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	07:38:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	07:38:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	07:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	08:12:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/26	08:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	09:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	09:18:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	09:18:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	09:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	09:51:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/26	09:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	10:40:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	10:40:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2015/07/26	10:40:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02	00 00 00 00			
2015/07/26	10:40:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/26	10:40:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/26	10:40:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/26	10:40:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/26	10:40:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	10:43:26.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/07/26	10:43:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/26	10:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	11:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	11:01:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	11:01:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	11:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	11:29:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/26	11:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	12:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 14/19

2015/07/26	12:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	12:39:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	12:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	13:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	13:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	14:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	14:18:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	14:18:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	14:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	14:56:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	14:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	15:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	15:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	15:56:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	15:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	16:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	16:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	17:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	17:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/26	17:35:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/26	17:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/26	18:19:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	18:19:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/26	18:19:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/26	18:19:46.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/26	18:22:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/26	18:22:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/26	18:22:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/07/26	18:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	18:29:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	18:29:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	18:29:28.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/07/26	18:29:30.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 ad 59 00 00				
2015/07/26	18:29:48.0	XRT_FLD_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/26	18:29:50.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/26	18:29:52.0	XRT_ARS_DIS_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/26	18:44:28.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2015/07/26	18:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	20:29:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	20:29:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/26	20:29:28.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/07/26	20:29:30.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 00 00 54 00				
2015/07/26	20:29:48.0	XRT_FLD_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/26	20:29:50.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/26	20:29:52.0	XRT_ARS_DIS_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/26	20:44:28.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2015/07/26	20:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/26	22:29:24.0	XRT_CTRL_MANU_402_OG [0x192]								



Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 15/19

2015/07/26	22:29:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/26	22:29:30.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/26	22:29:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2015/07/26	22:29:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/26	22:29:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/26	22:29:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/26	22:29:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/26	22:32:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/26	22:32:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/07/26	23:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/26	23:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/07/27	00:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/27	00:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/27	00:08:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/27	00:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/27	00:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/27	00:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/07/27	01:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/27	01:47:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/27	01:47:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/27	01:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/27	02:15:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/27	02:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/07/27	03:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/27	03:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/27	03:23:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/27	03:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/27	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/27	03:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/27	04:00:00.0	AOCs_OrE-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/07/27	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 ee ea b1 30
2015/07/27	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/27	04:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/27	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/27	04:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/27	04:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/27	04:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/07/27	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/07/27	04:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0
2015/07/27	04:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/27	04:54:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/27	04:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/27	05:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/27	05:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/27	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/07/27	06:04:56.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/27	06:05:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_RECAL	2	07-F8	78 00
2015/07/27	06:08:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCU_NM	5	02-76	00 00 00 00 00
		XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

Jul 21, 15 12:16

XRT\_OGLIST\_0278.chk

Page 16/19

2015/07/27	06:09:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/27	06:09:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/27	06:09:20.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/27	06:09:22.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/07/27	06:09:24.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/27	06:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/27	06:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/07/27	06:15:00.0	AOCs_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/07/27	06:15:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/27	06:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/27	06:15:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/27	06:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/27	06:15:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/27	06:17:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2015/07/27	06:17:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/27	06:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/27	06:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/27	06:34:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/27	06:34:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/27	06:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/27	07:10:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/27	07:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/27	08:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/27	08:14:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/27	08:14:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/27	08:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/27	08:48:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/27	08:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/27	09:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/27	09:55:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/27	09:55:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/27	09:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/27	10:27:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/27	10:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/27	11:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/27	11:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/07/27	11:15:00.0	AOCs_OrE-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	ee	ea	b1	30
2015/07/27	11:15:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/27	11:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/27	11:15:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/27	11:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/27	11:15:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/27	11:17:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/07/27	11:17:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/07/27	11:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/27	11:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/27	11:37:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/27	11:37:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

2015/07/27	11:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/07/27	12:05:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/27	12:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/07/27	13:15:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/07/27	13:15:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/07/27	13:15:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/07/27	13:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/07/27	13:43:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/27	13:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/07/27	14:54:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/07/27	14:54:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/07/27	14:54:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/07/27	14:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/07/27	15:41:30.0	XRT_Custom_430_OG [0x1ae]								
2015/07/27	15:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/07/27	16:32:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/07/27	16:32:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/07/27	16:32:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/07/27	16:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/07/27	17:18:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/27	17:19:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/07/27	17:57:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/07/27	17:57:26.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2015/07/27	17:57:30.0	AOCS_OrE-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2015/07/27	17:57:46.0	XRT_FLD_DIS_418_OG [0x1a2]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2015/07/27	18:00:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2015/07/27	18:00:26.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/07/27	18:00:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e					
2015/07/27	18:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/07/27	18:07:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/07/27	18:07:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2015/07/27	18:07:30.0	AOCS_OrE-point_Start_7_OG [0x09d]								
		AOCU_NM	5	02-76	00 ee ea b1 30					
2015/07/27	18:07:46.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2015/07/27	18:07:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2015/07/27	18:07:50.0	XRT_AEC_RESET_441_OG [0x1b9]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2015/07/27	18:07:52.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/07/27	18:07:54.0	XRT_FLD_RESET_443_OG [0x1bb]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/07/27	18:10:26.0	XRT_QT_PROG_SET_401_OG [0x191]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06					
2015/07/27	18:10:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03					
2015/07/27	18:55:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/27	18:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/07/27	19:49:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/07/27	19:49:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/07/27	19:49:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/07/27	19:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/07/27	20:32:00.0	XRT_Custom_430_OG [0x1ae]								
2015/07/27	20:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/07/27	21:27:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/07/27	21:27:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/07/27	21:27:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]								

2015/07/27	21:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8		
2015/07/27	22:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/27	22:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/27	23:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/27	23:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/27	23:06:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/27	23:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/27	23:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/27	23:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/28	00:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/28	00:44:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/28	00:44:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/28	00:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/28	01:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/28	01:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/28	02:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/28	02:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/28	02:22:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/28	02:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/28	02:50:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/28	02:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/28	03:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/28	03:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/28	03:50:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/28	03:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/28	04:29:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/28	04:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/28	05:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/28	05:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/28	05:31:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/28	05:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/07/28	06:11:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/07/28	06:11:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/28	06:11:30.0	AOCS_Orе-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/07/28	06:11:46.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/07/28	06:14:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/07/28	06:14:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/07/28	06:14:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/28	06:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2015/07/28	06:21:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/07/28	06:21:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/07/28	06:21:30.0	AOCS_Orе-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/07/28	06:21:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 ee ea b1 30		
2015/07/28	06:21:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/07/28	06:21:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/07/28	06:21:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/07/28	06:21:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/07/28	06:24:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/07/28	06:24:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		

Jul 21, 15 12:16

## XRT\_OGLIST\_0278.chk

Page 19/19

2015/07/28	06:24:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/28	07:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	07:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/28	07:11:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/28	07:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/28	07:45:30.0	XRT_Custom_430_OG [0x1ae]					
2015/07/28	07:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/28	08:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	08:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/28	08:50:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/28	08:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/28	09:24:00.0	XRT_Custom_430_OG [0x1ae]					
2015/07/28	09:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/28	10:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	10:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/28	10:32:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/28	10:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/28	11:04:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00