

XRT Timeline to be uploaded on 2015/07/28

Period: 2015/07/28 10:54:00 - 2015/08/05 10:45:00

* * * * *

Normal mode

* * * * *

XOB #1AB8: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 80s cad												
Term	Pointing (x, y)							Comment				
07/28 11:07:00 - 07/28 17:08:00	Track (280.0, -380.2) ^{Ⓢ 07/28 11:04:00}	# OP start + 10min, AR 12390										
07/28 18:08:00 - 07/29 05:39:24	Track (336.5, -378.6) ^{Ⓢ 07/28 18:05:00}	AR 12390										
07/29 05:52:30 - 07/29 17:44:00	Track (427.5, -374.9) ^{Ⓢ 07/29 05:49:30}	AR 12390										
07/29 18:41:30 - 07/30 05:59:24	Track (520.6, -369.7) ^{Ⓢ 07/29 18:38:30}	AR 12390										
07/30 06:12:30 - 07/30 17:59:54	Track (597.6, -364.1) ^{Ⓢ 07/30 06:09:30}	AR 12390										
07/30 18:13:00 - 07/31 05:40:00	Track (670.2, -357.3) ^{Ⓢ 07/30 18:10:00}	AR 12390										
07/31 06:33:00 - 07/31 17:17:30	Track (735.5, -349.3) ^{Ⓢ 07/31 06:30:00}	AR 12390										
07/31 18:15:00 - 08/01 05:48:24	Track (788.1, -341.0) ^{Ⓢ 07/31 18:12:00}	AR 12390										
08/01 06:01:30 - 08/01 15:59:54	Track (831.0, -332.0) ^{Ⓢ 08/01 05:58:30}	AR 12390										
08/01 21:03:00 - 08/02 01:59:54	Track (870.5, -319.7) ^{Ⓢ 08/01 21:00:00}	AR 12390										
PROG= 20 1-time(s)												
└─ Subr= 1 45-time(s) 80.0sec												
└─ Seqn= 29 1-time(s) 4.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 26 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-												
Term	Pointing (x, y)							Comment				
07/28 17:58:00 - 07/28 18:04:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min										
07/29 05:42:30 - 07/29 05:49:24	Fixed (0.0, 0.0)	synoptic, shifted -20.5 min										
07/29 18:31:30 - 07/29 18:38:24	Fixed (0.0, 0.0)	synoptic, shifted 28.5 min										
07/30 06:02:30 - 07/30 06:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min										
07/30 18:03:00 - 07/30 18:09:54	Fixed (0.0, 0.0)	synoptic										
07/31 06:23:00 - 07/31 06:29:54	Fixed (0.0, 0.0)	synoptic, shifted 20.0 min										
07/31 18:05:00 - 07/31 18:11:54	Fixed (0.0, 0.0)	synoptic, shifted 2.0 min										
08/01 05:52:54 - 08/01 05:58:24	Fixed (0.0, 0.0)	synoptic, shifted -11.5 min										
08/02 06:03:00 - 08/02 06:13:00	Fixed (0.0, 0.0)	synoptic										
PROG= 14 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 33 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 61 1-time(s) 4.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 49 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 39 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AB3: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/1024), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45-												
Term	Pointing (x, y)							Comment				
08/01 16:03:00 - 08/01 16:09:54	Fixed (0.0, 0.0)	synoptic, shifted -19.5 min										
PROG= 08 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 33 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												

Seqn=	1-time(s)	2.0sec																		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec							
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec							
Seqn= 61	1-time(s)	4.0sec																		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Seqn= 77	1-time(s)	2.0sec																		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Seqn= 54	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec							
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Subr= 2	1-time(s)	12.0sec																		
Seqn= 46	2-time(s)	2.0sec																		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Seqn= 25	2-time(s)	4.0sec																		
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Seqn= 21	2-time(s)	4.0sec																		
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Seqn= 37	2-time(s)	2.0sec																		
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

XOB #1ABA: Coronal Cavity (2 filters) - HOP264/HOP289 - DPCM 2x2 - 768FOV - 60s cad - AEC0 with light leak													
Term	Pointing (x, y)		Comment										
08/01 17:01:30 - 08/01 20:59:54	Track (189.4,	53.7)	08/01 16:10:00 HOP289 filament									
Prog= 13	Inf.-time(s)												
Subr= 1	1-time(s)	2.0sec											
Seqn= 97	1-time(s)	2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	768x768	(1064, 1048)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	768x768	(1064, 1048)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 51	60-time(s)	60.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	768x768	(1064, 1048)	DPCM	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	768x768	(1064, 1048)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1A9C: G-Band Alignment with North Pole Q90 2x2(G-band and VLS=CLS) - 1msec - 5min cadence - Partial Sun-wNGT													
Term	Pointing (x, y)		Comment										
08/02 02:33:00 - 08/02 03:35:30	Fixed (0.0,	930.0)	N-limb coalignment									
Prog= 09	1-time(s)												
Subr= 1	24-time(s)	300.0sec											
Seqn= 98	1-time(s)	2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536	(1024, 768)	Q=90	0	0	2.0sec
Seqn= 63	1-time(s)	2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536	(1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1A9D: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec- 8 min cadence-wNGT													
Term	Pointing (x, y)		Comment										
08/02 04:15:00 - 08/02 05:59:54	Fixed (-945.0,	0.0)	E-limb coalignment									
Prog= 12	1-time(s)												
Subr= 1	15-time(s)	480.0sec											
Seqn= 19	1-time(s)	2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1536x2048	(1280, 1024)	Q=90	0	0	2.0sec
Seqn= 43	1-time(s)	2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1536x2048	(1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

* * * * *

Flare mode

* * * * *

XOB #1ABB: Flare - CME watch -Be-thin - 4x4 - AEC 2 - 20s cadence													
Term	Pointing (x, y)		Comment										
07/28 11:07:00 - 07/28 17:08:00	Track (280.0,	-380.2)	07/28 11:04:00 # OP start + 10min, AR 12390									
07/28 18:08:00 - 07/29 05:39:24	Track (336.5,	-378.6)	07/28 18:05:00 AR 12390									
07/29 05:52:30 - 07/29 17:44:00	Track (427.5,	-374.9)	07/29 05:49:30 AR 12390									
07/29 18:41:30 - 07/30 05:59:24	Track (520.6,	-369.7)	07/29 18:38:30 AR 12390									

07/30 06:12:30 - 07/30 17:59:54 Track (597.6, -364.1) @ 07/30 06:09:30 AR 12390
 07/30 18:13:00 - 07/31 05:40:00 Track (670.2, -357.3) @ 07/30 18:10:00 AR 12390
 07/31 06:33:00 - 07/31 17:17:30 Track (735.5, -349.3) @ 07/31 06:30:00 AR 12390
 07/31 18:15:00 - 08/01 05:48:24 Track (788.1, -341.0) @ 07/31 18:12:00 AR 12390
 08/01 06:01:30 - 08/01 15:59:54 Track (831.0, -332.0) @ 08/01 05:58:30 AR 12390
 08/01 17:01:30 - 08/01 20:59:54 Track (189.4, 53.7) @ 08/01 16:10:00 HOP289 filament
 08/01 21:03:00 - 08/02 01:59:54 Track (870.5, -319.7) @ 08/01 21:00:00 AR 12390

PROG= 07 Inf.-time(s)

└ Subr= 1 90-time(s) 20.0sec

└ Seqn= 41 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

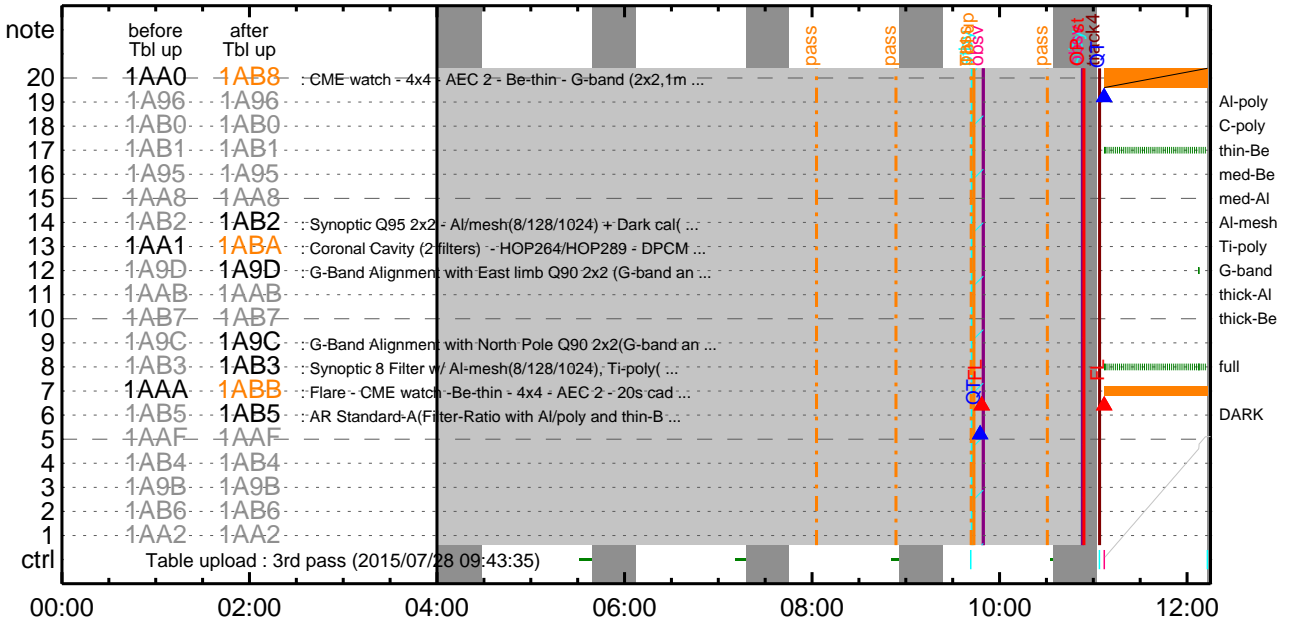
Flare Detection

* * * * *

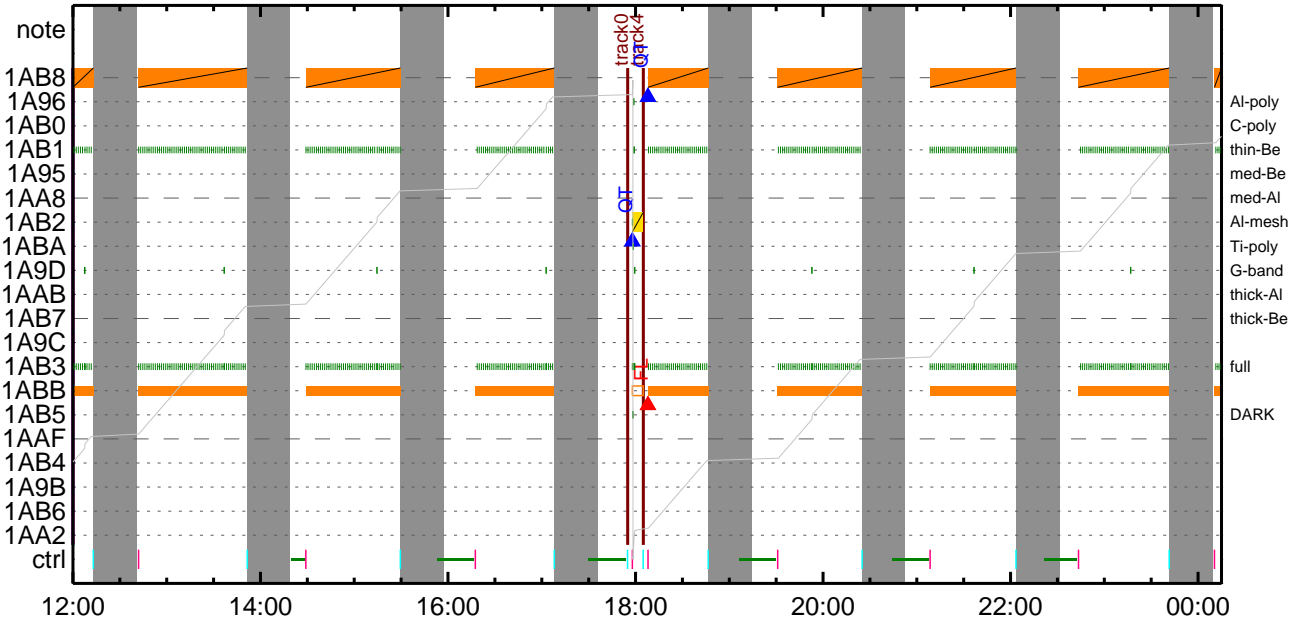
FLD Patrol

Term	Pointing (x, y)	Comment								
07/28 18:05:16 - 07/29 05:39:46	Track (336.5, -378.6) @ 07/28 18:05:00	AR 12390								
07/29 05:49:46 - 07/29 18:28:46	Track (427.5, -374.9) @ 07/29 05:49:30	AR 12390								
07/29 18:38:46 - 07/30 05:59:46	Track (520.6, -369.7) @ 07/29 18:38:30	AR 12390								
07/30 06:09:46 - 07/30 18:00:16	Track (597.6, -364.1) @ 07/30 06:09:30	AR 12390								
07/30 18:10:16 - 07/31 06:20:16	Track (670.2, -357.3) @ 07/30 18:10:00	AR 12390								
07/31 06:30:16 - 07/31 18:02:16	Track (735.5, -349.3) @ 07/31 06:30:00	AR 12390								
07/31 18:12:16 - 08/01 05:52:46	Track (788.1, -341.0) @ 07/31 18:12:00	AR 12390								
08/01 05:58:46 - 08/01 16:00:16	Track (831.0, -332.0) @ 08/01 05:58:30	AR 12390								
08/01 16:10:16 - 08/02 02:00:18	Track (189.4, 53.7) @ 08/01 16:10:00	HOP289 filament								
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

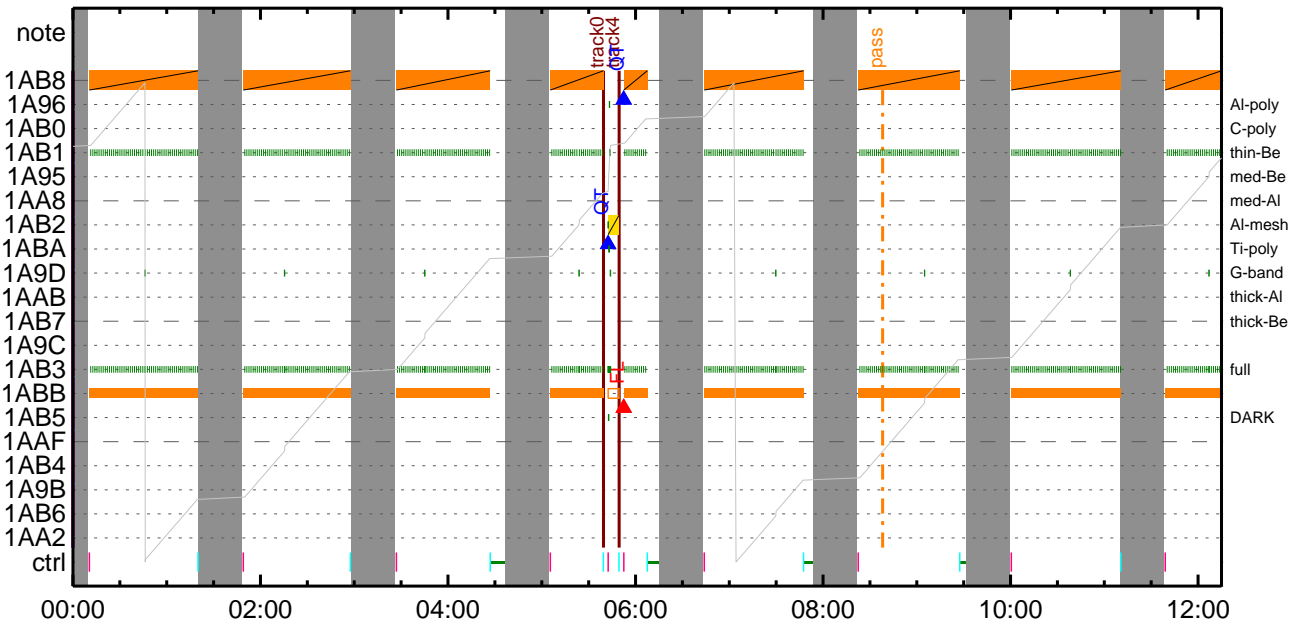
CMDI #0292 2015/07/28



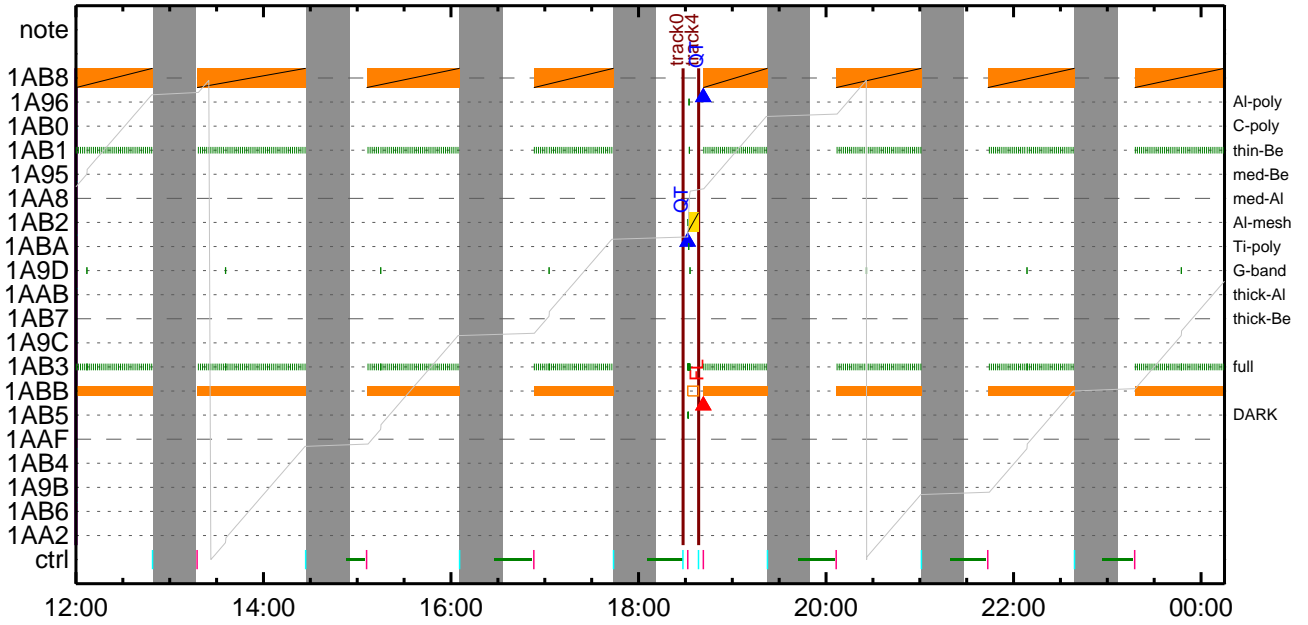
CMDI #0292 2015/07/28



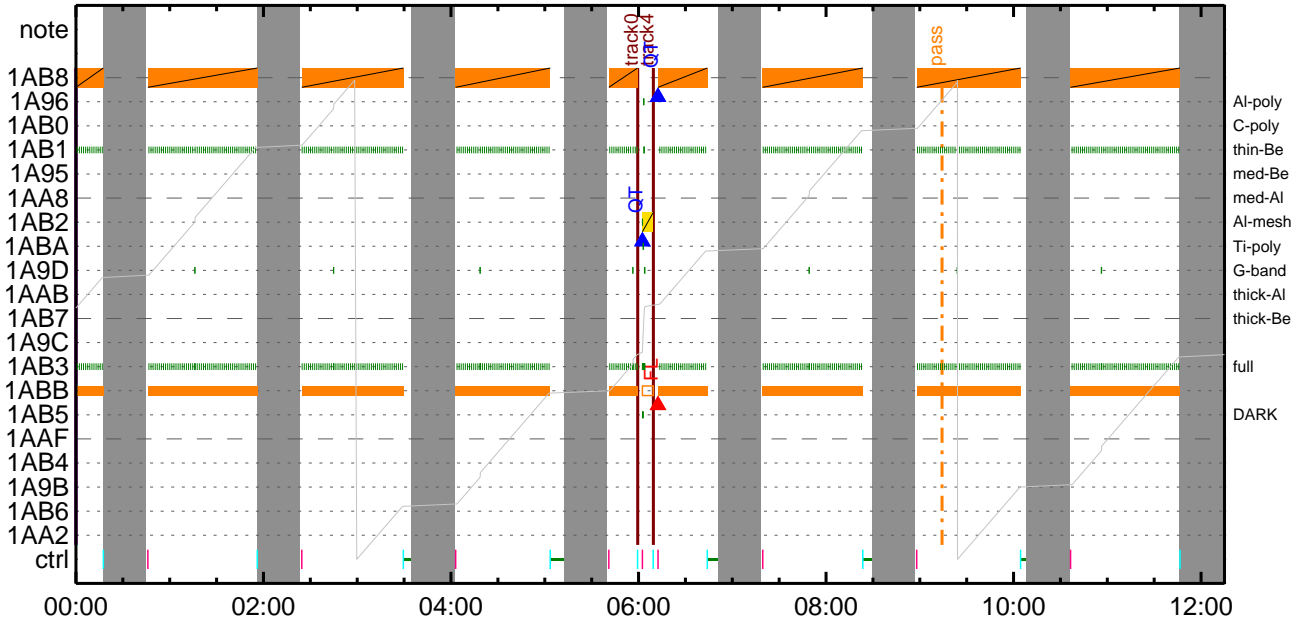
CMDI #0292 2015/07/29



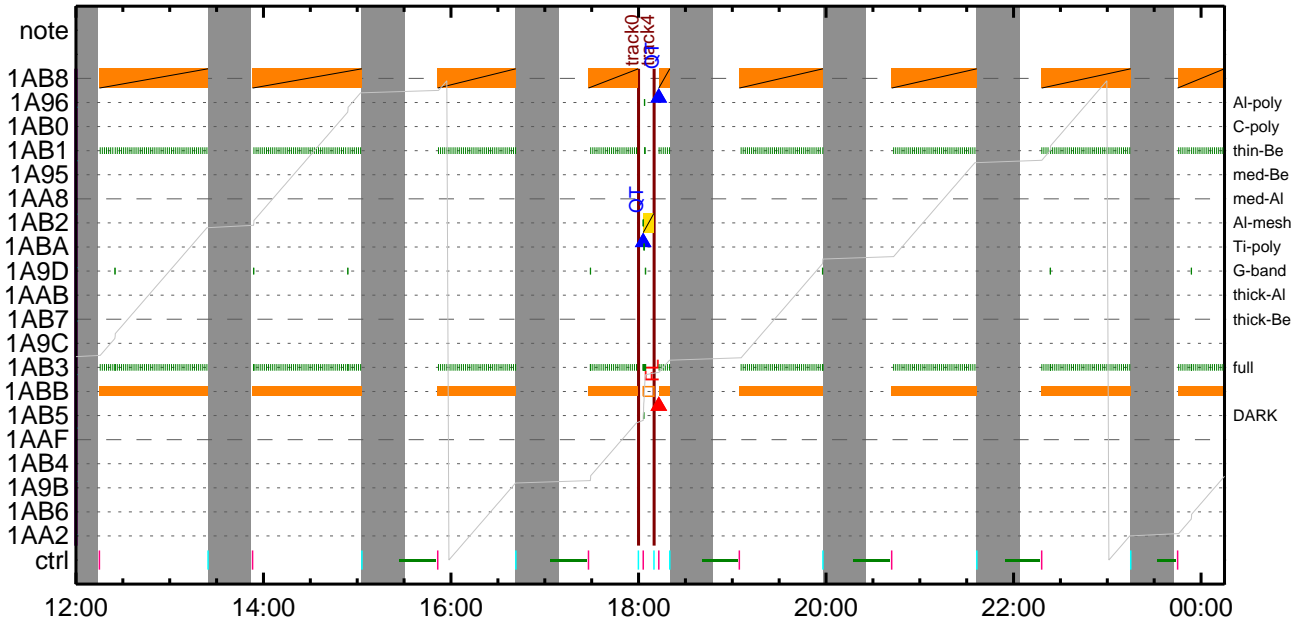
CMDI #0292 2015/07/29



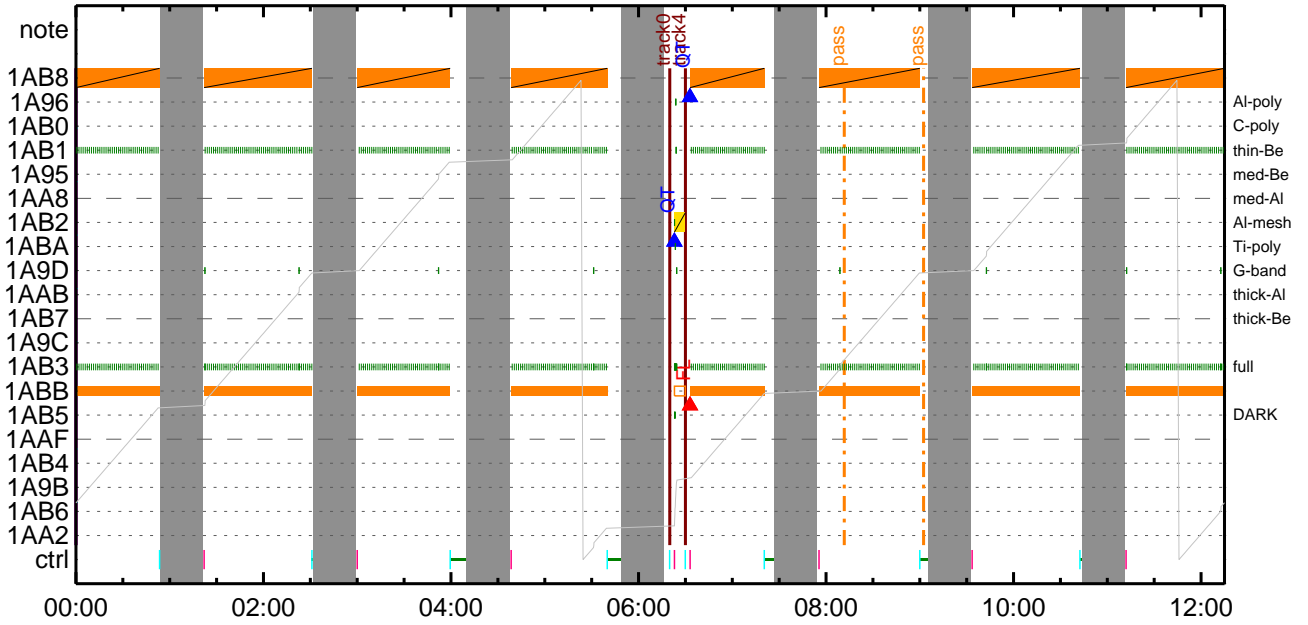
CMDI #0292 2015/07/30



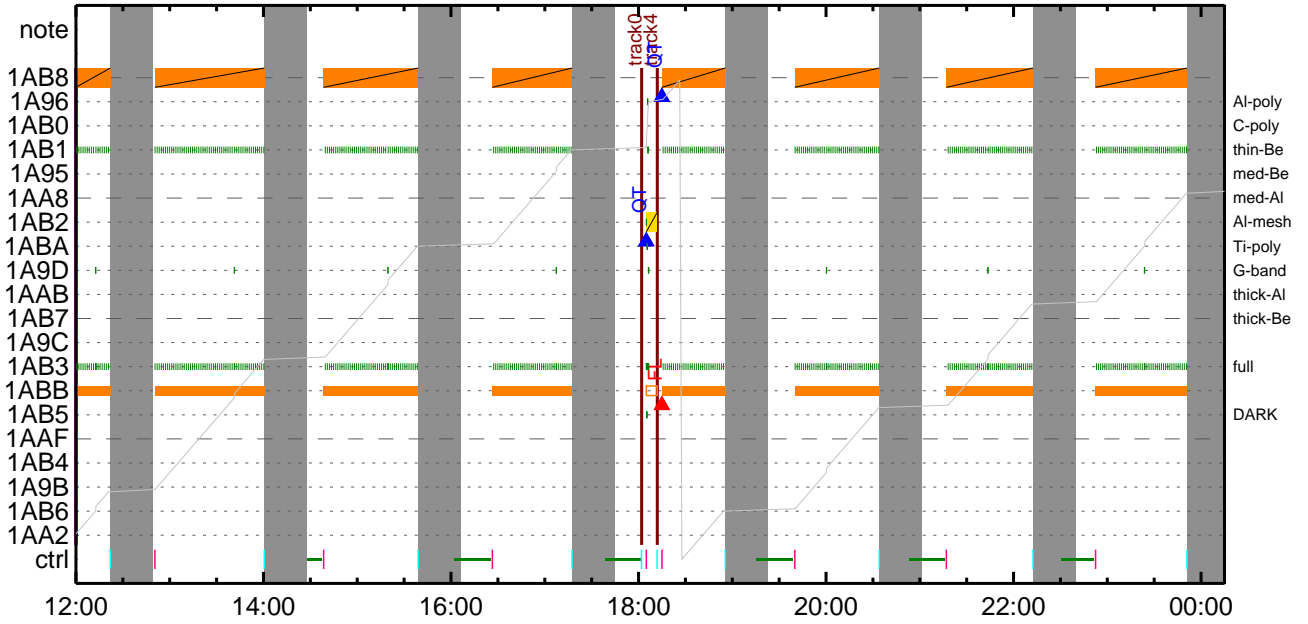
CMDI #0292 2015/07/30



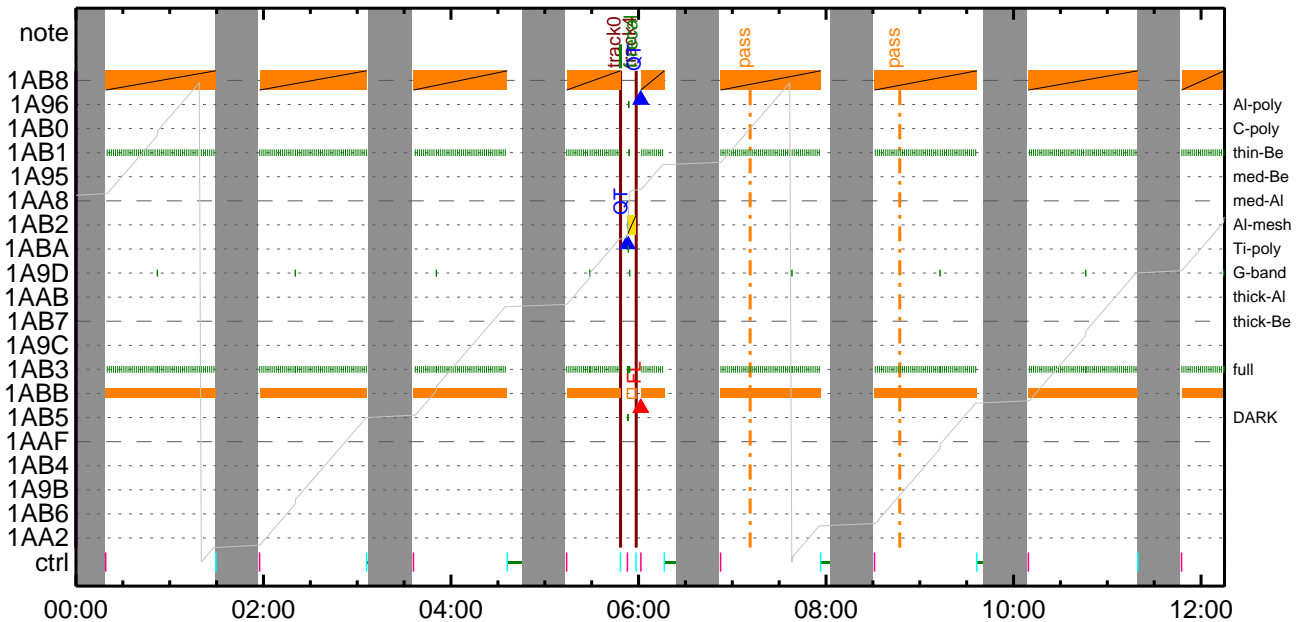
CMDI #0292 2015/07/31



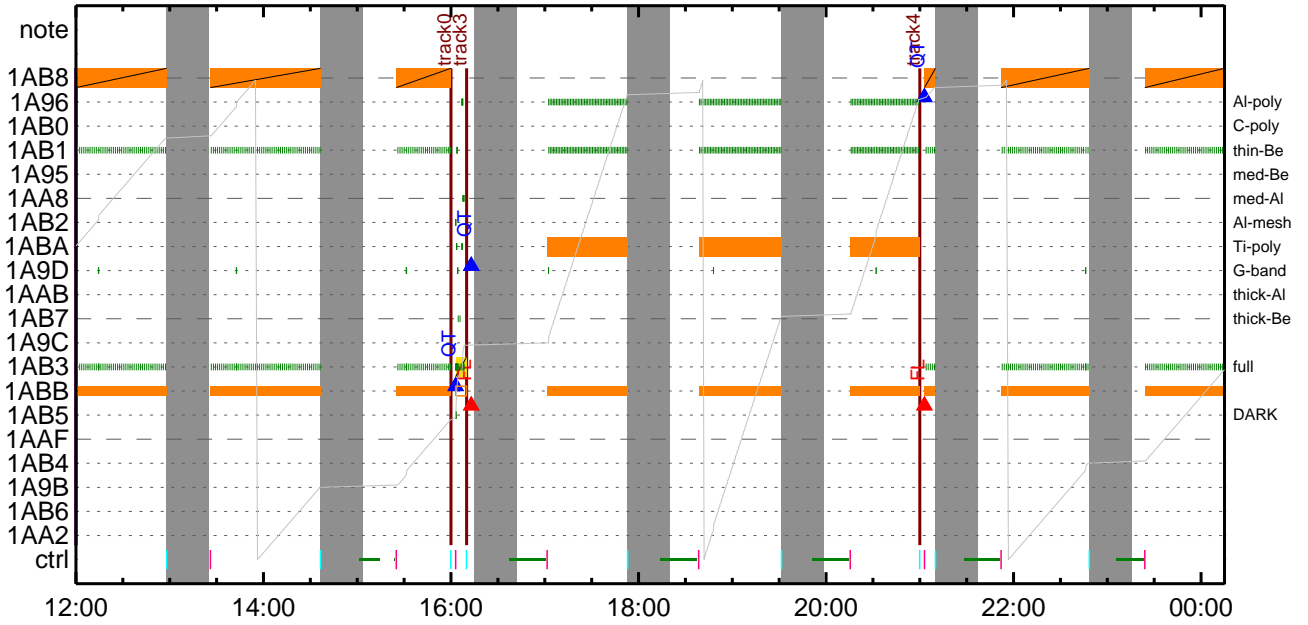
CMDI #0292 2015/07/31



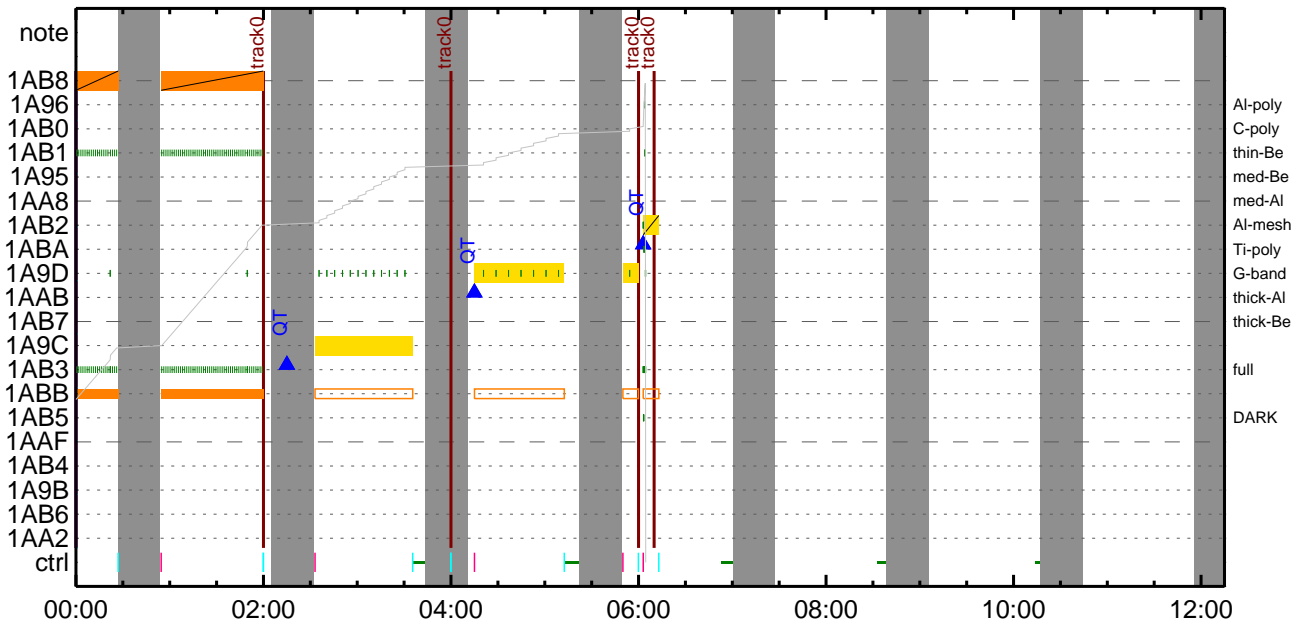
CMDI #0292 2015/08/01



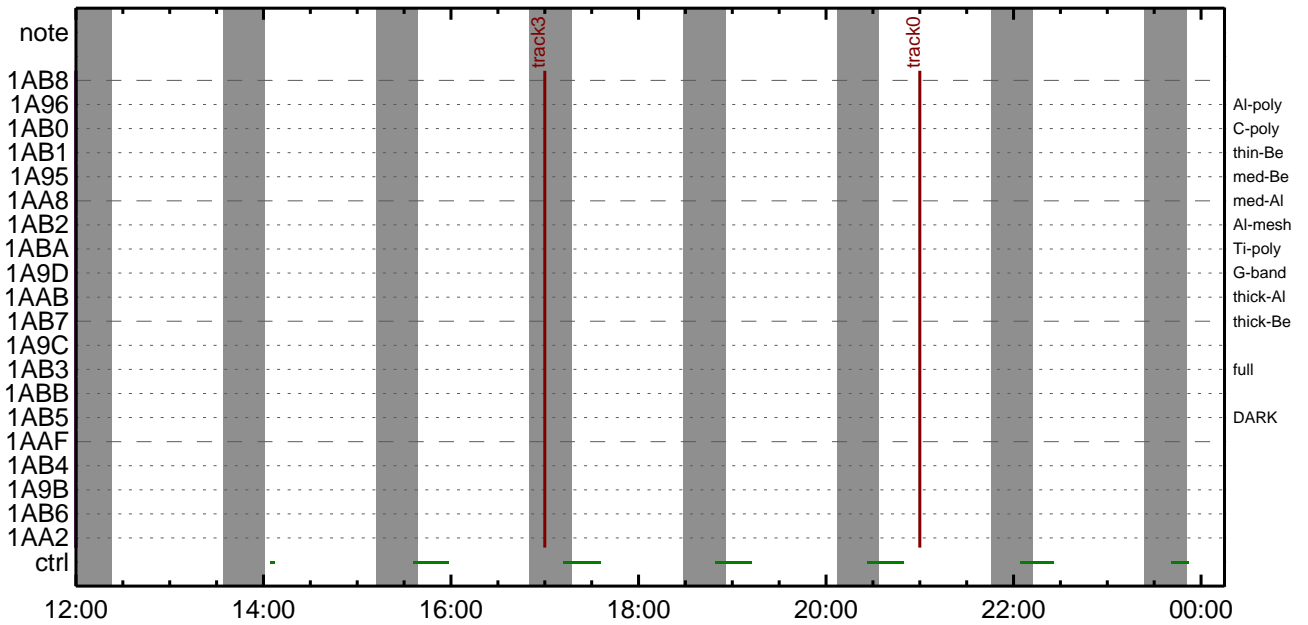
CMDI #0292 2015/08/01



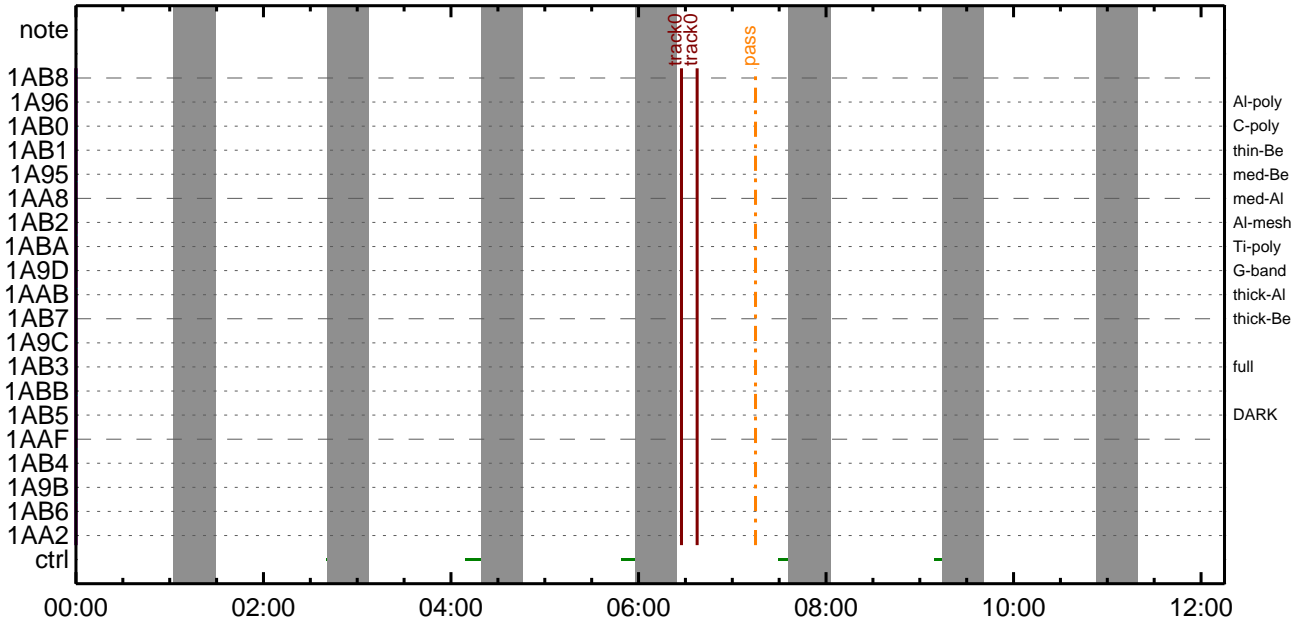
CMDI #0292 2015/08/02



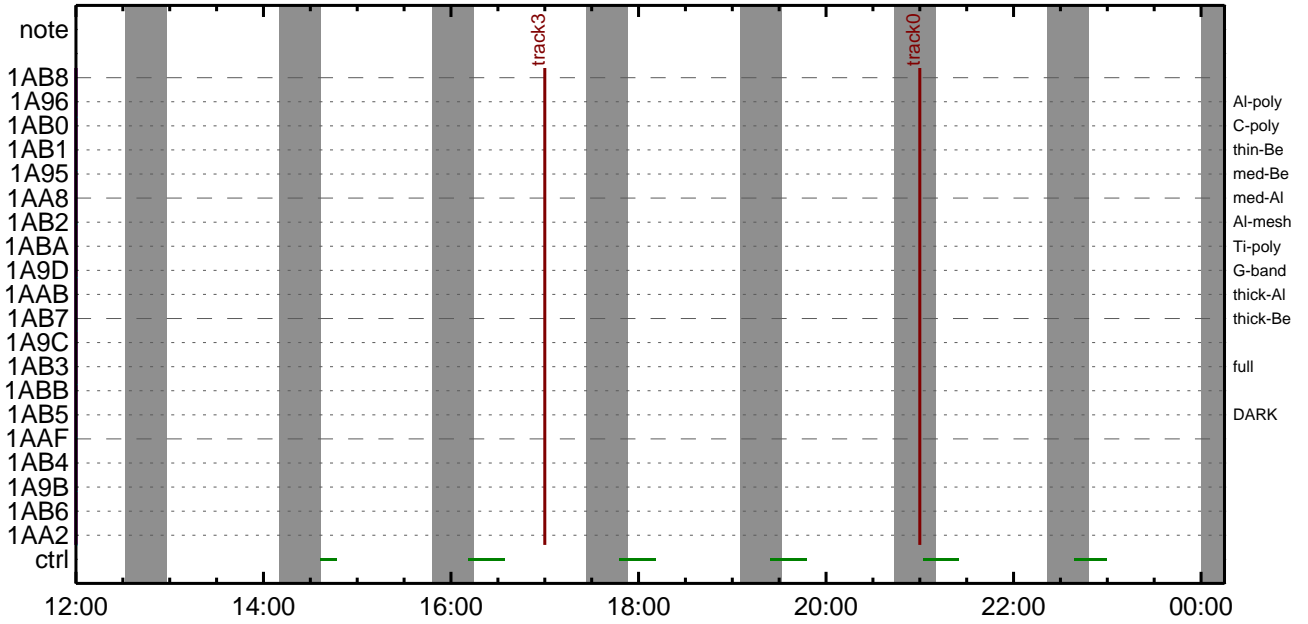
CMDI #0292 2015/08/02



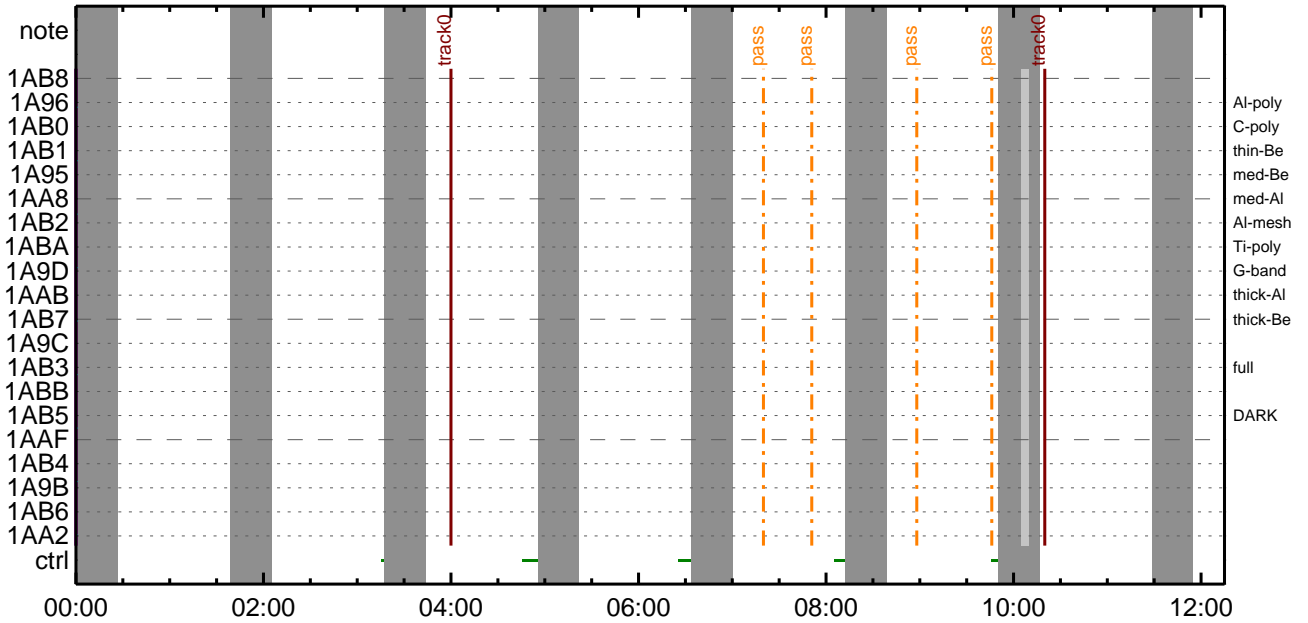
CMDI #0292 2015/08/03



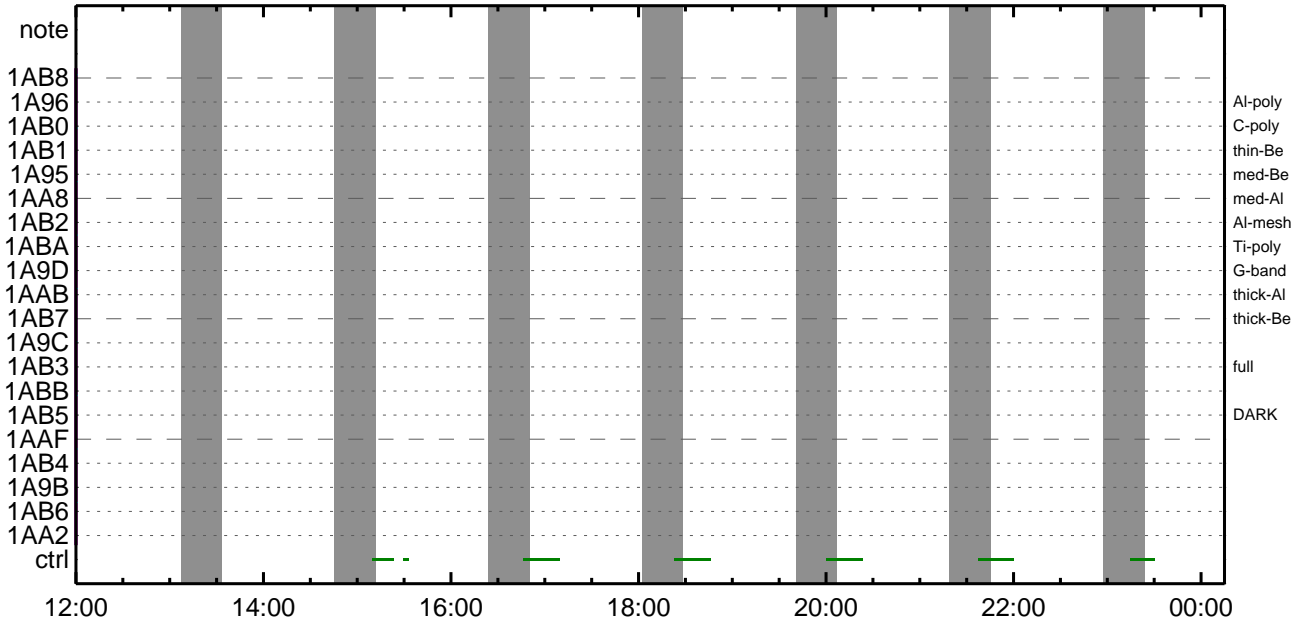
CMDI #0292 2015/08/03



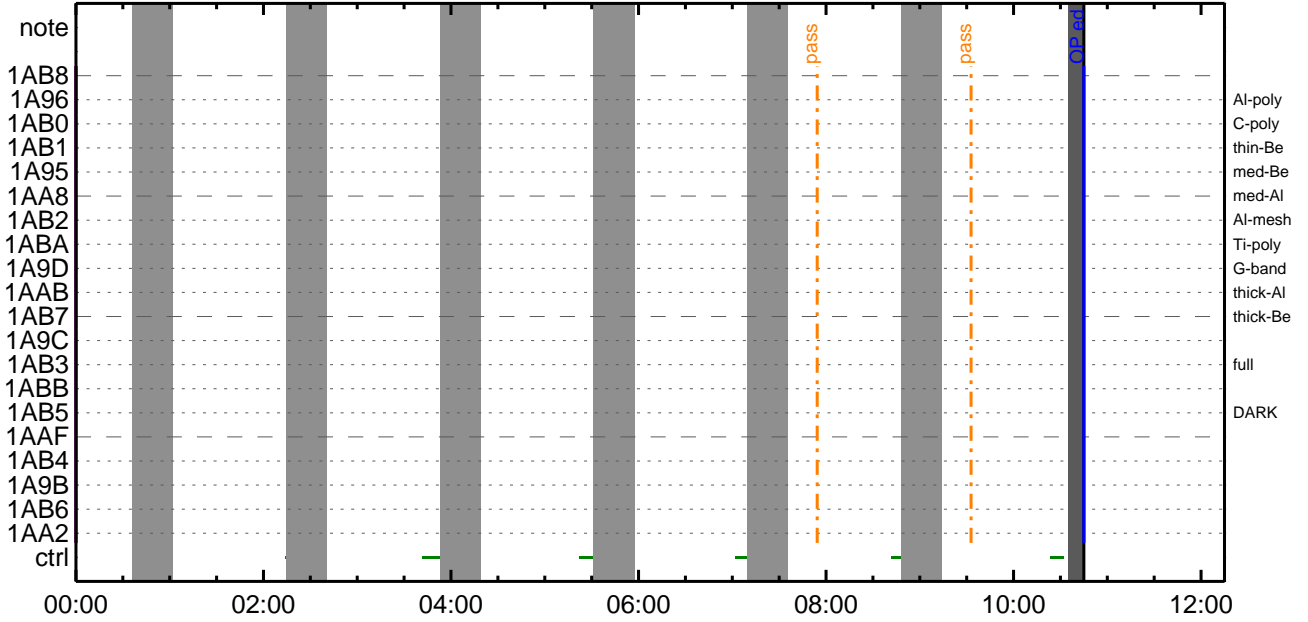
CMDI #0292 2015/08/04



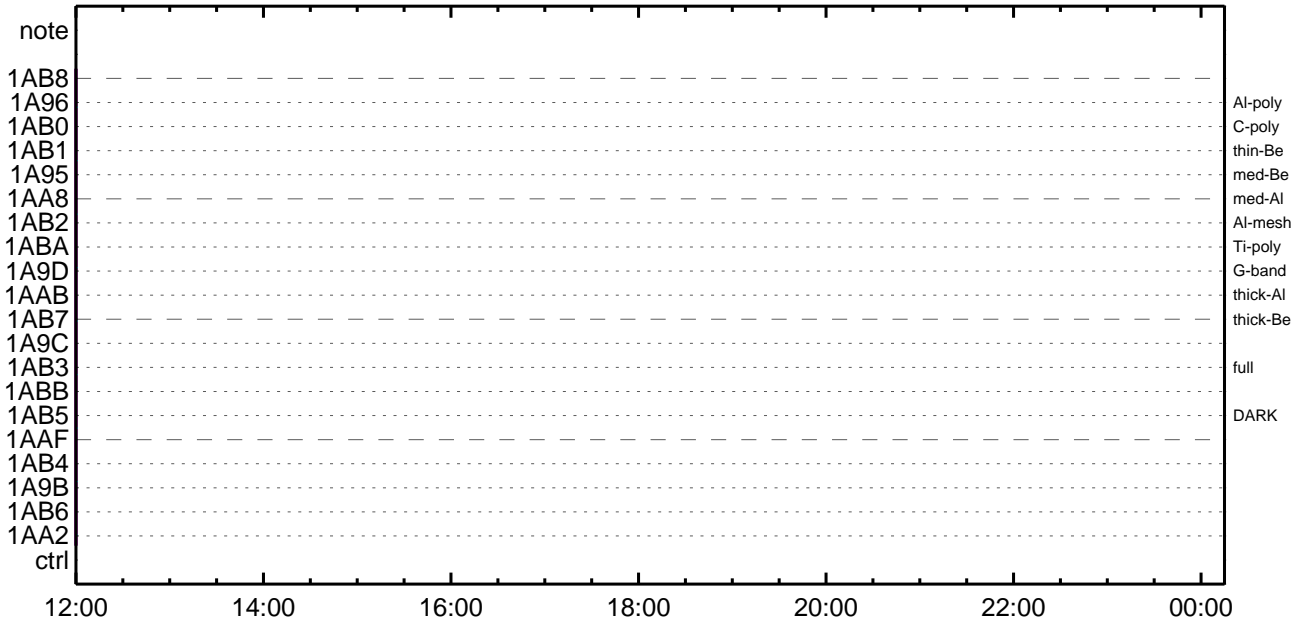
CMDI #0292 2015/08/04



CMDI #0292 2015/08/05



CMDI #0292 2015/08/05




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-283:OP
0104 ( )
0105 S. OG og-283:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** òE²¼òî¼Ã´¶Á°òEÈ-ò°Á÷¿@ (¼âµ-YAYOXx½ªî»ò³îÇ§ãòÃÕæòÇ¼ª°"òE¼î¹çòÇãâ) *****
0167 C. DHUYâ;4YE;E¼Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADã-Á÷¿@NGãî¼î¹ç;ç°E²¼òî¼TI-CMDÁ÷¿@ãî¼Á¹Òã•òEòòò³òE;f
0180 C. òEò¿;çSETòEDUMPAîE±°îYNY¹ç¹Òã|ò³òE;f
0181 C.
0182 C. TIY³YþYóYEòðÃDî¿(UT)
0183 +. TI 2015-07-28 10:49:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-07-28 10:49:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-07-28 10:49:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```

main-284 2015-07-28 13:45:01 94 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èq¿BAs•µ°E»ÍxÁÇuíYçYÁYxYí;¼YÉ;ÈÈ%µ•ííÉ;ÈBÈ¼°ÇÏa•a¿¼i¹çqí;çÁ®,ùa¹aèBPaçÁ+¿®a•BÈaqa³aÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 1876180.2 +- 1.0 (s) [ ]
0052 C.
0053 C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_CHG_ENA
0056 BC (20)
0057 . C. Verify EIS_MODE_CHG_FLG is ENA
0058 +. DC 07-FC EIS_MODE_MANU
0059 BC (21 02)
0060 . C. Verify EIS in MANUAL mode
0061 . C. Estimated OBSTBL upload time is 1m29s
0062 C. *****
0063 C. EIS START OBSTBL LOAD
0064 C. *****
0065 . S. RAM ram-820:EIS_OBSTBL
0066 ( )
0067 +. DC 07-FC EIS_DUMP_OBSTBL
0068 BC (07 07 07 00 00 70 00)
0069 C.
0070 C. Execute, after the success of OBSTBL upload.
0071 C. Set EIS TI-commands
0072 +. TI 2015-07-28 10:53:50.0
0073 DC 07-FC EIS_MODE_CHG_ENA
0074 BC (20)
0075 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0076 C. *****
0077 C. EIS END OBSTBL LOAD
0078 C. *****
0079 C.
0080 . C. ***** MDP `ûÁÎaÎ»ò¼YªEÁÐª¹aèDCBC•x²è *****
0081 C. (¼á°íYÓYÁYÈYbYÈYáYçYèªE¾¼ªª¼Á»Û¹aè)
0082 . S. DC-BC dcbc-402:DCBC
0083 (MDP_known_event)
0084 C.
0085 C.
0086 . C. ***** YD¥¹•İ Daily±¿ÎÑaÈ´Øª¹aèDCBC•x²è *****
0087 . S. DC-BC dcbc-153:DCBC
0088 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 C.
0090 C.
0091 . C. ;ãLOSYÁY$YÁY-¼Á»Û;ã
0092 C.
0093 . C. ***** LOS *****
0094 C.

```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-265:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-288:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¥ç¥Ã¥×¥í;¼¥É°âÊSTS_CHKðOFFðËð¹ðë
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHKðONðËð¹ðë
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700[ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2015-07-28 10:53:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 . C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** XRT START *****
0174 C.
0175 +. DC 07-F0 MDP_XRT_CTRL_MANU
0176 BC (c1)
0177 + DC 07-F0 MDP_XRT_MODE_STBY
0178 BC (c3)
0179 . C. ----- Success Verify ? OK / NG____
0180 C.
0181 C. XRT Obs. Table Upload
0182 . S. RAM ram-291:MDP_OBS_X
0183 ( )
0184 C.
0185 +. DC 07-F0 MDP_DUMP_XRTTBL
0186 BC (84 07 00 00 00 3a d4)
0187 . C. ----- Comparison Check ? OK / ERR ____
0188 C.
0189 C.
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 01 b1 b1 04 04)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 02 b1 b1 08 08)

```

```
0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 03 b1 b1 08 08)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 04 b1 b1 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 05 85 83 06 06)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 06 85 83 06 06)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 07 85 83 08 08)
0204 + DC 07-F0 MDP_XRT_ROI_SET
0205 BC (cd 08 80 80 20 20)
0206 + DC 07-F0 MDP_XRT_ROI_SET
0207 BC (cd 09 80 80 20 08)
0208 + DC 07-F0 MDP_XRT_ROI_SET
0209 BC (cd 0a 80 80 08 20)
0210 + DC 07-F0 MDP_XRT_ROI_SET
0211 BC (cd 0b 85 83 0c 0c)
0212 + DC 07-F0 MDP_XRT_ROI_SET
0213 BC (cd 0c 80 60 20 18)
0214 + DC 07-F0 MDP_XRT_ROI_SET
0215 BC (cd 0d a0 80 18 20)
0216 + DC 07-F0 MDP_XRT_ROI_SET
0217 BC (cd 0f 80 80 20 20)
0218 + DC 07-F0 MDP_XRT_FLD_ENA
0219 BC (d8)
0220 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0221 BC (c8)
0222 + DC 07-F0 MDP_XRT_AEC_RESET
0223 BC (d0)
0224 + DC 07-F0 MDP_XRT_ARS_DIS
0225 BC (d5)
0226 + DC 07-F0 MDP_XRT_FLD_RESET
0227 BC (da)
0228 + DC 07-F0 MDP_XRT_QT_PROG_SET
0229 BC (c4 06)
0230 + DC 07-F0 MDP_XRT_FL_PROG_SET
0231 BC (c5 07)
0232 . C. ----- Success Verify ? OK / NG ____
0233 . C.
0234 . C.
0235 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0236 . C.
0237 + DC 07-F0 MDP_XRT_MODE_OBSV
0238 BC (c2)
0239 + TI 2015-07-28 10:53:02.0
0240 DC 07-F0 MDP_XRT_MODE_OBSV
0241 BC (c2)
0242 . C. ----- Success Verify ? OK / NG ____
0243 . C.
0244 . C. ***** XRT END *****
0245 . C.
0246 . C. ***** MDP `ûÃîñî»ô%ÿñÈÄð¹ñèDCBC•x²è *****
0247 . C. (%ã°îÿÓÿÃÿÈÿPÿÈÿÿãÿçÿèñÈ%¼ññ¼Ã»Ûñ¹ñè)
0248 . S. DC-BC dcbc-402:DCBC
0249 (MDP_known_event)
0250 . C.
0251 . C.
0252 . C. ***** ÿDÿ¹.Ï Daily±;îÑñÈ´Øñ¹ñèDCBC•x²è *****
0253 . S. DC-BC dcbc-153:DCBC
0254 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0255 . C.
0256 . C.
0257 . C. ÿãLOSÿÃÿSÿÿÃÿ-¼Ã»Û;ã
0258 . C.
0259 . C. ***** LOS *****
0260 . C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-286 2015-07-28 13:45:01 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÁY^-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿æµ°Æ»ÍxÁÇúÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÈ;ÈÈÈ¼°ÇÖæ•¿¼i¹çæí;çÀ®,ùæ¹æèæBæÇÁ+¿®æ•æÈæææ³æÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP ‘úÁîæî»ö¼YæÈÁæ¹æèDCBC•x²è *****
0016 C. (%ã°íYÓYÁYÈYB YÈYÁYçYèæÈ¼æ¼Á»Ûæ¹æè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YDÿ¹•Ï Daily+¿ÎÑæÈ´Øæ¹æèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁYŠYÁY^-¼Á»Û;ä
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

Jul 28, 15 13:45

XRT_OGLIST_0292.chk

Page 1/13

*** OP Sequence for XRT ***

2015/07/28	11:03:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	11:03:56.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/28	11:04:00.0	AOCS_Ore-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	04 00 00 00 00	
2015/07/28	11:04:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/28	11:04:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/28	11:04:20.0	XRT_AEC_RESET_441_OG [0x1b9]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/07/28	11:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/28	11:04:24.0	XRT_FLD_RESET_443_OG [0x1bb]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/28	11:06:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2015/07/28	11:06:58.0	XRT_FL_PROG_SET_406_OG [0x196]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/07/28	11:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/28	12:13:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	12:13:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/28	12:13:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/28	12:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/28	12:41:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/28	12:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/28	13:51:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	13:51:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/28	13:51:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/28	13:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/28	14:28:00.0	XRT_Custom_430_OG [0x1ae]				
2015/07/28	14:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/28	15:29:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	15:29:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/28	15:29:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/28	15:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/28	16:16:30.0	XRT_Custom_430_OG [0x1ae]				
2015/07/28	16:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/28	17:08:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	17:08:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2015/07/28	17:08:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/07/28	17:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/07/28	17:54:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	17:54:56.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/28	17:55:00.0	AOCS_Ore-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2015/07/28	17:55:16.0	XRT_FLD_DIS_418_OG [0x1a2]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/07/28	17:57:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/07/28	17:57:56.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/07/28	17:57:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2015/07/28	17:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/07/28	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/07/28	18:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/07/28	18:05:00.0	AOCS_Ore-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	04 00 00 00 00	
2015/07/28	18:05:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/07/28	18:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/07/28	18:05:20.0	XRT_AEC_RESET_441_OG [0x1b9]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	

Tuesday July 28, 2015

1/13

2015/07/28	18:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/28	18:05:24.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/28	18:07:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2015/07/28	18:07:58.0	XRT_FL_PROG_SET_406_OG [0x196]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/07/28	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/28	18:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/28	18:46:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/28	18:46:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/28	18:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/28	19:30:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/28	19:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/28	20:25:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/28	20:25:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/28	20:25:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/28	20:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/28	21:07:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/28	21:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/28	22:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/28	22:03:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/28	22:03:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/28	22:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/28	22:42:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/28	22:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/28	23:41:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/28	23:41:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/28	23:41:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/28	23:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/29	00:09:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/29	00:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/29	01:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/29	01:20:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/29	01:20:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/29	01:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/29	01:48:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/29	01:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/29	02:57:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/29	02:57:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/29	02:57:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/29	03:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/29	03:26:00.0	XRT_Custom_430_OG [0x1ae]			
2015/07/29	03:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/29	04:27:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/29	04:27:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/29	04:27:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/29	04:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/29	05:04:30.0	XRT_Custom_430_OG [0x1ae]			
2015/07/29	05:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/29	05:39:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/29	05:39:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/29	05:39:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00

2015/07/29	05:39:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/29	05:42:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/29	05:42:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/29	05:42:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/07/29	05:42:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	05:49:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	05:49:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/29	05:49:30.0	AOCs_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/29	05:49:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/29	05:49:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/29	05:49:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/29	05:49:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/29	05:49:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	05:52:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2015/07/29	05:52:28.0	XRT_FL_PROG_SET_406_OG [0x196]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/07/29	05:52:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	06:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	06:07:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	06:07:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	06:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	06:43:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/29	06:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	07:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	07:47:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	07:47:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	07:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	08:21:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/29	08:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	09:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	09:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	09:27:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	09:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	09:59:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/29	10:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	11:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	11:10:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	11:10:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	11:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	11:38:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/29	11:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	12:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	12:49:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	12:49:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	12:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	13:16:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/29	13:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	14:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	14:27:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	14:27:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

2015/07/29	14:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	15:05:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/29	15:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	16:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	16:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	16:05:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	16:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	16:52:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/29	16:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	17:44:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	17:44:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	17:44:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	17:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	18:28:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	18:28:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/29	18:28:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/29	18:28:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/29	18:31:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/29	18:31:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/29	18:31:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/07/29	18:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	18:38:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	18:38:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/29	18:38:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/29	18:38:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/29	18:38:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/29	18:38:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/29	18:38:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/29	18:38:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	18:41:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2015/07/29	18:41:28.0	XRT_FL_PROG_SET_406_OG [0x196]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/07/29	18:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	19:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	19:22:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	19:22:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	19:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	20:05:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/29	20:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	21:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	21:01:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	21:01:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	21:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	21:42:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/29	21:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/29	22:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/29	22:39:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/29	22:39:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/29	22:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/29	23:16:30.0	XRT_Custom_430_OG [0x1ae]							

Jul 28, 15 13:45

XRT_OGLIST_0292.chk

Page 5/13

2015/07/29	23:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	00:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	00:17:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	00:17:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	00:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	00:45:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	00:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	01:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	01:56:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	01:56:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	01:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	02:23:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	02:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	03:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	03:29:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	03:29:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	03:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	04:02:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	04:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	05:03:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	05:03:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	05:03:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	05:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	05:40:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	05:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	05:59:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	05:59:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/30	05:59:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/30	05:59:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/30	06:02:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/30	06:02:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/30	06:02:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/07/30	06:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	06:09:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	06:09:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/30	06:09:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/30	06:09:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/30	06:09:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/30	06:09:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/30	06:09:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/30	06:09:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	06:12:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2015/07/30	06:12:28.0	XRT_FL_PROG_SET_406_OG [0x196]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/07/30	06:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	06:44:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	06:44:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	06:44:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	06:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	07:18:30.0	XRT_Custom_430_OG [0x1ae]							

Tuesday July 28, 2015

5/13

Jul 28, 15 13:45

XRT_OGLIST_0292.chk

Page 6/13

2015/07/30	07:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	08:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	08:23:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	08:23:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	08:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	08:57:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	08:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	10:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	10:04:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	10:04:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	10:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	10:35:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	10:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	11:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	11:46:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	11:46:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	11:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	12:14:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	12:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	13:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	13:24:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	13:24:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	13:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	13:52:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	13:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	15:03:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	15:03:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	15:03:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	15:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	15:50:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	15:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	16:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	16:41:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/30	16:41:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/30	16:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/30	17:27:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/30	17:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/30	18:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/30	18:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/30	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/30	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/30	18:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/07/30	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/30	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/30	18:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/30	18:10:00.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/30	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/30	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							

Jul 28, 15 13:45

XRT_OGLIST_0292.chk

Page 7/13

2015/07/30	18:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/30	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/30	18:10:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/30	18:12:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/30	18:12:58.0	XRT_FL_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2015/07/30	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/07/30	18:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/30	18:20:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/30	18:20:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/30	18:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/30	19:03:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/30	19:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/07/30	19:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/30	19:58:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/30	19:58:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/30	20:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/30	20:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/30	20:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/07/30	21:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/30	21:36:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/30	21:36:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/30	21:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/30	22:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/30	22:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/07/30	23:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/30	23:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/30	23:15:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/30	23:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/30	23:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/30	23:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/07/31	00:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	00:53:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	00:53:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31	00:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/31	01:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/31	01:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/07/31	02:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	02:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	02:31:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31	02:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/31	02:59:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/31	03:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/07/31	03:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	03:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	03:59:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31	04:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/31	04:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/31	04:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2015/07/31	05:40:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	05:40:02.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2015/07/31	05:40:04.5	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31	05:43:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/31	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/31	06:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	06:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/31	06:20:16.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00 00 00 00
2015/07/31	06:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/07/31	06:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/07/31	06:22:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/31	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/07/31	06:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	06:29:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	06:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/07/31	06:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00
2015/07/31	06:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/07/31	06:30:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/07/31	06:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/07/31	06:30:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/07/31	06:32:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31	06:32:58.0	XRT_FL_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2015/07/31	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/07/31	07:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	07:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	07:20:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31	07:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/31	07:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/31	07:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/07/31	09:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	09:00:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	09:00:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31	09:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/31	09:32:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/31	09:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/07/31	10:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	10:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	10:42:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31	10:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/31	10:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/31	11:11:00.0	XRT_Custom_430_OG [0x1ae]	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2015/07/31	11:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2015/07/31	12:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	12:22:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	12:22:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31	12:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/07/31	12:49:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/07/31	12:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/07/31	14:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/07/31	14:00:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/07/31	14:00:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/07/31			MDP_XRT_PREFLR_STRT	1	07-F0	e8

2015/07/31	14:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/31	14:37:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/31	14:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/31	15:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/31	15:39:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/31	15:39:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/31	15:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/31	16:25:30.0	XRT_Custom_430_OG [0x1ae]							
2015/07/31	16:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/31	17:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/31	17:17:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/31	17:17:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/31	17:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/31	18:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/31	18:01:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/31	18:02:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/07/31	18:02:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/07/31	18:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/07/31	18:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/31	18:04:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/07/31	18:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/31	18:11:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/31	18:11:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/07/31	18:12:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/07/31	18:12:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/07/31	18:12:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/07/31	18:12:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/07/31	18:12:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/07/31	18:12:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/31	18:14:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2015/07/31	18:14:58.0	XRT_FL_PROG_SET_406_OG [0x196]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/07/31	18:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/31	18:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/31	18:55:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/31	18:55:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/31	18:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/31	19:39:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/31	19:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/31	20:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/31	20:34:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/31	20:34:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/31	20:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/31	21:16:00.0	XRT_Custom_430_OG [0x1ae]							
2015/07/31	21:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/31	22:12:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/31	22:12:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/31	22:12:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/31	22:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/07/31	22:51:30.0	XRT_Custom_430_OG [0x1ae]							

Jul 28, 15 13:45

XRT_OGLIST_0292.chk

Page 10/13

2015/07/31	22:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/07/31	23:51:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/07/31	23:51:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/07/31	23:51:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/07/31	23:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/01	00:18:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/01	00:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	01:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	01:29:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	01:29:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/01	01:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/01	01:56:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/01	01:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	03:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	03:06:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	03:06:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/01	03:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/01	03:35:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/01	03:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	04:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	04:36:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	04:36:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/01	04:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/01	05:13:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/01	05:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	05:48:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	05:48:26.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2015/08/01	05:48:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/01	05:52:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/01	05:52:46.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/01	05:52:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/01	05:52:50.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/01	05:52:52.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/08/01	05:52:54.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	05:58:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/01	05:58:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/08/01	05:58:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/01	05:58:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/01	05:58:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/01	05:58:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/01	05:58:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	06:01:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2015/08/01	06:01:28.0	XRT_FL_PROG_SET_406_OG [0x196]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/08/01	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	06:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	06:16:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	06:16:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/01	06:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

Jul 28, 15 13:45

XRT_OGLIST_0292.chk

Page 11/13

2015/08/01	06:51:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/01	06:52:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/08/01	07:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/01	07:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/01	07:56:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/01	07:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/01	08:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/01	08:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/08/01	09:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/01	09:36:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/01	09:36:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/01	09:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/01	10:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/01	10:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/08/01	11:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/01	11:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/01	11:19:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/01	11:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/01	11:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/01	11:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/08/01	12:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/01	12:58:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/01	12:58:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/01	13:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/01	13:25:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/01	13:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/08/01	14:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/01	14:36:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/01	14:36:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/01	14:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/01	15:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/01	15:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/08/01	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/01	15:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/01	16:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/08/01	16:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/08/01	16:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/08/01	16:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/08/01	16:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/08/01	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2015/08/01	16:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/01	16:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/01	16:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/08/01	16:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00		
2015/08/01	16:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/08/01	16:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/08/01	16:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/08/01	16:10:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/08/01	16:12:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		

Jul 28, 15 13:45

XRT_OGLIST_0292.chk

Page 12/13

2015/08/01	16:12:58.0	XRT_FL_PROG_SET_406_OG [0x196]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/08/01	17:00:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/01	17:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	17:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	17:53:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	17:53:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/01	17:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/01	18:37:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/01	18:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	19:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	19:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	19:31:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/01	19:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/01	20:14:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/01	20:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	20:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00			
2015/08/01	21:00:00.0	AOCS_Or-e-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00	00			
2015/08/01	21:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/01	21:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/01	21:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/01	21:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/01	21:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	21:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2015/08/01	21:02:58.0	XRT_FL_PROG_SET_406_OG [0x196]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/08/01	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	21:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	21:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	21:10:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/01	21:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/01	21:51:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/01	21:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/01	22:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/01	22:48:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/01	22:48:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/01	22:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/01	23:23:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/01	23:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/02	00:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/02	00:27:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/02	00:27:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/02	00:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/02	00:53:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/02	00:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/02	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/02	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/02	01:59:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00			
2015/08/02	02:00:00.0	AOCS_Or-e-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 ad 59 00	00			
2015/08/02	02:00:18.0	XRT_FLD_DIS_440_OG [0x1b8]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/02	02:00:20.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]							

2015/08/02	02:00:22.0	XRT_ARS_DIS_439_OG [0x1b7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/08/02	02:14:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/02	02:32:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2015/08/02	02:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/02	03:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/02	03:35:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/02	03:35:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/02	03:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/02	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/02	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/02	03:59:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/08/02	04:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 00 00	54 00
2015/08/02	04:00:18.0	XRT_FLD_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/08/02	04:00:20.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/08/02	04:00:22.0	XRT_ARS_DIS_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/02	04:14:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2015/08/02	04:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/02	05:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/02	05:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/02	05:12:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/02	05:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/02	05:49:00.0	XRT_Custom_430_OG [0x1ae]					
2015/08/02	05:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/02	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/02	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2015/08/02	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00	00 00
2015/08/02	06:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/08/02	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/08/02	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/02	06:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2015/08/02	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/02	06:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 1b 1a	b1 cb
2015/08/02	06:13:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/02	06:15:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_407_OG [0x197]	TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2015/08/02	17:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 00 00	00 00
2015/08/02	21:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 1b 1a	b1 cb
2015/08/03	06:27:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00	00 00
2015/08/03	06:37:30.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 1b 1a	b1 cb
2015/08/03	17:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 00 00	00 00
2015/08/03	21:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 1b 1a	b1 cb
2015/08/04	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00	00 00
2015/08/04	10:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00	00 00