

# XRT Timeline to be uploaded on 2015/08/04

Period: 2015/08/04 10:10:00 - 2015/08/12 09:28:00

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Normal mode

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XOB #1AAB: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Ti/Poly(1024ms) - w leak image-3ms												
Term		Pointing (x, y)					Comment					
08/05 12:38:00 - 08/05 12:44:54		Fixed ( -528.4, -528.4)					XRT Post-Bakeout Q1					
<b>PROG= 11 1-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 86 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 34 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1A9F: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1024ms) - w leak image-3 ms												
Term		Pointing (x, y)					Comment					
08/05 12:48:00 - 08/05 12:54:54		Fixed ( 528.4, -528.4)					XRT Post-Bakeout Q2					
<b>PROG= 05 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 15 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 34 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AA0: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1024ms) - w leak image-3 ms												
Term		Pointing (x, y)					Comment					
08/05 12:58:00 - 08/05 13:04:54		Fixed ( 528.4, 528.4)					XRT Post-Baekout Q3					
<b>PROG= 01 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 35 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 34 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AA1: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1024ms) - w leak image-3 ms												
Term		Pointing (x, y)					Comment					
08/05 13:08:00 - 08/05 13:14:54		Fixed ( -528.4, 528.4)					XRT Post-Baekout Q4					
<b>PROG= 17 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 3 1-time(s) 12.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												

└─ Seqn= 34		2-time(s)	2.0sec										
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3		2-time(s)	2.0sec										
└─ Seqn= 54		1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

**XOB #1AB5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)**

Term	Pointing (x, y)	Comment
08/05 13:18:00 - 08/05 16:49:54	Track ( 79.6, 68.4) @ 08/05 13:15:00	Track AR 12394
08/05 21:03:00 - 08/06 05:30:54	Track ( 149.0, 68.9) @ 08/05 21:00:00	Track AR 12394
08/06 05:44:00 - 08/06 16:49:54	Track ( 225.7, 70.2) @ 08/06 05:41:00	Track AR 12394
08/06 21:35:00 - 08/07 06:04:54	Track ( 356.8, 74.1) @ 08/06 21:00:00	Track AR 12394
08/07 06:18:00 - 08/07 16:22:54	Track ( 432.3, 77.5) @ 08/07 06:15:00	Track AR 12394
08/07 21:03:00 - 08/08 05:31:00	Track ( 545.4, 84.5) @ 08/07 21:00:00	Track AR 12394
08/08 06:20:30 - 08/08 16:49:54	Track ( 611.0, 89.8) @ 08/08 06:17:30	Track AR 12394
08/09 01:03:00 - 08/09 05:39:54	Track ( 727.6, 102.4) @ 08/09 01:00:00	Track AR 12394
08/09 05:53:00 - 08/09 09:27:30	Track ( 754.0, 106.1) @ 08/09 05:50:00	Track AR 12394
08/09 18:30:00 - 08/10 05:59:54	Track ( 811.9, 115.7) @ 08/09 17:45:00	Track AR 12394
08/10 06:13:00 - 08/10 08:24:00	Track ( 860.7, 126.5) @ 08/10 06:10:00	Track AR 12394
08/10 08:56:30 - 08/10 09:59:54	Track ( 390.6, -394.6) @ 08/10 08:30:00	HOP 287
08/10 18:13:00 - 08/11 04:39:54	Fixed ( 896.1, 137.6)	AR 12394 Fixed Pointing
08/11 08:33:00 - 08/11 10:07:54	Track ( 564.7, -382.5) @ 08/11 08:30:00	HOP 287

**PROG= 06 Inf.-time(s)**

└─ Subr= 1		1-time(s)	2.0sec										
└─ Seqn= 9		2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2		1-time(s)	2.0sec										
└─ Seqn= 92		1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42		4-time(s)	2.0sec										
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 17		8-time(s)	360.0sec										
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	110.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	110.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	110.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

**XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-**

Term	Pointing (x, y)	Comment
08/05 16:53:00 - 08/05 16:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted 0.5 min
08/06 05:34:00 - 08/06 05:40:54	Fixed ( 0.0, 0.0)	synoptic, shifted -29.0 min
08/06 16:53:00 - 08/06 16:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted 20.0 min
08/07 06:08:00 - 08/07 06:14:54	Fixed ( 0.0, 0.0)	synoptic, shifted 5.0 min
08/07 16:26:00 - 08/07 16:32:54	Fixed ( 0.0, 0.0)	synoptic
08/08 06:10:30 - 08/08 06:17:24	Fixed ( 0.0, 0.0)	synoptic, shifted 7.5 min
08/09 05:43:00 - 08/09 05:49:54	Fixed ( 0.0, 0.0)	synoptic, shifted -20.0 min
08/09 17:38:00 - 08/09 17:44:54	Fixed ( 0.0, 0.0)	synoptic, shifted -25.0 min
08/10 06:03:00 - 08/10 06:09:54	Fixed ( 0.0, 0.0)	synoptic
08/10 18:03:00 - 08/10 18:09:54	Fixed ( 0.0, 0.0)	synoptic
08/11 04:43:00 - 08/11 08:29:54	Fixed ( 0.0, 0.0)	synoptic, for SOT F/F

**PROG= 14 1-time(s)**

└─ Subr= 1		1-time(s)	12.0sec										
└─ Seqn= 33		1-time(s)	4.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5		1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 61		1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 49		1-time(s)	2.0sec										
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 39</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 54</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1ABA: Coronal Cavity (2 filters) - HOP264/HOP289 - DPCM 2x2 - 768FOV - 60s cad - AEC0 with light leak**

Term	Pointing (x, y)	Comment
08/05 17:46:01 - 08/05 20:16:30	Track ( 113.6, 65.1) @ 08/05 17:00:00	# HOP 289
08/06 17:03:00 - 08/06 20:52:30	Track ( 323.7, 69.4) @ 08/06 17:00:00	# HOP 289
08/07 17:19:30 - 08/07 20:59:54	Track ( 513.0, 78.8) @ 08/07 16:33:00	# HOP 289
08/08 17:03:00 - 08/08 20:26:00	Track ( 681.2, 93.3) @ 08/08 17:00:00	# HOP 289
<b>PROG= 13</b> 1-time(s)		
<b>Subr= 1</b> 1-time(s)		<b>2.0sec</b>
<b>Seqn= 97</b> 1-time(s)		<b>2.0sec</b>
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 1024x1024 (1536, 1536) Q=95 0 0 2.0sec
<b>Subr= 2</b> 1-time(s)		<b>2.0sec</b>
<b>Seqn= 51</b> 60-time(s)		<b>60.0sec</b>
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 2x2 1024x1024 (1536, 1536) DPCM 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 5.66s Obs 2x2 1024x1024 (1536, 1536) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1AB3: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Ti-poly(16/362/1024), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Al-poly(45**

Term	Pointing (x, y)	Comment
08/08 16:53:00 - 08/08 16:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted -4.5 min
<b>PROG= 08</b> 1-time(s)		
<b>Subr= 1</b> 1-time(s)		<b>12.0sec</b>
<b>Seqn= 33</b> 1-time(s)		<b>4.0sec</b>
Open/Al-mesh	Open/Al-mesh close	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 5</b> 1-time(s)		<b>2.0sec</b>
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
<b>Seqn= 61</b> 1-time(s)		<b>4.0sec</b>
Open/Ti-poly	Open/Ti-poly close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 77</b> 1-time(s)		<b>2.0sec</b>
thin-Be/Open	thin-Be/Open close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 54</b> 1-time(s)		<b>2.0sec</b>
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Subr= 2</b> 1-time(s)		<b>12.0sec</b>
<b>Seqn= 46</b> 2-time(s)		<b>2.0sec</b>
Open/thick-Be	Open/thick-Be close	Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 25</b> 2-time(s)		<b>4.0sec</b>
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 21</b> 2-time(s)		<b>4.0sec</b>
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 37</b> 2-time(s)		<b>2.0sec</b>
med-Al/Open	med-Al/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1A9C: G-Band Alignment with North Pole Q90 2x2(G-band and VLS=CLS) - 1msec - 5min cadence - Partial Sun-wNGT**

Term	Pointing (x, y)	Comment
08/08 21:15:00 - 08/08 22:59:54	Fixed ( 0.0, 930.0)	Co-alignment Program Rum @ N-Limb
<b>PROG= 09</b> 1-time(s)		
<b>Subr= 1</b> 24-time(s)		<b>300.0sec</b>
<b>Seqn= 98</b> 1-time(s)		<b>2.0sec</b>
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 1024x1024 (512, 1536) Q=90 0 0 2.0sec
<b>Seqn= 63</b> 1-time(s)		<b>2.0sec</b>

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1A9D: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec- 8 min cadence-wNGT**

Term	Pointing (x, y)	Comment
08/08 23:15:00 - 08/09 00:59:54	Fixed ( -945.0, 0.0)	Co-alignment Program Run @ E-Limb
<b>PROG= 12 1-time(s)</b>		
└─ Subr= 1 15-time(s) 480.0sec		
└─ Seqn= 19 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Seqn= 43 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 1024x1024 (512, 512) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms**

Term	Pointing (x, y)	Comment
08/09 10:03:00 - 08/09 17:34:54	Fixed ( -15.0, 870.0)	HOP 81 Day 1 North Pole (0,+900)
08/10 10:03:00 - 08/10 17:59:54	Fixed ( -15.0, -950.0)	HOP 81 Day 2 South Pole (0,-920)
<b>PROG= 18 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 9 2-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 7 1-time(s) 30.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
└─ Subr= 3 30-time(s) 2.0sec		
└─ Seqn= 57 1-time(s) 60.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

\* \* \* \* \*

**Flare mode**

\* \* \* \* \*

**XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
08/05 13:18:00 - 08/05 16:49:54	Track ( 79.6, 68.4) @ 08/05 13:15:00	Track AR 12394
08/05 17:46:01 - 08/05 20:16:30	Track ( 113.6, 65.1) @ 08/05 17:00:00	# HOP 289
08/05 21:03:00 - 08/06 05:30:54	Track ( 149.0, 68.9) @ 08/05 21:00:00	Track AR 12394
08/06 05:44:00 - 08/06 16:49:54	Track ( 225.7, 70.2) @ 08/06 05:41:00	Track AR 12394
08/06 17:03:00 - 08/06 20:52:30	Track ( 323.7, 69.4) @ 08/06 17:00:00	# HOP 289
08/06 21:35:00 - 08/07 06:04:54	Track ( 356.8, 74.1) @ 08/06 21:00:00	Track AR 12394
08/07 06:18:00 - 08/07 16:22:54	Track ( 432.3, 77.5) @ 08/07 06:15:00	Track AR 12394
08/07 17:19:30 - 08/07 20:59:54	Track ( 513.0, 78.8) @ 08/07 16:33:00	# HOP 289
08/07 21:03:00 - 08/08 05:31:00	Track ( 545.4, 84.5) @ 08/07 21:00:00	Track AR 12394
08/08 06:20:30 - 08/08 16:49:54	Track ( 611.0, 89.8) @ 08/08 06:17:30	Track AR 12394
08/08 17:03:00 - 08/08 20:26:00	Track ( 681.2, 93.3) @ 08/08 17:00:00	# HOP 289
08/09 01:03:00 - 08/09 05:39:54	Track ( 727.6, 102.4) @ 08/09 01:00:00	Track AR 12394
08/09 05:53:00 - 08/09 09:27:30	Track ( 754.0, 106.1) @ 08/09 05:50:00	Track AR 12394
08/09 10:03:00 - 08/09 17:34:54	Fixed ( -15.0, 870.0)	HOP 81 Day 1 North Pole (0,+900)
08/09 18:30:00 - 08/10 05:59:54	Track ( 811.9, 115.7) @ 08/09 17:45:00	Track AR 12394
08/10 06:13:00 - 08/10 08:24:00	Track ( 860.7, 126.5) @ 08/10 06:10:00	Track AR 12394
08/10 08:56:30 - 08/10 09:59:54	Track ( 390.6, -394.6) @ 08/10 08:30:00	HOP 287
08/10 10:03:00 - 08/10 17:59:54	Fixed ( -15.0, -950.0)	HOP 81 Day 2 South Pole (0,-920)
08/10 18:13:00 - 08/11 04:39:54	Fixed ( 896.1, 137.6)	AR 12394 Fixed Pointing
08/11 08:33:00 - 08/11 10:07:54	Track ( 564.7, -382.5) @ 08/11 08:30:00	HOP 287

<b>PROG= 03 30-time(s)</b>		
└─ Subr= 1 20-time(s) 2.0sec		
└─ Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Seqn= 53 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

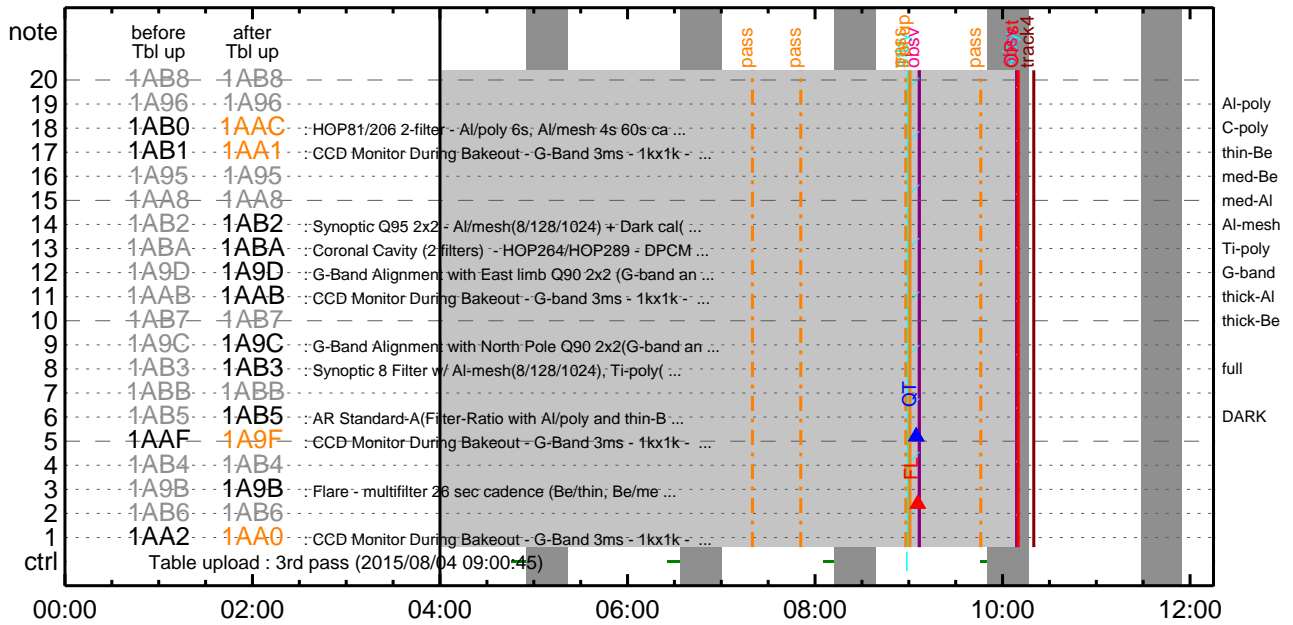
\* \* \* \* \*

### Flare Detection

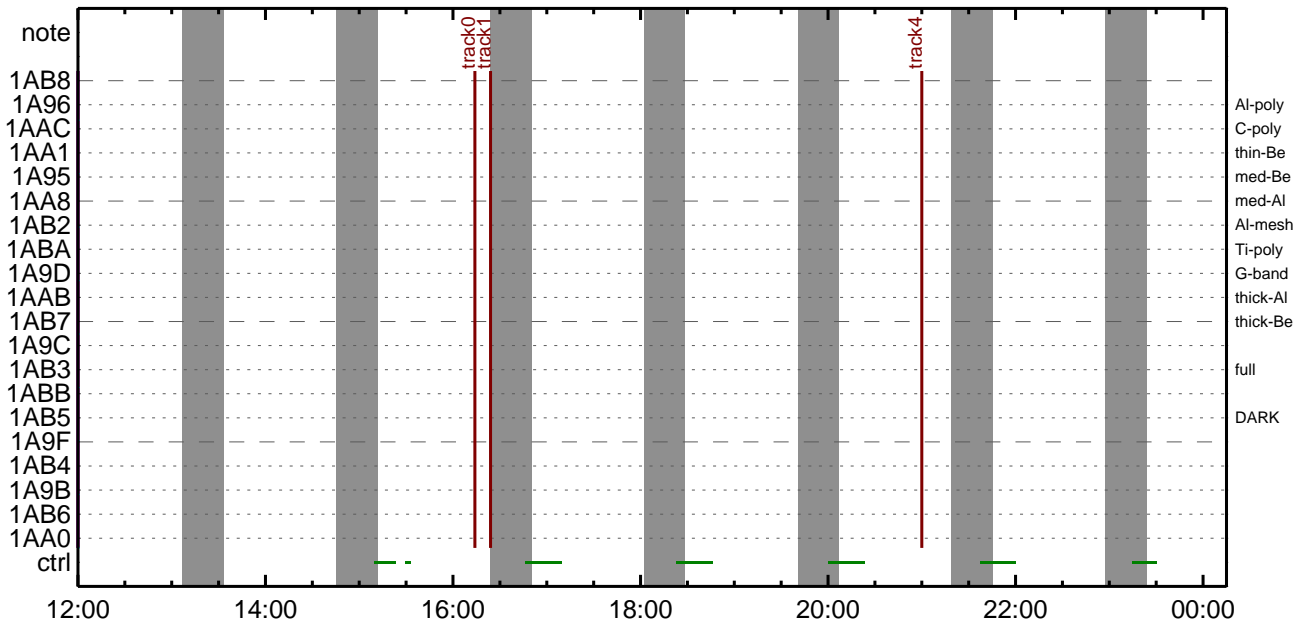
\* \* \* \* \*

FLD Patrol										
Term	Pointing (x, y)					Comment				
08/05 13:15:16 - 08/05 16:50:16	Track (	79.6,	68.4)	@ 08/05 13:15:00		Track AR 12394				
08/05 17:00:21 - 08/06 05:31:16	Track (	113.6,	65.1)	@ 08/05 17:00:00		# HOP 289				
08/06 05:41:16 - 08/06 16:50:16	Track (	225.7,	70.2)	@ 08/06 05:41:00		Track AR 12394				
08/06 17:00:16 - 08/07 06:05:16	Track (	323.7,	69.4)	@ 08/06 17:00:00		# HOP 289				
08/07 06:15:16 - 08/07 16:23:16	Track (	432.3,	77.5)	@ 08/07 06:15:00		Track AR 12394				
08/07 16:33:16 - 08/08 06:07:46	Track (	513.0,	78.8)	@ 08/07 16:33:00		# HOP 289				
08/08 06:17:46 - 08/08 16:50:16	Track (	611.0,	89.8)	@ 08/08 06:17:30		Track AR 12394				
08/08 17:00:16 - 08/08 21:00:18	Track (	681.2,	93.3)	@ 08/08 17:00:00		# HOP 289				
08/09 01:00:16 - 08/09 05:40:16	Track (	727.6,	102.4)	@ 08/09 01:00:00		Track AR 12394				
08/09 05:50:16 - 08/09 17:35:16	Track (	754.0,	106.1)	@ 08/09 05:50:00		Track AR 12394				
08/09 17:45:16 - 08/10 06:00:16	Track (	811.9,	115.7)	@ 08/09 17:45:00		Track AR 12394				
08/10 06:10:16 - 08/10 18:00:16	Track (	860.7,	126.5)	@ 08/10 06:10:00		Track AR 12394				
08/10 18:10:16 - 08/11 04:40:16	Fixed (	896.1,	137.6)			AR 12394 Fixed Pointing				
08/11 08:30:16 - 08/12 09:28:00	Track (	564.7,	-382.5)	@ 08/11 08:30:00		HOP 287				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

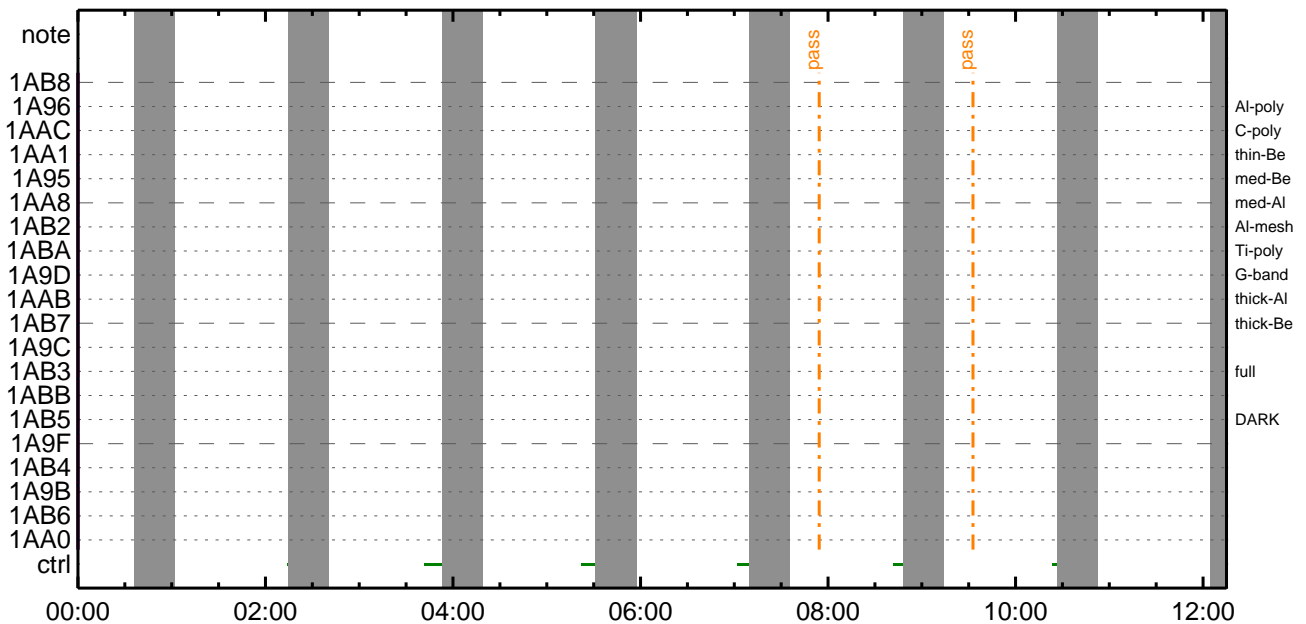
### CMDI #0308 2015/08/04



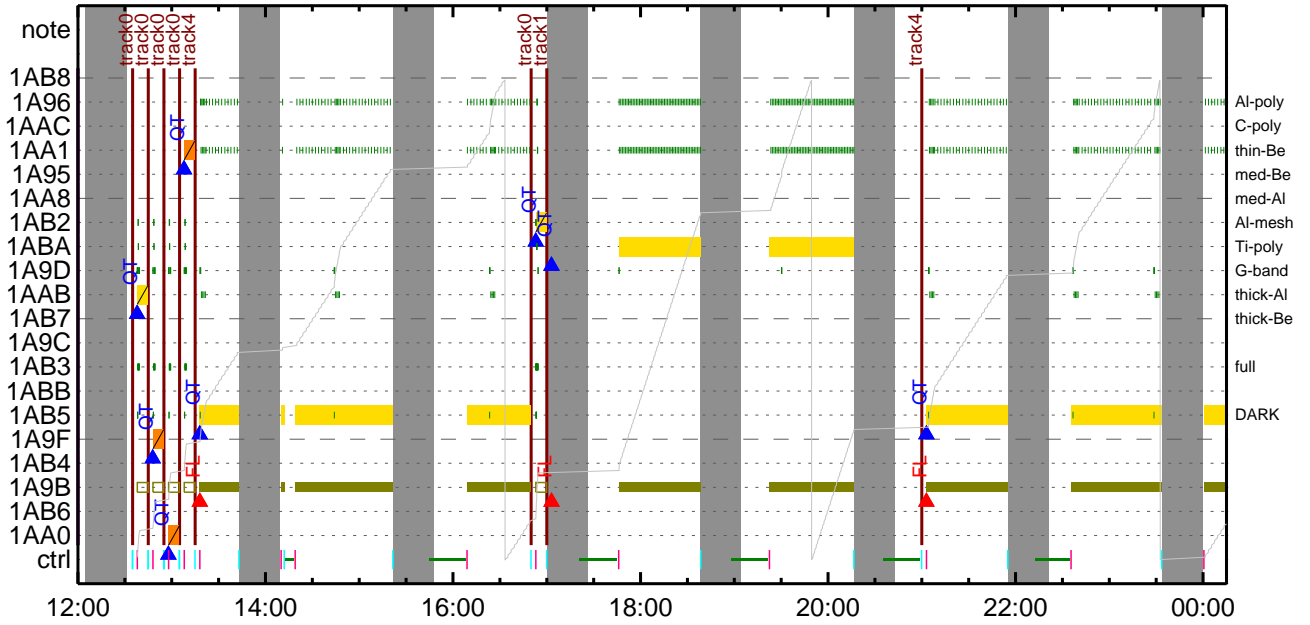
### CMDI #0308 2015/08/04



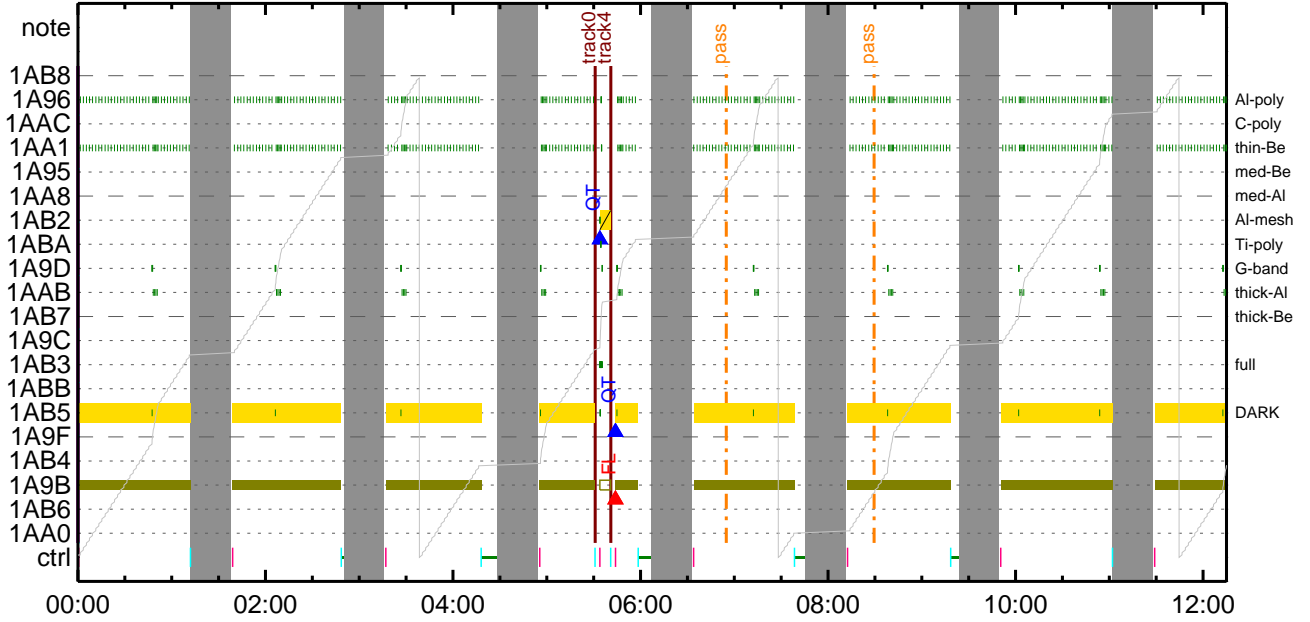
### CMDI #0308 2015/08/05



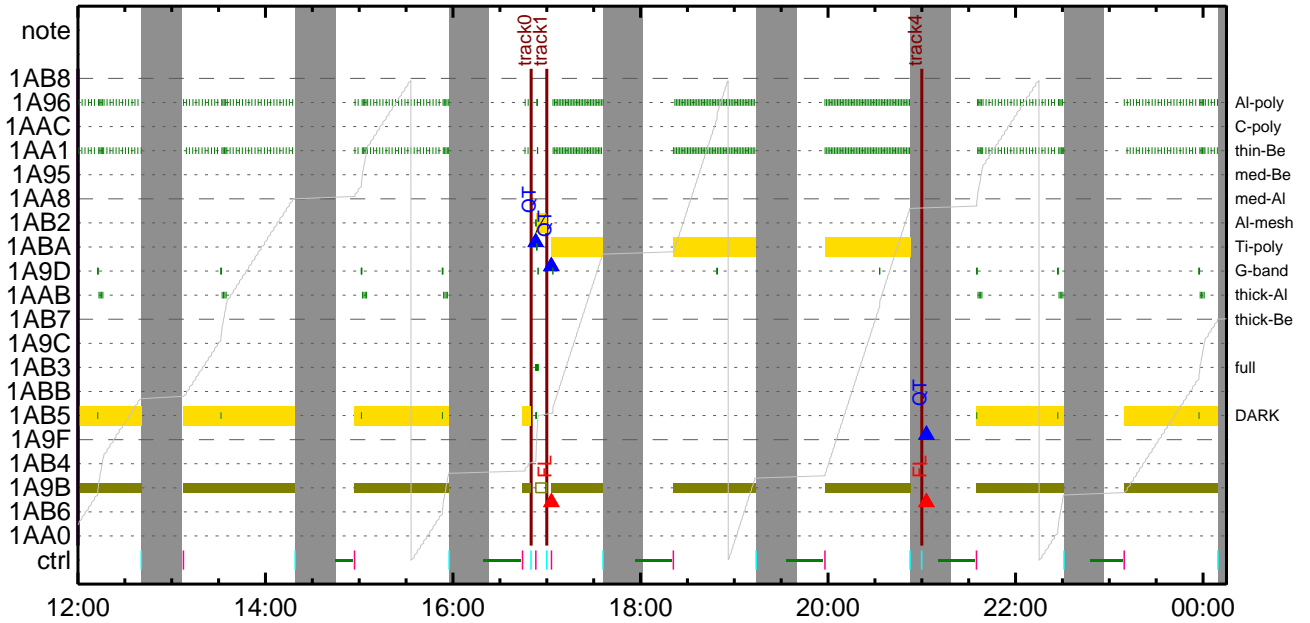
CMDI #0308 2015/08/05



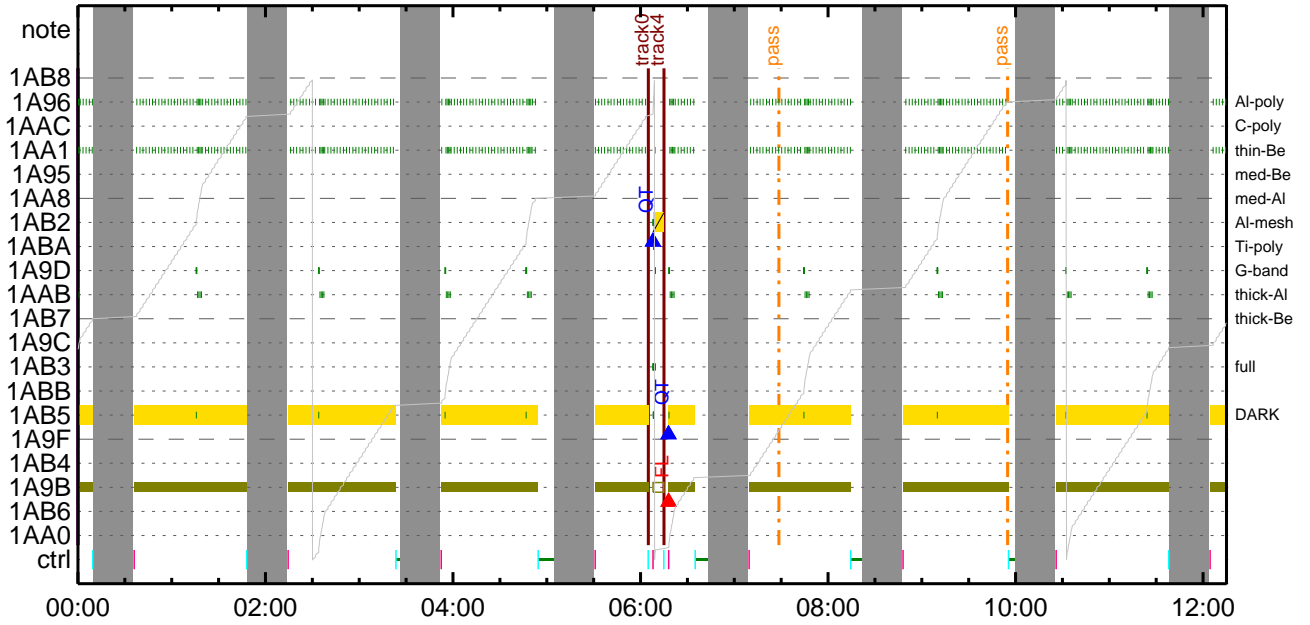
CMDI #0308 2015/08/06



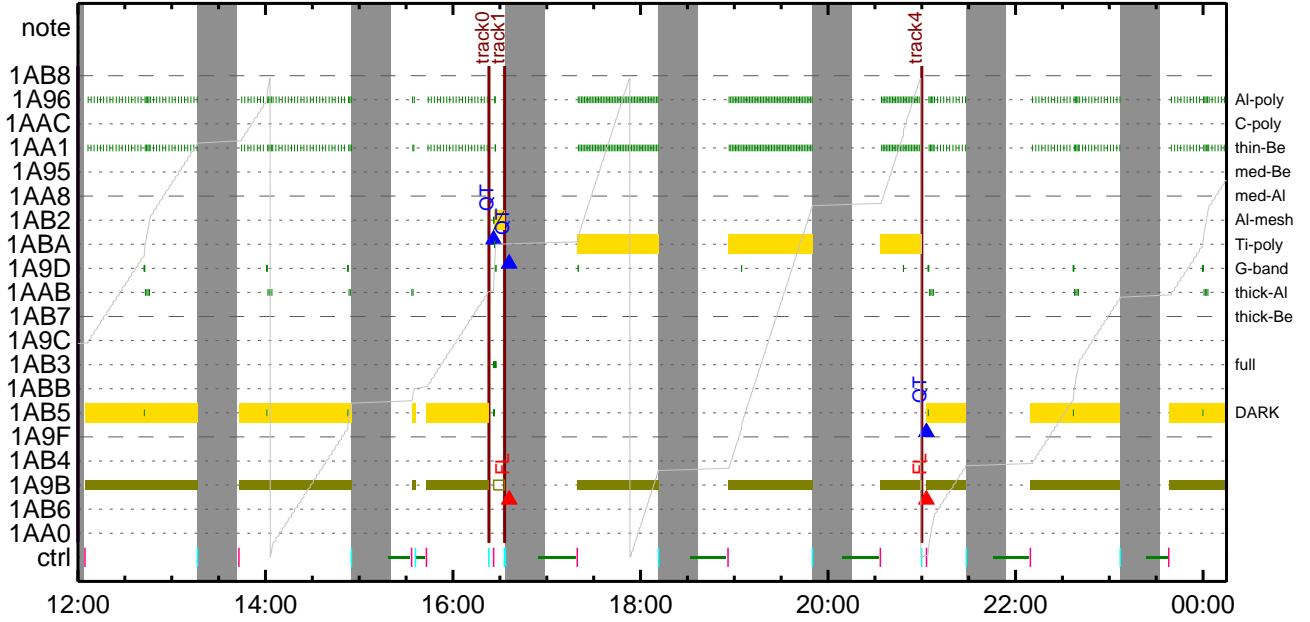
CMDI #0308 2015/08/06



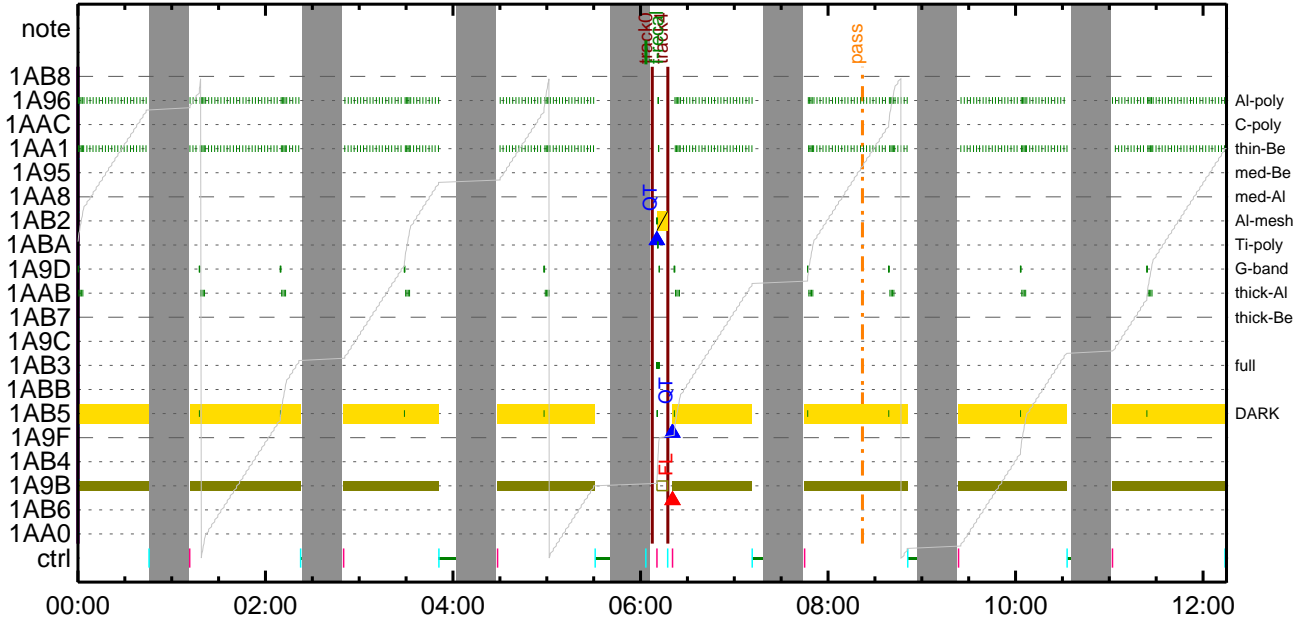
CMDI #0308 2015/08/07



CMDI #0308 2015/08/07

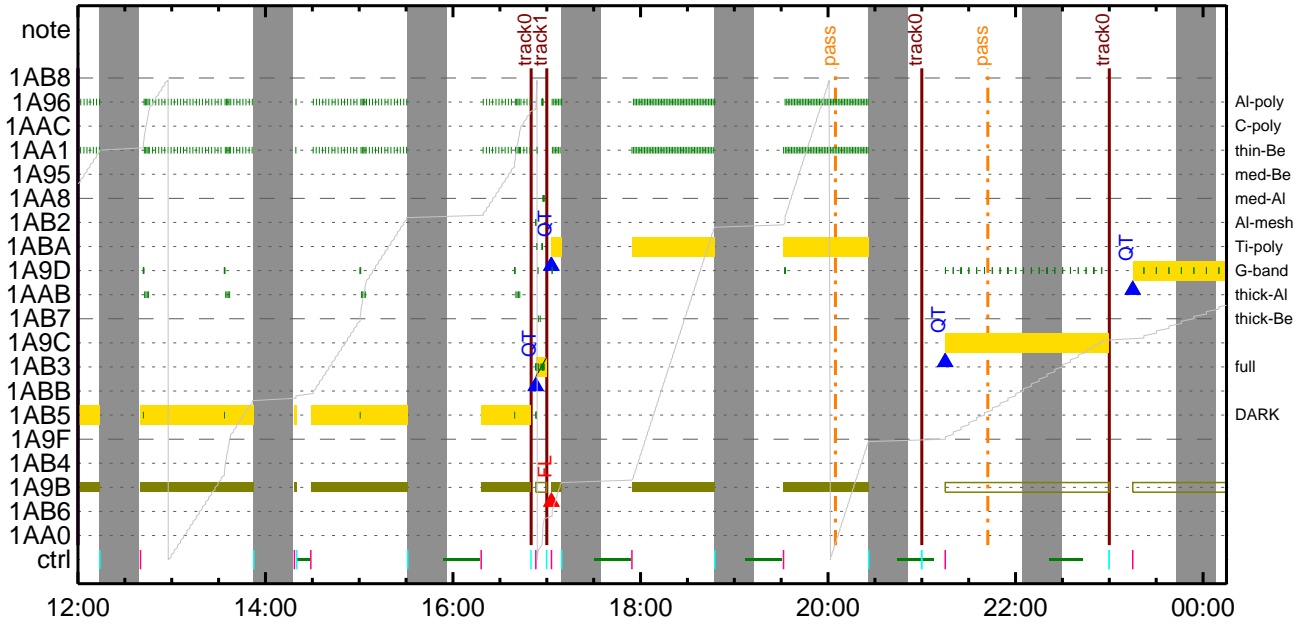


CMDI #0308 2015/08/08

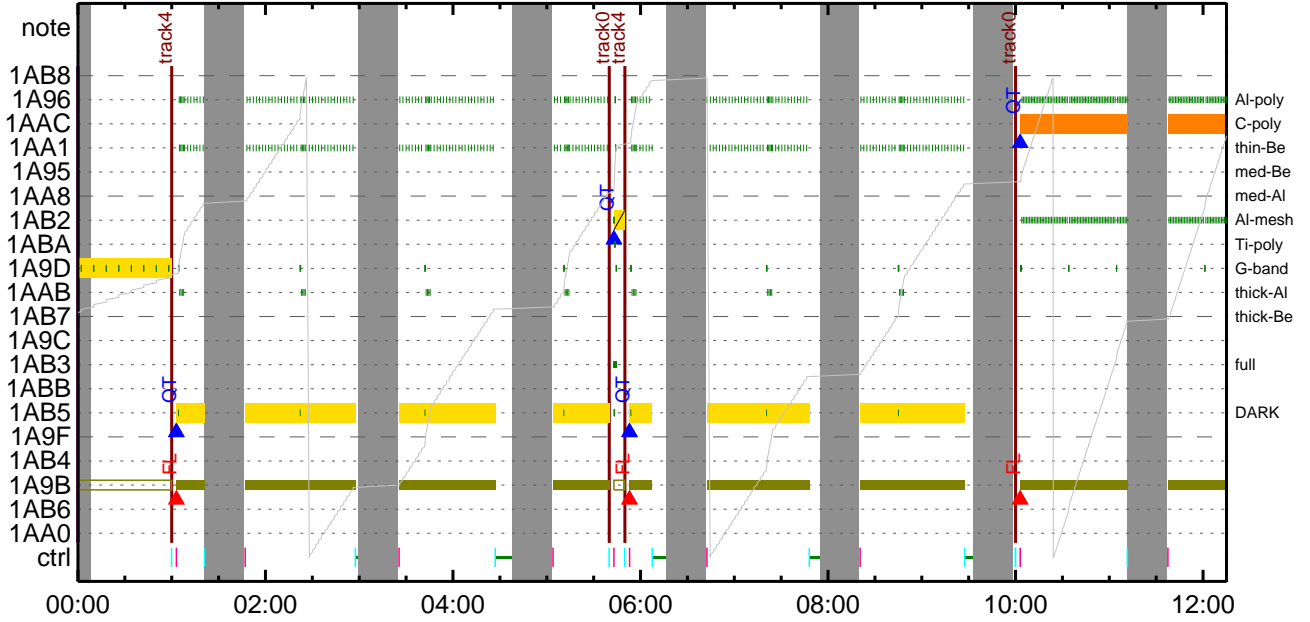




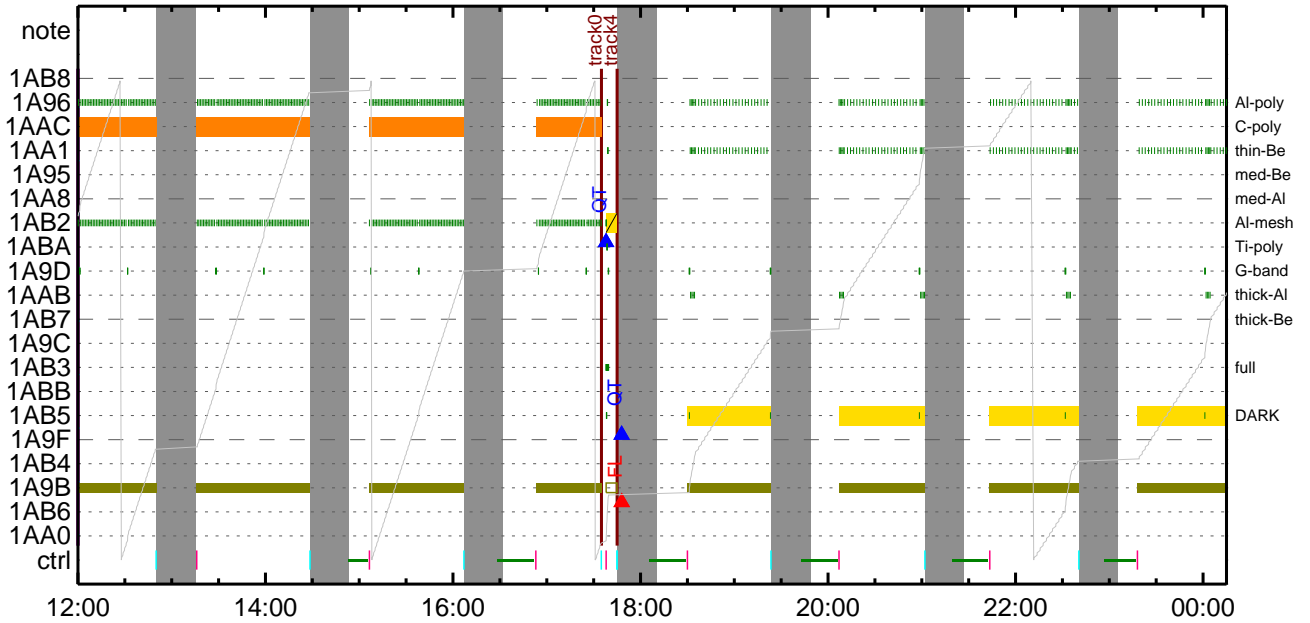
CMDI #0308 2015/08/08



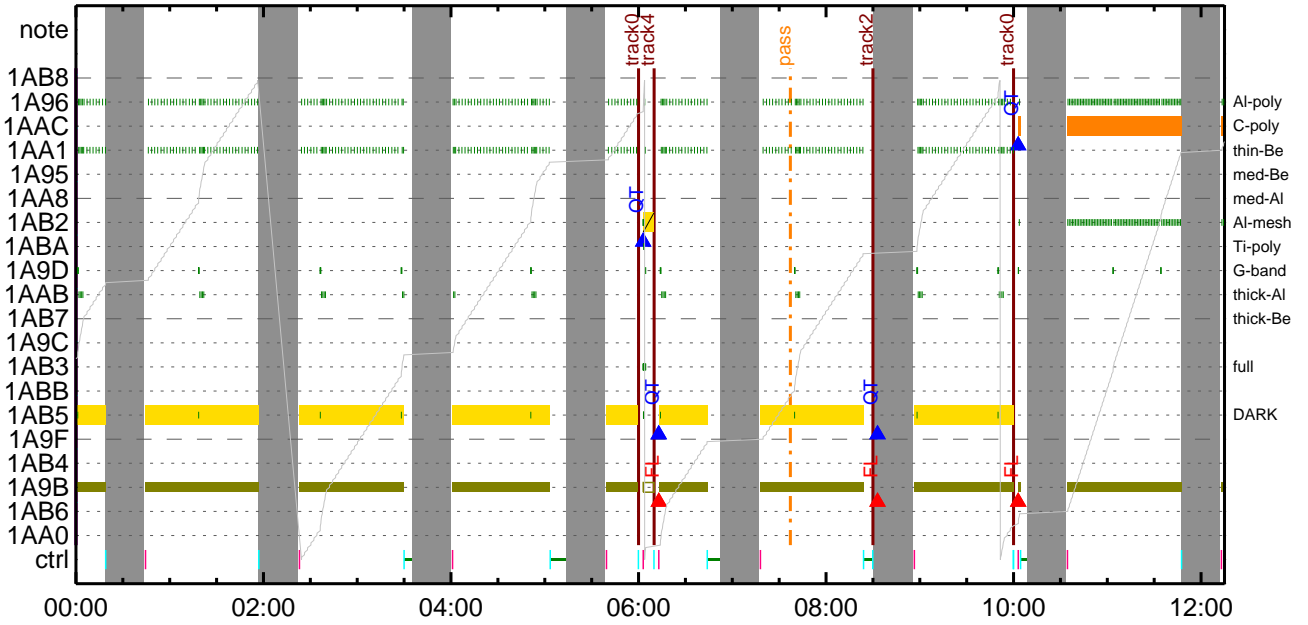
CMDI #0308 2015/08/09



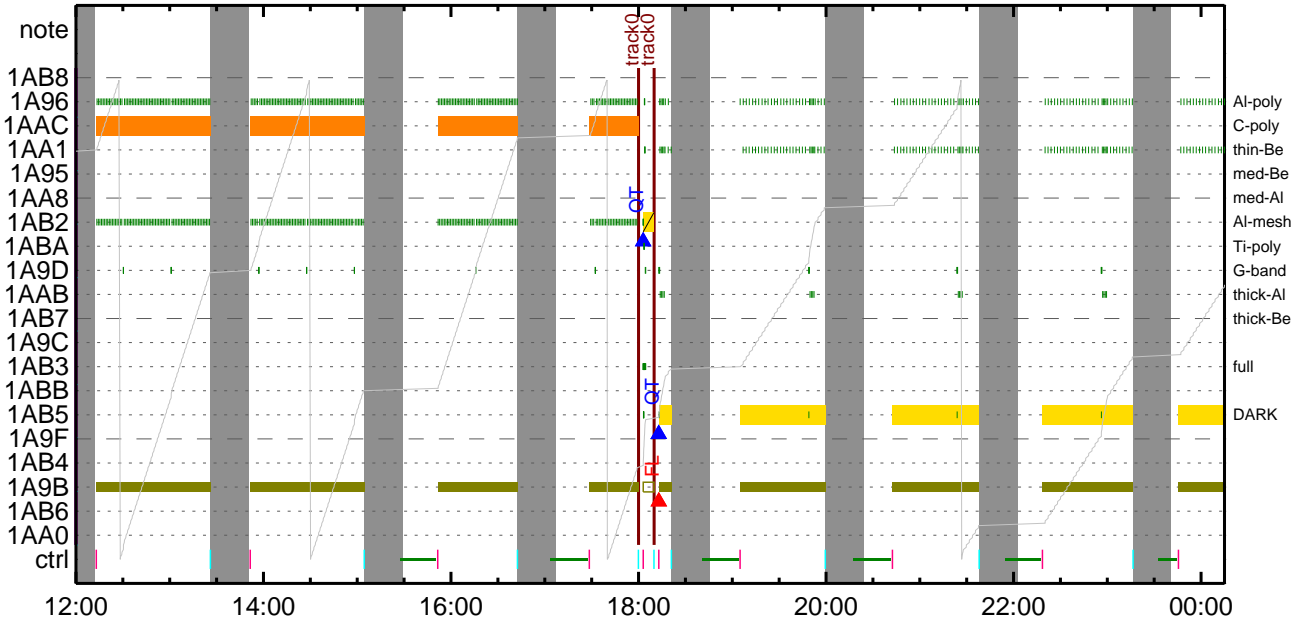
CMDI #0308 2015/08/09



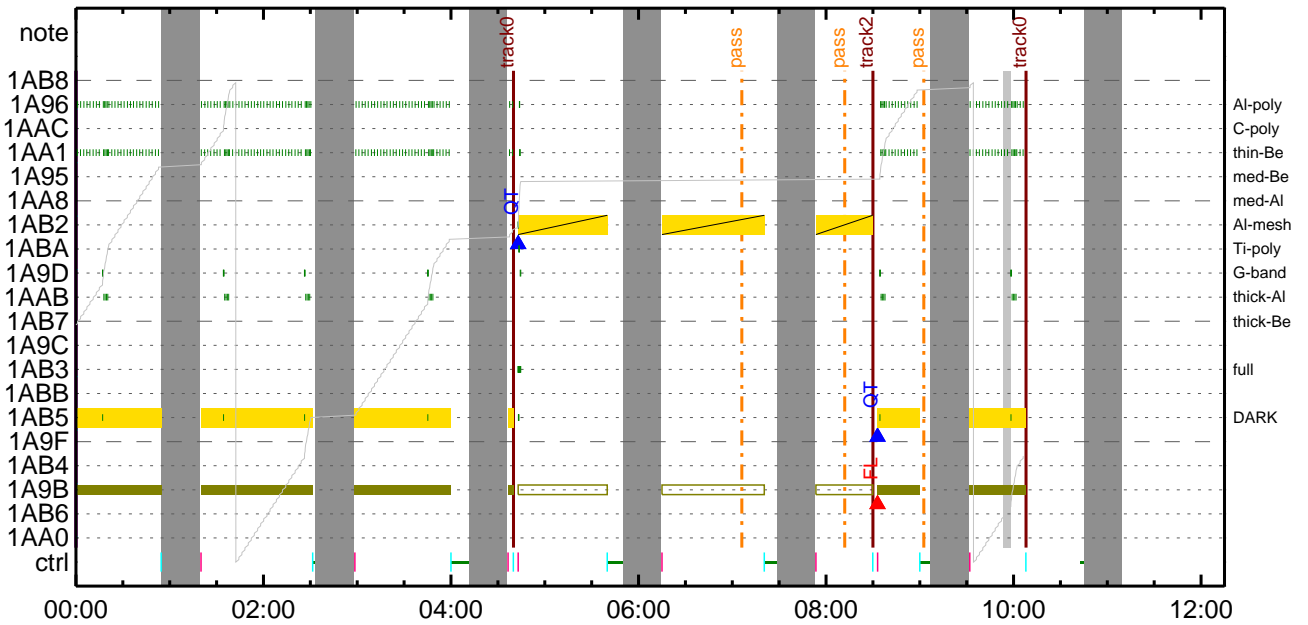
CMDI #0308 2015/08/10



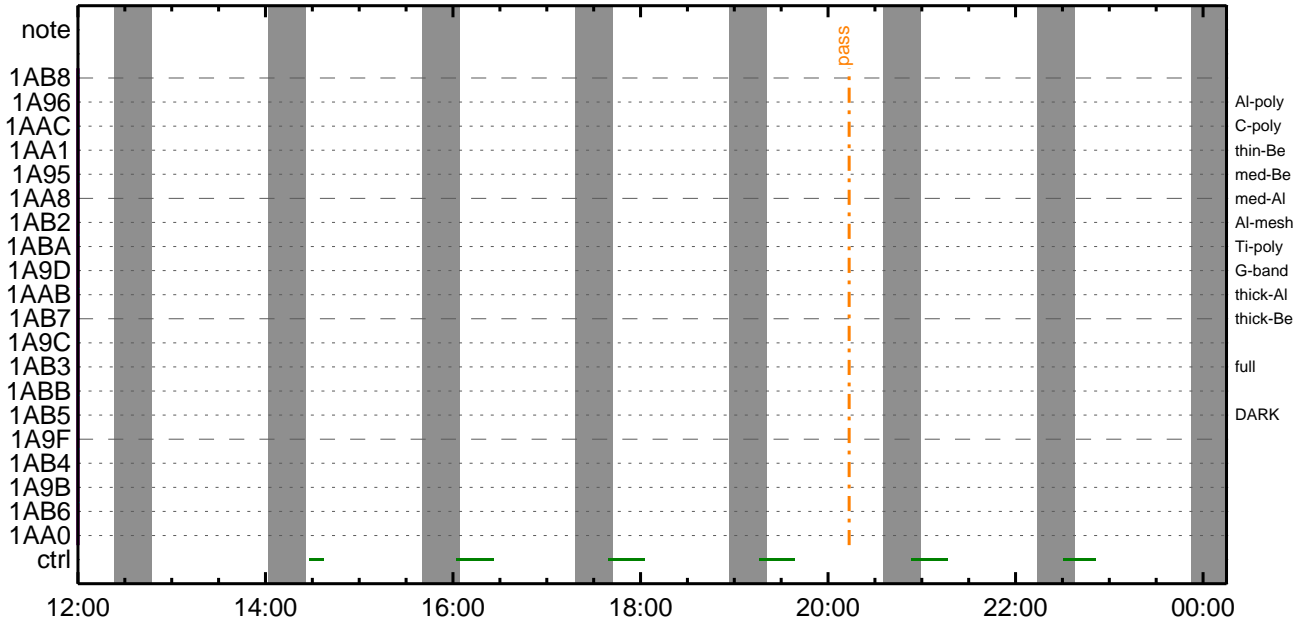
CMDI #0308 2015/08/10



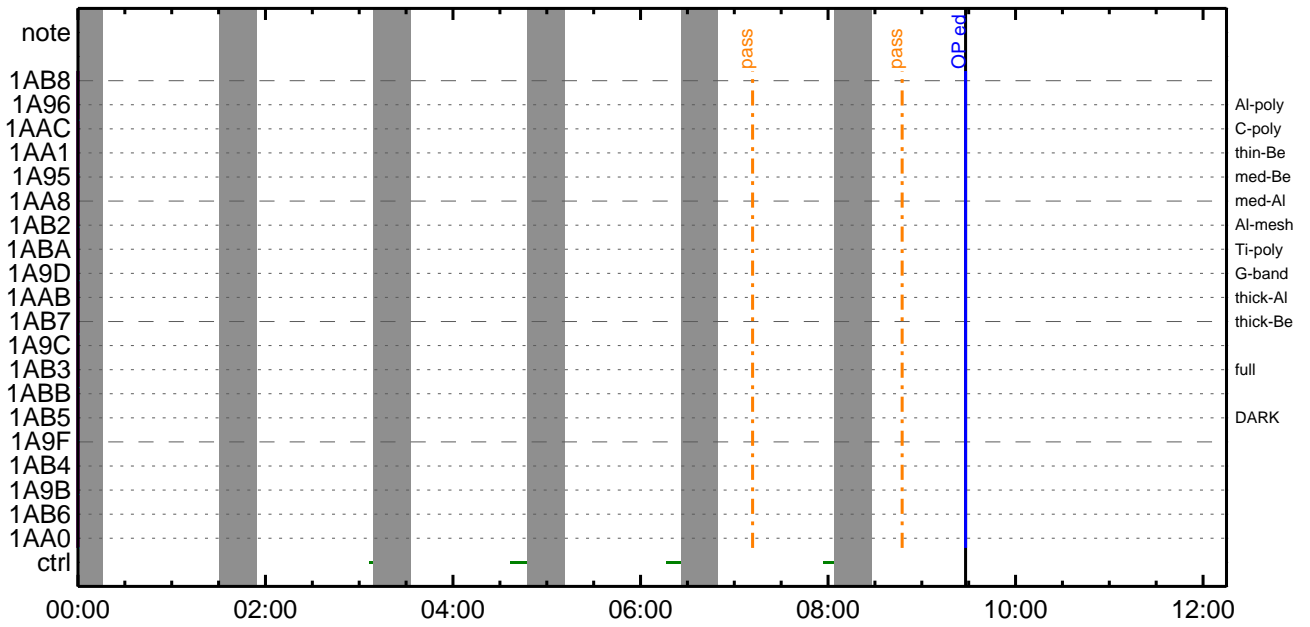
CMDI #0308 2015/08/11



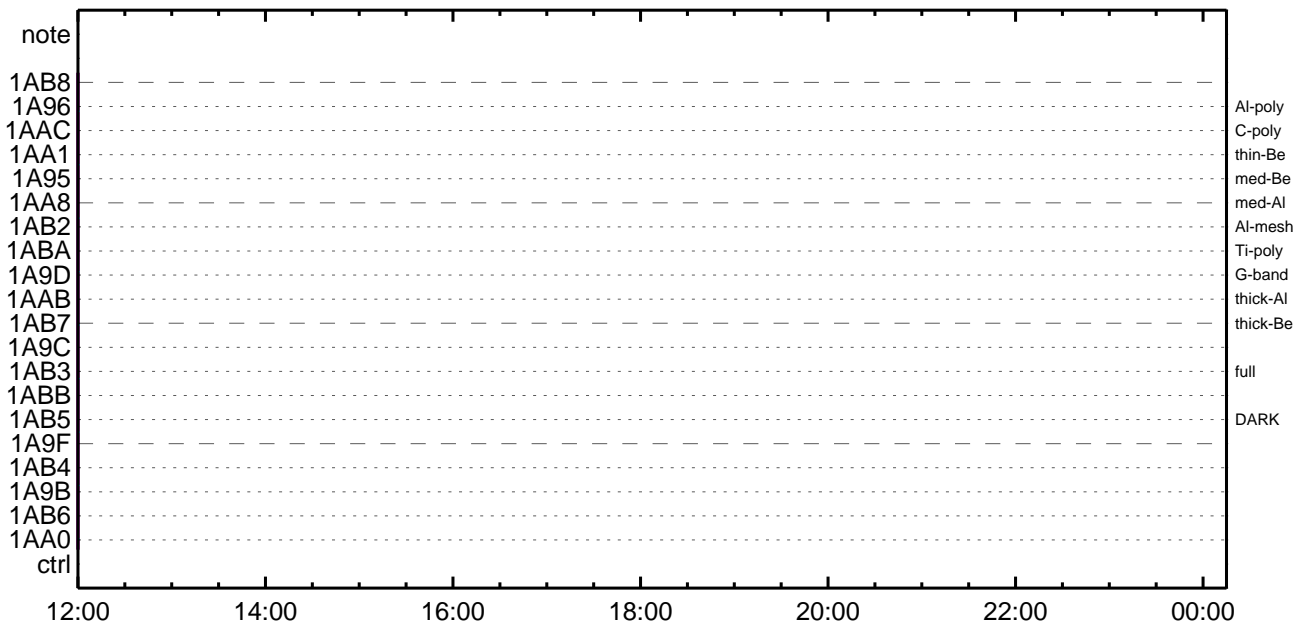
CMDI #0308 2015/08/11



CMDI #0308 2015/08/12



CMDI #0308 2015/08/12





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-296:OP
0104 ( )
0105 S. OG og-296:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî¼Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê¼çòðÁÔÃæç¼ª°¬²è¼î¹çòçðâ) *****
0167 C. DHUYâ;4YE;E¼Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼î¹ç;ç°E²¼òî¼TI-CMDÁ÷¿@²î¼Á¹Ô²•²E²²²³²E;f
0180 C. ²²²¿;çSET²E²DUMP²î¼E±²îY²¹²ç¹Ô²|²³²E;f
0181 C.
0182 C. TIY³Y²Y²Y²E²òðÁDî¿(UT)
0183 +. TI 2015-08-04 10:05:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-08-04 10:05:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-08-04 10:05:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2015-08-04 10:09:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      ¤¤[HK1_TI_CMD_NUM]      EQ      1COUNTUP
0198 C.
0199 C.      °ê²¼öïäè%ïíñóîŷäÿ§ŷäÿ¹àiÛ
0200 C.      ¤¤[HK1_TI_CMD_ENA/DIS]    EQ      ENA
0201 C.      ¤¤[HK1_TI_CMD_NUM]        EQ      4
0202 C.      ¤¤[HK1_NEXT_EXEC_PIM]     EQ      DHU
0203 C.      ¤¤[HK1_NEXT_EXEC_DC]      EQ      0xB3
0204 C.
0205 C.      *****
0206 C.      TIîí°èŷäÿÖÿ×
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      ¤¤[HK1_DMP_TOP_ADRS_1]    EQ      07
0213 C.      ¤¤[HK1_DMP_TOP_ADRS_0]    EQ      2B
0214 C.      ¤¤[HK1_DMP_BLOCK_NUM]     EQ      3
0215 C.      ¤¤[HK1_DMP_REPEAT_NUM]   EQ      0
0216 C.      ¤¤[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      ¤¤[HK1_PKT_FORM_NO]       EQ      7
0220 C.      ¤¤[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.      ¤¤[HK1_S_TLM_BIT_RATE]   EQ      32k
0222 C.      ¤¤[HK1_X_TLM_BIT_RATE]   EQ      4M
0223 C.      ¤¤[HK1_DMP_CHK_FLG]      EQ      EXEC
0224 C.
0225 C.      ŷäÿÖÿ×½ªî»ò³îÇ§
0226 C.      ¤¤[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C.      RAM ID=TI_TBLóî½è¹ç•è²ìOKò³îÇ§
0229 C.
0230 C.      DHUŷä;¼ŷÉ;è½ÿ¼.ŷì;¼ŷÈ;Èòòîáó¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      ¤¤[HK1_PKT_FORM_NO]       EQ      2
0234 C.      ¤¤[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.      ¤¤[HK1_S_TLM_BIT_RATE]   EQ      32K
0236 C.      ¤¤[HK1_X_TLM_BIT_RATE]   EQ      4M
0237 C.
0238 C.      *****
0239 C.      SOT TI command set
0240 C.      *****
0241 C.      Execute, after the success of OP upload.
0242 +. TI 2015-08-04 10:09:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C.      -----
0246 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0247 C.      -----
0248 C.      ***** SOT END *****
0249 C.      Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C.      ***** Start EIS operation (TI set) *****
0253 C.      Execute, after the success of OP upload.
0254 C.      Set EIS TI-commands
0255 +. TI 2015-08-04 10:09:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2015-08-04 10:09:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C.      ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2015-08-04 10:09:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 C.      ***** MDP `ûãîáî»ö¼ŷöëÄò¹óèDCBC•x²è *****
0276 C.      (¼á°îŷÖŷäÿÈŷÿŷäÿçŷèö½¼óó¼Ä»Ûó¹è)
0277 C.      S. DC-BC dcbc-402:DCBC
0278 C.      (MDP_known_event)
0279 C.
0280 C.
0281 C.      ***** ŷäÿ¹•î Daily±¿îñöÈ¹ó¹óèDCBC•x²è *****
0282 C.      S. DC-BC dcbc-153:DCBC
0283 C.      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.      ;ãLOSŷäÿ§ŷäÿ¹¼Ä»Û;ã
0287 C.
0288 C.      ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-297 2015-08-04 13:37:18 94 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÄY$YÄY-¼Ä»Ü;ã
0005 C.
0006 C. YÄYB;¼Y³YFYOYEÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;ÈçðÅB•µ°Æ»Í×ÄÇçÍYçYÄY×Yí;¼YÉ;ÈÈ½µ•íÊ;ÈBÈ¼°ÇÔB•Bç¼i¹çBÍ;çÄ®, ùB¹BÈBðBçÄ+ç®B•BÈBçB³BÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 2477384.5 +- 1.0 (s) [ ]
0052 C.
0053 C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_CHG_ENA
0056 BC (20)
0057 . C. Verify EIS_MODE_CHG_FLG is ENA
0058 +. DC 07-FC EIS_MODE_MANU
0059 BC (21 02)
0060 . C. Verify EIS in MANUAL mode
0061 C. Estimated OBSTBL upload time is 39s
0062 C. *****
0063 C. EIS START OBSTBL LOAD
0064 C. *****
0065 . S. RAM ram-820:EIS_OBSTBL
0066 ( )
0067 +. DC 07-FC EIS_DUMP_OBSTBL
0068 BC (07 07 07 00 00 70 00)
0069 C.
0070 C. Execute, after the success of OBSTBL upload.
0071 C. Set EIS TI-commands
0072 +. TI 2015-08-04 10:09:50.0
0073 DC 07-FC EIS_MODE_CHG_ENA
0074 BC (20)
0075 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0076 C. *****
0077 C. EIS END OBSTBL LOAD
0078 C. *****
0079 C.
0080 . C. ***** MDP `úÄîðí»ò¼YðÈÄðB¹BèDCBC•x²è *****
0081 C. (¼á°íYóYÄYÈYþYÈYáYçYèBÈ½¼Bç¼¼Ä»ÜB¹Bè)
0082 . S. DC-BC dcbc-402:DCBC
0083 (MDP_known_event)
0084 C.
0085 C.
0086 . C. ***** YDÝ¹.İ Daily±;jÎÑB`ØB¹BèDCBC•x²è *****
0087 . S. DC-BC dcbc-153:DCBC
0088 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 C.
0090 C.
0091 . C. ;ãLOSÄY$YÄY-¼Ä»Ü;ã
0092 C.
0093 . C. ***** LOS *****
0094 C.
```





```

0096 C.
0097 C. XRT Obs. Table Upload
0098 . S. RAM ram-291:MDP_OBS_X
0099 ( )
0100 C.
0101 +. DC 07-F0 MDP_DUMP_XRTTBL
0102 BC (84 07 00 00 00 3a d4)
0103 . C. ----- Comparison Check ? OK / ERR ____
0104 C.
0105 C.
0106 +. DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 01 b1 b1 04 04)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 02 b1 b1 08 08)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 03 b1 b1 08 08)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 04 b1 b1 06 06)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 05 85 83 06 06)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 06 85 83 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 07 85 83 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 08 c0 c0 10 10)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 09 80 80 20 20)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 0a 40 c0 10 10)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 0b 40 40 10 10)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 0c c0 40 10 10)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 0d 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0e 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0f 80 80 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 10 80 80 08 08)
0138 + DC 07-F0 MDP_XRT_FLD_ENA
0139 BC (d8)
0140 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0141 BC (c8)
0142 + DC 07-F0 MDP_XRT_AEC_RESET
0143 BC (d0)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_FLD_RESET
0147 BC (da)
0148 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0149 BC (c4 06)
0150 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0151 BC (c5 03)
0152 . C. ----- Success Verify ? OK / NG ____
0153 C.
0154 C.
0155 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0156 C.
0157 +. DC 07-F0 MDP_XRT_MODE_OBSV
0158 BC (c2)
0159 +. TI 2015-08-04 10:09:02.0
0160 DC 07-F0 MDP_XRT_MODE_OBSV
0161 BC (c2)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C. ***** XRT END *****
0165 C.
0166 . C. ***** MDP `uAÎoÎ»ô%YôEÂÐo¹oèDCBC•x²è *****
0167 C. (%â°îYôYÄYÈYËYËYâYçYèoE½¼o¼Ä»Ûo¹oè)
0168 . S. DC-BC dcbc-402:DCBC
0169 (MDP_known_event)
0170 C.
0171 C.
0172 . C. ***** YDÿ¹•Ï Daily±;jÎÑoE`Øo¹oèDCBC•x²è *****
0173 . S. DC-BC dcbc-153:DCBC
0174 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0175 C.
0176 C.
0177 . C. ;ãLOSÿÁÿSÿÄÿ¼Ä»Û;ã
0178 C.
0179 . C. ***** LOS *****
0180 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-299 2015-08-04 13:37:18 29 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOSYÁYŠYÁY-¼Á»Û;ã  
0005 C.  
0006 C. YÀYB;¼Y³YFÏÓYÉÁ+¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****  
0010 C. Áí;È□¿□Á□•μ°Æ»Í×ÁÇ□ÍYçYÁY×Yí;¼YÉ;ÈÈè%μ•íÍÈ;È□È¼°ÇÔ□•□¿¼í¹ç□Í;çÀ®, ù□¹□è□□çÁ+¿®□•□È□□□³□È;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 C.  
0015 . C. ***** MDP `úÁÍ□Í»ô¼Y□ÈÁ□¹□èDCBC•x²è *****  
0016 C. (%á°íYÓYÁYÈYBÏYÉYáYçYè□È%¼□□¼Á»Û□¹□è)  
0017 . S. DC-BC dcbc-402:DCBC  
0018 (MDP_known_event)  
0019 C.  
0020 C.  
0021 . C. ***** YDÏ¹•Ï Daily+¿ÍÑ□È´Ø□¹□èDCBC•x²è *****  
0022 . S. DC-BC dcbc-153:DCBC  
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0024 C.  
0025 C.  
0026 . C. ;ãLOSÁYŠYÁY-¼Á»Û;ã  
0027 C.  
0028 . C. ***** LOS *****  
0029 C.
```

Aug 04, 15 13:37

XRT\_OGLIST\_0308.chk

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\*\*\* OP Sequence for XRT \*\*\*

2015/08/04	10:20:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2015/08/04	16:14:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/08/04	16:24:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	00	00	00	00
2015/08/04	21:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2015/08/05	06:15:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_406_OG [0x196]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2015/08/05	12:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	12:34:56.0	XRT_CTRL_MANU_407_OG [0x197]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	12:35:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2015/08/05	12:37:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/05	12:37:52.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2015/08/05	12:37:54.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/05	12:37:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/05	12:37:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/05	12:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/05	12:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	12:44:56.0	XRT_CTRL_MANU_407_OG [0x197]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	12:45:00.0	AOCS_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2015/08/05	12:47:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/05	12:47:52.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2015/08/05	12:47:54.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/05	12:47:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/05	12:47:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/05	12:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/05	12:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	12:54:56.0	XRT_CTRL_MANU_407_OG [0x197]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	12:55:00.0	AOCS_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2015/08/05	12:57:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/05	12:57:52.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2015/08/05	12:57:54.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/05	12:57:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/05	12:57:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/05	12:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/05	13:04:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	13:04:56.0	XRT_CTRL_MANU_407_OG [0x197]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	13:05:00.0	AOCS_OrE-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2015/08/05	13:07:32.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/05	13:07:52.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2015/08/05	13:07:54.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/05	13:07:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/05	13:07:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/05	13:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/05	13:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	13:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/05	13:15:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2015/08/05	13:15:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/05	13:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							

2015/08/05	13:15:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/05	13:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/05	13:15:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/05	13:17:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/08/05	13:17:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/05	13:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/05	13:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	13:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/05	13:43:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/05	13:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/05	14:09:00.0	XRT_Custom_430_OG [0x1ae]								
2015/08/05	14:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/05	14:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	14:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/05	14:12:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/05	14:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/05	14:18:00.0	XRT_Custom_430_OG [0x1ae]								
2015/08/05	14:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/05	15:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	15:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/05	15:21:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/05	15:21:34.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/05	15:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/05	16:08:00.0	XRT_Custom_430_OG [0x1ae]								
2015/08/05	16:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/05	16:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	16:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/05	16:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2015/08/05	16:50:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/05	16:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/05	16:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/05	16:52:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/08/05	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/05	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/05	16:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/05	17:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00	00
2015/08/05	17:00:16.0	XRT_ROI_A_417_OG [0x1a1]	MDP_XRT_ROI_SET	6	07-F0	cd	05	85	83	06
			MDP_XRT_ROI_SET	6	07-F0	cd	06	85	83	06
			MDP_XRT_ROI_SET	6	07-F0	cd	07	85	83	08
			MDP_XRT_ROI_SET	6	07-F0	cd	08	85	83	0c
			MDP_XRT_ROI_SET	6	07-F0	cd	09	80	80	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0a	80	60	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0b	a0	80	18
			MDP_XRT_ROI_SET	6	07-F0	cd	0d	80	80	20
2015/08/05	17:00:16.5	XRT_ROI_B_422_OG [0x1a6]	MDP_XRT_ROI_SET	6	07-F0	cd	0d	80	80	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0e	80	80	08
			MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06
			MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08
2015/08/05	17:00:21.5	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/05	17:00:23.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/05	17:00:25.5	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/05	17:00:27.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/05	17:00:29.5	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/05	17:03:01.5	XRT_QT_PROG_SET_432_OG [0x1b0]								

2015/08/05	17:03:03.5	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/08/05	17:45:01.0	XRT_Custom_430_OG [0x1ae]							
2015/08/05	17:46:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/05	18:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/05	18:38:32.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/05	18:38:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/05	18:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/05	19:21:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/05	19:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/05	20:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/05	20:16:32.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/05	20:16:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/05	20:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/05	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/05	20:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/08/05	21:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
			AOCU_NM	5	02-76	04	00	00	00
2015/08/05	21:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/08/05	21:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/08/05	21:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/08/05	21:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/05	21:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/05	21:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2015/08/05	21:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/08/05	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/05	21:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/05	21:55:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/05	21:55:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/05	21:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/05	22:34:30.5	XRT_Custom_430_OG [0x1ae]							
2015/08/05	22:35:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/05	23:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/05	23:33:32.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/05	23:33:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/05	23:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/05	23:59:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/06	00:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	01:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/06	01:12:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/06	01:12:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/06	01:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/06	01:38:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/06	01:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	02:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/06	02:48:32.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/06	02:48:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/06	02:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/06	03:16:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/06	03:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	04:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2015/08/06	04:18:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/06	04:18:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/06	04:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/06	04:54:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/06	04:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/06	05:30:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/06	05:30:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/06	05:31:00.5	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/08/06	05:31:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/08/06	05:33:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/08/06	05:33:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/06	05:33:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/08/06	05:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/06	05:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/06	05:40:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/06	05:41:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2015/08/06	05:41:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/06	05:41:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/06	05:41:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/06	05:41:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/06	05:41:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/06	05:43:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/08/06	05:43:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/06	05:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/06	05:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/06	05:58:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/06	05:58:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/06	06:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/06	06:33:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/06	06:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/06	07:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/06	07:38:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/06	07:38:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/06	07:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/06	08:11:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/06	08:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/06	09:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/06	09:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/06	09:18:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/06	09:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/06	09:49:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/06	09:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/06	11:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/06	11:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/06	11:02:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/06	11:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/06	11:28:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/06	11:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/06	12:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2015/08/06	12:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/06	12:40:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/06	12:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/06	13:06:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/06	13:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	14:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/06	14:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/06	14:19:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/06	14:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/06	14:56:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/06	14:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	15:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/06	15:57:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/06	15:57:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/06	16:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/06	16:43:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/06	16:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	16:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/06	16:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2015/08/06	16:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2015/08/06	16:50:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/08/06	16:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/08/06	16:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/06	16:52:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2015/08/06	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/06	16:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/08/06	17:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00			
2015/08/06	17:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/08/06	17:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/08/06	17:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/08/06	17:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/06	17:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/06	17:02:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2015/08/06	17:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2015/08/06	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	17:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/06	17:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/06	17:36:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/06	17:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/06	18:20:01.5	XRT_Custom_430_OG [0x1ae]							
2015/08/06	18:21:01.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	19:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/06	19:14:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/06	19:14:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/06	19:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/06	19:57:01.0	XRT_Custom_430_OG [0x1ae]							
2015/08/06	19:58:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/06	20:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2015/08/06	20:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/06	20:52:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/06	20:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/06	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/06	20:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/06	21:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2015/08/06	21:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/06	21:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/06	21:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/06	21:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/06	21:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/06	21:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/08/06	21:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/06	21:34:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/06	21:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/06	22:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/06	22:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/06	22:31:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/06	22:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/06	23:08:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/06	23:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	00:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	00:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	00:09:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	00:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	00:35:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	00:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	01:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	01:48:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	01:48:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	01:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	02:13:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	02:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	03:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	03:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	03:23:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	03:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	03:51:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	03:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	04:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	04:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	04:54:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	04:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	05:30:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	05:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	06:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/07	06:05:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/08/07	06:05:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/08/07	06:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]				



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2015/08/07	06:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/07	06:07:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/08/07	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	06:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	06:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/07	06:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2015/08/07	06:15:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/07	06:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/07	06:15:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/07	06:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/07	06:15:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	06:17:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/08/07	06:17:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/07	06:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	06:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	06:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	06:35:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	06:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	07:08:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	07:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	08:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	08:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	08:14:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	08:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	08:47:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	08:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	09:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	09:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	09:55:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	09:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	10:25:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	10:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	11:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	11:38:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	11:38:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	11:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	12:03:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	12:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	13:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	13:16:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	13:16:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	13:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	13:42:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	13:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	14:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	14:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	14:55:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	14:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	15:32:30.0	XRT_Custom_430_OG [0x1ae]				

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2015/08/07	15:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/07	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/07	15:36:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/07	15:36:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/07	15:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/07	15:42:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/07	15:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/07	16:22:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/07	16:22:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/07	16:23:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/07	16:23:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/07	16:25:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/07	16:25:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/07	16:25:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/08/07	16:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/07	16:32:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/07	16:32:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/07	16:33:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/08/07	16:33:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/07	16:33:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/07	16:33:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/07	16:33:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/07	16:33:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/07	16:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/07	16:33:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/07	16:33:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/07	16:35:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2015/08/07	16:35:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/07	16:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/07	17:18:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/07	17:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/07	18:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/07	18:11:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/07	18:11:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/07	18:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/07	18:55:01.0	XRT_Custom_430_OG [0x1ae]							
2015/08/07	18:56:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/07	19:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/07	19:50:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/07	19:50:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/07	19:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/07	20:32:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/07	20:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/07	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/07	20:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/07	21:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/08/07	21:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/07	21:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/07	21:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]							

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2015/08/07	21:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/07	21:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	21:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/08/07	21:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/07	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	21:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	21:28:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	21:28:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	21:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	22:08:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	22:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/07	23:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/07	23:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/07	23:07:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/07	23:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/07	23:37:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/07	23:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	00:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	00:45:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	00:45:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	00:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	01:10:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/08	01:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	02:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	02:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	02:22:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	02:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	02:49:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/08	02:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	03:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	03:51:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	03:51:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	03:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	04:27:30.5	XRT_Custom_430_OG [0x1ae]				
2015/08/08	04:28:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	05:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	05:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	05:31:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	05:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	06:03:26.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78 00
2015/08/08	06:07:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/08	06:07:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/08/08	06:07:46.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/08/08	06:10:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/08/08	06:10:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/08	06:10:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/08/08	06:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	06:17:24.0	XRT_CTRL_MANU_402_OG [0x192]				

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2015/08/08	06:17:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2015/08/08	06:17:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2015/08/08	06:17:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/08	06:17:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/08	06:17:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/08	06:17:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/08	06:17:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	06:20:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/08/08	06:20:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/08	06:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	07:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	07:11:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	07:11:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	07:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	07:44:00.5	XRT_Custom_430_OG [0x1ae]				
2015/08/08	07:45:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	08:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	08:51:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	08:51:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	08:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	09:22:30.5	XRT_Custom_430_OG [0x1ae]				
2015/08/08	09:23:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	10:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	10:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	10:33:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	10:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	11:01:00.5	XRT_Custom_430_OG [0x1ae]				
2015/08/08	11:02:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	12:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	12:14:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	12:14:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	12:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	12:39:00.5	XRT_Custom_430_OG [0x1ae]				
2015/08/08	12:40:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	13:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	13:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	13:52:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	13:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	14:17:30.5	XRT_Custom_430_OG [0x1ae]				
2015/08/08	14:18:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	14:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	14:20:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	14:20:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/08	14:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/08	14:28:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/08	14:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/08	15:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/08	15:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/08	15:31:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2015/08/08	15:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/08	16:17:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/08	16:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/08	16:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/08	16:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/08	16:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/08	16:50:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/08	16:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/08	16:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/08	16:52:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2015/08/08	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/08	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/08	16:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/08	17:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/08/08	17:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/08	17:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/08	17:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/08	17:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/08	17:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/08	17:02:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2015/08/08	17:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/08	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/08	17:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/08	17:09:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/08	17:09:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/08	17:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/08	17:53:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/08	17:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/08	18:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/08	18:47:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/08	18:47:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/08	18:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/08	19:30:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/08	19:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/08	20:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/08	20:26:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/08	20:26:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/08	20:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/08	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/08	20:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/08	20:59:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/08	21:00:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 ad 59 00 00				
2015/08/08	21:00:18.0	XRT_FLD_DIS_440_OG [0x1b8]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/08	21:00:20.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/08	21:00:22.0	XRT_ARS_DIS_439_OG [0x1b7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/08	21:14:58.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2015/08/08	21:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/08	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2015/08/08	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/08	22:59:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/08/08	23:00:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 00 00 54 00	
2015/08/08	23:00:18.0	XRT_FLD_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/08/08	23:00:20.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/08/08	23:00:22.0	XRT_ARS_DIS_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/08	23:14:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2015/08/08	23:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/09	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/09	00:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/08/09	01:00:00.5	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	
2015/08/09	01:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/09	01:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/09	01:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/09	01:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/09	01:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/09	01:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2015/08/09	01:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/08/09	01:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/09	01:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/09	01:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/09	01:21:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/09	01:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/09	01:46:00.0	XRT_Custom_430_OG [0x1ae]					
2015/08/09	01:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/09	02:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/09	02:57:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/09	02:57:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/09	03:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/09	03:24:30.0	XRT_Custom_430_OG [0x1ae]					
2015/08/09	03:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/09	04:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/09	04:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/09	04:27:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/09	04:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/09	05:03:00.5	XRT_Custom_430_OG [0x1ae]					
2015/08/09	05:04:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/09	05:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/09	05:39:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/08/09	05:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/08/09	05:40:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/08/09	05:42:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/08/09	05:42:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/09	05:42:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2015/08/09	05:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/09	05:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/09	05:49:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/08/09	05:50:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	

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2015/08/09	05:50:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/09	05:50:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/09	05:50:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/09	05:50:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/09	05:50:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/09	05:52:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/08/09	05:52:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/09	05:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/09	06:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/09	06:07:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/09	06:07:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/09	06:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/09	06:41:30.5	XRT_Custom_430_OG [0x1ae]							
2015/08/09	06:42:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/09	07:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/09	07:48:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/09	07:48:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/09	07:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/09	08:19:30.5	XRT_Custom_430_OG [0x1ae]							
2015/08/09	08:20:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/09	09:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/09	09:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/09	09:27:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/09	09:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/09	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/09	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/09	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2015/08/09	10:00:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	b2 a8 01 58			
2015/08/09	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/09	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/09	10:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/09	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/09	10:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/09	10:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2015/08/09	10:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/09	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/09	11:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/09	11:11:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/09	11:11:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/09	11:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/09	11:36:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/09	11:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/09	12:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/09	12:50:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/09	12:50:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/09	12:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/09	13:15:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/09	13:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/09	14:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							

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2015/08/09	14:28:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	14:28:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/09	14:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/09	15:05:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/09	15:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/09	16:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	16:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/09	16:07:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/09	16:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/09	16:52:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/09	16:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	17:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	17:34:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/08/09	17:35:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/08/09	17:35:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/08/09	17:37:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/08/09	17:37:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/08/09	17:37:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2015/08/09	17:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/09	17:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	17:44:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/08/09	17:45:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00		
2015/08/09	17:45:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	17:45:02.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/09	17:45:04.5	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/09	17:45:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/08/09	17:45:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/08/09	17:45:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/08/09	17:45:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/08/09	17:45:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/09	17:47:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2015/08/09	17:47:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2015/08/09	17:48:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/09	18:29:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/09	18:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	19:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	19:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/09	19:23:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/09	19:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/09	20:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/09	20:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	21:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	21:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/09	21:02:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/09	21:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/09	21:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/09	21:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	22:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/09	22:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		



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2015/08/09	22:40:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/09	22:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/09	23:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/09	23:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	00:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	00:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	00:19:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/10	00:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/10	00:43:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	00:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	01:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	01:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	01:57:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/10	02:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/10	02:22:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	02:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	03:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	03:30:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	03:30:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/10	03:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/10	04:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	04:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	05:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	05:03:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	05:03:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/10	05:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/10	05:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	05:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/10	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/10	06:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/10	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/10	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/10	06:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/08/10	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/10	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00				
2015/08/10	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/10	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/10	06:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/10	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/10	06:10:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	06:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2015/08/10	06:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/10	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	06:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	06:44:02.0	XRT_FLD_RESET_415_OG [0x19f]								

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2015/08/10	06:44:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/10	06:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/10	07:17:00.0	XRT_Custom_430_OG [0x1ae]								
2015/08/10	07:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	08:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	08:24:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	08:24:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/10	08:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/10	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	08:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/10	08:30:00.0	AOCS_OrE-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	02	02	a7	01	58
2015/08/10	08:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/10	08:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/10	08:30:20.5	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/10	08:30:22.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/10	08:30:24.5	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	08:32:56.5	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/08/10	08:32:58.5	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/10	08:55:30.0	XRT_Custom_430_OG [0x1ae]								
2015/08/10	08:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/10	10:00:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	54	72	01	58
2015/08/10	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/10	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/10	10:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/10	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/10	10:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	10:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2015/08/10	10:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/10	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	10:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	10:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	10:04:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/10	10:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/10	10:33:30.0	XRT_Custom_430_OG [0x1ae]								
2015/08/10	10:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	11:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	11:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	11:47:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/10	11:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/10	12:12:00.0	XRT_Custom_430_OG [0x1ae]								
2015/08/10	12:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/10	13:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/10	13:26:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/10	13:26:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/10	13:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2015/08/10	13:50:30.0	XRT_Custom_430_OG [0x1ae]								
2015/08/10	13:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/10	15:04:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/10	15:04:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/10	15:04:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/08/10	15:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/08/10	15:50:30.0	XRT_Custom_430_OG [0x1ae]								
2015/08/10	15:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/10	16:42:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/10	16:42:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/10	16:42:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/08/10	16:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/08/10	17:27:30.0	XRT_Custom_430_OG [0x1ae]								
2015/08/10	17:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/10	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/10	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2015/08/10	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2015/08/10	18:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2015/08/10	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2015/08/10	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/08/10	18:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e					
2015/08/10	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/10	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/10	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2015/08/10	18:10:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]								
		AOCU_NM	5	02-76	00 f3 c7 b0 5b					
2015/08/10	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2015/08/10	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2015/08/10	18:10:20.0	XRT_AEC_RESET_441_OG [0x1b9]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2015/08/10	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/08/10	18:10:24.0	XRT_FLD_RESET_443_OG [0x1bb]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/10	18:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06					
2015/08/10	18:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03					
2015/08/10	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/10	18:21:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/10	18:21:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/10	18:21:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/08/10	18:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/08/10	19:04:00.0	XRT_Custom_430_OG [0x1ae]								
2015/08/10	19:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/10	19:59:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/10	19:59:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/10	19:59:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/08/10	20:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/08/10	20:41:30.0	XRT_Custom_430_OG [0x1ae]								
2015/08/10	20:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/10	21:38:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/10	21:38:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/10	21:38:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/08/10	21:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					

2015/08/10	22:17:30.0	XRT_Custom_430_OG [0x1ae]			
2015/08/10	22:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/10	23:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/10	23:16:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/10	23:16:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/10	23:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/10	23:44:30.0	XRT_Custom_430_OG [0x1ae]			
2015/08/10	23:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	00:54:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	00:54:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/11	00:54:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/11	00:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/11	01:19:00.0	XRT_Custom_430_OG [0x1ae]			
2015/08/11	01:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	02:31:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	02:31:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/11	02:31:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/11	02:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/11	02:57:30.0	XRT_Custom_430_OG [0x1ae]			
2015/08/11	02:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	04:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	04:00:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/11	04:00:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/11	04:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/11	04:35:30.0	XRT_Custom_430_OG [0x1ae]			
2015/08/11	04:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	04:39:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	04:39:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/11	04:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/08/11	04:40:16.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/08/11	04:42:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/08/11	04:42:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/11	04:42:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/08/11	04:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	05:40:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	05:40:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/11	05:40:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/11	05:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/11	06:14:00.0	XRT_Custom_430_OG [0x1ae]			
2015/08/11	06:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	07:20:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	07:20:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/11	07:20:34.0	XRT_PREFLR_STRT_444_OG [0x1bc]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/11	07:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/11	07:52:30.0	XRT_Custom_430_OG [0x1ae]			
2015/08/11	07:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	08:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/11	08:30:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76	02 02 a7 01 58
2015/08/11	08:30:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8

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2015/08/11	08:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/11	08:30:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/11	08:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/11	08:30:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/11	08:32:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/08/11	08:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/11	08:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/11	09:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/11	09:00:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/11	09:00:04.0	XRT_PREFLR_STRT_444_OG [0x1bc]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/11	09:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/11	09:31:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/11	09:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/11	10:07:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/11	10:08:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00