

XRT Timeline to be uploaded on 2015/08/11

Period: 2015/08/11 09:58:00 - 2015/08/15 10:10:00

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Normal mode

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XOB #1AC1: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with dark(16s) and

Term	Pointing (x, y)	Comment
08/11 10:11:00 - 08/11 10:42:30	Track (590.5, -351.5) @ 08/11 10:08:00	# OP start + 10min HOP287 AR12396
08/11 16:26:06 - 08/11 16:59:54	Track (576.8, -394.3) @ 08/11 11:00:00	# HOP292(11:30-15:00) and Track AR12396
08/11 18:05:30 - 08/12 05:48:54	Track (621.1, -389.9) @ 08/11 18:02:30	# Track AR12396
08/12 06:02:00 - 08/12 07:57:00	Track (689.7, -381.7) @ 08/12 05:59:00	# Cont,
08/12 08:33:00 - 08/12 11:21:00	Track (691.4, -392.5) @ 08/12 08:30:00	# HOP287 at AR12396
08/12 15:15:30 - 08/12 17:44:24	Track (735.6, -374.9) @ 08/12 15:00:00	# Track AR12396
08/12 18:38:30 - 08/12 20:51:24	Track (749.2, -372.6) @ 08/12 17:54:30	# Cont,
08/13 07:26:00 - 08/13 08:33:00	Track (802.9, -361.8) @ 08/13 06:51:30	# Track AR12396
08/13 09:04:30 - 08/13 10:14:00	Track (799.2, -373.3) @ 08/13 08:30:00	# HOP287 at AR12396

PROG= 10 Inf.-time(s)

Subr= 2	1-time(s)	2.0sec													
Seqn= 56 1-time(s) 2.0sec			Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
			Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 4-time(s) 2.0sec			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
			Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62 8-time(s) 240.0sec			thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

XOB #1AC0: HOP292_AR-A(Filter-Ratio), 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms/3ms leak), 80s cad

Term	Pointing (x, y)	Comment
08/11 11:10:00 - 08/11 15:40:00	Track (576.8, -394.3) @ 08/11 11:00:00	# HOP292(11:30-15:00) and Track AR12396
08/12 11:45:30 - 08/12 14:37:30	Track (-3.0, 125.8) @ 08/12 11:30:00	# HOP292 at AR12396

PROG= 02 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec													
Seqn= 87 1-time(s) 2.0sec			Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
			Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 20-time(s) 60.0sec			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	354ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-

Term	Pointing (x, y)	Comment
08/11 17:03:00 - 08/11 17:12:24	Fixed (0.0, 0.0)	synoptic, shifted manually and XRT stray light measurement
08/12 05:52:00 - 08/12 05:58:54	Fixed (0.0, 0.0)	synoptic, shifted -11.0 min
08/12 17:47:30 - 08/12 17:54:24	Fixed (0.0, 0.0)	synoptic, shifted -15.5 min
08/13 06:42:00 - 08/13 06:51:24	Fixed (0.0, 0.0)	# SOT Flat fields and synoptic

PROG= 14 1-time(s)

Subr= 1	1-time(s)	12.0sec													
Seqn= 33 1-time(s) 4.0sec			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 61 1-time(s) 4.0sec			Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49 1-time(s) 2.0sec			Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 39 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ABF: Stray light study 2015-20 ;C-poly, 2x2 full FOV(1min-cad) and 2x2 256 on AR(10sec-cad)

Term	Pointing (x, y)	Comment
08/11 17:15:30 - 08/11 18:02:24	Fixed (0.0, 0.0)	synoptic, shifted manually and XRT stray light measurement
PROG= 19 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 67 1-time(s) 150.0sec		
C-poly/Open	C-poly/Open	close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
C-poly/Open	C-poly/Open	close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 21-time(s) 2.0sec		
Seqn= 27 1-time(s) 10.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Norm 250ms Obs 2x2 512x512 (1024, 1200) Q=95 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 2x2 512x512 (1024, 1200) Q=95 0 0 2.0sec
Subr= 3 9-time(s) 2.0sec		
Seqn= 99 1-time(s) 2.0sec		
C-poly/Open	C-poly/Open	close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 27 5-time(s) 10.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Norm 250ms Obs 2x2 512x512 (1024, 1200) Q=95 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 2x2 512x512 (1024, 1200) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A9C: G-Band Alignment with North Pole Q90 2x2(G-band and VLS=CLS) - 1msec - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
08/12 21:06:30 - 08/12 22:51:24	Fixed (0.0, 930.0)	# Co-alignment at N-pole
PROG= 09 1-time(s)		
Subr= 1 24-time(s) 300.0sec		
Seqn= 98 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 1ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Seqn= 63 1-time(s) 2.0sec		
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A9D: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec- 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
08/12 23:06:30 - 08/13 00:51:24	Fixed (-945.0, 0.0)	# Co-alignment at E-limb
PROG= 12 1-time(s)		
Subr= 1 15-time(s) 480.0sec		
Seqn= 19 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 1ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Seqn= 43 1-time(s) 2.0sec		
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1ABC: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Ti-poly(16/362/2x1024), Al-poly+Ti-poly

Term	Pointing (x, y)	Comment
08/13 00:54:30 - 08/13 01:02:00	Fixed (0.0, 0.0)	# SOT Flat fields and synoptic
PROG= 16 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
Seqn= 33 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 49 1-time(s) 4.0sec		
Al-poly/Open	Al-poly/Open	close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 77 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open	close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 54 1-time(s) 2.0sec		

Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)	12.0sec									
Seqn= 46		2-time(s)	2.0sec									
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 61		1-time(s)	2.0sec									
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 72		2-time(s)	4.0sec									
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 59		2-time(s)	2.0sec									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A76: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,8ms) - Leak (33ms) - 120s cad												
Term	Pointing (x, y)		Comment									
08/13 01:05:06 - 08/13 06:38:54	Fixed (0.0, 0.0)		# SOT Flat fields and synoptic									
PROG= 04		Inf.-time(s)										
Subr= 1		30-time(s)	120.0sec									
Seqn= 29		1-time(s)	4.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 6		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512												
Term	Pointing (x, y)		Comment									
08/11 10:11:00 - 08/11 10:42:30	Track (590.5, -351.5) ^{Ⓢ 08/11 10:08:00}		# OP start + 10min HOP287 AR12396									
08/11 11:10:00 - 08/11 15:40:00	Track (576.8, -394.3) ^{Ⓢ 08/11 11:00:00}		# HOP292(11:30-15:00) and Track AR12396									
08/11 16:26:06 - 08/11 16:59:54	Track (576.8, -394.3) ^{Ⓢ 08/11 11:00:00}		# HOP292(11:30-15:00) and Track AR12396									
08/11 18:05:30 - 08/12 05:48:54	Track (621.1, -389.9) ^{Ⓢ 08/11 18:02:30}		# Track AR12396									
08/12 06:02:00 - 08/12 07:57:00	Track (689.7, -381.7) ^{Ⓢ 08/12 05:59:00}		# Cont,									
08/12 08:33:00 - 08/12 11:21:00	Track (691.4, -392.5) ^{Ⓢ 08/12 08:30:00}		# HOP287 at AR12396									
08/12 11:45:30 - 08/12 14:37:30	Track (-3.0, 125.8) ^{Ⓢ 08/12 11:30:00}		# HOP292 at AR12398									
08/12 15:15:30 - 08/12 17:44:24	Track (735.6, -374.9) ^{Ⓢ 08/12 15:00:00}		# Track AR12396									
08/12 18:38:30 - 08/12 20:51:24	Track (749.2, -372.6) ^{Ⓢ 08/12 17:54:30}		# Cont,									
08/13 01:05:06 - 08/13 06:38:54	Fixed (0.0, 0.0)		# SOT Flat fields and synoptic									
08/13 07:26:00 - 08/13 08:33:00	Track (802.9, -361.8) ^{Ⓢ 08/13 06:51:30}		# Track AR12396									
08/13 09:04:30 - 08/13 10:14:00	Track (799.2, -373.3) ^{Ⓢ 08/13 08:30:00}		# HOP287 at AR12396									
PROG= 03		30-time(s)										
Subr= 1		20-time(s)	2.0sec									
Seqn= 11		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100		1-time(s)	10.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 10		1-time(s)	2.0sec									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 53		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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Flare Detection

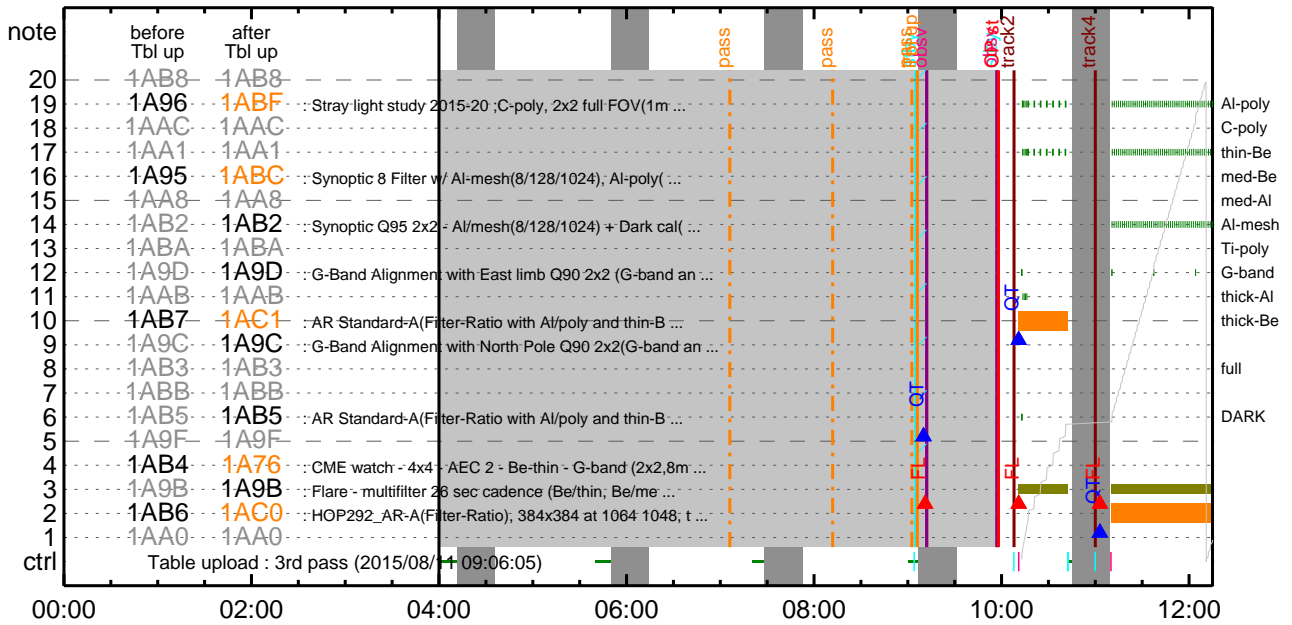
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FLD Patrol												
Term	Pointing (x, y)		Comment									
08/11 18:02:46 - 08/12 05:49:16	Track (621.1, -389.9) ^{Ⓢ 08/11 18:02:30}		# Track AR12396									
08/12 05:59:16 - 08/12 17:44:46	Track (689.7, -381.7) ^{Ⓢ 08/12 05:59:00}		# Cont,									

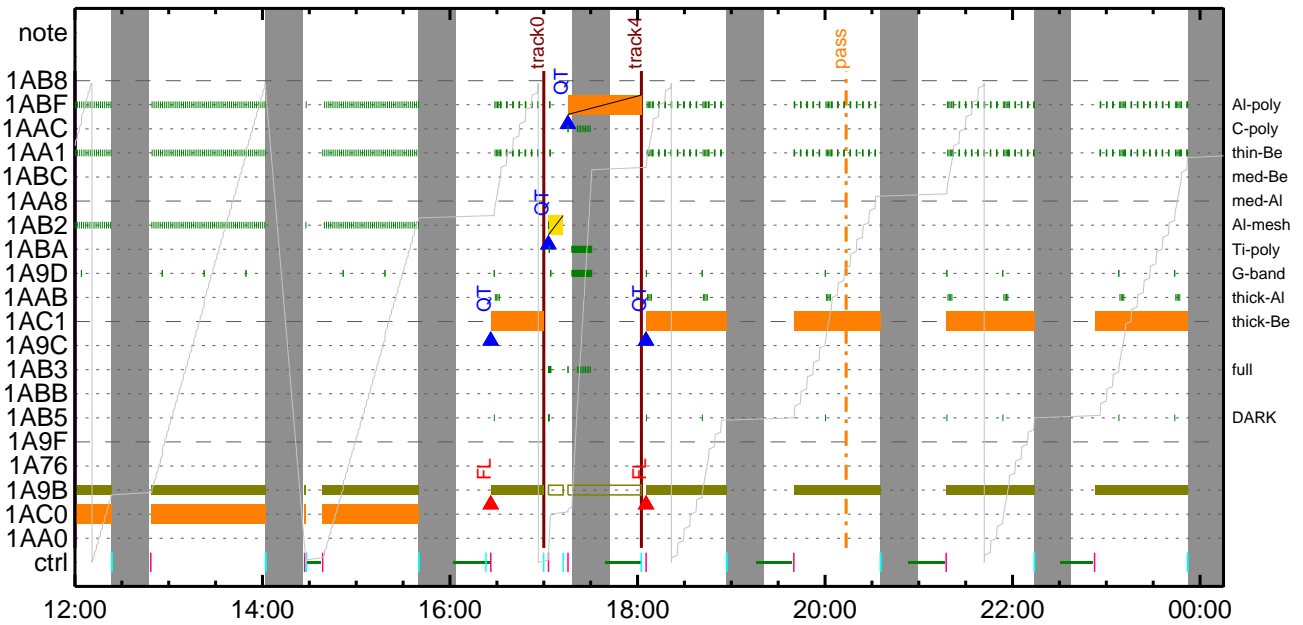
08/12 17:54:46 - 08/12 20:51:48 Track (749.2, -372.6) @ 08/12 17:54:30 # Cont,
 08/13 01:02:22 - 08/13 06:39:16 Fixed (0.0, 0.0) # SOT Flat fields and synoptic
 08/13 06:51:46 - 08/15 10:10:00 Track (802.9, -361.8) @ 08/13 06:51:30 # Track AR12396

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

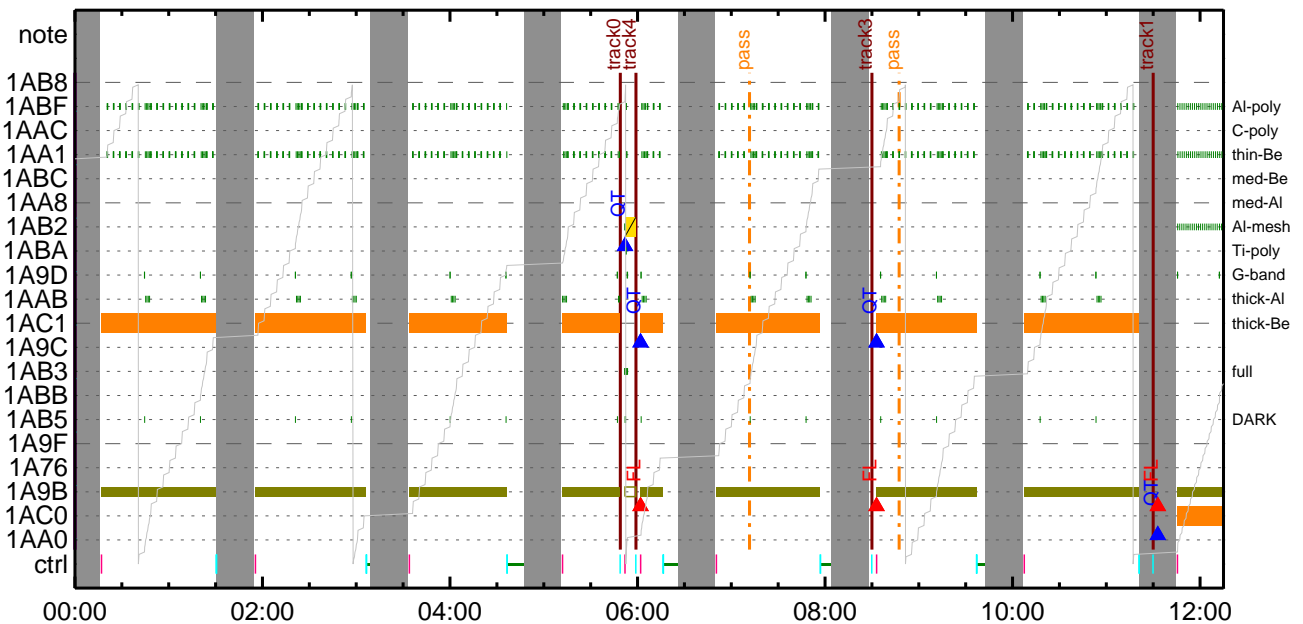
CMDI #0322 2015/08/11



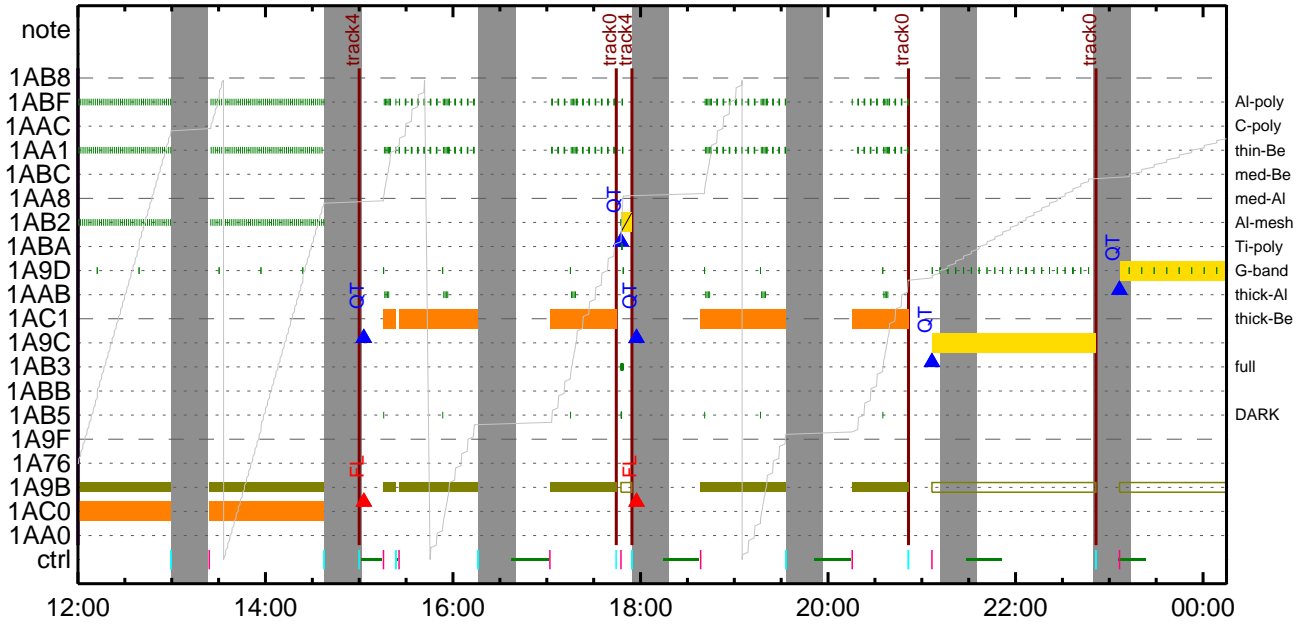
CMDI #0322 2015/08/11



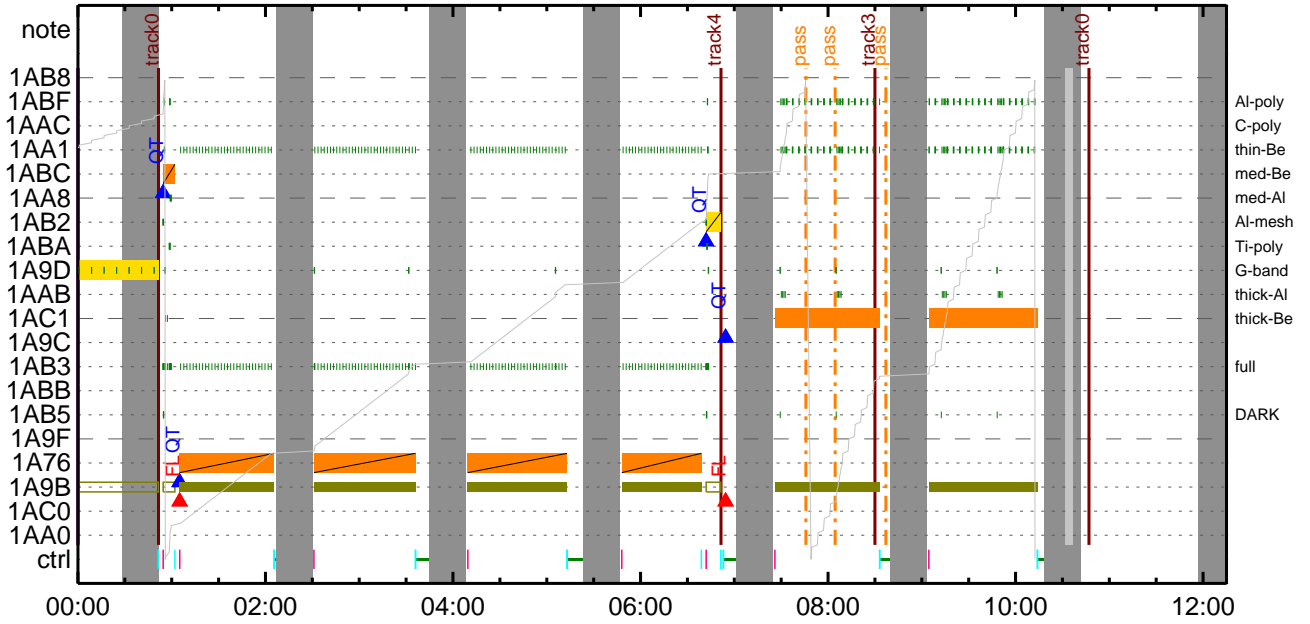
CMDI #0322 2015/08/12



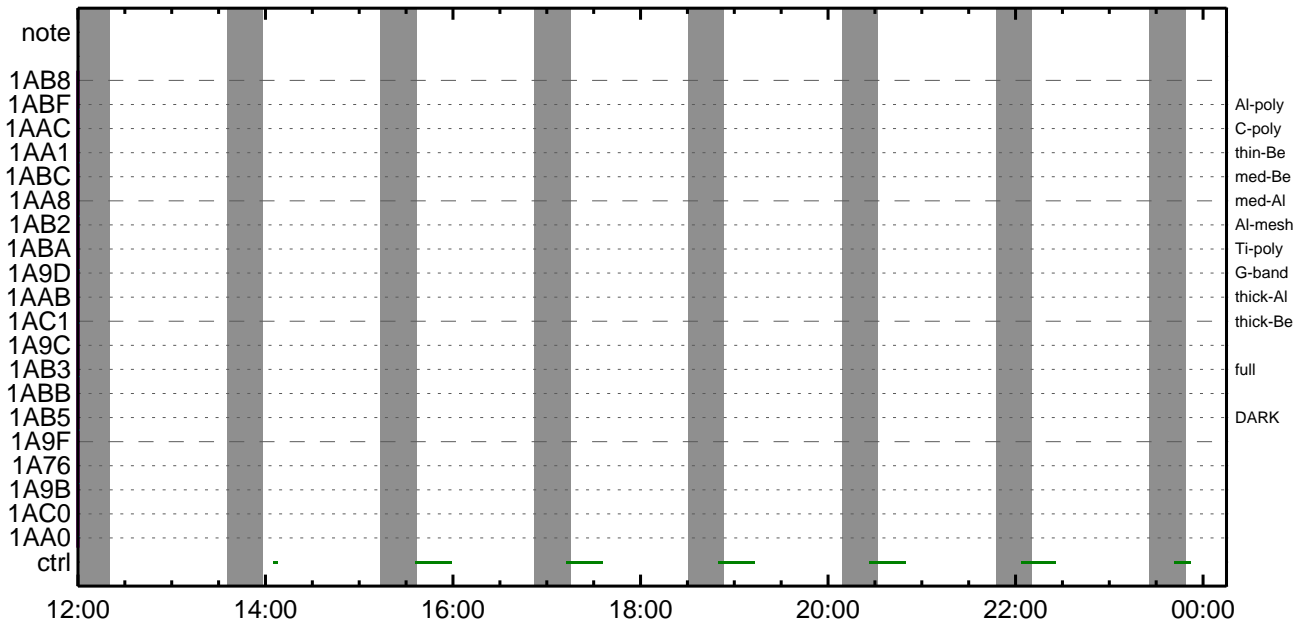
CMDI #0322 2015/08/12



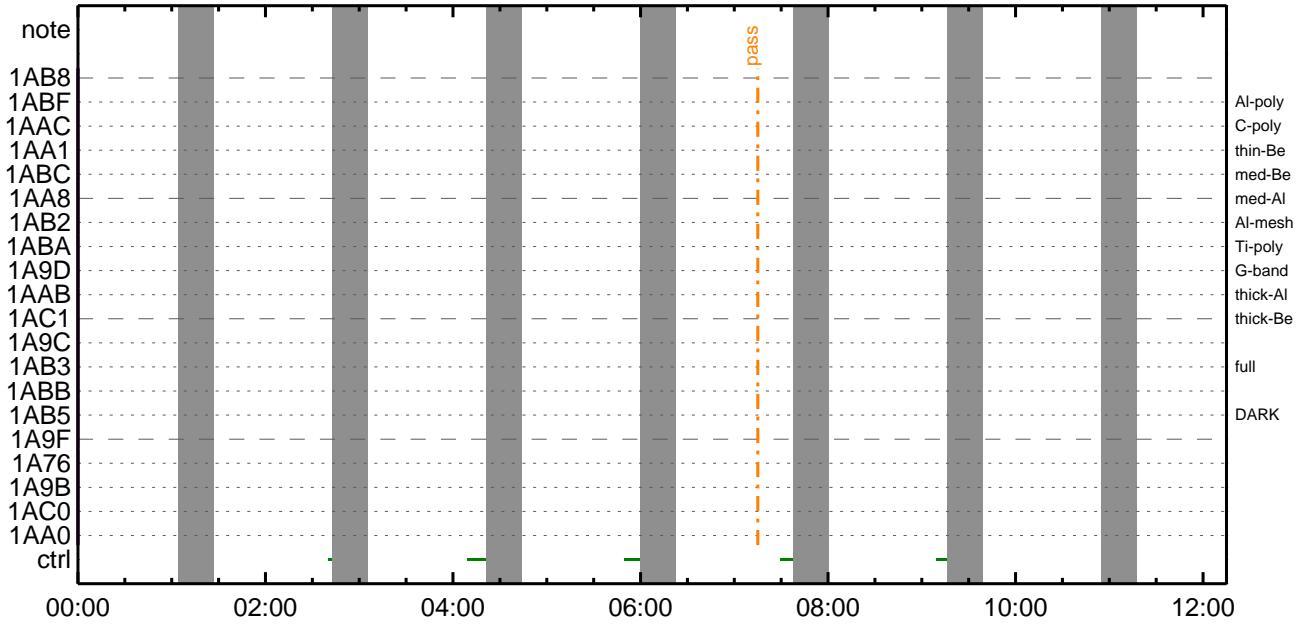
CMDI #0322 2015/08/13



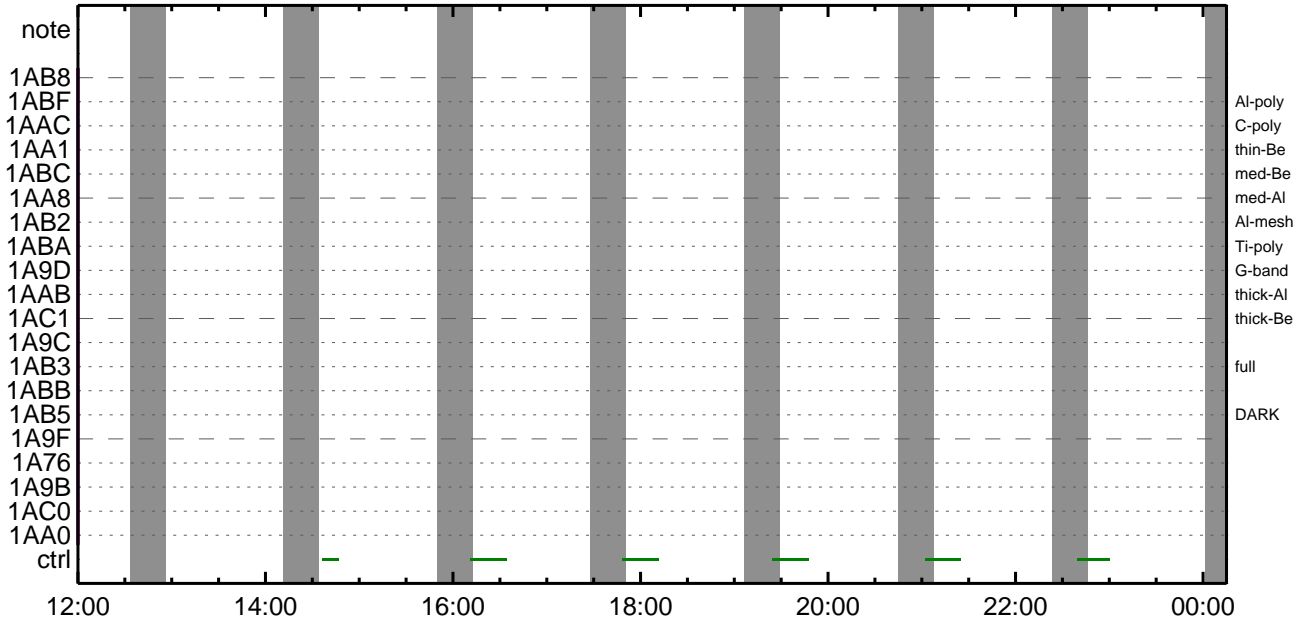
CMDI #0322 2015/08/13



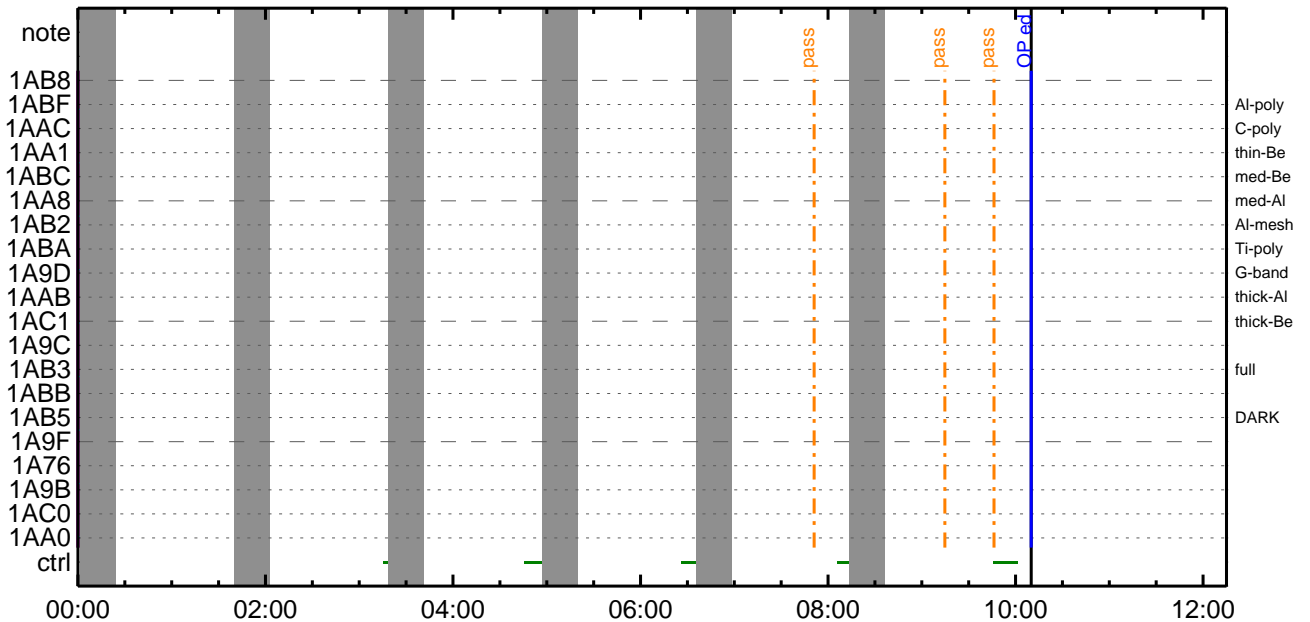
CMDI #0322 2015/08/14



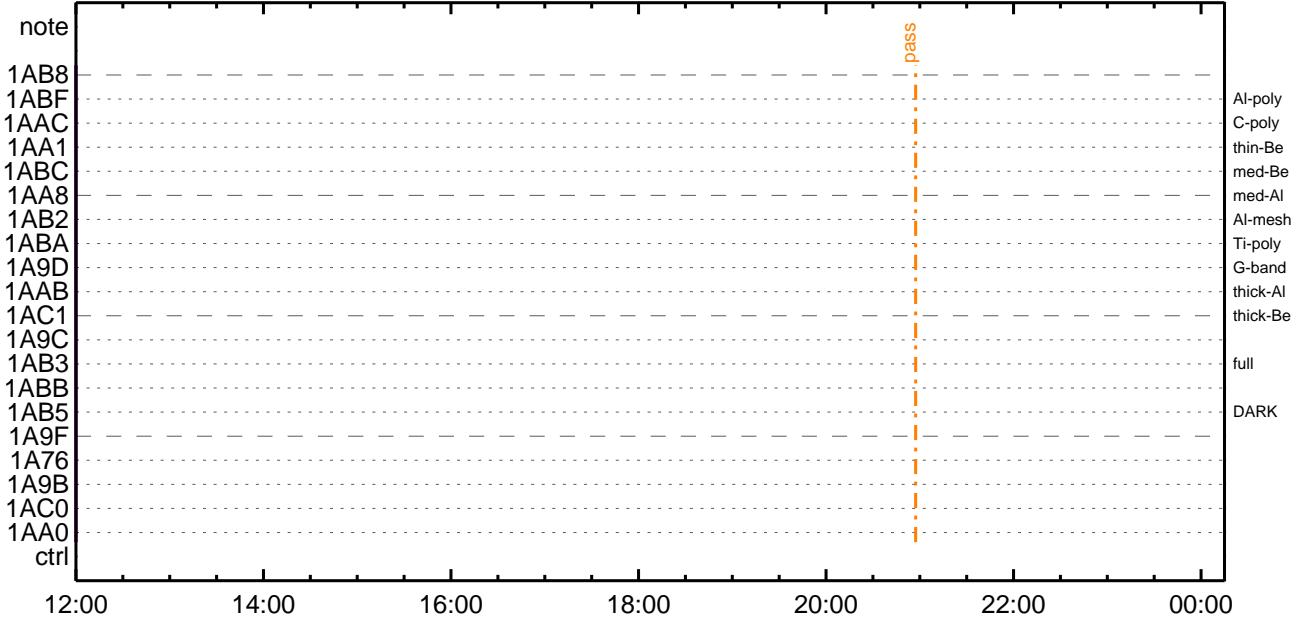
CMDI #0322 2015/08/14



CMDI #0322 2015/08/15



CMDI #0322 2015/08/15




```

0096 C.                0300; SET EDUMP I±°iYNY¹aÇ¹Ôa|³³E;E
0097 C.
0098 C. TIY³YFÿÖYÉ0dÁDÍ¿(UT)
0099 +. TI 2015-08-11 09:53:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2015-08-11 09:53:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2015-08-11 09:53:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2015-08-11 09:57:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.  °E²¼0İÄê%îÍÑ0İYÁY§YÁY-¹àİÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]            EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]            EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]            EQ        0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÁYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]            EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]            EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]            EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]            EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]              EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]              EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]            EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]            EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]              EQ        EXEC
0140 C.
0141 C.  YÁYÖY×½ªİ»0d³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]              EQ        NON
0143 C.
0144 C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y½,¥ì;¼YÈ;È0dİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]              EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]            EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]            EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2015-08-11 09:57:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2015-08-11 09:57:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2015-08-11 09:57:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                [ ] [HK1_TI_CMD_NUM]            EQ        2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2015-08-11 09:57:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM]            EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP `üÄİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (%â°İYÖYÁYÈY½YÉY¼YÇYè0È¼00¼Ä»Ü0¹0é)
0193 C. DC-BC dcbc-402:DCBC

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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


*** OP Sequence for XRT ***

```

2015/08/11 10:07:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 10:07:56.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2015/08/11 10:08:00.0 AOCS_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 02 00 00 00 00
2015/08/11 10:08:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/08/11 10:08:18.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2015/08/11 10:08:20.0 XRT_AEC_RESET_441_OG [0x1b9]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2015/08/11 10:08:22.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2015/08/11 10:08:24.0 XRT_FLD_RESET_443_OG [0x1bb]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/08/11 10:10:56.0 XRT_QT_PROG_SET_448_OG [0x1c0]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2015/08/11 10:10:58.0 XRT_FL_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 03
2015/08/11 10:11:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/08/11 10:42:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 10:42:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 10:42:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/08/11 10:42:36.0 XRT_PREFLR_STRT_425_OG [0x1a9]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/08/11 10:45:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/08/11 10:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 10:59:56.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2015/08/11 11:00:00.0 AOCS_Ore-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 04 00 00 00 00
2015/08/11 11:00:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2015/08/11 11:00:18.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2015/08/11 11:00:20.0 XRT_AEC_RESET_441_OG [0x1b9]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2015/08/11 11:00:22.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2015/08/11 11:00:24.0 XRT_FLD_RESET_443_OG [0x1bb]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/08/11 11:02:56.0 XRT_QT_PROG_SET_413_OG [0x19d]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 02
2015/08/11 11:02:58.0 XRT_FL_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 03
2015/08/11 11:09:00.0 XRT_Custom_430_OG [0x1ae]
2015/08/11 11:10:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/08/11 12:23:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 12:23:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 12:23:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/08/11 12:23:36.0 XRT_PREFLR_STRT_425_OG [0x1a9]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/08/11 12:26:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/08/11 12:47:30.0 XRT_Custom_430_OG [0x1ae]
2015/08/11 12:48:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/08/11 14:02:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 14:02:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 14:02:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/08/11 14:02:06.0 XRT_PREFLR_STRT_425_OG [0x1a9]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/08/11 14:05:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2015/08/11 14:26:00.0 XRT_Custom_430_OG [0x1ae]
2015/08/11 14:27:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2015/08/11 14:28:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 14:28:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2015/08/11 14:28:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2015/08/11 14:28:06.0 XRT_PREFLR_STRT_425_OG [0x1a9]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2015/08/11 14:31:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9

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Aug 11, 15 12:09

XRT_OGLIST_0322.chk

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2015/08/11	14:37:30.0	XRT_Custom_430_OG [0x1ae]			
2015/08/11	14:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	15:40:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	15:40:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	15:40:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/11	15:40:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/11	15:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/11	16:23:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	16:23:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/11	16:23:22.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/11	16:23:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/11	16:23:26.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/11	16:23:28.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/11	16:23:30.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/11	16:26:02.0	XRT_QT_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/08/11	16:26:04.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/11	16:26:06.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	16:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/11	17:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/08/11	17:00:16.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/08/11	17:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/08/11	17:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/11	17:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/08/11	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	17:12:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	17:12:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/11	17:12:46.0	XRT_FLD_DIS_418_OG [0x1a2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/08/11	17:15:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/08/11	17:15:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/11	17:15:28.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2015/08/11	17:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	18:02:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	18:02:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/11	18:02:30.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/08/11	18:02:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/11	18:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/11	18:02:50.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/11	18:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/11	18:02:54.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/11	18:05:26.0	XRT_QT_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/08/11	18:05:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/11	18:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/11	18:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	18:57:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/11	18:57:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/11	18:57:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2015/08/11	19:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/11	19:39:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/11	19:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/11	20:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/11	20:35:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/11	20:35:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/11	20:35:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/11	20:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/11	21:16:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/11	21:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/11	22:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/11	22:14:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/11	22:14:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/11	22:14:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/11	22:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/11	22:51:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/11	22:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/11	23:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/11	23:52:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/11	23:52:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/11	23:52:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/11	23:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/12	00:16:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/12	00:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/12	01:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/12	01:30:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/12	01:30:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/12	01:30:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/12	01:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/12	01:54:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/12	01:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/12	03:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/12	03:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/12	03:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/12	03:06:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/12	03:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/12	03:33:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/12	03:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/12	04:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/12	04:36:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/12	04:36:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/12	04:36:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/12	04:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/12	05:11:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/12	05:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/12	05:48:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/12	05:48:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/12	05:49:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/12	05:49:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/12	05:51:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				

2015/08/12	05:51:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/12	05:51:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2015/08/12	05:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	05:58:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	05:58:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/08/12	05:59:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	04	00	00	00
2015/08/12	05:59:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/08/12	05:59:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/08/12	05:59:20.0	XRT_AEC_RESET_441_OG [0x1b9]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/08/12	05:59:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/12	05:59:24.0	XRT_FLD_RESET_443_OG [0x1bb]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/12	06:01:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2015/08/12	06:01:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/08/12	06:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	06:16:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	06:16:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	06:16:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/12	06:16:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/12	06:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/12	06:49:30.0	XRT_Custom_430_OG [0x1ae]						
2015/08/12	06:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	07:57:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	07:57:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	07:57:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/12	07:57:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/12	08:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/12	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	08:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/08/12	08:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	03	00	00	00
2015/08/12	08:30:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/08/12	08:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/08/12	08:30:20.0	XRT_AEC_RESET_441_OG [0x1b9]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/08/12	08:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/12	08:30:24.0	XRT_FLD_RESET_443_OG [0x1bb]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/12	08:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2015/08/12	08:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/08/12	08:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	09:37:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	09:37:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	09:37:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/12	09:37:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/12	09:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/12	10:06:30.0	XRT_Custom_430_OG [0x1ae]						
2015/08/12	10:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	11:21:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	11:21:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	11:21:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/12	11:21:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]						

2015/08/12	11:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/12	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	11:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/08/12	11:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/08/12	11:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/12	11:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/12	11:30:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/12	11:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/12	11:30:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/12	11:32:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2015/08/12	11:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/08/12	11:44:30.0	XRT_Custom_430_OG [0x1ae]					
2015/08/12	11:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/12	12:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	12:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	12:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/12	12:59:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/12	13:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/12	13:23:00.0	XRT_Custom_430_OG [0x1ae]					
2015/08/12	13:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/12	14:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	14:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	14:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/12	14:37:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/12	14:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/12	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	14:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/08/12	15:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04 00 00 00 00	
2015/08/12	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/12	15:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/12	15:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/12	15:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/12	15:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/12	15:02:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2015/08/12	15:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/08/12	15:14:30.0	XRT_Custom_430_OG [0x1ae]					
2015/08/12	15:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/12	15:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	15:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	15:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/12	15:23:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/12	15:24:30.0	XRT_Custom_430_OG [0x1ae]					
2015/08/12	15:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/12	15:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/12	16:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	16:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/12	16:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/12	16:16:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2015/08/12	16:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/12	17:01:00.0	XRT_Custom_430_OG [0x1ae]						
2015/08/12	17:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	17:44:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	17:44:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2015/08/12	17:44:30.0	AOCS_OrE-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2015/08/12	17:44:46.0	XRT_FLD_DIS_418_OG [0x1a2]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/08/12	17:47:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/08/12	17:47:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/12	17:47:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2015/08/12	17:47:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	17:54:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/08/12	17:54:30.0	AOCS_OrE-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	04 00 00 00 00			
2015/08/12	17:54:30.5	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	17:54:32.5	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	17:54:34.5	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/12	17:54:36.5	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/12	17:54:46.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/08/12	17:54:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/08/12	17:54:50.0	XRT_AEC_RESET_441_OG [0x1b9]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/08/12	17:54:52.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/12	17:54:54.0	XRT_FLD_RESET_443_OG [0x1bb]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/12	17:57:26.0	XRT_QT_PROG_SET_448_OG [0x1c0]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2015/08/12	17:57:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2015/08/12	17:57:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/12	18:37:30.5	XRT_Custom_430_OG [0x1ae]						
2015/08/12	18:38:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	19:33:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	19:33:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	19:33:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/12	19:33:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/12	19:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/12	20:14:30.0	XRT_Custom_430_OG [0x1ae]						
2015/08/12	20:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	20:51:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	20:51:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	20:51:28.0	XRT_FOCUS_POSITION_437_OG [0x1b5]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/08/12	20:51:30.0	AOCS_OrE-point_Start_6_OG [0x09c]						
		AOCU_NM	5	02-76	00 ad 59 00 00			
2015/08/12	20:51:48.0	XRT_FLD_DIS_440_OG [0x1b8]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/08/12	20:51:50.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/08/12	20:51:52.0	XRT_ARS_DIS_439_OG [0x1b7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/12	21:06:28.0	XRT_QT_PROG_SET_446_OG [0x1be]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2015/08/12	21:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/12	22:51:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	22:51:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/12	22:51:28.0	XRT_FOCUS_POSITION_437_OG [0x1b5]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/08/12	22:51:30.0	AOCS_OrE-point_Start_7_OG [0x09d]						
		AOCU_NM	5	02-76	00 00 00 54 00			

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2015/08/12	22:51:48.0	XRT_FLD_DIS_440_OG [0x1b8]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/12	22:51:50.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/12	22:51:52.0	XRT_ARS_DIS_439_OG [0x1b7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/12	23:06:28.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2015/08/12	23:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	00:51:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	00:51:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/13	00:51:30.0	AOCs_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/08/13	00:51:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/13	00:54:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/13	00:54:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/13	00:54:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2015/08/13	00:54:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	01:02:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	01:02:02.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/13	01:02:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/13	01:02:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/13	01:02:26.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/13	01:02:28.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/13	01:02:30.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	01:05:02.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2015/08/13	01:05:04.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/13	01:05:06.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	02:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	02:05:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	02:05:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	02:05:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	02:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	02:30:00.5	XRT_Custom_430_OG [0x1ae]							
2015/08/13	02:31:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	03:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	03:36:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	03:36:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	03:36:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	03:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	04:08:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/13	04:09:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	05:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	05:13:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	05:13:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	05:13:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	05:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	05:47:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/13	05:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	06:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	06:38:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/13	06:39:16.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/13	06:41:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				

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2015/08/13	06:41:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/13	06:41:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/08/13	06:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	06:51:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	06:51:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/13	06:51:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/08/13	06:51:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/13	06:51:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/13	06:51:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/13	06:51:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/13	06:51:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	06:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	06:53:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	06:53:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	06:53:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	06:54:26.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2015/08/13	06:54:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/13	06:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	07:25:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/13	07:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	08:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/08/13	08:33:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	08:33:03.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	08:33:05.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	08:33:07.5	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	08:36:15.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	09:03:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/13	09:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	10:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	10:14:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	10:14:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	10:14:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	10:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	10:47:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				