

# XRT Timeline to be uploaded on 2015/08/13

Period: 2015/08/13 10:37:00 - 2015/08/18 10:19:00

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Normal mode

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## XOB #1ABE: HOP292\_AR-A(Filter-Ratio), 256x256 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms/3ms leak), 60s cad

Term	Pointing (x, y)	Comment
08/13 10:50:00 - 08/13 14:59:54	Track ( -285.5, 157.2) @ 08/13 10:47:00	# OP start + 10min   HOP292 at AR12400
08/14 11:18:30 - 08/14 15:00:54	Track ( -78.0, 151.6) @ 08/14 11:00:00	# HOP292 at AR12400
<b>PROG= 20 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 50 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 256x256 (1064, 1048) DPCM 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 256x256 (1064, 1048) DPCM 0 0 2.0sec	
└─ Subr= 2 30-time(s) 60.0sec		
└─ Seqn= 73 1-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 125ms Obs 1x1 256x256 (1064, 1048) Q=95 1 0 2.0sec	
└─ Al-poly/Open	thin-Be/Open close Safe Norm 177ms Obs 1x1 256x256 (1064, 1048) Q=95 1 0 2.0sec	
└─ thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 256x256 (1064, 1048) Q=95 1 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1AC1: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with dark(16s) and

Term	Pointing (x, y)	Comment
08/13 15:03:00 - 08/13 17:53:24	Track ( 830.5, -354.6) @ 08/13 15:00:00	# Track AR12396
08/13 18:06:30 - 08/13 22:59:54	Track ( 839.5, -351.9) @ 08/13 18:03:30	# Cont,
08/13 23:03:00 - 08/14 04:09:00	Fixed ( 852.7, -347.3)	# IRIS-EIS co-obs at AR12396
08/14 04:45:00 - 08/14 06:25:24	Fixed ( 862.0, -343.1)	# AR12396 obs by fixed
08/14 06:38:30 - 08/14 08:29:54	Fixed ( 862.0, -343.1)	# Cont,
08/14 08:33:00 - 08/14 10:54:30	Fixed ( 862.0, -343.1)	# HOP287 at AR12396
08/15 06:11:00 - 08/15 08:06:00	Track ( 89.6, 151.1) @ 08/15 06:08:00	# Track AR12400
08/15 08:37:00 - 08/15 09:46:00	Track ( 110.2, 151.3) @ 08/15 08:30:00	# HOP287 at AR12400
<b>PROG= 10 Inf.-time(s)</b>		
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 56 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
└─ Seqn= 42 4-time(s) 2.0sec		
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
└─ thin-Be/Open	med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec	
└─ Open/thick-Al	Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
└─ Seqn= 62 8-time(s) 240.0sec		
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec	
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec	
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec	
└─ Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-

Term	Pointing (x, y)	Comment
08/13 17:56:30 - 08/13 18:03:24	Fixed ( 0.0, 0.0)	synoptic, shifted -6.5 min
08/14 06:28:30 - 08/14 06:35:24	Fixed ( 0.0, 0.0)	synoptic, shifted 25.5 min
08/14 15:04:00 - 08/14 15:49:30	Fixed ( 0.0, 0.0)	# Synoptic, shifted manually
08/15 06:01:00 - 08/15 06:07:54	Fixed ( 0.0, 0.0)	synoptic, shifted -2.0 min
<b>PROG= 14 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 33 1-time(s) 4.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	
└─ Seqn= 61 1-time(s) 4.0sec		
└─ Open/Ti-poly	Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Open/Ti-poly	Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Open/Ti-poly	Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Open/Ti-poly	Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Seqn= 49 1-time(s) 2.0sec		
└─ Al-poly/Open	Al-poly/Open close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	

<b>Seqn= 39</b>		<b>1-time(s)</b>	<b>2.0sec</b>																	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
<b>Seqn= 54</b>		<b>1-time(s)</b>	<b>2.0sec</b>																	
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec						
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval						

<b>XOB #1AC2: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 120s cad</b>																				
Term		Pointing (x, y)				Comment														
08/14 19:52:00 - 08/14 20:11:54		Fixed ( -15.0, -475.0)				6/20														
08/14 20:15:00 - 08/14 20:45:00		Fixed ( -15.0, -375.0)				7/20														
08/14 21:29:00 - 08/14 21:48:54		Fixed ( -15.0, -275.0)				8/20														
08/14 21:52:00 - 08/14 22:23:30		Fixed ( -15.0, -175.0)				9/20														
08/14 23:04:00 - 08/14 23:23:54		Fixed ( -15.0, -75.0)				10/20														
08/14 23:40:00 - 08/15 00:01:30		Fixed ( -15.0, 15.0)				11/20 and synoptic														
08/15 00:28:30 - 08/15 00:48:24		Fixed ( -15.0, 115.0)				12/20														
08/15 00:51:30 - 08/15 01:11:24		Fixed ( -15.0, 215.0)				13/20														
08/15 01:14:30 - 08/15 01:40:00		Fixed ( -15.0, 315.0)				14/20														

<b>PROG= 07 Inf.-time(s)</b>																				
<b>Subr= 1</b>		<b>45-time(s)</b>	<b>120.0sec</b>																	
	<b>Seqn= 29</b>		<b>1-time(s)</b>	<b>4.0sec</b>																
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec						
	<b>Subr= 2</b>		<b>1-time(s)</b>	<b>2.0sec</b>																
	<b>Seqn= 26</b>		<b>1-time(s)</b>	<b>2.0sec</b>																
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec						
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval						

<b>XOB #1ABC: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Ti-poly(16/362/2x1024), Al-poly+Ti-poly</b>																				
Term		Pointing (x, y)				Comment														
08/14 23:27:00 - 08/14 23:36:54		Fixed ( -15.0, 15.0)				11/20 and synoptic														

<b>PROG= 16 1-time(s)</b>																				
<b>Subr= 1</b>		<b>1-time(s)</b>	<b>12.0sec</b>																	
	<b>Seqn= 33</b>		<b>1-time(s)</b>	<b>4.0sec</b>																
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	<b>Seqn= 5</b>		<b>1-time(s)</b>	<b>2.0sec</b>																
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec						
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec						
	<b>Seqn= 49</b>		<b>1-time(s)</b>	<b>4.0sec</b>																
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	<b>Seqn= 77</b>		<b>1-time(s)</b>	<b>2.0sec</b>																
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	<b>Seqn= 54</b>		<b>1-time(s)</b>	<b>2.0sec</b>																
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec						
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	<b>Subr= 2</b>		<b>1-time(s)</b>	<b>12.0sec</b>																
	<b>Seqn= 46</b>		<b>2-time(s)</b>	<b>2.0sec</b>																
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
	<b>Seqn= 61</b>		<b>1-time(s)</b>	<b>2.0sec</b>																
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	<b>Seqn= 72</b>		<b>2-time(s)</b>	<b>4.0sec</b>																
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
	<b>Seqn= 59</b>		<b>2-time(s)</b>	<b>2.0sec</b>																
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
	med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval						

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**Flare mode**

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<b>XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)</b>																				
Term		Pointing (x, y)				Comment														
08/13 10:50:00 - 08/13 14:59:54		Track ( -285.5, 157.2)				© 08/13 10:47:00 # OP start + 10min   HOP292 at AR12400														

08/13 15:03:00 - 08/13 17:53:24	Track ( 830.5, -354.6) @ 08/13 15:00:00	# Track AR12396
08/13 18:06:30 - 08/13 22:59:54	Track ( 839.5, -351.9) @ 08/13 18:03:30	# Cont,
08/13 23:03:00 - 08/14 04:09:00	Fixed ( 852.7, -347.3)	# IRIS-EIS co-obs at AR12396
08/14 04:45:00 - 08/14 06:25:24	Fixed ( 862.0, -343.1)	# AR12396 obs by fixed
08/14 06:38:30 - 08/14 08:29:54	Fixed ( 862.0, -343.1)	# Cont,
08/14 08:33:00 - 08/14 10:54:30	Fixed ( 862.0, -343.1)	# HOP287 at AR12396
08/14 11:18:30 - 08/14 15:00:54	Track ( -78.0, 151.6) @ 08/14 11:00:00	# HOP292 at AR12400
08/14 19:52:00 - 08/14 20:11:54	Fixed ( -15.0, -475.0)	6/20
08/14 20:15:00 - 08/14 20:45:00	Fixed ( -15.0, -375.0)	7/20
08/14 21:29:00 - 08/14 21:48:54	Fixed ( -15.0, -275.0)	8/20
08/14 21:52:00 - 08/14 22:23:30	Fixed ( -15.0, -175.0)	9/20
08/14 23:04:00 - 08/14 23:23:54	Fixed ( -15.0, -75.0)	10/20
08/14 23:40:00 - 08/15 00:01:30	Fixed ( -15.0, 15.0)	11/20 and synoptic
08/15 00:28:30 - 08/15 00:48:24	Fixed ( -15.0, 115.0)	12/20
08/15 00:51:30 - 08/15 01:11:24	Fixed ( -15.0, 215.0)	13/20
08/15 01:14:30 - 08/15 01:40:00	Fixed ( -15.0, 315.0)	14/20
08/15 06:11:00 - 08/15 08:06:00	Track ( 89.6, 151.1) @ 08/15 06:08:00	# Track AR12400
08/15 08:37:00 - 08/15 09:46:00	Track ( 110.2, 151.3) @ 08/15 08:30:00	# HOP287 at AR12400

**PROG= 03 30-time(s)**

<b>Subr= 1 20-time(s) 2.0sec</b>	
<b>Seqn= 11 1-time(s) 2.0sec</b>	Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn=100 1-time(s) 10.0sec</b>	thin-Be/Open med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
	med-Be/Open Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
	Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>	
<b>Seqn= 10 1-time(s) 2.0sec</b>	med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
	Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>	Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn= 53 1-time(s) 2.0sec</b>	Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
	Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
	Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

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NOT USED

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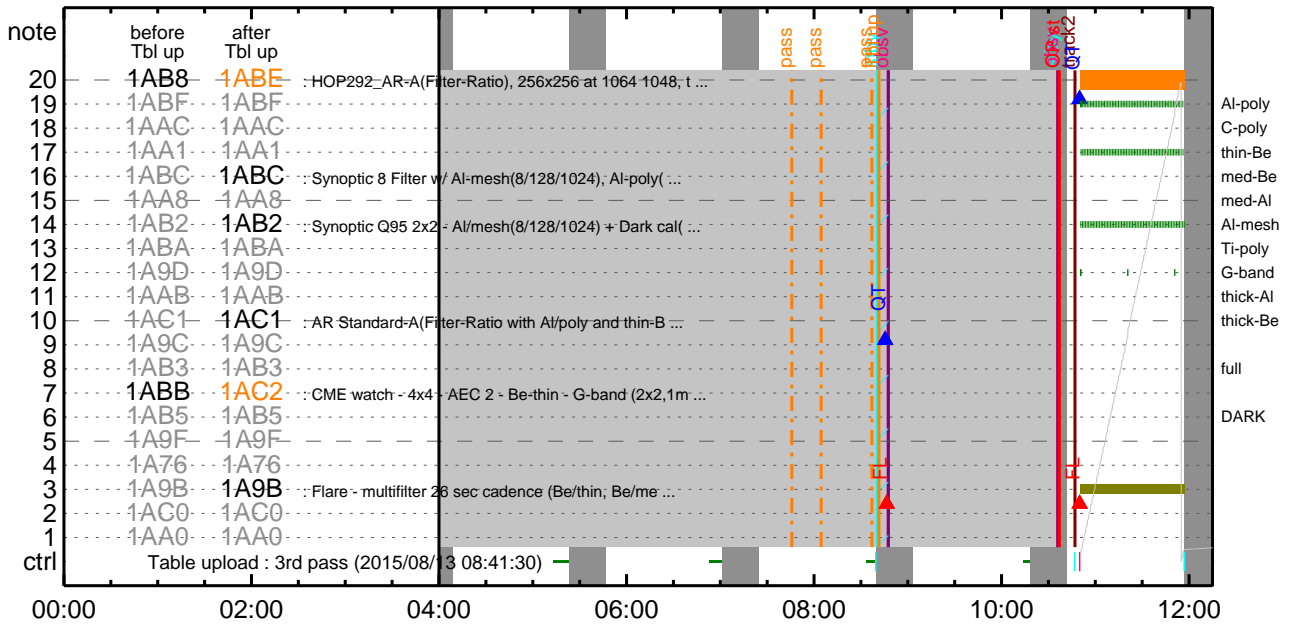
**Flare Detection**

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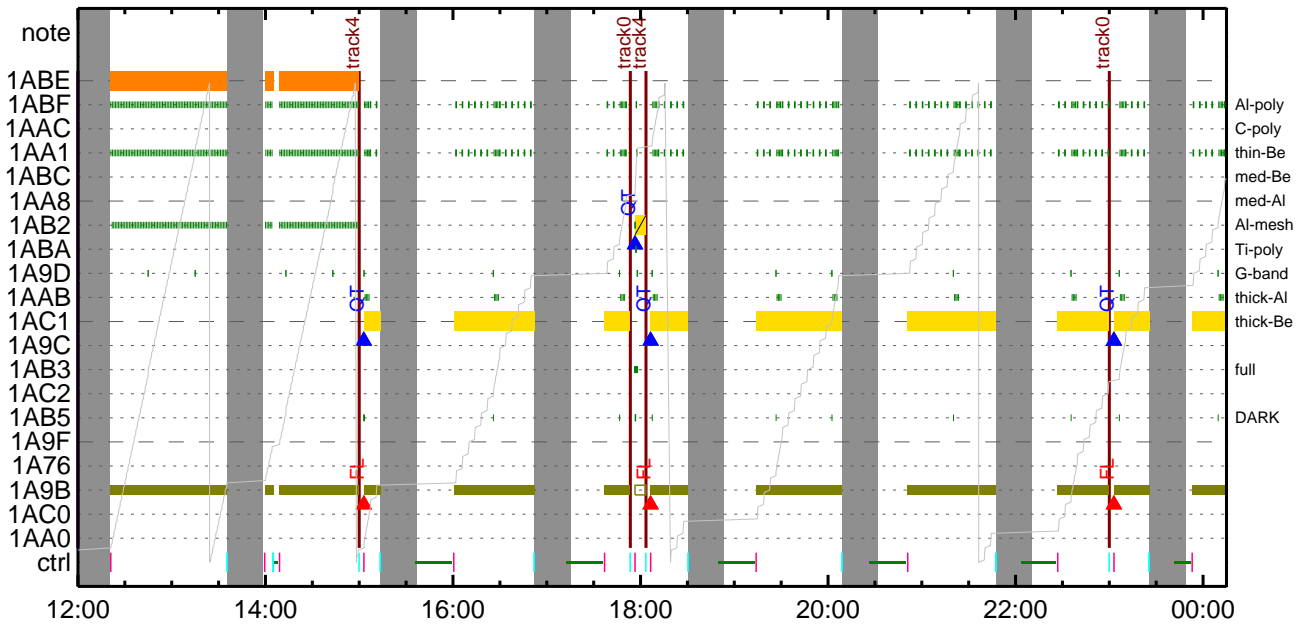
**FLD Patrol**

Term	Pointing (x, y)	Comment
08/13 18:03:46 - 08/14 06:25:46	Track ( 839.5, -351.9) @ 08/13 18:03:30	# Cont,
08/14 06:35:46 - 08/14 15:01:16	Fixed ( 862.0, -343.1)	# Cont,
08/14 19:49:16 - 08/14 23:24:16	Fixed ( -15.0, -475.0)	6/20
08/14 23:37:16 - 08/15 05:58:16	Fixed ( -15.0, 15.0)	11/20 and synoptic
08/15 06:08:16 - 08/18 10:19:00	Track ( 89.6, 151.1) @ 08/15 06:08:00	# Track AR12400
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 30sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

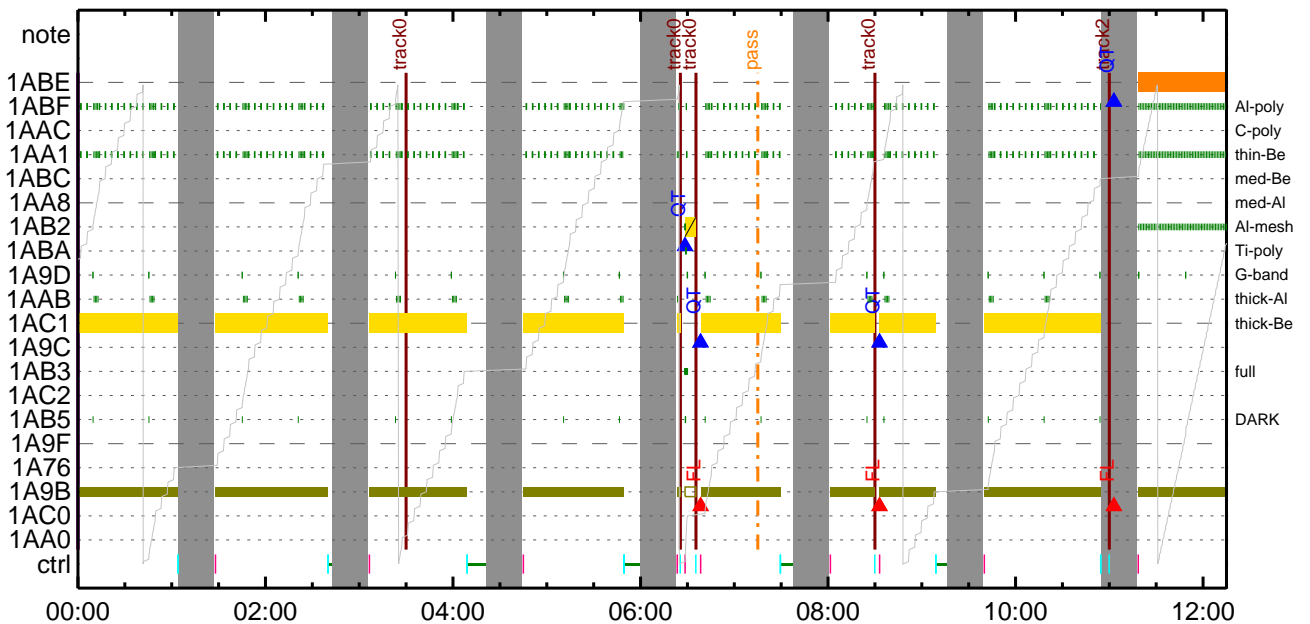
### CMDI #0327 2015/08/13



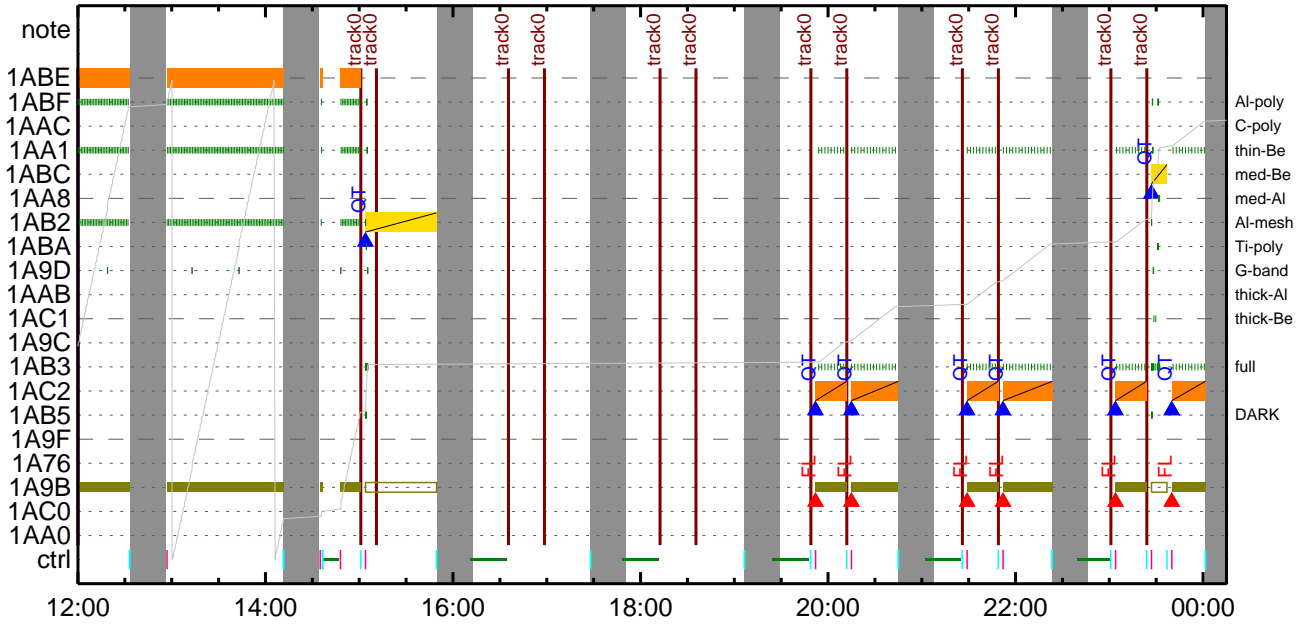
### CMDI #0327 2015/08/13



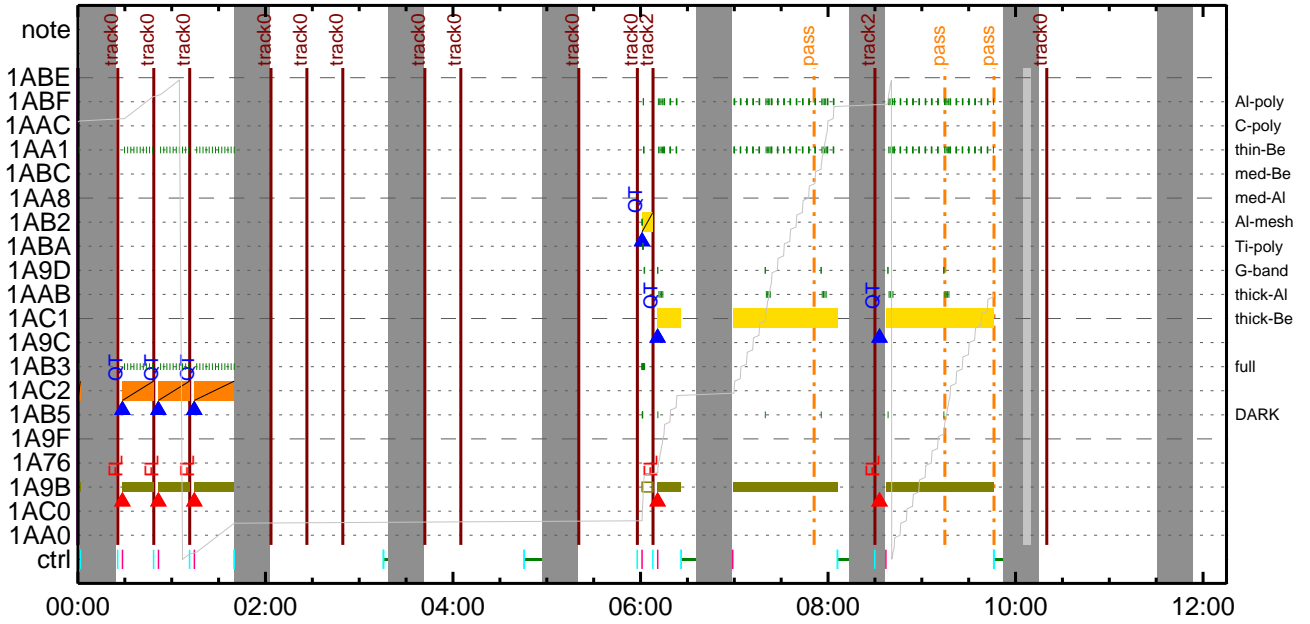
### CMDI #0327 2015/08/14



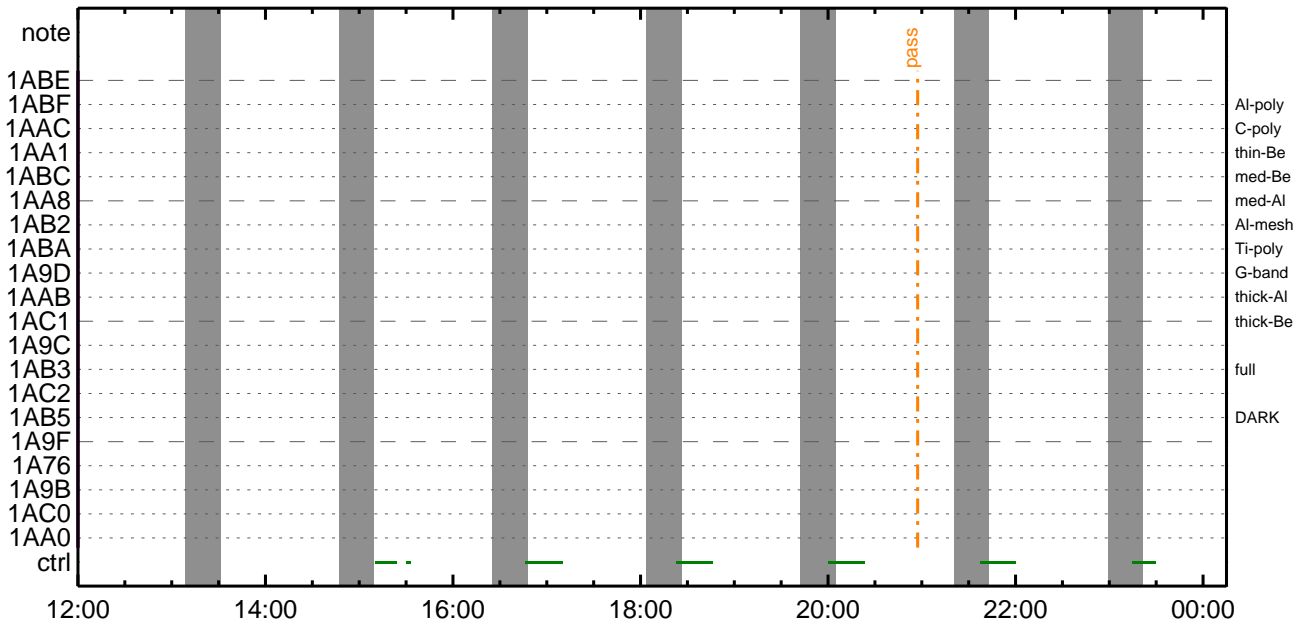
CMDI #0327 2015/08/14



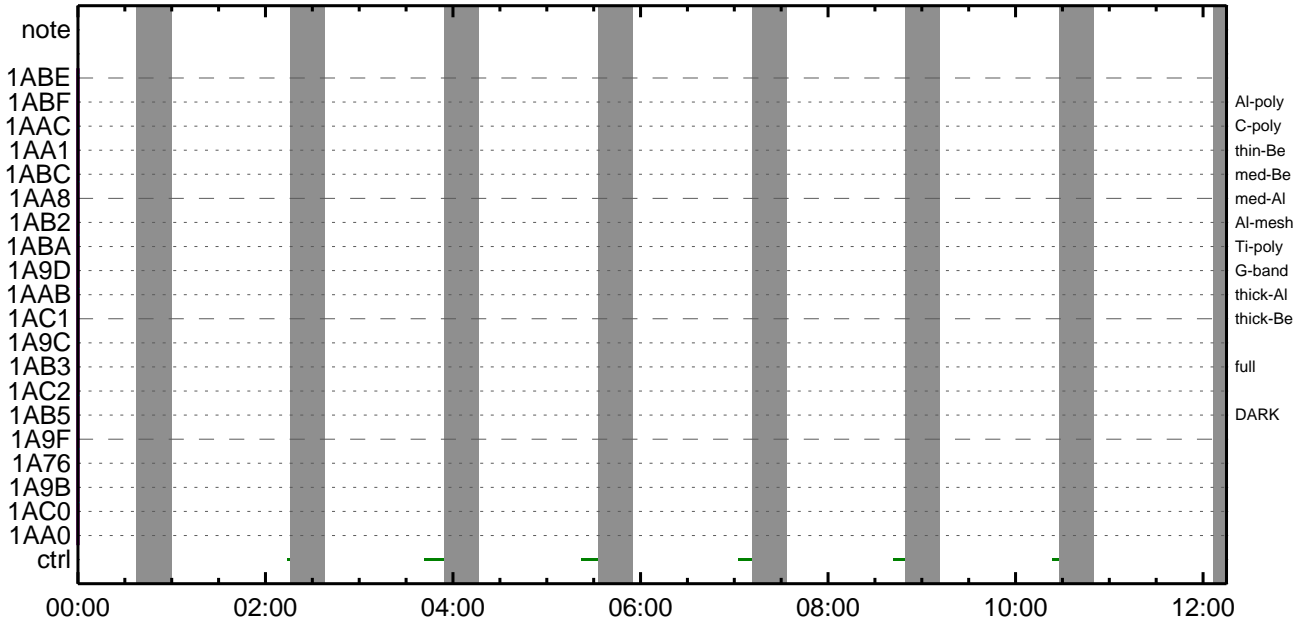
CMDI #0327 2015/08/15



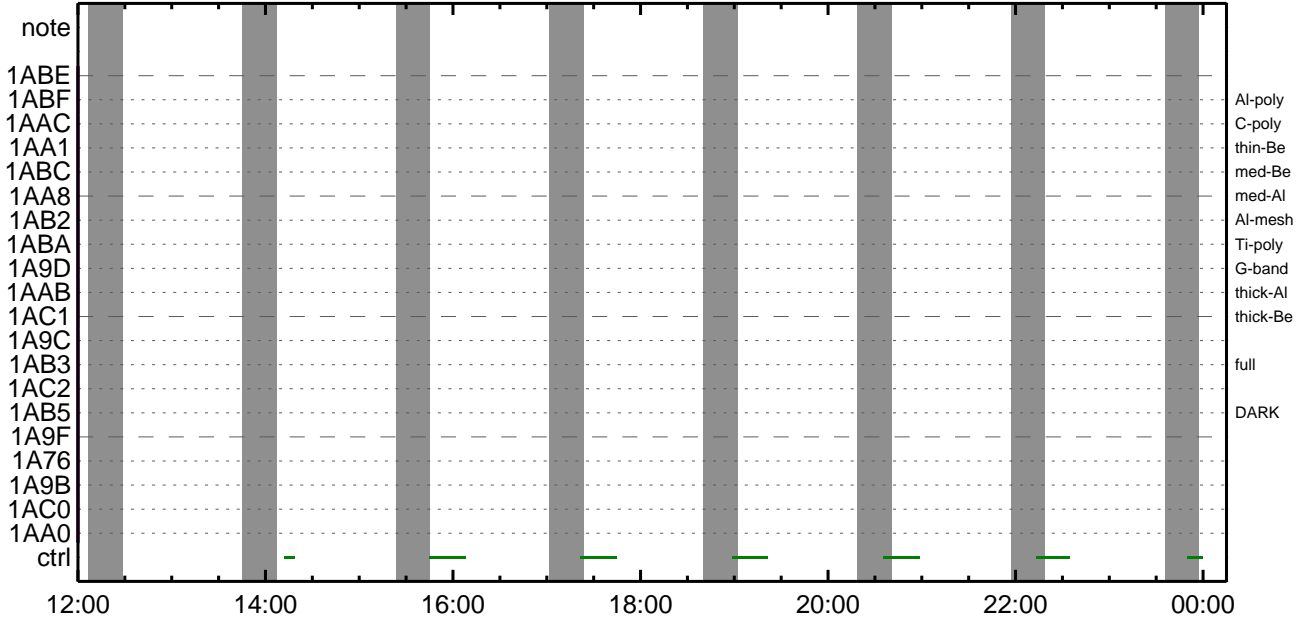
CMDI #0327 2015/08/15



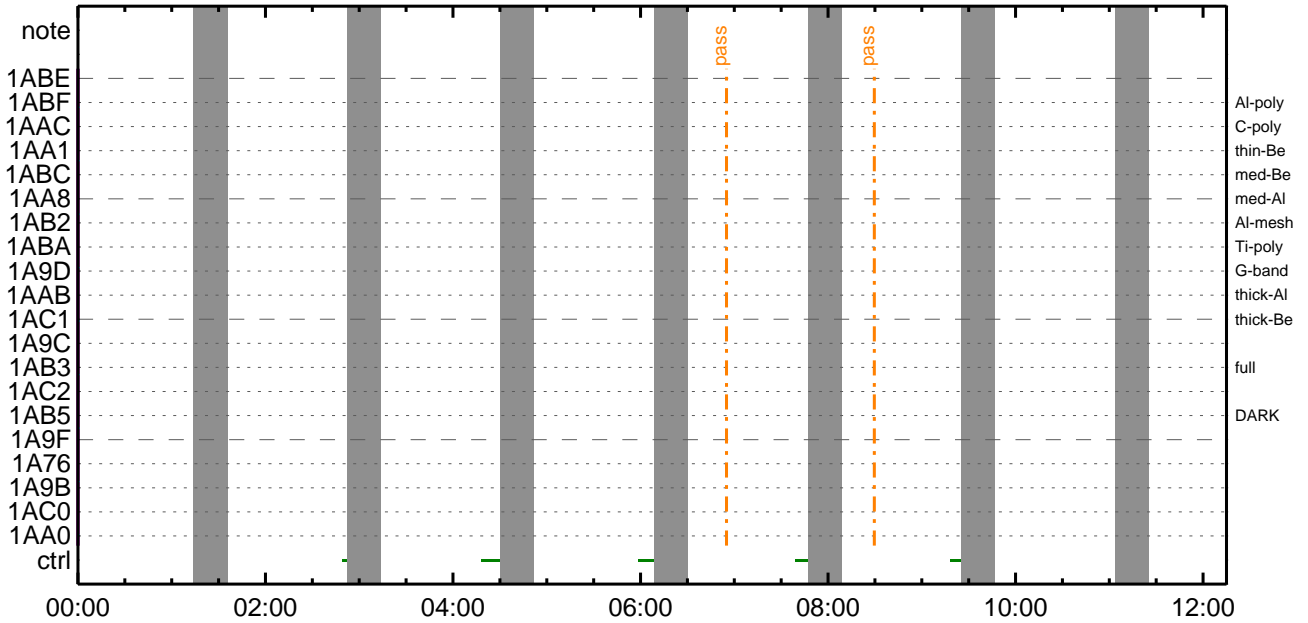
CMDI #0327 2015/08/16



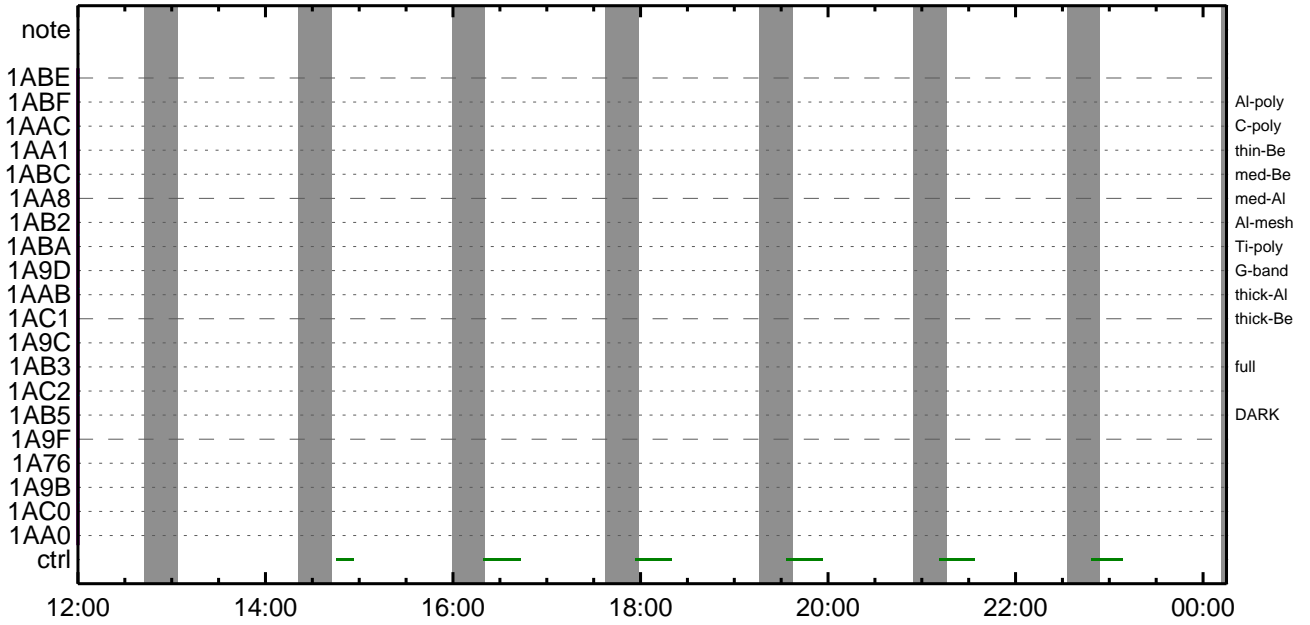
CMDI #0327 2015/08/16



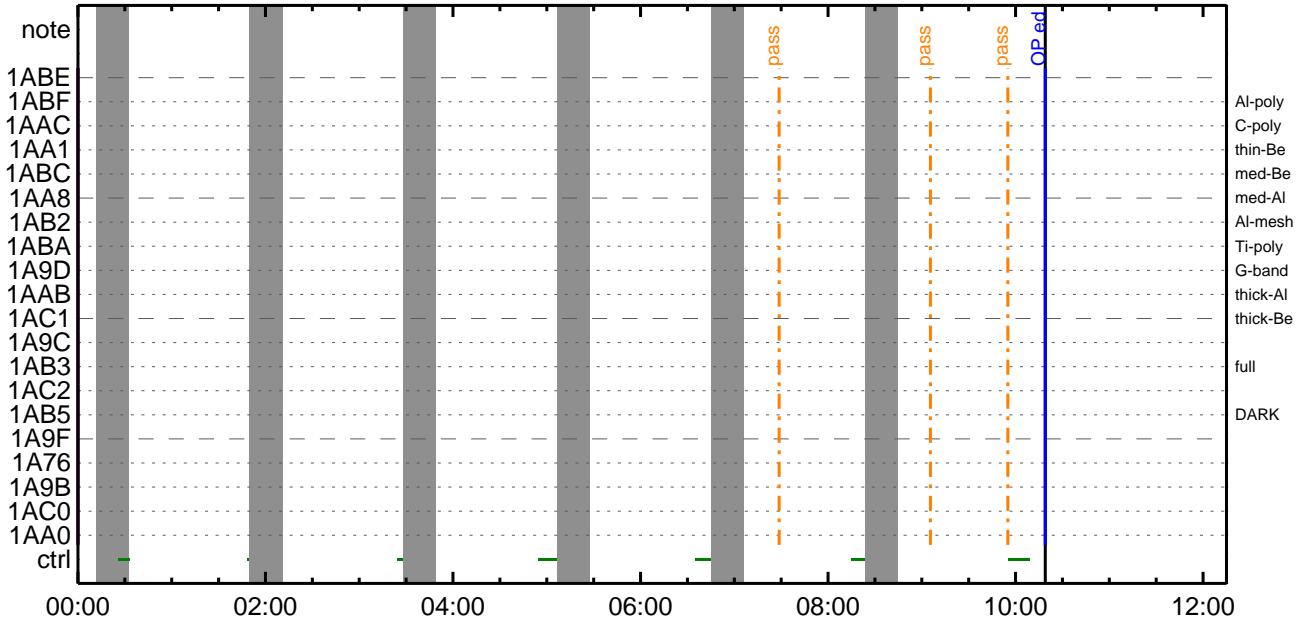
CMDI #0327 2015/08/17



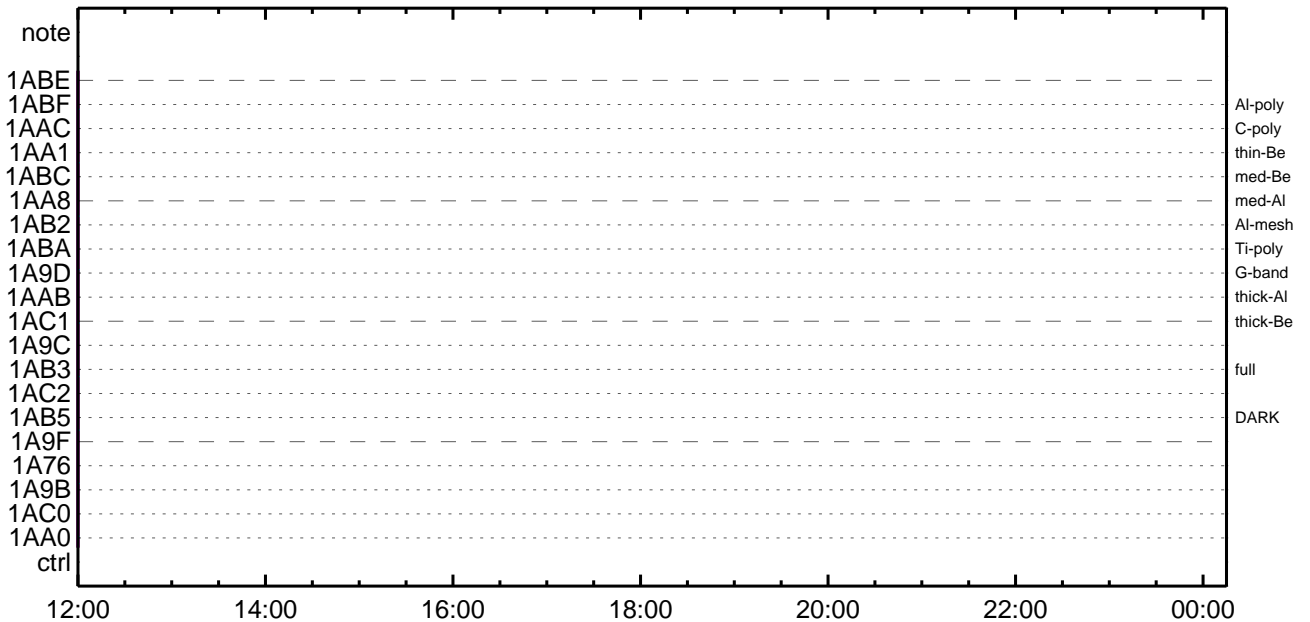
CMDI #0327 2015/08/17



CMDI #0327 2015/08/18



CMDI #0327 2015/08/18



(a) Spacecraft Operation Procedure (real-commands)

```

main-314 2015-08-13 12:39:01 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŠYÁY`¼Ä»Û;ã
0005 C.
0006 C. YÁYŞ;¼Y³YŞYÓYÉÁ;¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ê¿µÄµ•µ°¸»Í×ÁÇµÍYÇYÁY×YÍ;¼YÉ;ÊÊ%µ•ííÉ;ÊµÉ%ÇÖµ•µ¿¼l¹Çµí;¿À®, ùµ¹µËµµÇÁ+¿®µ•µÉµµµµµË; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ¸ °¸Ä, í×ÉYµãLOSPµÇµÍ»´Öµð¹íí, µ•; ¿ÉÖÍ×µÊXÁÖONµí¹ÖµÊµíµËµµµµµË; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.             ¿¿[HK1_XPA_ON/OFF]              EQ          ON
0025 C.             ¿¿[HK1_XPA_PWR_HI/LO]           EQ          HI
0026 C.             ¿¿[HK1_XMOD_ON/OFF]             EQ          ON
0027 C.             ¿¿[HK1_XMOD_QPSK/PM]            EQ          QPSK
0028 C.
0029 . C. XYDYÓYÉYíYÁY`¾ÖÄÖµ-°ÁÁêµ•µ¿µé; ¿°Ê²¼µí°¸Ä, ¼Ê%¿µð¼Ä¹Öµ¹µé; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°¸Ä,
0033 C. *****
0034 C. ¸" RESTART;ÊPT1;Êµ•µ¿µ¼¼l¹Çµí; ¿°Ê²¼µí°¸Ä¹Öµ»°; ¿DCBC-150µ¿¿Êµã; f
0035 C.
0036 . C. ;ãPT1°¸Ä, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.             ¿¿[HK1_REP_PT_1/2]              EQ          PT1    (¼Ä¹Ö, ;¼Ú)
0043 C.             ¿¿[HK1_REP_STA/STP]             EQ          START  (¼Ä¹Ö, ;¼Ú)
0044 C.             ¿¿[HK1_X_VC4_ON/OFF]            EQ          ON     (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYÉÁÜÁÖ;ÊÁ•Á°²óÈð;Ë, áµí°¸Ä, °¸³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.             ¿¿[HK1_REP_PT_1/2]              EQ          PT1    (¼Ä¹Ö, ;¼Ú)
0050 C.             ¿¿[HK1_REP_STA/STP]             EQ          START  (¼Ä¹Ö, ;¼Ú)
0051 C.             ¿¿[HK1_X_VC4_ON/OFF]            EQ          ON     (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°¸Ä, µ-¼«¸°Áá»ßµ•µ¿, á; ¿°Ê²¼µð¼Ä¹Öµ¹µé; f
0055 C. YÇYÓYÉYÉÁÜÁÖµãÁ•Á°²óÈðµ-¶áµ¼¼l¹Çµí°í»µ¹µËµµµµµË; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°¸Ä,
0059 C. *****
0060 C. ¸" RESTART;ÊPT2;Êµ•µ¿µ¼¼l¹Çµí; ¿°Ê²¼µí°¸Ä¹Öµ»°; ¿DCBC-151µ¿¿Êµã; f
0061 C.
0062 . C. ;ãPT2°¸Ä, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.             ¿¿[HK1_REP_PT_1/2]              EQ          PT2    (¼Ä¹Ö, ;¼Ú)
0069 C.             ¿¿[HK1_REP_STA/STP]             EQ          START  (¼Ä¹Ö, ;¼Ú)
0070 C.             ¿¿[HK1_X_VC4_ON/OFF]            EQ          ON     (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYÉÁÜÁÖ;ÊÁ•Á°²óÈð;Ë, áµí°¸Ä, °¸³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.             ¿¿[HK1_REP_PT_1/2]              EQ          PT2    (¼Ä¹Ö, ;¼Ú)
0076 C.             ¿¿[HK1_REP_STA/STP]             EQ          START  (¼Ä¹Ö, ;¼Ú)
0077 C.             ¿¿[HK1_X_VC4_ON/OFF]            EQ          ON     (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°¸Ä, Áá»ß;¿XÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°¸Ä, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.             ¿¿[HK1_REP_STA/STP]             EQ          STOP
0087 C.             ¿¿[HK1_S_VC4_ON/OFF]           EQ          OFF
0088 C.             ¿¿[HK1_X_VC4_ON/OFF]           EQ          OFF
0089 C.
0090 C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.             ¿¿[HK1_XMOD_ON/OFF]             EQ          OFF
0095 C.             ¿¿[HK1_XPA_ON/OFF]             EQ          OFF
    
```



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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-314:OP
0104 ( )
0105 S. OG og-314:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°EÉ¬òÁ÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½î¹ç°E²¼î¹ç²òE;f
0180 C. çç[HK1_PKT_FORM_NO] EQ 2
0181 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0182 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0183 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-08-13 10:32:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-08-13 10:32:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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```
0194 C.
0195 +. TI 2015-08-13 10:36:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄè%îíñαîîŷÄŷ§ŷÄŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¼È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èαòîáα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2015-08-13 10:36:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2015-08-13 10:36:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2015-08-13 10:36:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2015-08-13 10:36:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŷαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷÈŷŷŷÄŷçŷèè¼αα¼Ä»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñαèË'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```



(a) Spacecraft Operation Procedure (real-commands)

```
main-316 2015-08-13 12:39:01 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÄα•μ°È»Í×ÁÇαÍY¿Y×Yí;¼YÉ;ÈÈè%μ•ííÉ;ÈÈÈ¼°ÇÔα•α¿¼l¹çαÍ;çÀ®, ùα¹αèαÈαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-268:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2015-08-13 10:36:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 85 83 04 04)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 20 08)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 80 80 08 20)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0f 80 80 06 06)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 10 80 80 08 08)
0090 + DC 07-F0 MDP_XRT_FLD_ENA
0091 BC (d8)
0092 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0093 BC (c8)
0094 + DC 07-F0 MDP_XRT_AEC_RESET
0095 BC (d0)
```



\*\*\* OP Sequence for XRT \*\*\*

2015/08/13	10:46:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	10:46:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/13	10:47:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2015/08/13	10:47:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/13	10:47:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/13	10:47:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/13	10:47:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/13	10:47:24.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/13	10:49:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2015/08/13	10:49:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/13	10:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/13	11:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	11:57:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	11:57:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/13	11:57:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/13	12:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/13	12:20:00.0	XRT_Custom_430_OG [0x1ae]			
2015/08/13	12:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/13	13:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	13:35:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	13:35:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/13	13:35:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/13	13:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/13	13:58:30.0	XRT_Custom_430_OG [0x1ae]			
2015/08/13	13:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/13	14:05:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	14:05:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	14:05:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/13	14:05:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/13	14:08:00.0	XRT_Custom_430_OG [0x1ae]			
2015/08/13	14:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/13	14:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/13	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	14:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/13	15:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/08/13	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/13	15:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/13	15:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/13	15:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/13	15:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/13	15:02:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/08/13	15:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/13	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/13	15:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	15:13:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	15:13:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/13	15:13:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

Aug 13, 15 12:39

## XRT\_OGLIST\_0327.chk

Page 2/10

2015/08/13	15:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	15:59:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/13	16:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	16:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	16:52:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	16:52:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	16:52:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	16:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	17:36:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/13	17:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	17:53:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/13	17:53:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/13	17:53:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/13	17:56:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/13	17:56:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/13	17:56:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/08/13	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	18:03:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/13	18:03:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/08/13	18:03:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/13	18:03:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/13	18:03:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/13	18:03:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/13	18:03:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	18:06:26.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2015/08/13	18:06:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/13	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	18:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	18:30:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	18:30:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	18:30:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	18:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	19:13:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/13	19:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	20:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	20:09:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	20:09:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	20:09:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	20:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	20:50:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/13	20:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/13	21:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	21:47:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/13	21:47:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/13	21:47:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/13	21:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/13	22:26:00.0	XRT_Custom_430_OG [0x1ae]							

Aug 13, 15 12:39

XRT\_OGLIST\_0327.chk

Page 3/10

2015/08/13	22:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/13	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/13	23:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 1e e1 b4 32
2015/08/13	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/13	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/13	23:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/13	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/13	23:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/13	23:02:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/08/13	23:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/13	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/13	23:25:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	23:25:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/13	23:25:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/13	23:25:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/13	23:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/13	23:52:00.0	XRT_Custom_430_OG [0x1ae]			
2015/08/13	23:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	01:04:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	01:04:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	01:04:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	01:04:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/14	01:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/14	01:27:00.0	XRT_Custom_430_OG [0x1ae]			
2015/08/14	01:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	02:40:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	02:40:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	02:40:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	02:40:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/14	02:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/14	03:05:30.0	XRT_Custom_430_OG [0x1ae]			
2015/08/14	03:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	03:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	00 1e 7e b3 65
2015/08/14	04:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	04:09:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	04:09:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	04:09:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/14	04:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/14	04:44:00.0	XRT_Custom_430_OG [0x1ae]			
2015/08/14	04:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	05:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	05:49:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	05:49:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	05:49:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/14	05:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/14	06:22:30.0	XRT_Custom_430_OG [0x1ae]			
2015/08/14	06:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	06:25:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1



Aug 13, 15 12:39

## XRT\_OGLIST\_0327.chk

Page 4/10

2015/08/14	06:25:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/14	06:25:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/08/14	06:25:46.0	XRT_FLD_DIS_418_OG [0x1a2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/14	06:28:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/14	06:28:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/14	06:28:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/08/14	06:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/14	06:35:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/14	06:35:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/14	06:35:30.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	1e	7e	b3	65
2015/08/14	06:35:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/14	06:35:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/14	06:35:50.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/14	06:35:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/14	06:35:54.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/14	06:38:26.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2015/08/14	06:38:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/14	06:38:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/14	07:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/14	07:29:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/14	07:29:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/14	07:29:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/14	07:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/14	08:00:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/14	08:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/14	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/14	08:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/14	08:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	1e	7e	b3	65
2015/08/14	08:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/14	08:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/14	08:30:20.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/14	08:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/14	08:30:24.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/14	08:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2015/08/14	08:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/14	08:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/14	09:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/14	09:09:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/14	09:09:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/14	09:09:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/14	09:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/14	09:39:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/14	09:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/14	10:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/14	10:54:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/14	10:54:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/14	10:54:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/14	10:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

Aug 13, 15 12:39

## XRT\_OGLIST\_0327.chk

Page 5/10

2015/08/14	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/14	10:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	11:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/14	11:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2015/08/14	11:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/14	11:00:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/14	11:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/14	11:00:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/14	11:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	11:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2015/08/14	11:17:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/14	11:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/08/14	12:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	12:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	12:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	12:33:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	12:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/14	12:56:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/14	12:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/08/14	14:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	14:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	14:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	14:11:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	14:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/14	14:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/14	14:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/08/14	14:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	14:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	14:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	14:36:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	14:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/14	14:47:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/14	14:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/08/14	15:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	15:00:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	15:01:00.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/14	15:01:16.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00 00 00 00 00
2015/08/14	15:03:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/08/14	15:03:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/08/14	15:03:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/14	15:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/08/14	15:11:00.0	AOCs_OrE-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	15:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 56 a7 01 58
2015/08/14	15:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	15:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	15:49:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	15:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/14	16:35:30.0	AOCs_OrE-point_Start_7_OG [0x09d]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM	AOCs_OrE-point_Start_7_OG [0x09d]	5	02-76	00 4d ca 01 58

Aug 13, 15 12:39

## XRT\_OGLIST\_0327.chk

Page 6/10

2015/08/14	16:58:30.0	AOCS_OrE-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00	44	e5	01	58
2015/08/14	17:28:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0			c1		
2015/08/14	17:28:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0			c1		
2015/08/14	17:28:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0			da		
2015/08/14	17:28:06.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0			e8		
2015/08/14	17:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0			e9		
2015/08/14	18:12:30.0	AOCS_OrE-point_Start_9_OG [0x09f] AOCU_NM	5	02-76	00	3c	00	01	58
2015/08/14	18:35:30.0	AOCS_OrE-point_Start_10_OG [0x0a0] AOCU_NM	5	02-76	00	33	1a	01	58
2015/08/14	19:06:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0			c1		
2015/08/14	19:06:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0			c1		
2015/08/14	19:06:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0			da		
2015/08/14	19:06:36.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0			e8		
2015/08/14	19:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0			e9		
2015/08/14	19:48:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0			c1		
2015/08/14	19:48:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/14	19:49:00.0	AOCS_OrE-point_Start_11_OG [0x0a1] AOCU_NM	5	02-76	00	2a	35	01	58
2015/08/14	19:49:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0			d8		
2015/08/14	19:49:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0			c8		
2015/08/14	19:49:20.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0			d0		
2015/08/14	19:49:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0			d5		
2015/08/14	19:49:24.0	XRT_FLD_RESET_443_OG [0x1bb] MDP_XRT_FLD_RESET	1	07-F0			da		
2015/08/14	19:51:56.0	XRT_QT_PROG_SET_442_OG [0x1ba] MDP_XRT_QT_PROG_SET	2	07-F0			c4	07	
2015/08/14	19:51:58.0	XRT_FL_PROG_SET_428_OG [0x1ac] MDP_XRT_FL_PROG_SET	2	07-F0			c5	03	
2015/08/14	19:52:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0			c0		
2015/08/14	20:11:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0			c1		
2015/08/14	20:11:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/14	20:12:00.0	AOCS_OrE-point_Start_12_OG [0x0a2] AOCU_NM	5	02-76	00	21	58	01	58
2015/08/14	20:12:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0			d8		
2015/08/14	20:12:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0			c8		
2015/08/14	20:12:20.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0			d0		
2015/08/14	20:12:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0			d5		
2015/08/14	20:12:24.0	XRT_FLD_RESET_443_OG [0x1bb] MDP_XRT_FLD_RESET	1	07-F0			da		
2015/08/14	20:14:56.0	XRT_QT_PROG_SET_442_OG [0x1ba] MDP_XRT_QT_PROG_SET	2	07-F0			c4	07	
2015/08/14	20:14:58.0	XRT_FL_PROG_SET_428_OG [0x1ac] MDP_XRT_FL_PROG_SET	2	07-F0			c5	03	
2015/08/14	20:15:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0			c0		
2015/08/14	20:45:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0			c1		
2015/08/14	20:45:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0			c1		
2015/08/14	20:45:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0			da		
2015/08/14	20:45:06.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0			e8		
2015/08/14	20:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0			e9		
2015/08/14	21:25:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0			c1		
2015/08/14	21:25:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/14	21:26:00.0	AOCS_OrE-point_Start_13_OG [0x0a3] AOCU_NM	5	02-76	00	18	72	01	58
2015/08/14	21:26:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0			d8		
2015/08/14	21:26:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0			c8		
2015/08/14	21:26:20.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0			d0		
2015/08/14	21:26:22.0	XRT_ARS_DIS_423_OG [0x1a7]							

Aug 13, 15 12:39

## XRT\_OGLIST\_0327.chk

Page 7/10

2015/08/14	21:26:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/14	21:28:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	21:28:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2015/08/14	21:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/14	21:48:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	21:48:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	21:49:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/14	21:49:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 0f 8d 01 58
2015/08/14	21:49:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/14	21:49:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/14	21:49:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/14	21:49:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/14	21:51:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	21:51:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2015/08/14	21:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/14	22:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	22:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	22:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	22:23:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	22:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/14	23:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/14	23:00:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	23:01:00.5	AOCS_OrE-point_Start_15_OG [0x0a5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/14	23:01:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 06 a7 01 58
2015/08/14	23:01:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/14	23:01:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/14	23:01:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/14	23:01:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/14	23:03:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/14	23:03:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2015/08/14	23:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/14	23:23:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	23:23:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	23:24:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/14	23:24:16.0	XRT_FLD_DIS_418_OG [0x1a2]	AOCU_NM	5	02-76	00 fe a8 01 58
2015/08/14	23:26:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/08/14	23:26:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/08/14	23:26:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/14	23:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2015/08/14	23:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/14	23:36:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/14	23:37:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/08/14	23:37:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/14	23:37:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/14	23:37:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/14	23:37:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/14			MDP_XRT_FLD_RESET	1	07-F0	da

Aug 13, 15 12:39

XRT\_OGLIST\_0327.chk

Page 8/10

2015/08/14	23:39:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2015/08/14	23:39:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/08/14	23:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/15	00:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/15	00:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/15	00:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/15	00:01:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/15	00:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/15	00:25:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/15	00:25:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2015/08/15	00:25:30.0	AOCS_Orе-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00	f5 cb 01 58
2015/08/15	00:25:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/15	00:25:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/15	00:25:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/15	00:25:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/15	00:25:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/15	00:28:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2015/08/15	00:28:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/08/15	00:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/15	00:48:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/15	00:48:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2015/08/15	00:48:30.0	AOCS_Orе-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	ec e6 01 58
2015/08/15	00:48:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/15	00:48:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/15	00:48:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/15	00:48:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/15	00:48:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/15	00:51:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2015/08/15	00:51:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/08/15	00:51:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/15	01:11:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/15	01:11:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2015/08/15	01:11:30.0	AOCS_Orе-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	e4 01 01 58
2015/08/15	01:11:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/15	01:11:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/15	01:11:50.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/15	01:11:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/15	01:11:54.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/15	01:14:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2015/08/15	01:14:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/08/15	01:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/15	01:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/15	01:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/15	01:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/15	01:40:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/15	01:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/15	02:03:30.0	AOCS_Orе-point_Start_20_OG [0x0aa]					

2015/08/15	02:26:30.0	AOCs_OrE-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00	db	1b	01	58
2015/08/15	02:49:30.0	AOCs_OrE-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00	d2	36	01	58
2015/08/15	03:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	03:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	03:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da			
2015/08/15	03:15:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2015/08/15	03:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2015/08/15	03:42:00.0	AOCs_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	c0	73	01	58
2015/08/15	04:05:00.0	AOCs_OrE-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	00	b7	8e	01	58
2015/08/15	04:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	04:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	04:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da			
2015/08/15	04:45:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2015/08/15	04:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2015/08/15	05:20:30.0	AOCs_OrE-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00	ae	a8	01	58
2015/08/15	05:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	05:57:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/15	05:58:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2015/08/15	05:58:16.0	XRT_FLD_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0		d9			
2015/08/15	06:00:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2015/08/15	06:00:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2015/08/15	06:00:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	0e		
2015/08/15	06:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2015/08/15	06:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	06:07:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/15	06:08:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00	00	00	00
2015/08/15	06:08:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8			
2015/08/15	06:08:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2015/08/15	06:08:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0		d0			
2015/08/15	06:08:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2015/08/15	06:08:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0		da			
2015/08/15	06:10:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	0a		
2015/08/15	06:10:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	03		
2015/08/15	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2015/08/15	06:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	06:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	06:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da			
2015/08/15	06:26:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2015/08/15	06:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2015/08/15	06:58:00.0	XRT_Custom_430_OG [0x1ae]								
2015/08/15	06:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2015/08/15	08:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	08:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2015/08/15	08:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da			
2015/08/15	08:06:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2015/08/15	08:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2015/08/15	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]								

Aug 13, 15 12:39

## XRT\_OGLIST\_0327.chk

Page 10/10

2015/08/15	08:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/15	08:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00
2015/08/15	08:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/15	08:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/15	08:30:20.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/15	08:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/15	08:30:24.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/15	08:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/08/15	08:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/15	08:36:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/15	08:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/15	09:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/15	09:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/15	09:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/15	09:46:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/15	09:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/15	10:20:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00