

# XRT Timeline to be uploaded on 2015/08/15

Period: 2015/08/15 10:10:00 - 2015/08/20 10:25:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

## XOB #1ABE: HOP292\_AR-A(Filter-Ratio), 256x256 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms/3ms leak), 60s cad

Term	Pointing (x, y)	Comment
08/15 10:23:00 - 08/15 14:47:00	Track ( 174.3, 152.9) <sup>Ⓢ 08/15 10:20:00</sup>	# OP start + 10min   HOP292 at AR12400
<b>PROG= 20 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 50 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 256x256 (1064, 1048) DPCM 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 256x256 (1064, 1048) DPCM 0 0 2.0sec	
└─ Subr= 2 30-time(s) 60.0sec		
└─ Seqn= 73 1-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close Safe Norm 125ms Obs 1x1 256x256 (1064, 1048) Q=95 1 0 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 177ms Obs 1x1 256x256 (1064, 1048) Q=95 1 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 256x256 (1064, 1048) Q=95 1 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1AC1: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with dark(16s) and

Term	Pointing (x, y)	Comment
08/15 15:34:30 - 08/15 16:03:54	Track ( -480.8, -306.1) <sup>Ⓢ 08/15 15:00:00</sup>	# Track AR12401
08/16 05:04:00 - 08/16 05:22:00	Track ( -370.3, -313.3) <sup>Ⓢ 08/16 05:01:00</sup>	# Track AR12401
08/16 06:13:00 - 08/16 08:29:54	Track ( -360.9, -313.8) <sup>Ⓢ 08/16 06:10:00</sup>	# Cont,
08/16 08:33:00 - 08/16 10:23:30	Track ( 335.0, 150.1) <sup>Ⓢ 08/16 08:30:00</sup>	# HOP287 at AR12400
08/16 10:51:00 - 08/16 18:24:54	Track ( -325.1, -315.6) <sup>Ⓢ 08/16 10:30:00</sup>	# Track AR12401
08/16 22:53:00 - 08/17 00:29:54	Track ( -220.2, -319.8) <sup>Ⓢ 08/16 22:50:00</sup>	# Track AR12401
08/17 02:33:00 - 08/17 05:29:28	Track ( -188.4, -320.8) <sup>Ⓢ 08/17 02:30:00</sup>	# Track AR12401
08/17 05:44:00 - 08/17 08:29:54	Track ( -160.5, -321.5) <sup>Ⓢ 08/17 05:41:00</sup>	# Cont,
08/17 08:33:00 - 08/17 10:29:54	Track ( 520.8, 160.9) <sup>Ⓢ 08/17 08:30:00</sup>	# HOP287 at AR12400
08/17 10:33:00 - 08/17 13:59:54	Track ( -118.1, -322.4) <sup>Ⓢ 08/17 10:30:00</sup>	# Track AR12401
08/17 21:35:00 - 08/18 00:11:00	Track ( -24.9, -323.6) <sup>Ⓢ 08/17 21:00:00</sup>	# Track AR12401
08/18 02:38:00 - 08/18 06:05:24	Track ( 24.8, -323.8) <sup>Ⓢ 08/18 02:35:00</sup>	# Track AR12401
08/18 06:18:30 - 08/18 08:15:00	Track ( 57.6, -323.7) <sup>Ⓢ 08/18 06:15:30</sup>	# Cont,
08/18 08:45:00 - 08/18 09:55:30	Track ( 678.9, 176.5) <sup>Ⓢ 08/18 08:30:00</sup>	# HOP287 at AR12400
<b>PROG= 10 Inf.-time(s)</b>		
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
└─ Seqn= 42 4-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec	
Open/thick-Al	Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
└─ Seqn= 62 8-time(s) 240.0sec		
thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-

Term	Pointing (x, y)	Comment
08/15 19:05:30 - 08/15 19:42:30	Fixed ( 85.0, -30.0)	6/21 and synoptic, shifted manually
08/16 06:03:00 - 08/16 06:09:54	Fixed ( 0.0, 0.0)	synoptic
08/16 18:28:00 - 08/16 18:34:00	Fixed ( 0.0, 0.0)	synoptic, shifted 25.0 min and stray light measurement
08/17 05:34:00 - 08/17 05:40:54	Fixed ( 0.0, 0.0)	synoptic, shifted -29.0 min
08/17 18:23:00 - 08/17 18:29:54	Fixed ( 0.0, 0.0)	synoptic, shifted 20.0 min
08/18 06:08:30 - 08/18 06:15:24	Fixed ( 0.0, 0.0)	synoptic, shifted 5.5 min
<b>PROG= 14 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 33 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh	Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh	Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	
└─ Seqn= 61 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	

	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 49</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 39</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 54</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1AC2: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 120s cad**

Term	Pointing (x, y)	Comment
08/16 02:04:00 - 08/16 05:00:54	Fixed ( 576.0, 690.0)	# Spectral Atlas at off-limb
08/16 19:22:30 - 08/16 22:49:54	Fixed ( 0.0, 0.0)	# SOT Flat fields
08/17 18:33:00 - 08/17 20:54:30	Track ( -10.5, -30.0) @ 08/17 18:30:00	# HOP286 at QS
<b>PROG= 07 Inf.-time(s)</b>		
<b>Subr= 1</b>		<b>45-time(s)</b>
<b>Seqn= 29</b>		<b>1-time(s)</b>
thin-Be/Open	med-Be/Open	close
Safe	Norm	1.41s
Obs	4x4	2048x2048 (1024, 1024)
Q=98	2	0
2.0sec		
<b>Subr= 2</b>		<b>1-time(s)</b>
<b>Seqn= 26</b>		<b>1-time(s)</b>
Open/G-band	Open/G-band	open
Safe	Norm	1ms
Obs	2x2	2048x2048 (1024, 1024)
Q=90	0	0
2.0sec		
Open/G-band	Open/G-band	close
Safe	Norm	1ms
Obs	2x2	2048x2048 (1024, 1024)
Q=95	0	0
2.0sec		
Default Filter	Thicker Filter	VLS
mode	image	Exp.
CCD	Bin	ROI: size (center)
Comp.	AEC Buffer	Interval

**XOB #1ABF: Stray light study 2015-20 ;C-poly, 2x2 full FOV(1min-cad) and 2x2 256 on AR(10sec-cad)**

Term	Pointing (x, y)	Comment
08/16 18:37:00 - 08/16 19:19:24	Fixed ( 0.0, 0.0)	synoptic, shifted 25.0 min and stray light measurement
<b>PROG= 19 1-time(s)</b>		
<b>Subr= 1</b>		<b>1-time(s)</b>
<b>Seqn= 67</b>		<b>1-time(s)</b>
C-poly/Open	C-poly/Open	close
Safe	Norm	250ms
Obs	2x2	2048x2048 (1024, 1024)
Q=95	0	0
2.0sec		
C-poly/Open	C-poly/Open	close
Safe	Norm	2.00s
Obs	2x2	2048x2048 (1024, 1024)
Q=95	0	0
2.0sec		
<b>Subr= 2</b>		<b>21-time(s)</b>
<b>Seqn= 27</b>		<b>1-time(s)</b>
Open/Ti-poly	Open/thick-Al	close
Safe	Norm	250ms
Obs	2x2	512x512 (1024, 1200)
Q=95	0	0
2.0sec		
Open/G-band	Open/G-band	close
Safe	Norm	1ms
Obs	2x2	512x512 (1024, 1200)
Q=95	0	0
2.0sec		
<b>Subr= 3</b>		<b>9-time(s)</b>
<b>Seqn= 99</b>		<b>1-time(s)</b>
C-poly/Open	C-poly/Open	close
Safe	Norm	2.00s
Obs	2x2	2048x2048 (1024, 1024)
DPCM	0	0
2.0sec		
<b>Seqn= 27</b>		<b>5-time(s)</b>
Open/Ti-poly	Open/thick-Al	close
Safe	Norm	250ms
Obs	2x2	512x512 (1024, 1200)
Q=95	0	0
2.0sec		
Open/G-band	Open/G-band	close
Safe	Norm	1ms
Obs	2x2	512x512 (1024, 1200)
Q=95	0	0
2.0sec		
Default Filter	Thicker Filter	VLS
mode	image	Exp.
CCD	Bin	ROI: size (center)
Comp.	AEC Buffer	Interval

**XOB #1AC7: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)**

Term	Pointing (x, y)	Comment
08/17 00:33:00 - 08/17 02:29:54	Track ( 461.5, 156.7) @ 08/17 00:30:00	# HOP291 at AR12400
08/18 00:38:00 - 08/18 02:34:54	Track ( 7.0, -323.8) @ 08/18 00:35:00	# HOP291 at AR12401
<b>PROG= 05 Inf.-time(s)</b>		
<b>Subr= 2</b>		<b>1-time(s)</b>
<b>Seqn= 56</b>		<b>1-time(s)</b>
Open/G-band	Open/G-band	open
Safe	Norm	3ms
Obs	1x1	384x384 (1064, 1048)
DPCM	0	0
2.0sec		
Open/G-band	Open/G-band	close
Safe	Norm	3ms
Obs	1x1	384x384 (1064, 1048)
DPCM	0	0
2.0sec		
Open/Ti-poly	Open/thick-Al	close
Safe	Dark	16.0s
Obs	1x1	384x384 (1064, 1048)
Q=98	0	0
2.0sec		
<b>Seqn= 42</b>		<b>4-time(s)</b>
Al-poly/Open	thin-Be/Open	close
Safe	Norm	500ms
Obs	1x1	384x384 (1064, 1048)
Q=95	3	0
2.0sec		
thin-Be/Open	med-Be/Open	close
Safe	Norm	5.66s
Obs	1x1	512x512 (1064, 1048)
Q=95	3	0
2.0sec		
Open/thick-Al	Open/thick-Al	close
Safe	Norm	16.0s
Obs	1x1	384x384 (1064, 1048)
Q=95	3	0
2.0sec		
<b>Seqn= 22</b>		<b>20-time(s)</b>
thin-Be/Open	med-Be/Open	close
Safe	Norm	1.00s
Obs	1x1	512x512 (1064, 1048)
Q=95	3	0
2.0sec		
Al-poly/Open	thin-Be/Open	close
Safe	Norm	500ms
Obs	1x1	512x512 (1064, 1048)
Q=95	3	0
2.0sec		
thin-Be/Open	med-Be/Open	close
Safe	Norm	1.00s
Obs	1x1	384x384 (1064, 1048)
Q=95	3	1
2.0sec		
Al-poly/Open	thin-Be/Open	close
Safe	Norm	500ms
Obs	1x1	384x384 (1064, 1048)
Q=95	3	1
2.0sec		
thin-Be/Open	med-Be/Open	close
Safe	Norm	1.00s
Obs	1x1	384x384 (1064, 1048)
Q=95	3	2
2.0sec		
Al-poly/Open	thin-Be/Open	close
Safe	Norm	500ms
Obs	1x1	384x384 (1064, 1048)
Q=95	3	2
2.0sec		
Default Filter	Thicker Filter	VLS
mode	image	Exp.
CCD	Bin	ROI: size (center)
Comp.	AEC Buffer	Interval

**XOB #1A9C: G-Band Alignment with North Pole Q90 2x2(G-band and VLS=CLS) - 1msec - 5min cadence - Partial Sun-wNGT**

Term	Pointing (x, y)	Comment
------	-----------------	---------

08/17 14:03:00 - 08/17 16:19:54 Fixed ( 0.0, 930.0) # Co-alignment at N-pole

<b>PROG= 09 1-time(s)</b>													
└─ <b>Subr= 1 24-time(s) 300.0sec</b>													
└─ <b>Seqn= 98 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
└─ <b>Seqn= 63 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1A9D: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec- 8 min cadence-wNGT**

<b>Term</b>													<b>Pointing (x, y)</b>													<b>Comment</b>												
08/17 16:23:00 - 08/17 18:19:54													Fixed ( -945.0, 0.0)													# Co-alignment at E-limb												
<b>PROG= 12 1-time(s)</b>																																						
└─ <b>Subr= 1 15-time(s) 480.0sec</b>																																						
└─ <b>Seqn= 19 1-time(s) 2.0sec</b>																																						
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec																									
└─ <b>Seqn= 43 1-time(s) 2.0sec</b>																																						
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec																									
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval																										

\* \* \* \* \* **Flare mode** \* \* \* \* \*

**XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512)**

<b>Term</b>													<b>Pointing (x, y)</b>													<b>Comment</b>												
08/15 10:23:00 - 08/15 14:47:00													Track ( 174.3, 152.9) @ 08/15 10:20:00													# OP start + 10min   HOP292 at AR12400												
08/15 15:34:30 - 08/15 16:03:54													Track ( -480.8, -306.1) @ 08/15 15:00:00													# Track AR12401												
08/16 02:04:00 - 08/16 05:00:54													Fixed ( 576.0, 690.0)													# Spectral Atlas at off-limb												
08/16 05:04:00 - 08/16 05:22:00													Track ( -370.3, -313.3) @ 08/16 05:01:00													# Track AR12401												
08/16 06:13:00 - 08/16 08:29:54													Track ( -360.9, -313.8) @ 08/16 06:10:00													# Cont,												
08/16 08:33:00 - 08/16 10:23:30													Track ( 335.0, 150.1) @ 08/16 08:30:00													# HOP287 at AR12400												
08/16 10:51:00 - 08/16 18:24:54													Track ( -325.1, -315.6) @ 08/16 10:30:00													# Track AR12401												
08/16 19:22:30 - 08/16 22:49:54													Fixed ( 0.0, 0.0)													# SOT Flat fields												
08/16 22:53:00 - 08/17 00:29:54													Track ( -220.2, -319.8) @ 08/16 22:50:00													# Track AR12401												
08/17 00:33:00 - 08/17 02:29:54													Track ( 461.5, 156.7) @ 08/17 00:30:00													# HOP291 at AR12400												
08/17 02:33:00 - 08/17 05:29:28													Track ( -188.4, -320.8) @ 08/17 02:30:00													# Track AR12401												
08/17 05:44:00 - 08/17 08:29:54													Track ( -160.5, -321.5) @ 08/17 05:41:00													# Cont,												
08/17 08:33:00 - 08/17 10:29:54													Track ( 520.8, 160.9) @ 08/17 08:30:00													# HOP287 at AR12400												
08/17 10:33:00 - 08/17 13:59:54													Track ( -118.1, -322.4) @ 08/17 10:30:00													# Track AR12401												
08/17 18:33:00 - 08/17 20:54:30													Track ( -10.5, -30.0) @ 08/17 18:30:00													# HOP286 at QS												
08/17 21:35:00 - 08/18 00:11:00													Track ( -24.9, -323.6) @ 08/17 21:00:00													# Track AR12401												
08/18 00:38:00 - 08/18 02:34:54													Track ( 7.0, -323.8) @ 08/18 00:35:00													# HOP291 at AR12401												
08/18 02:38:00 - 08/18 06:05:24													Track ( 24.8, -323.8) @ 08/18 02:35:00													# Track AR12401												
08/18 06:18:30 - 08/18 08:15:00													Track ( 57.6, -323.7) @ 08/18 06:15:30													# Cont,												
08/18 08:45:00 - 08/18 09:55:30													Track ( 678.9, 176.5) @ 08/18 08:30:00													# HOP287 at AR12400												

<b>PROG= 03 30-time(s)</b>													
└─ <b>Subr= 1 20-time(s) 2.0sec</b>													
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn=100 1-time(s) 10.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 10 1-time(s) 2.0sec</b>													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn= 53 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \* **Active Region Search** \* \* \* \* \*

NOT USED

\* \* \* \* \* **Flare Detection** \* \* \* \* \*

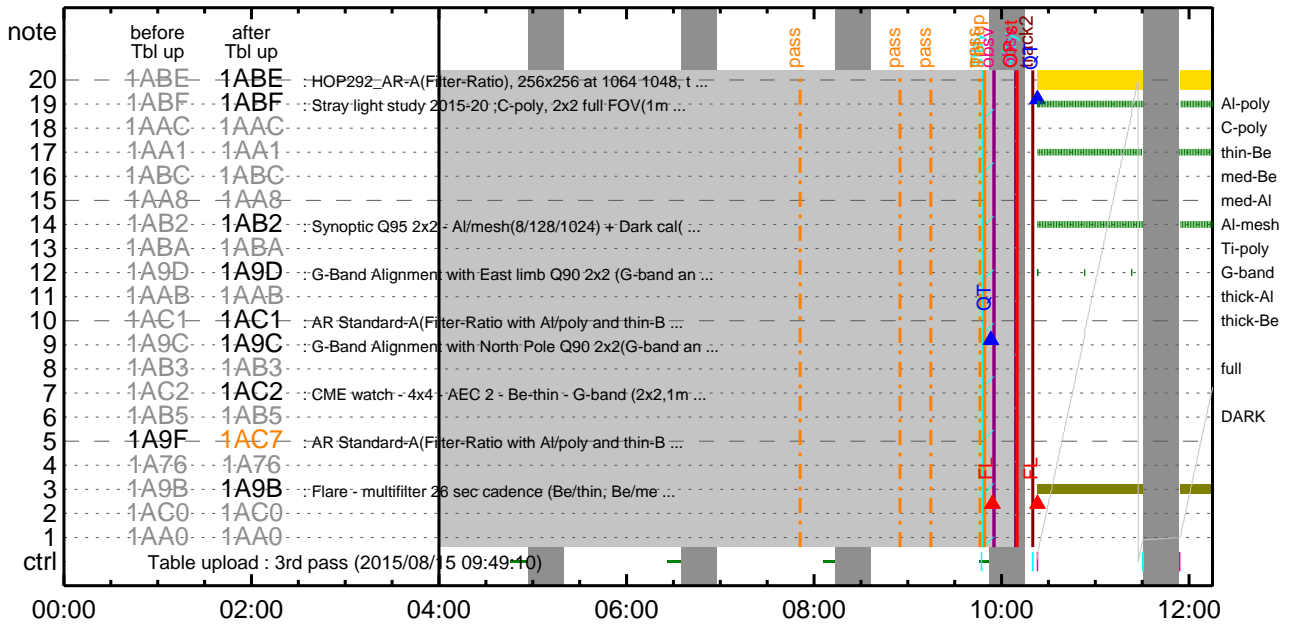
**FLD Patrol**

<b>Term</b>													<b>Pointing (x, y)</b>													<b>Comment</b>												
08/16 02:01:18 - 08/16 06:00:18													Fixed ( 576.0, 690.0)													# Spectral Atlas at off-limb												
08/16 06:10:18 - 08/16 18:25:18													Track ( -360.9, -313.8) @ 08/16 06:10:00													# Cont,												
08/16 19:19:48 - 08/17 05:33:52													Fixed ( 0.0, 0.0)													# SOT Flat fields												
08/17 05:41:18 - 08/17 14:00:18													Track ( -160.5, -321.5) @ 08/17 05:41:00													# Cont,												
08/17 18:30:18 - 08/18 06:05:48													Track ( -10.5, -30.0) @ 08/17 18:30:00													# HOP286 at QS												

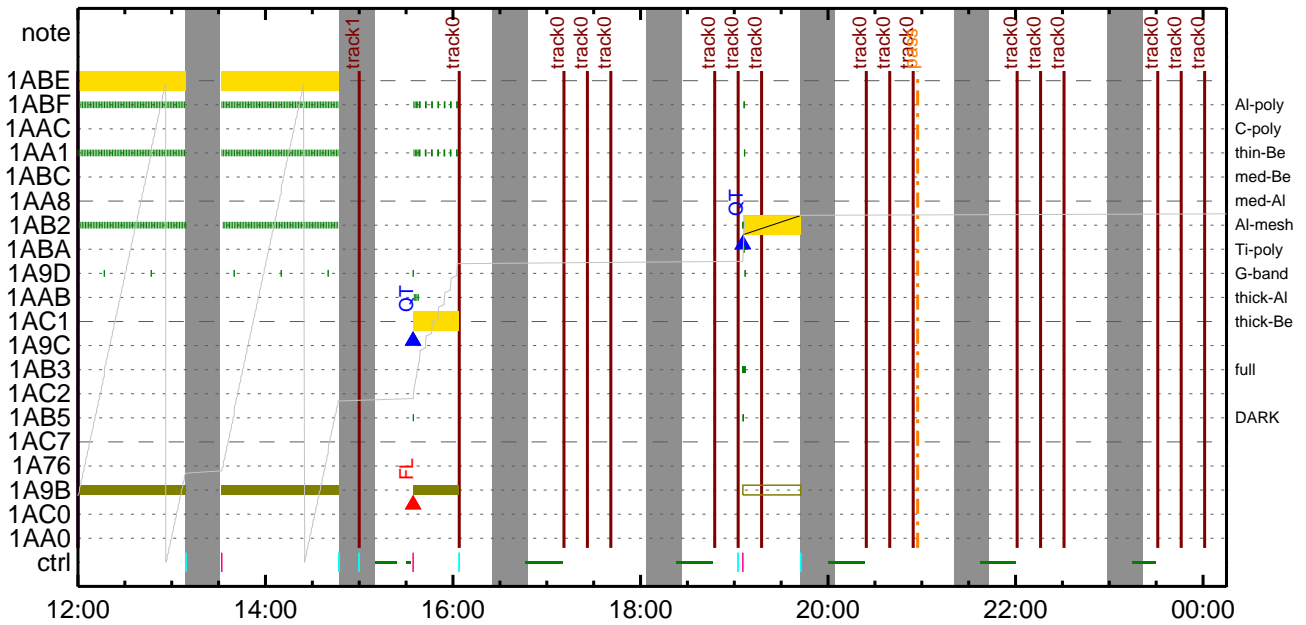
08/18 06:15:48 - 08/20 10:25:00 Track ( 57.6, -323.7) @ 08/18 06:15:30 # Cont,

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

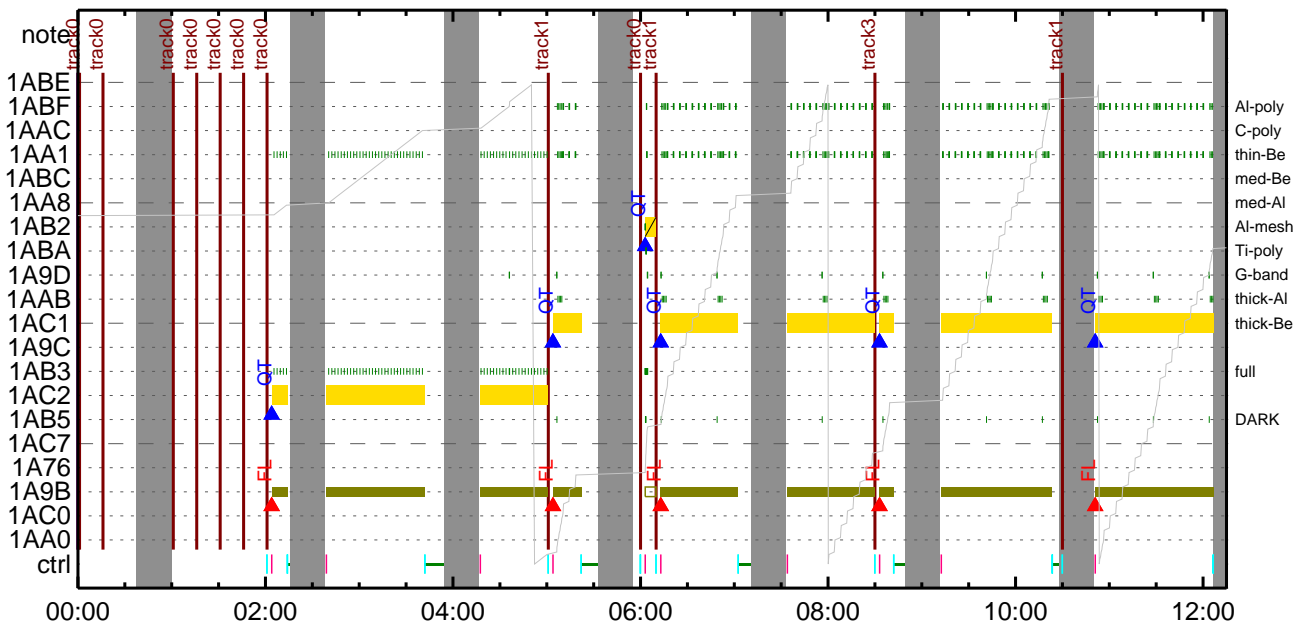
### CMDI #0331 2015/08/15



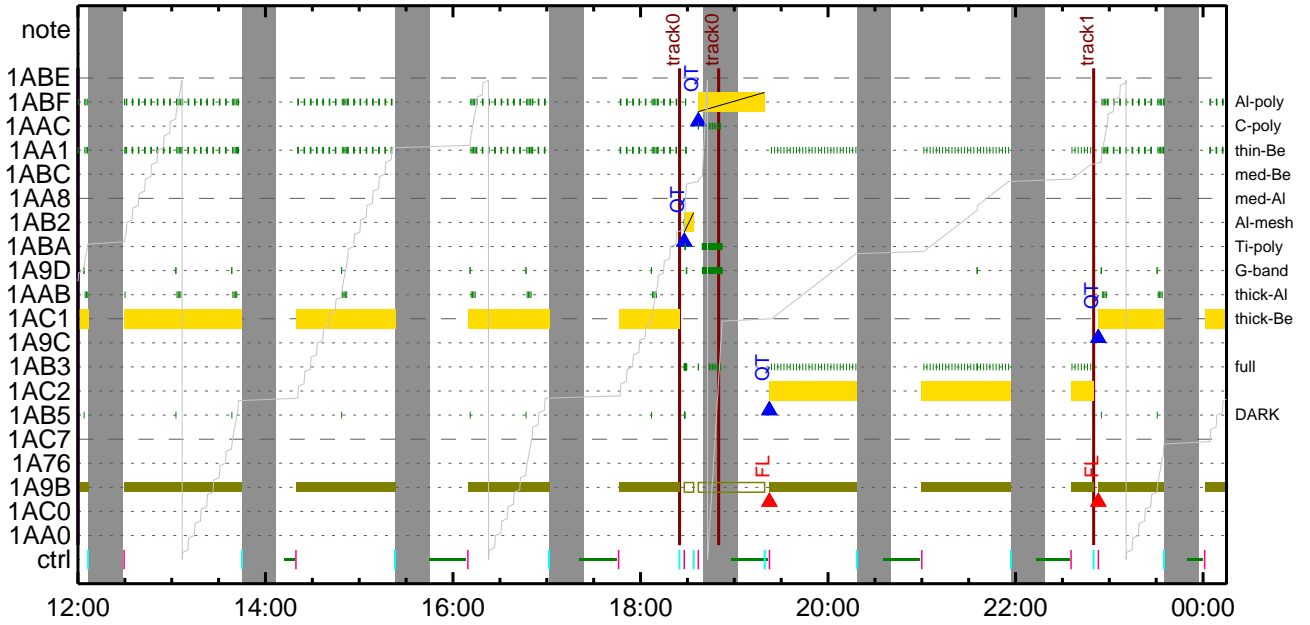
### CMDI #0331 2015/08/15



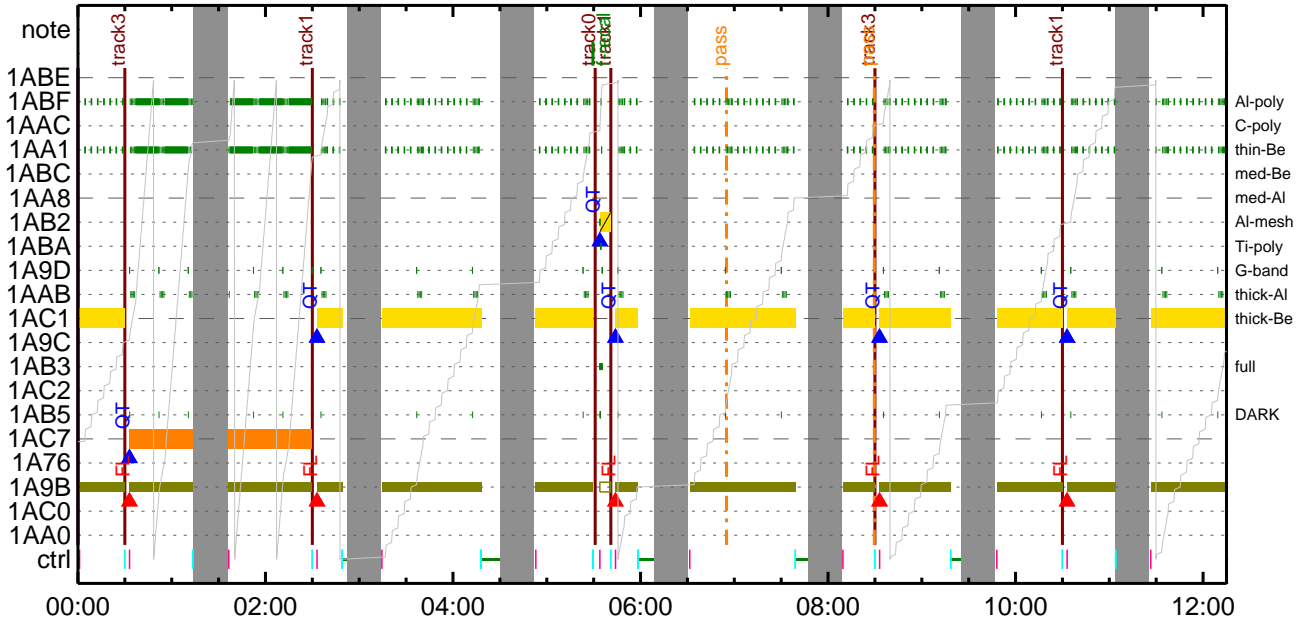
### CMDI #0331 2015/08/16



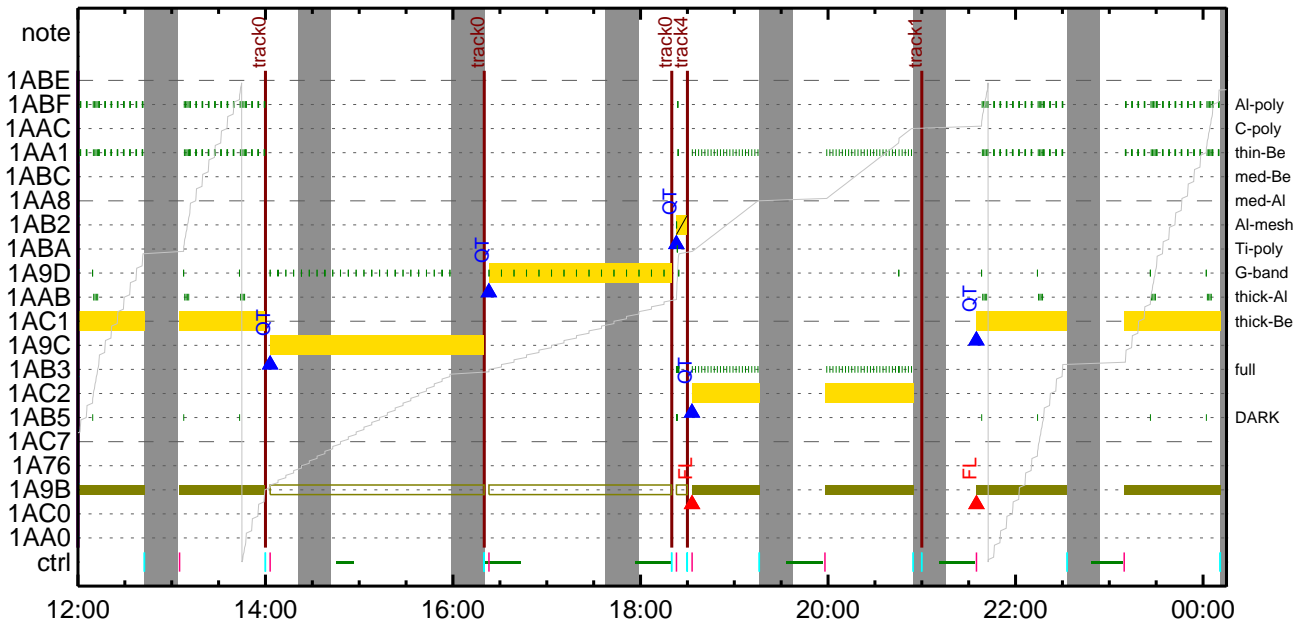
CMDI #0331 2015/08/16



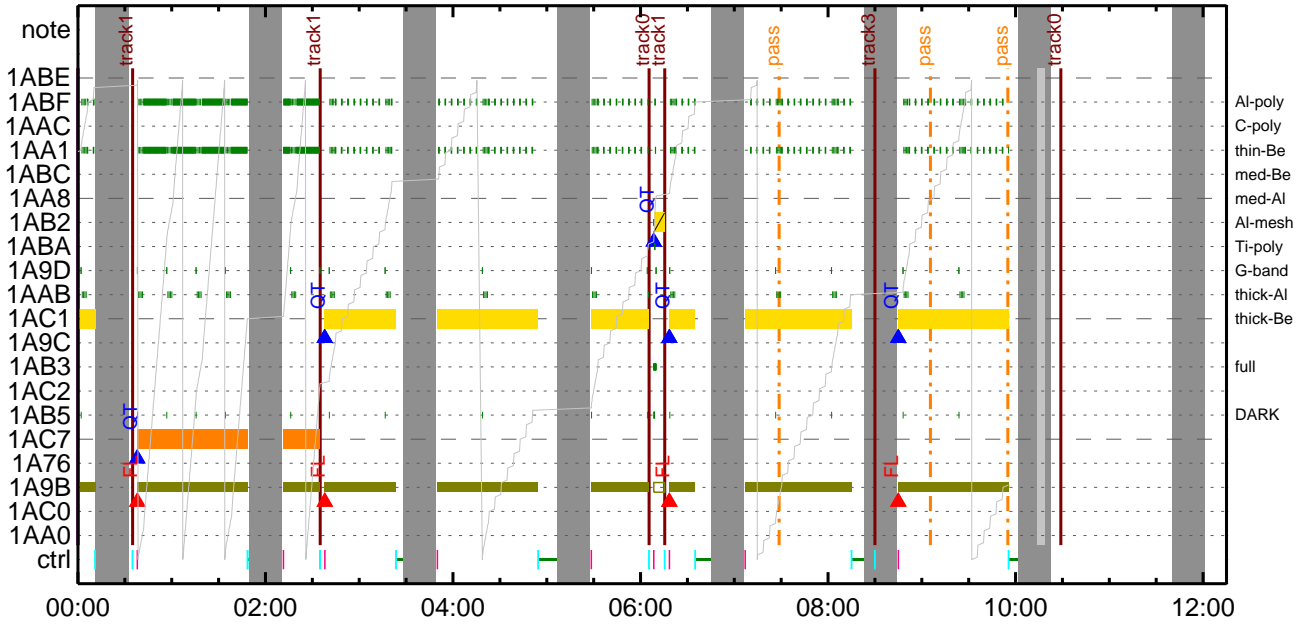
CMDI #0331 2015/08/17



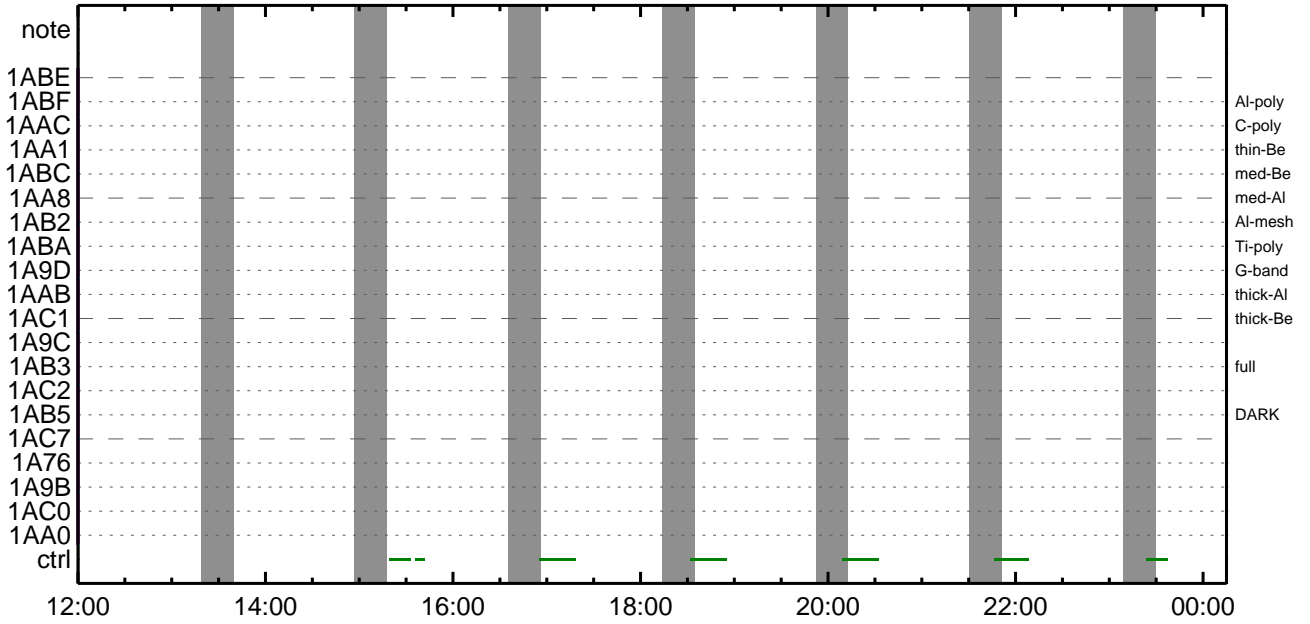
CMDI #0331 2015/08/17



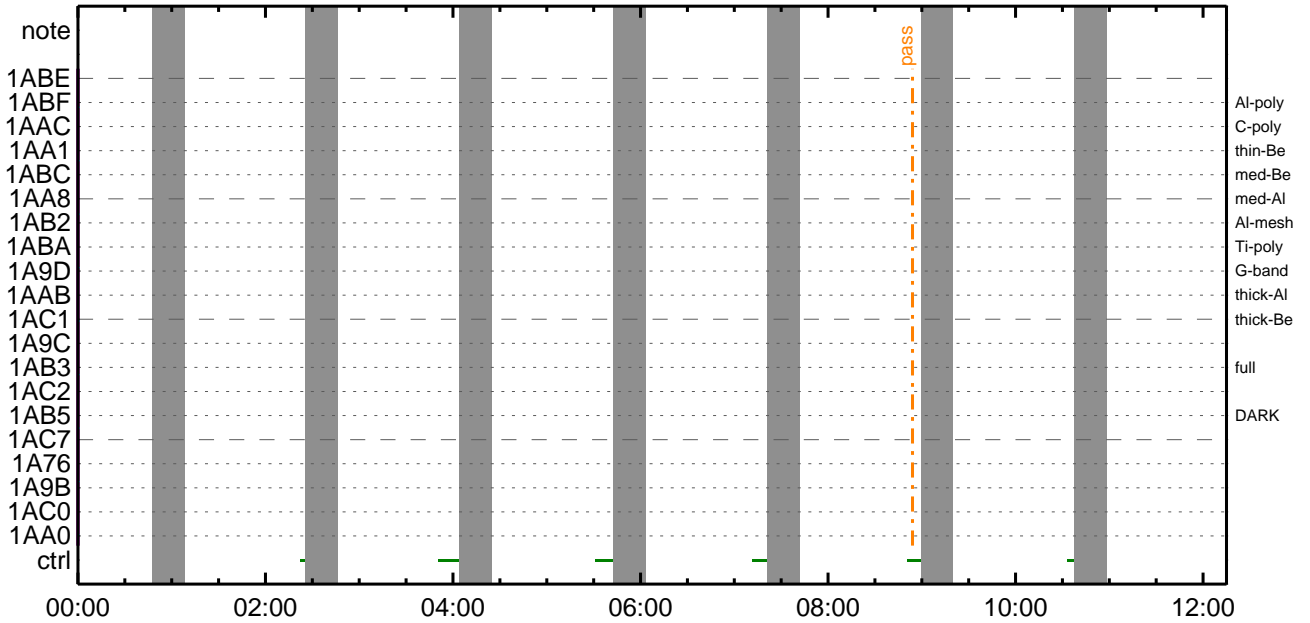
CMDI #0331 2015/08/18



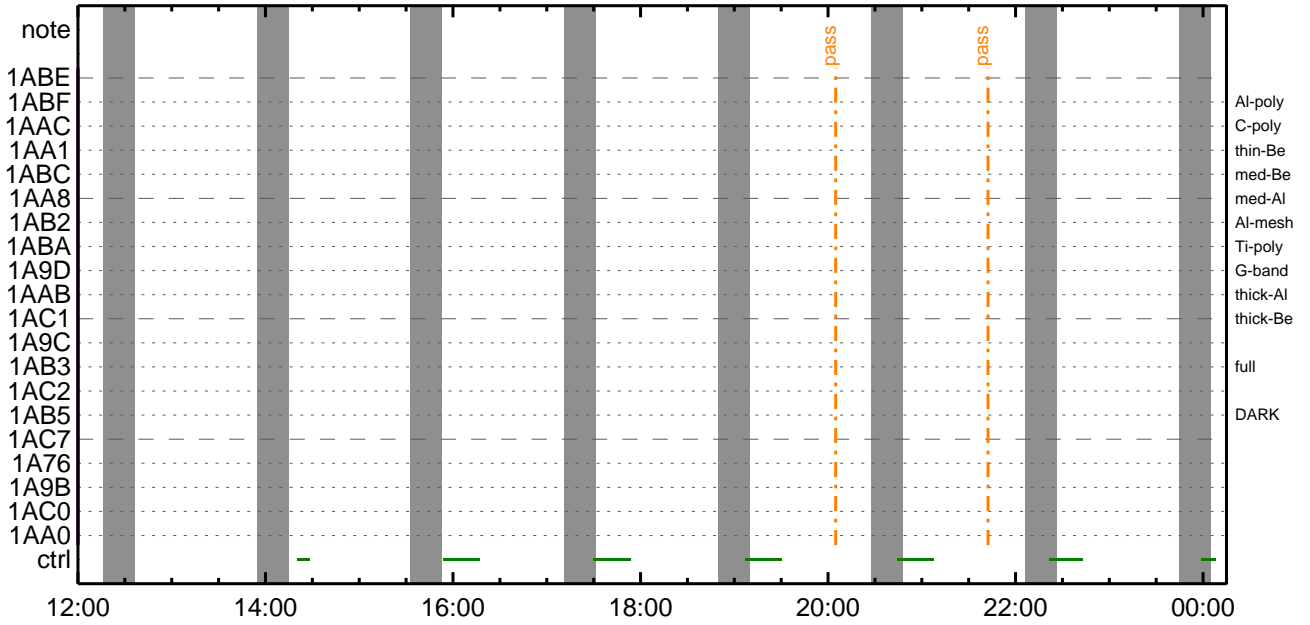
CMDI #0331 2015/08/18



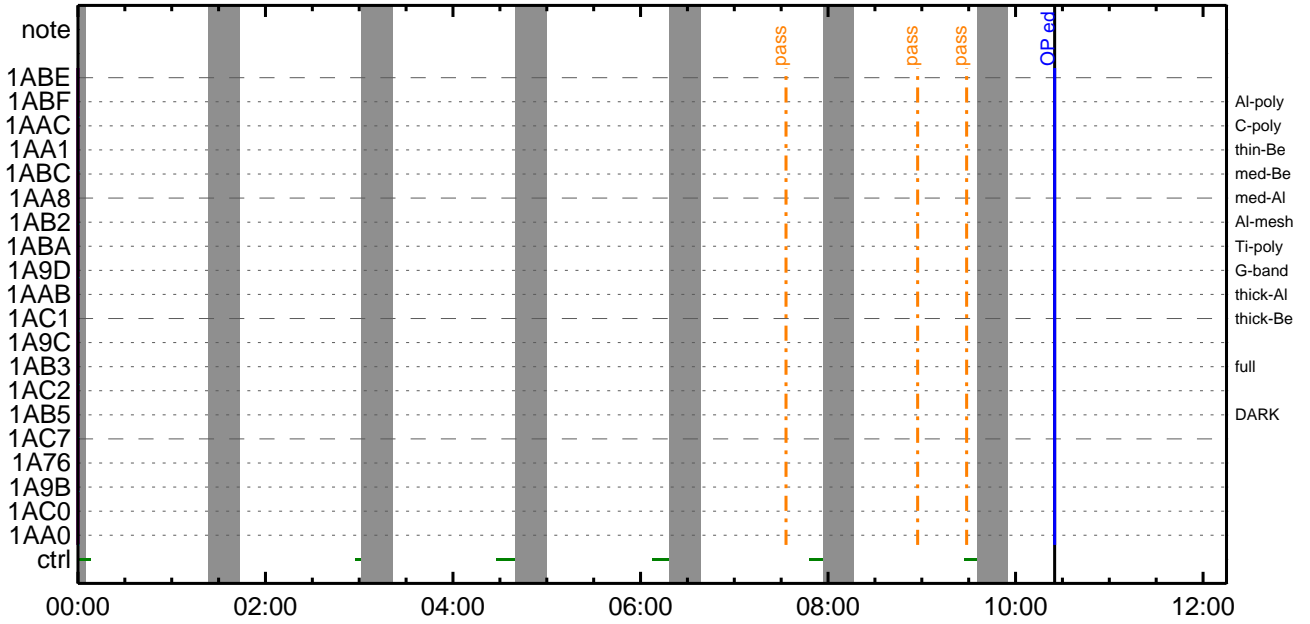
CMDI #0331 2015/08/19



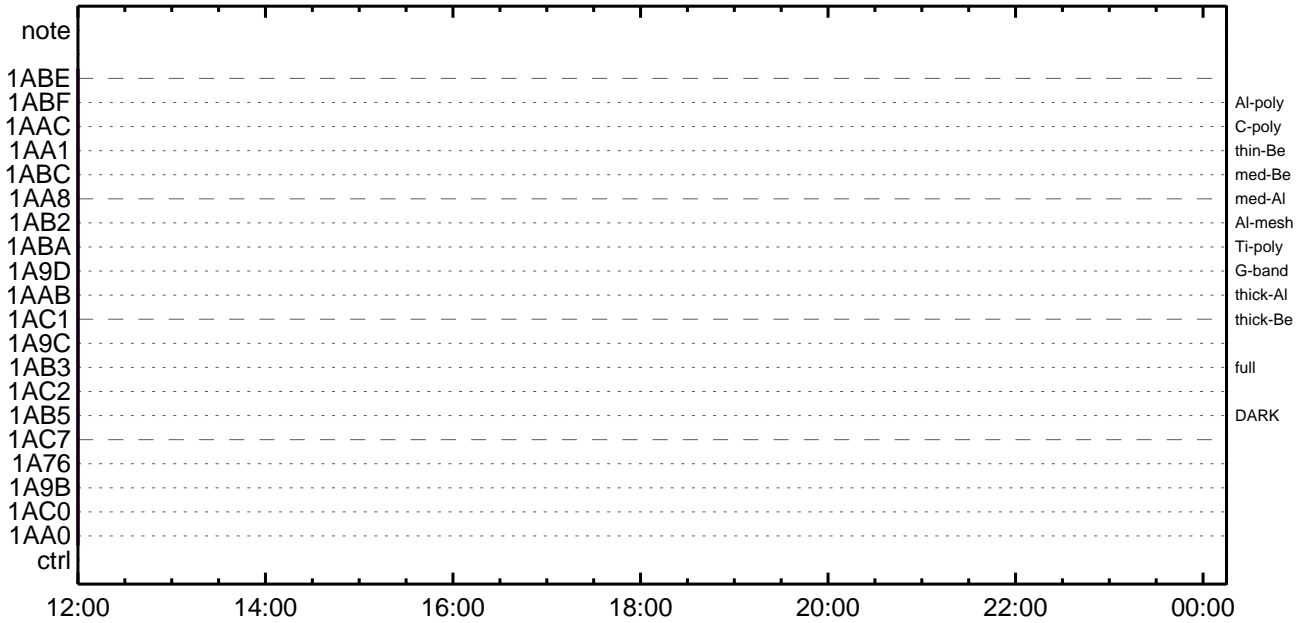
CMDI #0331 2015/08/19



CMDI #0331 2015/08/20



CMDI #0331 2015/08/20







```
0096 C.      01-01 SET DUMP 01-01 01-01 01-01 01-01 01-01
0097 C.
0098 C. TI 2015-08-15 10:05:00.0
0099 +. TI 2015-08-15 10:05:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.      [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2015-08-15 10:05:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.      [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2015-08-15 10:05:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.      [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2015-08-15 10:09:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.      [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.      01-01 01-01 01-01 01-01 01-01
0116 C.      [ ] [HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C.      [ ] [HK1_TI_CMD_NUM] EQ 4
0118 C.      [ ] [HK1_NEXT_EXEC_PIM] EQ DHU
0119 C.      [ ] [HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 C. *****
0122 C. TI 01-01 01-01 01-01
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.      [ ] [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.      [ ] [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.      [ ] [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.      [ ] [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.      [ ] [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.      [ ] [HK1_PKT_FORM_NO] EQ 7
0136 C.      [ ] [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.      [ ] [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.      [ ] [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.      [ ] [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C.      01-01 01-01 01-01 01-01
0142 C.      [ ] [HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 C. RAM ID=TI_TBL 01-01 01-01 01-01 01-01
0145 C.
0146 C. DHU 01-01 01-01 01-01 01-01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.      [ ] [HK1_PKT_FORM_NO] EQ 2
0150 C.      [ ] [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.      [ ] [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.      [ ] [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2015-08-15 10:09:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2015-08-15 10:09:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.      [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. *****
0171 C. SOT TI command set
0172 C. *****
0173 C. Execute, after the success of OP upload.
0174 +. TI 2015-08-15 10:09:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC (41)
0177 C. -----
0178 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0179 C. -----
0180 C. ***** SOT END *****
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2015-08-15 10:09:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.      [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 01-01 01-01 01-01 01-01 *****
0192 C. (%01-01 01-01 01-01 01-01 01-01)
0193 S. DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-319 2015-08-15 13:21:39 85 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŸYÄY~¼Ä»Û;ã
0005 C.
0006 C. YÄYB;¼Y³YFYóYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁÍ;È¿¿òÄò·µ°Æ»Í×ÀÇòÍYçYÄY×YÍ;¼YÉ;ÈÈè%µ·íÉ;ÈòÈ¼°ÇÔò·ò¿¼í¹çòÍ;çÄ®, ùò¹òèòòòÇÄ+¿®ò·òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_CHG_ENA
0047 BC (20)
0048 . C. Verify EIS_MODE_CHG_FLG is ENA
0049 +. DC 07-FC EIS_MODE_MANU
0050 BC (21 02)
0051 . C. Verify EIS in MANUAL mode
0052 . C. Estimated OBSTBL upload time is 2m2s
0053 C. *****
0054 C. EIS START OBSTBL LOAD
0055 C. *****
0056 . S. RAM ram-820:EIS_OBSTBL
0057 ( )
0058 +. DC 07-FC EIS_DUMP_OBSTBL
0059 BC (07 07 07 00 00 70 00)
0060 C.
0061 C. Execute, after the success of OBSTBL upload.
0062 C. Set EIS TI-commands
0063 +. TI 2015-08-15 10:09:50.0
0064 DC 07-FC EIS_MODE_CHG_ENA
0065 BC (20)
0066 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0067 C. *****
0068 C. EIS END OBSTBL LOAD
0069 C. *****
0070 C.
0071 . C. ***** MDP `ûÃîôî»ò%YòÈÄò¹òèDCBC•×²è *****
0072 C. (%á°ïYóYÄYÈYB;¼Y³YFYóYÉÄ+¿®%¼ò¼Ä»Ûò¹òè)
0073 . S. DC-BC dcbc-402:DCBC
0074 (MDP_known_event)
0075 C.
0076 C.
0077 . C. ***** YDÿ¹.İ Daily±¿İÑòÈ`øò¹òèDCBC•×²è *****
0078 . S. DC-BC dcbc-153:DCBC
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080 C.
0081 C.
0082 . C. ;ãLOSŸÁYŸYÄY~¼Ä»Û;ã
0083 C.
0084 . C. ***** LOS *****
0085 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-320 2015-08-15 13:21:39 156 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È×ÁÇøíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøí;çÁ®, ùø¹øèøøøçÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 85 83 04 04)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 20 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 80 08 20)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 96 08 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d 80 60 20 18)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0e a0 80 18 20)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 0f 80 80 06 06)
0062 + DC 07-F0 MDP_XRT_ROI_SET
0063 BC (cd 10 80 80 08 08)
0064 + DC 07-F0 MDP_XRT_FLD_ENA
0065 BC (d8)
0066 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0067 BC (c8)
0068 + DC 07-F0 MDP_XRT_AEC_RESET
0069 BC (d0)
0070 + DC 07-F0 MDP_XRT_ARS_DIS
0071 BC (d5)
0072 + DC 07-F0 MDP_XRT_FLD_RESET
0073 BC (da)
0074 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0075 BC (c4 0a)
0076 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0077 BC (c5 03)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C.
0081 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0082 C.
0083 +. DC 07-F0 MDP_XRT_MODE_OBSV
0084 BC (c2)
0085 +. TI 2015-08-15 10:09:02.0
0086 DC 07-F0 MDP_XRT_MODE_OBSV
0087 BC (c2)
0088 . C. ----- Success Verify ? OK / NG ____
0089 C.
0090 C. ***** XRT END *****
0091 . C. *****
0092 C. SOT table upload
0093 C. *****
0094 . C. < Stop FG table >
0095 +. DC 07-F0 MDP_FG_CTRL_MANU
```

```

0096 BC (51)
0097 . C. -----
0098 C. MDP_FG_CTRL_MODE = MANU [ ]
0099 C. -----
0100 C.
0101 . C. <Upload FG Observation Table>
0102 . S. RAM ram-269:MDP_OBS_F
0103 ( )
0104 C.
0105 . C. < Dump RAMID=MDP_OBS_F >
0106 +. DC 07-F0 MDP_DUMP_FGTBL
0107 BC (82 07 00 00 00 38 b8)
0108 C. -----
0109 C. MDP_OBS_F verify = OK/NG [ ]
0110 C. -----
0111 C.
0112 . C. < Stop SP table >
0113 +. DC 07-F0 MDP_SP_CTRL_MANU
0114 BC (61)
0115 C. -----
0116 C. MDP_SP_CTRL_MODE = MANU [ ]
0117 C. -----
0118 C.
0119 . C. <Upload SP Observation Table>
0120 . S. RAM ram-282:MDP_OBS_S
0121 ( )
0122 C.
0123 . C. < Dump RAMID=MDP_OBS_S >
0124 +. DC 07-F0 MDP_DUMP_SPTBL
0125 BC (83 07 00 00 00 38 b8)
0126 C. -----
0127 C. MDP_OBS_S verify = OK/NG [ ]
0128 C. -----
0129 C.
0130 C. *****
0131 C. SOT TI command set
0132 C. *****
0133 C. Execute, after the success of TBL upload.
0134 +. TI 2015-08-15 10:09:18.0
0135 DC 07-F0 MDP_SOT_MODE_OBSV
0136 BC (40)
0137 . C. -----
0138 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0139 C. -----
0140 C.
0141 C.
0142 . C. ***** MDP `ûÃîñî»ô%ÿñÊÃð¹ñèDCBC•x²è *****
0143 C. (%ã°îÿÓÿÃÿÈÿPÿËÿâÿçÿèñE%¼ñ¼Ã»Ûñ¹ñè)
0144 . S. DC-BC dcbc-402:DCBC
0145 (MDP_known_event)
0146 C.
0147 C.
0148 . C. ***** ÿDÿ¹.Ï Daily±;îÑñÊ´Øñ¹ñèDCBC•x²è *****
0149 . S. DC-BC dcbc-153:DCBC
0150 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0151 C.
0152 C.
0153 . C. ;ãLOSÿÃÿSÿÿÿÿ¼Ã»Û;ã
0154 C.
0155 . C. ***** LOS *****
0156 C.

```

Aug 15, 15 13:21

XRT\_OGLIST\_0331.chk

Page 1/13

\*\*\* OP Sequence for XRT \*\*\*

2015/08/15	10:19:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	10:19:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	10:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/08/15	10:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2015/08/15	10:20:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/08/15	10:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/08/15	10:20:22.0	XRT_AEC_RESET_441_OG [0x1b9]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/08/15	10:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/15	10:20:26.0	XRT_FLD_RESET_447_OG [0x1bf]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/15	10:22:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2015/08/15	10:22:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2015/08/15	10:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/15	11:30:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	11:30:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	11:30:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/15	11:30:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/15	11:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/15	11:53:00.0	XRT_Custom_430_OG [0x1ae]						
2015/08/15	11:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/15	13:09:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	13:09:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	13:09:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/15	13:09:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/15	13:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/15	13:31:00.0	XRT_Custom_430_OG [0x1ae]						
2015/08/15	13:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/15	14:47:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	14:47:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	14:47:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/15	14:47:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/15	14:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/15	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/08/15	15:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2015/08/15	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/08/15	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/08/15	15:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/08/15	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/15	15:00:26.0	XRT_FLD_RESET_404_OG [0x194]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/15	15:34:26.0	XRT_QT_PROG_SET_448_OG [0x1c0]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2015/08/15	15:34:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2015/08/15	15:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/15	16:03:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	16:03:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/15	16:04:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 02 a7 50 e5			
2015/08/15	17:11:00.5	AOCS_Ore-point_Start_4_OG [0x09a]						

2015/08/15	17:26:00.0	AOCs_OrE-point_Start_5_OG [0x09b]	AOCu_NM	5	02-76	00	02	a7	3f	33
2015/08/15	17:41:00.0	AOCs_OrE-point_Start_6_OG [0x09c]	AOCu_NM	5	02-76	00	02	a7	2d	81
2015/08/15	18:47:30.0	AOCs_OrE-point_Start_7_OG [0x09d]	AOCu_NM	5	02-76	00	02	a7	1b	ce
2015/08/15	19:02:24.0	XRT_CTRL_MAnU_402_OG [0x192]	AOCu_NM	5	02-76	00	02	a7	0a	24
2015/08/15	19:02:26.0	XRT_CTRL_MAnU_402_OG [0x192]	MDP_XRT_CTRL_MAnU	1	07-F0			c1		
2015/08/15	19:02:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MAnU	1	07-F0			c1		
2015/08/15	19:02:30.0	AOCs_OrE-point_Start_8_OG [0x09e]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/15	19:02:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCu_NM	5	02-76	00	02	a7	f8	73
2015/08/15	19:05:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0			d9		
2015/08/15	19:05:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0			c9		
2015/08/15	19:05:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0			d5		
2015/08/15	19:05:30.0	XRT_CTRL_AuTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0			c4	0e	
2015/08/15	19:17:30.0	AOCs_OrE-point_Start_9_OG [0x09f]	MDP_XRT_CTRL_AuTO	1	07-F0			c0		
2015/08/15	19:42:30.0	XRT_CTRL_MAnU_400_OG [0x190]	AOCu_NM	5	02-76	00	02	a7	e6	c1
2015/08/15	19:42:32.0	XRT_CTRL_MAnU_402_OG [0x192]	MDP_XRT_CTRL_MAnU	1	07-F0			c1		
2015/08/15	19:42:34.0	XRT_FLD_RESEt_415_OG [0x19f]	MDP_XRT_CTRL_MAnU	1	07-F0			c1		
2015/08/15	19:42:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESEt	1	07-F0			da		
2015/08/15	19:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0			e8		
2015/08/15	20:24:30.0	AOCs_OrE-point_Start_10_OG [0x0a0]	MDP_XRT_PREFLR_STOP	1	07-F0			e9		
2015/08/15	20:39:30.0	AOCs_OrE-point_Start_11_OG [0x0a1]	AOCu_NM	5	02-76	00	02	a7	d5	0f
2015/08/15	20:54:30.0	AOCs_OrE-point_Start_12_OG [0x0a2]	AOCu_NM	5	02-76	00	02	a7	c3	65
2015/08/15	22:01:00.0	AOCs_OrE-point_Start_13_OG [0x0a3]	AOCu_NM	5	02-76	00	02	a7	b1	b3
2015/08/15	22:16:00.0	AOCs_OrE-point_Start_14_OG [0x0a4]	AOCu_NM	5	02-76	00	ed	82	52	35
2015/08/15	22:31:00.0	AOCs_OrE-point_Start_15_OG [0x0a5]	AOCu_NM	5	02-76	00	f2	67	43	a5
2015/08/15	23:31:00.0	AOCs_OrE-point_Start_16_OG [0x0a6]	AOCu_NM	5	02-76	00	f4	5b	33	e7
2015/08/15	23:46:00.0	AOCs_OrE-point_Start_17_OG [0x0a7]	AOCu_NM	5	02-76	00	f5	82	23	8d
2015/08/16	00:01:00.0	AOCs_OrE-point_Start_18_OG [0x0a8]	AOCu_NM	5	02-76	00	f6	25	12	a7
2015/08/16	00:16:00.0	AOCs_OrE-point_Start_19_OG [0x0a9]	AOCu_NM	5	02-76	00	f6	4e	01	68
2015/08/16	01:01:00.0	AOCs_OrE-point_Start_20_OG [0x0aa]	AOCu_NM	5	02-76	00	f6	25	f0	00
2015/08/16	01:16:00.0	AOCs_OrE-point_Start_21_OG [0x0ab]	AOCu_NM	5	02-76	00	f5	82	df	1b
2015/08/16	01:31:00.0	AOCs_OrE-point_Start_22_OG [0x0ac]	AOCu_NM	5	02-76	00	f4	5b	ce	c1
2015/08/16	01:46:00.0	AOCs_OrE-point_Start_23_OG [0x0ad]	AOCu_NM	5	02-76	00	f2	67	bf	03
2015/08/16	02:00:54.0	XRT_CTRL_MAnU_402_OG [0x192]	AOCu_NM	5	02-76	00	ed	82	b0	73
2015/08/16	02:00:56.0	XRT_CTRL_MAnU_402_OG [0x192]	MDP_XRT_CTRL_MAnU	1	07-F0			c1		
2015/08/16	02:00:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MAnU	1	07-F0			c1		
2015/08/16	02:01:00.0	AOCs_OrE-point_Start_24_OG [0x0ae]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/16	02:01:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCu_NM	5	02-76	00	c2	a8	cc	cd
2015/08/16	02:01:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0			d8		
2015/08/16	02:01:22.0	XRT_AEC_RESEt_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0			c8		
2015/08/16	02:01:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESEt	1	07-F0			d0		
2015/08/16	02:01:26.0	XRT_FLD_RESEt_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0			d5		
2015/08/16	02:03:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESEt	1	07-F0			da		
2015/08/16	02:03:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0			c4	07	
2015/08/16	02:04:00.0	XRT_CTRL_AuTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0			c5	03	
2015/08/16	02:14:00.0	XRT_CTRL_MAnU_400_OG [0x190]	MDP_XRT_CTRL_AuTO	1	07-F0			c0		
2015/08/16	02:14:02.0	XRT_CTRL_MAnU_402_OG [0x192]	MDP_XRT_CTRL_MAnU	1	07-F0			c1		
2015/08/16	02:14:02.0	XRT_CTRL_MAnU_402_OG [0x192]	MDP_XRT_CTRL_MAnU	1	07-F0			c1		



Aug 15, 15 13:21

XRT\_OGLIST\_0331.chk

Page 3/13

2015/08/16	02:14:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/16	02:14:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/16	02:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/16	02:38:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/16	02:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	03:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	03:42:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	03:42:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/16	03:42:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/16	03:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/16	04:16:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/16	04:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	05:00:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	05:00:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	05:00:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/16	05:01:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/08/16	05:01:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/16	05:01:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/16	05:01:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/16	05:01:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/16	05:01:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/16	05:03:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2015/08/16	05:03:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/16	05:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	05:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	05:22:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	05:22:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/16	05:22:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/16	05:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/16	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/16	06:00:00.0	AOCS_Ore-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/16	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/16	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/16	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/16	06:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/08/16	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/16	06:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/08/16	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/16	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/16	06:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/16	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/16	06:10:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/16	06:12:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							

Aug 15, 15 13:21

## XRT\_OGLIST\_0331.chk

Page 4/13

2015/08/16	06:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/08/16	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/16	07:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	07:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	07:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	07:02:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/16	07:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/16	07:33:00.0	XRT_Custom_430_OG [0x1ae]					
2015/08/16	07:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/16	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	08:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	08:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/08/16	08:30:00.0	AOCS_Ore-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	03	00 00 00 00
2015/08/16	08:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/16	08:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/16	08:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/16	08:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/16	08:30:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	08:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2015/08/16	08:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/08/16	08:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/16	08:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	08:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	08:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	08:42:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/16	08:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/16	09:11:30.0	XRT_Custom_430_OG [0x1ae]					
2015/08/16	09:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/16	10:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	10:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	10:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	10:23:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/16	10:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/16	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	10:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/08/16	10:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01	00 00 00 00
2015/08/16	10:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/16	10:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/16	10:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/16	10:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/16	10:30:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	10:50:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2015/08/16	10:50:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/08/16	10:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/16	12:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	12:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2015/08/16	12:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/16	12:06:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/16	12:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/16	12:28:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/16	12:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	13:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	13:45:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	13:45:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/16	13:45:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/16	13:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/16	14:18:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/16	14:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	15:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	15:23:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	15:23:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/16	15:23:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/16	15:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/16	16:08:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/16	16:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	17:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	17:01:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	17:01:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/16	17:01:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/16	17:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/16	17:45:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/16	17:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	18:24:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	18:24:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	18:24:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/16	18:25:00.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/16	18:25:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/16	18:27:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/16	18:27:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/16	18:27:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/08/16	18:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	18:34:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	18:34:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	18:34:04.0	XRT_FOCUS_POSITION_409_OG [0x199]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/16	18:34:24.0	XRT_FLD_DIS_414_OG [0x19e]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/16	18:36:54.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/16	18:36:56.0	XRT_ARS_DIS_416_OG [0x1a0]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/16	18:36:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2015/08/16	18:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/16	18:50:00.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/16	19:19:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	19:19:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/16	19:19:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/16	19:19:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/16	19:19:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							

2015/08/16	19:19:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/16	19:19:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/16	19:19:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/16	19:22:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	19:22:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2015/08/16	19:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/08/16	20:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/16	20:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	20:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	20:18:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	20:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/16	20:59:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/16	21:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/08/16	21:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/16	21:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	21:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	21:57:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	22:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/16	22:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/16	22:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/08/16	22:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/16	22:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	22:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	22:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/08/16	22:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01	00 00 00 00
2015/08/16	22:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/16	22:50:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/16	22:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/16	22:50:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/16	22:52:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	22:52:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2015/08/16	22:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/08/16	23:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/16	23:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	23:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/16	23:35:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/16	23:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/17	00:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/17	00:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/08/17	00:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/17	00:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	00:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	00:30:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/08/17	00:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	00 00 00 00
2015/08/17	00:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/17	00:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/17	00:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/17	00:30:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	

Aug 15, 15 13:21

## XRT\_OGLIST\_0331.chk

Page 7/13

2015/08/17	00:32:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/17	00:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2015/08/17	00:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/17	01:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/17	01:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	01:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	01:13:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/17	01:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/17	01:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/17	01:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2015/08/17	02:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/08/17	02:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/17	02:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	02:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	02:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/17	02:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCS_Ore-point_Start_2_OG [0x098]							
2015/08/17	02:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]	AOCU_NM	5	02-76	01	00	00	00	00
2015/08/17	02:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	XRT_FLD_ENA_411_OG [0x19b]							
2015/08/17	02:30:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/17	02:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/17	02:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/17	02:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	XRT_ARS_DIS_423_OG [0x1a7]							
2015/08/17	02:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/17	02:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FLD_RESET_447_OG [0x1bf]							
2015/08/17	02:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/17	02:49:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	XRT_QT_PROG_SET_448_OG [0x1c0]							
2015/08/17	02:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2015/08/17	03:13:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/08/17	03:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/17	04:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	04:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	04:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	04:18:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/17	04:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/17	04:52:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/17	04:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CUSTOM_430_OG [0x1ae]							
2015/08/17	05:29:28.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/08/17	05:29:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/17	05:29:32.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	05:31:00.0	AOCS_Ore-point_Start_25_OG [0x0af]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	05:33:32.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_RECAL	2	07-F8	78	00			
2015/08/17	05:33:52.0	XRT_FLD_DIS_420_OG [0x1a4]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/17	05:33:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	XRT_FLD_DIS_420_OG [0x1a4]							
2015/08/17	05:33:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/17	05:33:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/17	05:34:00.5	XRT_CTRL_AUTO_408_OG [0x198]	XRT_QT_PROG_SET_427_OG [0x1ab]							
2015/08/17	05:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/08/17	05:40:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/17	05:40:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				

Aug 15, 15 13:21

## XRT\_OGLIST\_0331.chk

Page 8/13

2015/08/17	05:40:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00	
2015/08/17	05:41:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/08/17	05:41:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/17	05:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/17	05:41:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/17	05:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/17	05:41:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/17	05:43:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2015/08/17	05:43:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/08/17	05:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/17	05:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	05:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	05:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/17	05:58:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/17	06:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/17	06:30:30.0	XRT_Custom_430_OG [0x1ae]					
2015/08/17	06:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/17	07:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	07:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	07:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/17	07:39:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/17	07:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/17	08:08:30.0	XRT_Custom_430_OG [0x1ae]					
2015/08/17	08:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/17	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	08:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	08:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/08/17	08:30:00.0	AOCS_Ore-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/08/17	08:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/17	08:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/17	08:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/17	08:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/17	08:30:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/17	08:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2015/08/17	08:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/08/17	08:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/17	09:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	09:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	09:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/17	09:18:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/17	09:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/17	09:47:00.0	XRT_Custom_430_OG [0x1ae]					
2015/08/17	09:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/17	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/17	10:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/08/17	10:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/08/17	10:30:18.0	XRT_FLD_ENA_411_OG [0x19b]					

Aug 15, 15 13:21

## XRT\_OGLIST\_0331.chk

Page 9/13

2015/08/17	10:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/08/17	10:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/08/17	10:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/17	10:30:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/17	10:32:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2015/08/17	10:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/08/17	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/17	11:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	11:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	11:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/17	11:04:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/17	11:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/17	11:25:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/17	11:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/17	12:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	12:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	12:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/08/17	12:42:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/08/17	12:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/08/17	13:04:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/17	13:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/17	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	13:59:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/08/17	14:00:00.0	AOCS_Ore-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00	ad	59	00 00
2015/08/17	14:00:18.0	XRT_FLD_DIS_421_OG [0x1a5]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/08/17	14:02:54.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/08/17	14:02:56.0	XRT_ARS_DIS_416_OG [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/17	14:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2015/08/17	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/17	16:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	16:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	16:19:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/08/17	16:20:00.0	AOCS_Ore-point_Start_28_OG [0x0b2]	AOCU_NM	5	02-76	00	00	00	54 00
2015/08/17	16:20:18.0	XRT_FLD_DIS_421_OG [0x1a5]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/08/17	16:22:54.0	XRT_FLRCTRL_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/08/17	16:22:56.0	XRT_ARS_DIS_416_OG [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/17	16:22:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2015/08/17	16:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/08/17	18:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	18:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/08/17	18:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2015/08/17	18:20:00.0	AOCS_Ore-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00	00	00	00 00
2015/08/17	18:20:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/08/17	18:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/08/17	18:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/08/17	18:22:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		

Aug 15, 15 13:21

XRT\_OGLIST\_0331.chk

Page 10/13

2015/08/17	18:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/17	18:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	18:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	18:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/17	18:30:00.0	AOCS_Orе-point_Start_29_OG [0x0b3]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/08/17	18:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/17	18:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/17	18:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/17	18:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/17	18:30:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/17	18:32:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2015/08/17	18:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/17	18:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/17	19:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	19:16:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	19:16:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/17	19:16:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/17	19:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/17	19:57:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/17	19:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/17	20:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	20:54:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	20:54:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/17	20:54:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/17	20:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/17	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	20:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	20:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/17	21:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/08/17	21:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/17	21:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/17	21:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/17	21:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/17	21:00:26.0	XRT_FLD_RESET_422_OG [0x1a6]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/17	21:34:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2015/08/17	21:34:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/17	21:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/17	22:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	22:33:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/17	22:33:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/17	22:33:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/17	22:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/17	23:08:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/17	23:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/18	00:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/18	00:11:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/18	00:11:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/18	00:11:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							



Aug 15, 15 13:21

## XRT\_OGLIST\_0331.chk

Page 11/13

2015/08/18	00:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/18	00:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	00:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	00:34:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/18	00:35:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00
2015/08/18	00:35:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/18	00:35:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/18	00:35:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/18	00:35:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/18	00:35:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/18	00:37:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/08/18	00:37:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/18	00:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/18	01:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	01:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	01:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/18	01:48:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/18	01:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/18	02:10:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/18	02:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/18	02:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	02:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	02:34:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/08/18	02:35:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00
2015/08/18	02:35:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/08/18	02:35:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/08/18	02:35:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/08/18	02:35:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/08/18	02:35:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/18	02:37:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/08/18	02:37:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/08/18	02:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/18	03:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	03:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	03:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/18	03:23:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/18	03:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/18	03:49:00.0	XRT_Custom_430_OG [0x1ae]				
2015/08/18	03:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/18	04:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	04:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	04:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/08/18	04:54:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/08/18	04:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/18	05:27:30.0	XRT_Custom_430_OG [0x1ae]				
2015/08/18	05:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/18	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/18	06:05:26.0	XRT_CTRL_MANU_402_OG [0x192]				

Saturday August 15, 2015

11/13

Aug 15, 15 13:21

## XRT\_OGLIST\_0331.chk

Page 12/13

2015/08/18	06:05:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	06:05:30.0	AOCS_OrE-point_Start_25_OG [0x0af]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/08/18	06:05:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/08/18	06:08:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/08/18	06:08:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/08/18	06:08:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/18	06:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2015/08/18	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/18	06:15:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	06:15:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	06:15:30.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/08/18	06:15:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/08/18	06:15:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/18	06:15:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/18	06:15:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/18	06:15:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/18	06:18:26.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/18	06:18:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2015/08/18	06:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/08/18	06:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/18	06:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	06:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	06:35:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/18	06:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/18	07:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/18	07:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/08/18	08:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/18	08:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	08:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	08:15:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/18	08:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/08/18	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/08/18	08:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	08:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	08:30:00.0	AOCS_OrE-point_Start_26_OG [0x0b0]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/08/18	08:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/08/18	08:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/08/18	08:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/08/18	08:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/08/18	08:30:26.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/08/18	08:44:56.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/08/18	08:44:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2015/08/18	08:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/08/18	09:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/08/18	09:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	09:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/08/18	09:55:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	

2015/08/18	09:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/08/18	10:29:00.0	AOCS_OrE-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00 00 00 00 00