

XRT Timeline to be uploaded on 2015/08/25

Period: 2015/08/25 09:52:00 - 2015/08/29 10:19:00

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Normal mode

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XOB #1AAB: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Ti/Poly(1024ms) - w leak image-3ms

Term	Pointing (x, y)	Comment
08/25 13:03:00 - 08/25 13:09:54	Fixed (-528.4, -528.4)	Four Quadrant Q1
PROG= 05 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 86 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 34 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1A9F: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1024ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
08/25 13:13:00 - 08/25 13:19:54	Fixed (528.4, -528.4)	Four Quadrant Q2
PROG= 15 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 15 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 34 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1AA0: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1024ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
08/25 13:23:00 - 08/25 13:29:54	Fixed (528.4, 528.4)	Four Quadrant Q3
PROG= 04 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 35 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 34 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1AA1: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1024ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
08/25 13:33:00 - 08/25 13:39:54	Fixed (-528.4, 528.4)	Four Quadrant Q4
PROG= 06 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 3 1-time(s) 12.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		

Seqn= 34 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 54 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ACA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/25 13:47:00 - 08/25 15:39:54	Track (331.0, -375.2) ^{Ⓢ 08/25 13:40:00}	AR 12403 cont.
08/25 16:36:00 - 08/25 17:14:54	Track (348.5, -374.3) ^{Ⓢ 08/25 15:50:00}	AR 12403 cont. (HOPS 245 and 173)
08/26 04:13:00 - 08/26 08:06:00	Track (445.3, -368.7) ^{Ⓢ 08/26 04:10:00}	AR12403 cont. (HOP173 4-6UT)
08/26 08:33:00 - 08/26 17:57:24	Track (477.9, -366.4) ^{Ⓢ 08/26 08:30:00}	AR12403 (for contingency plan)
08/26 18:47:30 - 08/27 04:19:54	Track (547.3, -360.7) ^{Ⓢ 08/26 18:07:30}	AR12403 cont. (HOP173 2-4UT)
08/27 04:33:00 - 08/27 10:35:54	Track (616.8, -353.7) ^{Ⓢ 08/27 04:30:00}	AR12403 cont. (HOP173 4-6UT)

PROG= 18 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 56 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 4-time(s) 2.0sec												
Seqn= 75 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 23 4-time(s) 240.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-

Term	Pointing (x, y)	Comment
08/25 15:43:00 - 08/25 15:49:54	Fixed (0.0, 0.0)	synoptic, shifted manually
08/26 04:03:00 - 08/26 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually for HOP173
08/26 18:00:30 - 08/26 18:07:24	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min
08/27 04:23:00 - 08/27 04:29:54	Fixed (0.0, 0.0)	synoptic shifted manually for HOP173

PROG= 14 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 33 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 61 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 39 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ACE: HOP252 - Al/Poly - 384FOV - 1x1 - Q98 - 15s Cad

Term	Pointing (x, y)	Comment
08/25 17:18:00 - 08/25 18:12:00	Track (348.5, -374.3) ^{Ⓢ 08/25 15:50:00}	AR 12403 cont. (HOPS 245 and 173)

PROG= 02 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 56		1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		120-time(s)		15.0sec									
Seqn= 85		1-time(s)		2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	DPCM	3	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AD0: AR Abnormal-B(Morphology), HOP245 Be-thin w/ 24s cad and Be-med PFB, multifilter context w/ 20 min cad, G-band test w/ 60 min cad, 192x192

Term	Pointing (x, y)	Comment
08/25 18:15:06 - 08/26 03:59:54	Track (348.5, -374.3) @ 08/25 15:50:00	AR 12403 cont. (HOPS 245 and 173)

PROG= 13 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 81		1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	192x256 (1064, 1048)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	192x256 (1064, 1048)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	192x256 (1064, 1048)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Dark	250ms	Obs	1x1	192x256 (1064, 1048)	Q=98	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Dark	250ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		3-time(s)		2.0sec									
Seqn= 66		3-time(s)		2.0sec									
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 88		50-time(s)		2.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	192x256 (1064, 1048)	Q=95	3	0	8.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	8.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	8.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * Flare mode * * * * *

XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)

Term	Pointing (x, y)	Comment
08/25 13:47:00 - 08/25 15:39:54	Track (331.0, -375.2) @ 08/25 13:40:00	AR 12403 cont.
08/25 16:36:00 - 08/25 17:14:54	Track (348.5, -374.3) @ 08/25 15:50:00	AR 12403 cont. (HOPS 245 and 173)
08/25 18:15:06 - 08/26 03:59:54	Track (348.5, -374.3) @ 08/25 15:50:00	AR 12403 cont. (HOPS 245 and 173)
08/26 04:13:00 - 08/26 08:06:00	Track (445.3, -368.7) @ 08/26 04:10:00	AR12403 cont. (HOP173 4-6UT)
08/26 08:33:00 - 08/26 17:57:24	Track (477.9, -366.4) @ 08/26 08:30:00	AR12403 (for contingency plan)
08/26 18:47:30 - 08/27 04:19:54	Track (547.3, -360.7) @ 08/26 18:07:30	AR12403 cont. (HOP173 2-4UT)
08/27 04:33:00 - 08/27 10:35:54	Track (616.8, -353.7) @ 08/27 04:30:00	AR12403 cont. (HOP173 4-6UT)

PROG= 03 30-time(s)													
Subr= 1		20-time(s)		2.0sec									
Seqn= 11		1-time(s)		2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100		1-time(s)		10.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec									
Seqn= 10		1-time(s)		2.0sec									
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)		2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 53		1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * Active Region Search * * * * *

NOT USED

* * * * * Flare Detection * * * * *

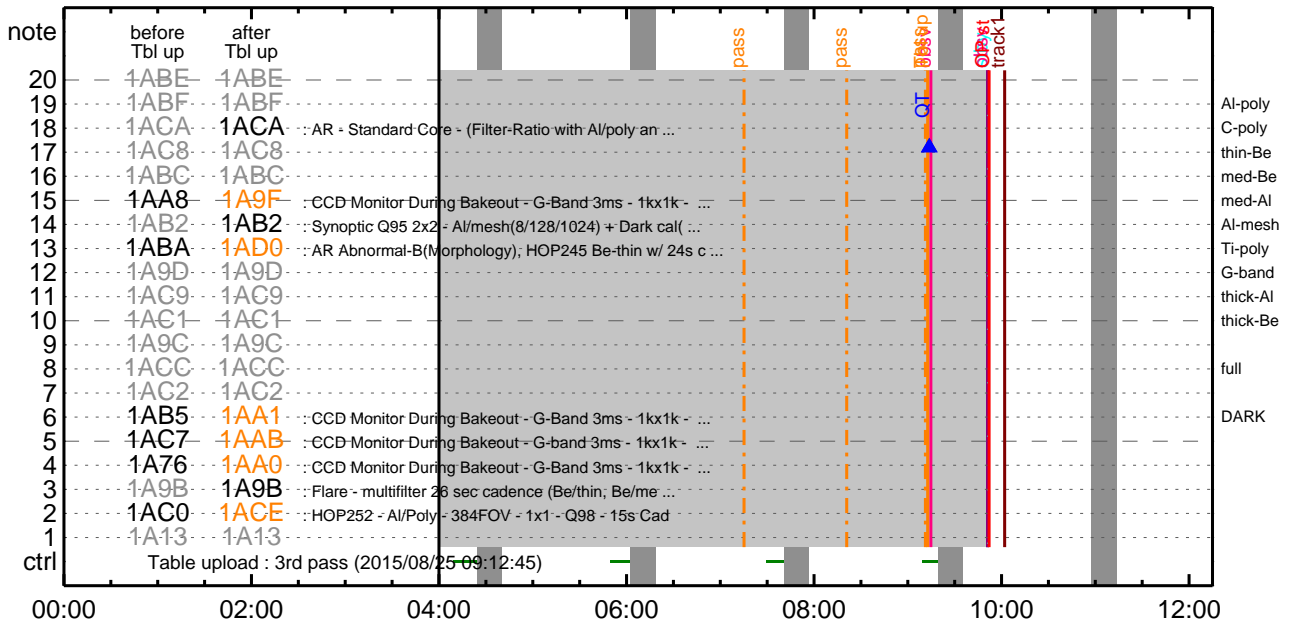
FLD Patrol

Term	Pointing (x, y)	Comment
08/25 13:44:18 - 08/25 15:40:18	Track (331.0, -375.2) @ 08/25 13:40:00	AR 12403 cont.
08/25 15:50:23 - 08/25 17:15:18	Track (348.5, -374.3) @ 08/25 15:50:00	AR 12403 cont. (HOPS 245 and 173)

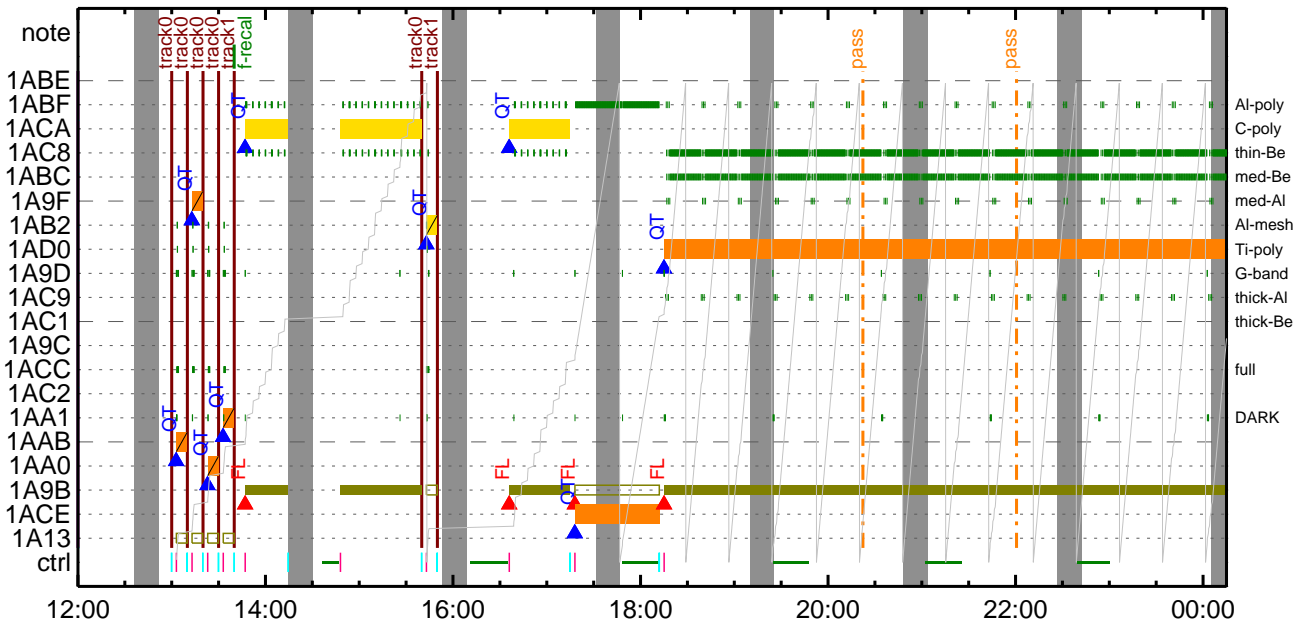
08/25 18:12:24 - 08/26 04:00:18 Track (348.5, -374.3) @ 08/25 15:50:00 AR 12403 cont. (HOPS 245 and 173)
 08/26 04:10:18 - 08/26 17:57:48 Track (445.3, -368.7) @ 08/26 04:10:00 AR12403 cont. (HOP173 4-6UT)
 08/26 18:07:48 - 08/27 04:20:18 Track (547.3, -360.7) @ 08/26 18:07:30 AR12403 cont. (HOP173 2-4UT)
 08/27 04:30:18 - 08/29 10:19:00 Track (616.8, -353.7) @ 08/27 04:30:00 AR12403 cont. (HOP173 4-6UT)

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

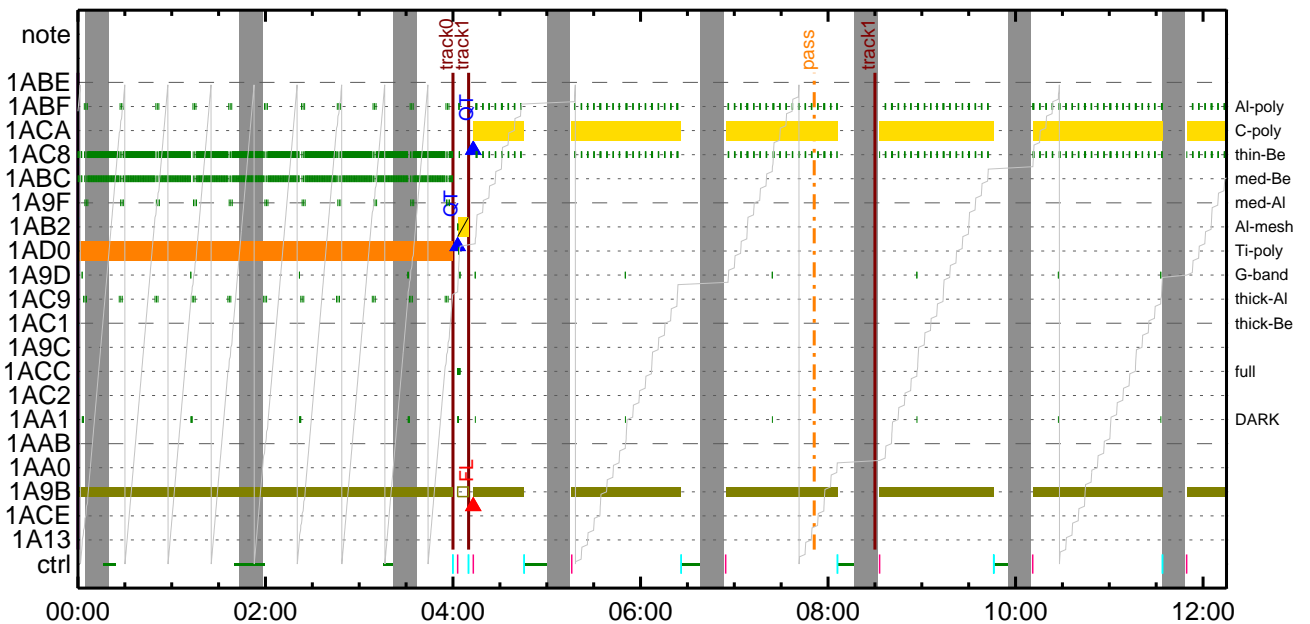
CMDI #0351 2015/08/25



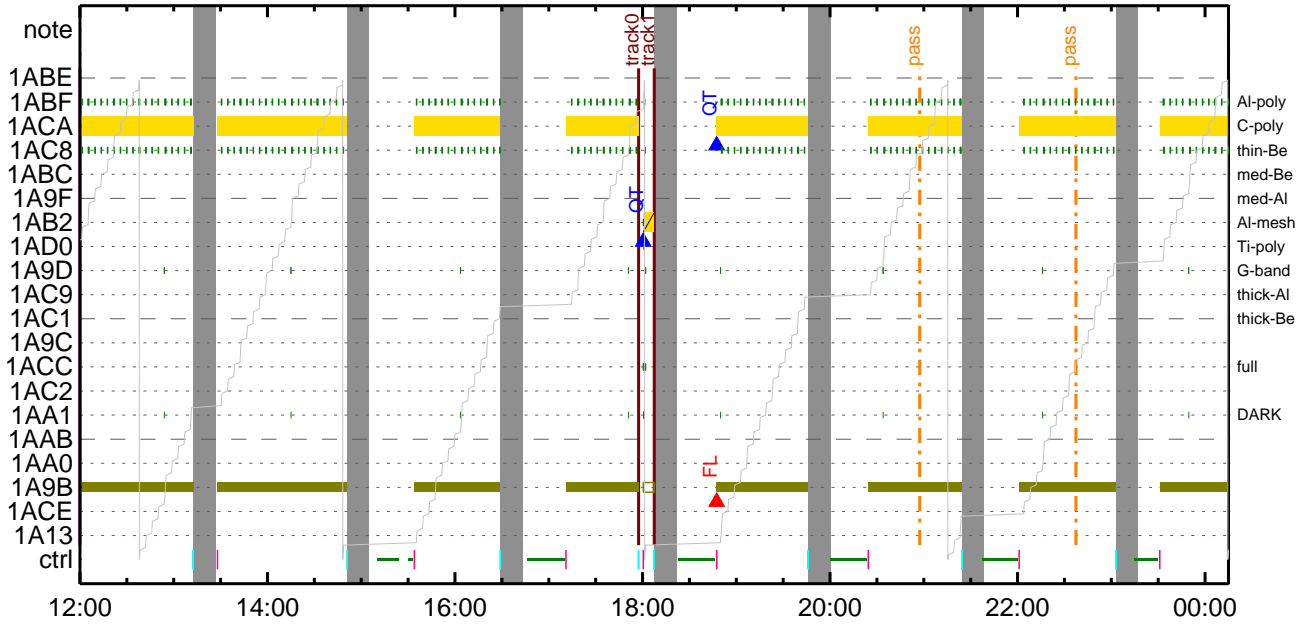
CMDI #0351 2015/08/25



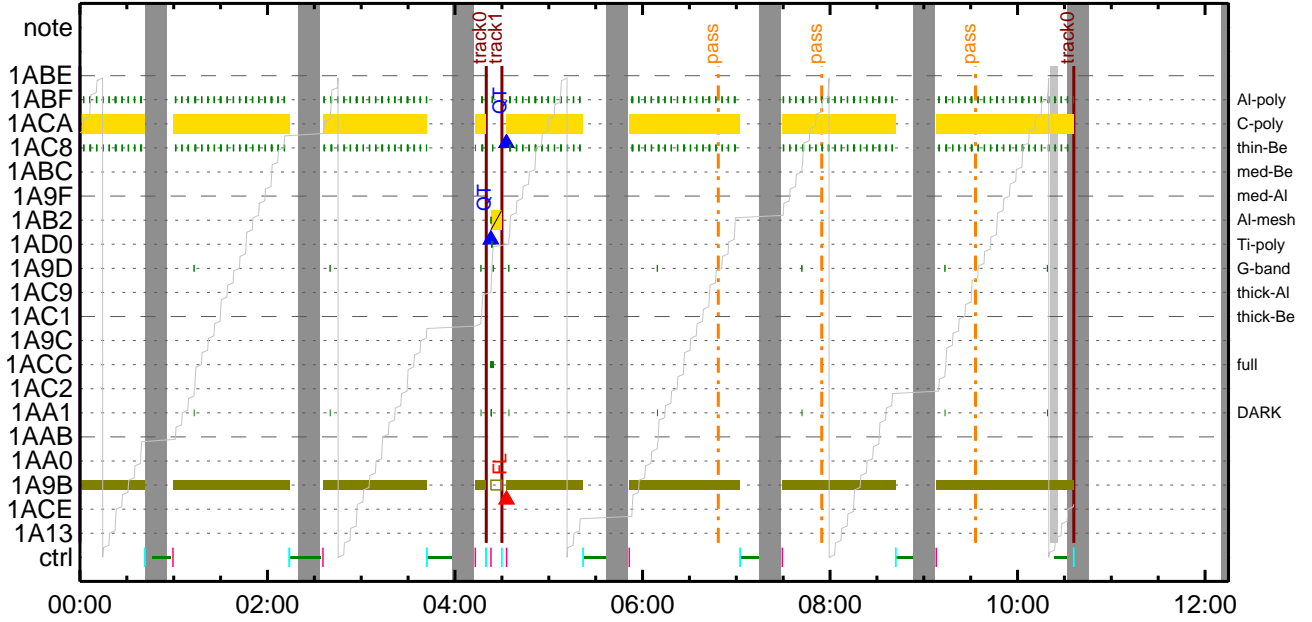
CMDI #0351 2015/08/26



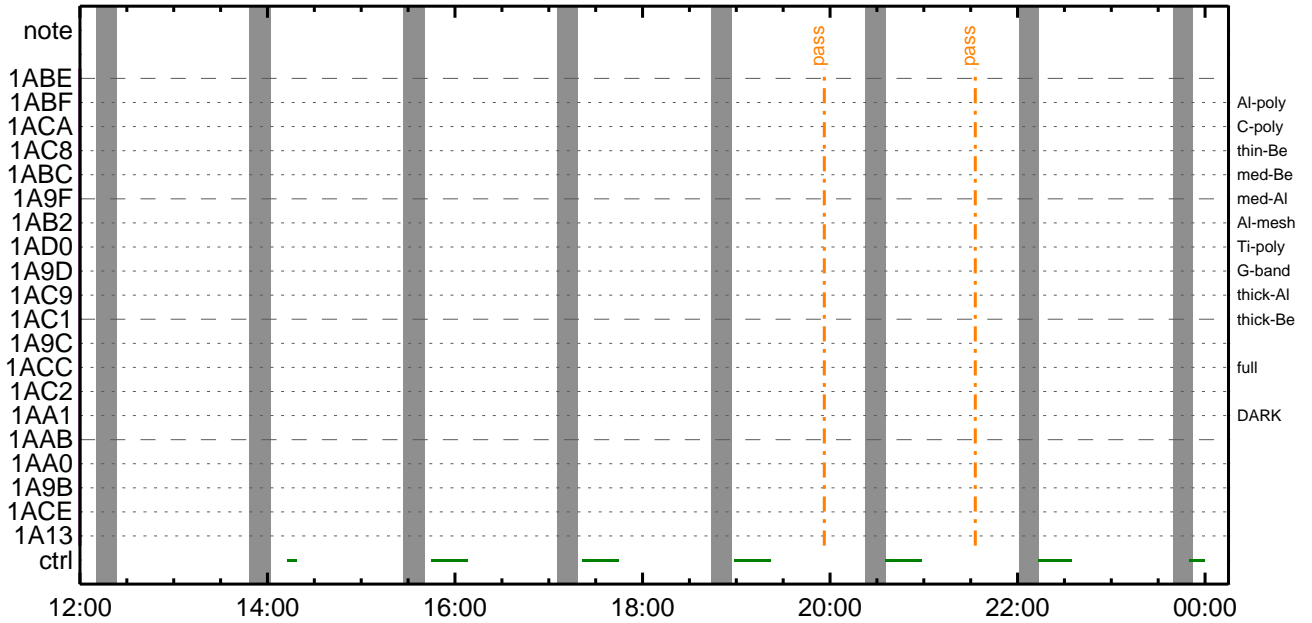
CMDI #0351 2015/08/26



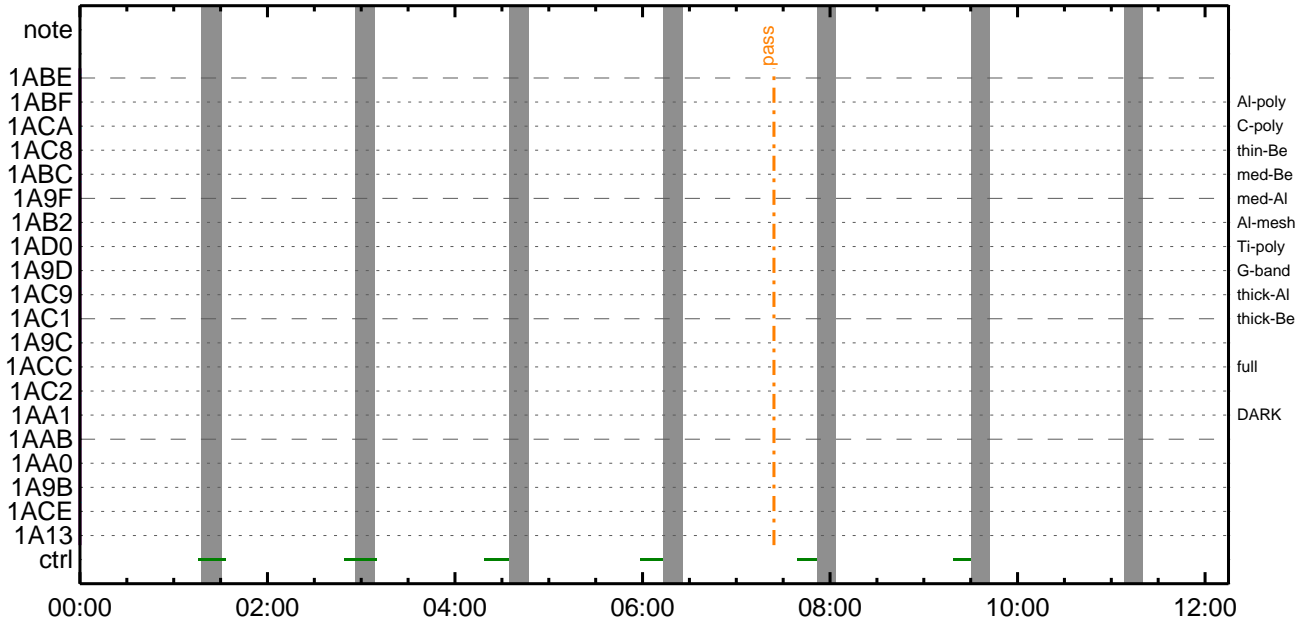
CMDI #0351 2015/08/27



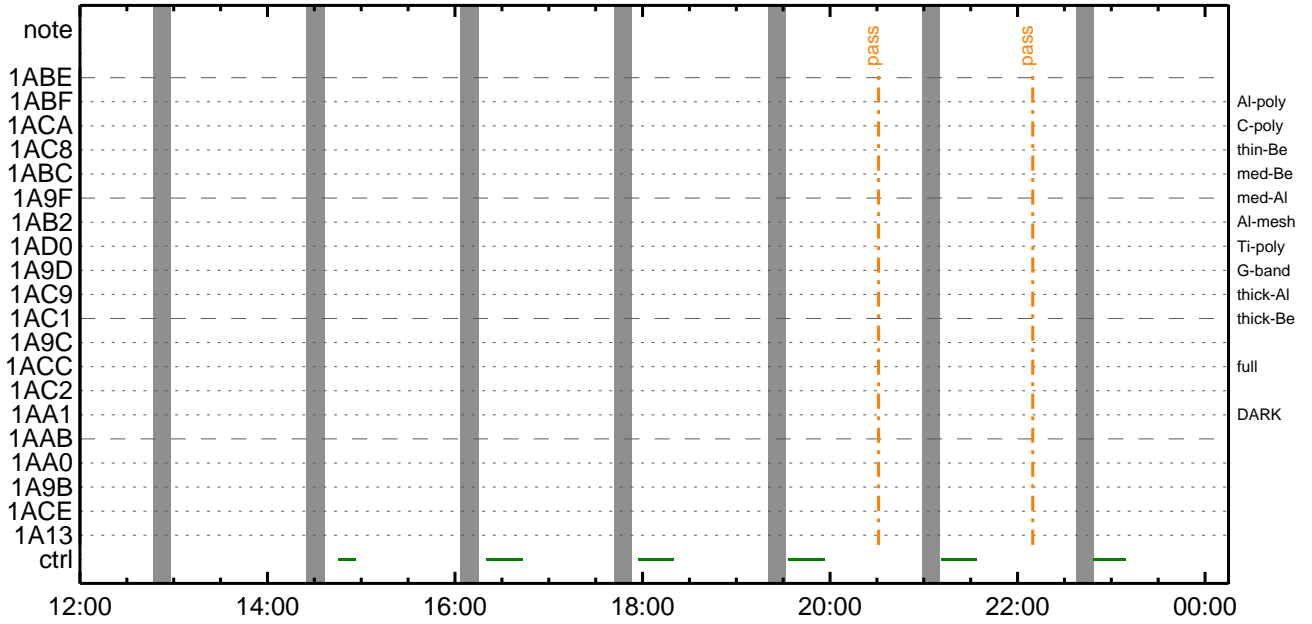
CMDI #0351 2015/08/27



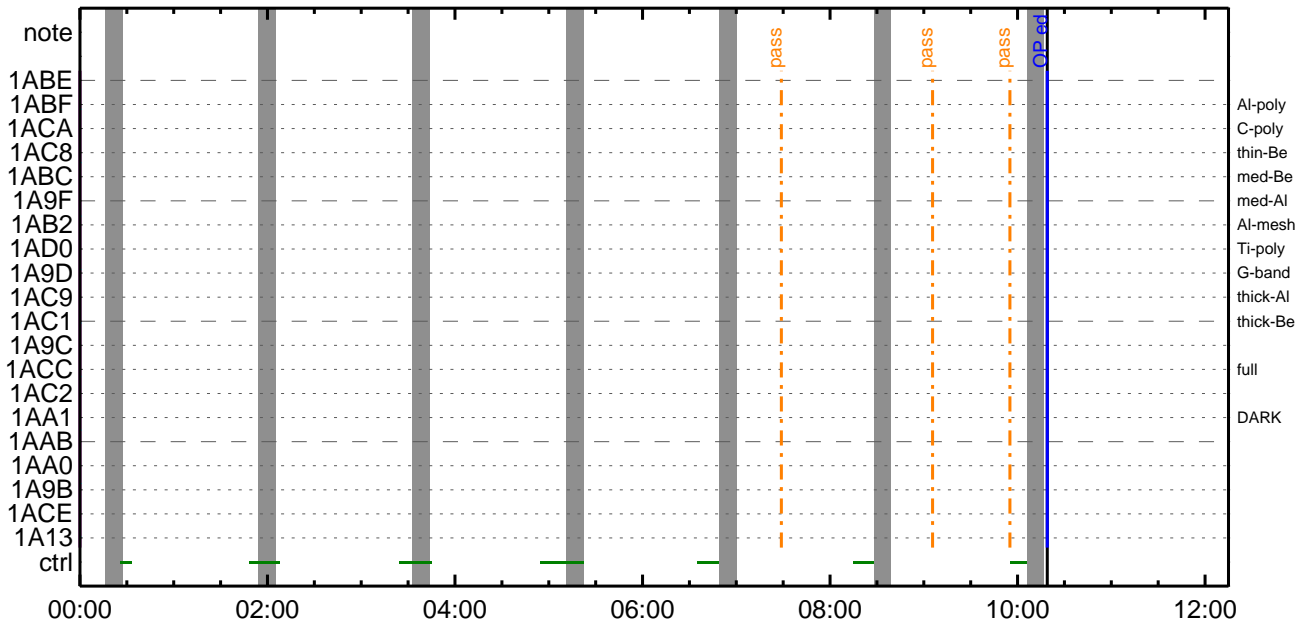
CMDI #0351 2015/08/28



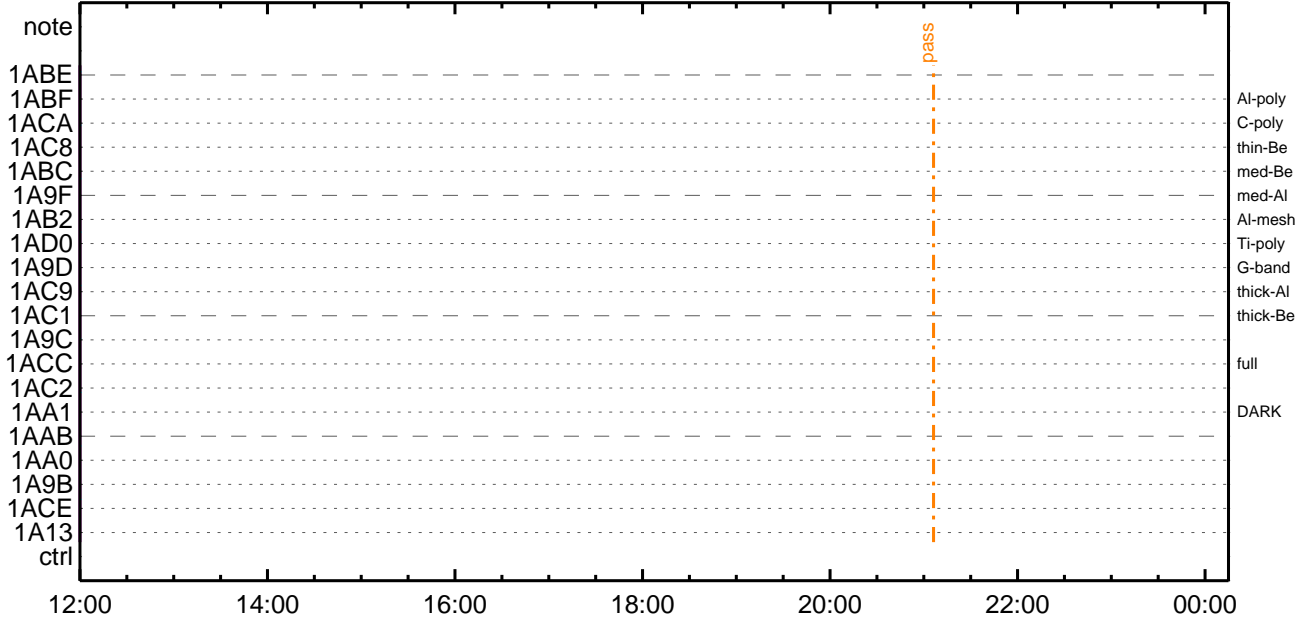
CMDI #0351 2015/08/28



CMDI #0351 2015/08/29



CMDI #0351 2015/08/29



(a) Spacecraft Operation Procedure (real-commands)

```
main-334 2015-08-25 12:39:10 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁY$;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¸¿¸Á¸¸µ°¸»Í×ÁÇ¸¸ÍYçYÁY×Yí;¼YÉ;ÈÈÈ¼µ•íÉ;ÈÈ¼°Ç¸¸µ¿¼í¹ç¸¸í;çÀ®, ù¸¹¸È¸¸¸çÁ+¿®¸¸¸È¸¸¸¸¸È; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-334:OP
0020 ( )
0021 . S. OG og-334:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPÍî°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYóY×¼ªªÎ»¸¸³ÍÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOG¸¸Í¼È¹ç•è²ÍOK¸¸³ÍÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYóY×¼ªªÎ»¸¸³ÍÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOG¸¸Í¼È¹ç•è²ÍOK¸¸³ÍÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYóY×¼ªªÎ»¸¸³ÍÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OP¸¸Í¼È¹ç•è²ÍOK¸¸³ÍÇ$
0081 C.
0082 . C. ***** ²¼¸¸Í¼Á´¼¼¸¸ÈÈ-¸¸Á+¿® (¼¼µ-YÁYóY×¼¼¿ç¸¸¸ÁÓÁ¸ç¼ªª¼¸¸¸¼¼í¹ç¸¸ç¸¸á) *****
0083 C. DHUYá;¼YÉ;È¼Y¼;Yí;¼YÉ;È¸¸Íá¸¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOAD¸¸-Á+¿®NG¸¸Í¼¹¹ç;ç°È²¼¸¸ÍTI-CMDÁ+¿®¼¼Á¹Ô¸¸¸È¸¸¸¸¸È; f
```

0096 C. 0p0z;çSET0ÈDUMP0İÆ±°iŸŸ¹0Ç¹Ö0|0³0È;£

0097 C.

0098 . C. TIŸ³ŸPŸÖŸÈ00ðÀDİç (UT)

0099 +. TI 2015-08-25 09:47:00.0

0100 DC 01-B3 DHU_OP_STOP

0101 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

0102 C.

0103 +. TI 2015-08-25 09:47:01.0

0104 DC 01-B4 DHU_OP_COPY

0105 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

0106 C.

0107 +. TI 2015-08-25 09:47:01.0

0108 DC 01-B5 DHU_OPOG_COPY

0109 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

0110 C.

0111 +. TI 2015-08-25 09:51:59.5

0112 DC 01-B2 DHU_OP_START

0113 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

0114 C.

0115 C. °È²¼0İÄè%îİŸ0İŸÄŸ§ŸÄŸ-¹àİÛ

0116 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA

0117 C. çç[HK1_TI_CMD_NUM] EQ 4

0118 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU

0119 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3

0120 C.

0121 . C. *****

0122 C. TIİİ°èŸÄŸÖŸ×

0123 C. *****

0124 C.

0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)

0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET

0127 BC (03 ab 03 01 02)

0128 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07

0129 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B

0130 C. çç[HK1_DMP_BLOCK_NUM] EQ 3

0131 C. çç[HK1_DMP_REPEAT_NUM] EQ 0

0132 C. çç[HK1_DMA_DMP_PIM] EQ DHU

0133 +. DC 01-22 DHU_MODE_CHNG

0134 BC (07 0b f8)

0135 C. çç[HK1_PKT_FORM_NO] EQ 7

0136 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s

0137 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k

0138 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M

0139 C. çç[HK1_DMP_CHK_FLG] EQ EXEC

0140 C.

0141 . C. ŸÄŸÖŸ×½ªİ»0ð³İÇ§

0142 C. çç[HK1_DMP_CHK_FLG] EQ NON

0143 C.

0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0ð³İÇ§

0145 C.

0146 . C. DHUŸâ;¼ŸÈ;È¼Ÿ½,Ÿi;¼ŸÈ;È0ðİã0¹

0147 +. DC 01-22 DHU_MODE_CHNG

0148 BC (02 0a f8)

0149 C. çç[HK1_PKT_FORM_NO] EQ 2

0150 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S

0151 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K

0152 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M

0153 C.

0154 C. *****

0155 C. SOT TI command set

0156 C. *****

0157 C. Execute, after the success of OP upload.

0158 +. TI 2015-08-25 09:51:16.0

0159 DC 07-F0 MDP_SOT_MODE_STBY

0160 BC (41)

0161 . C. -----

0162 C. HK1_TI_CMD_NUM = 1 CNTUP []

0163 C. -----

0164 C. ***** SOT END *****

0165 C.

0166 C. ***** XRT START *****

0167 C. Execute, after the success of OP upload.

0168 +. TI 2015-08-25 09:51:00.0

0169 DC 07-F0 MDP_XRT_MODE_STBY

0170 BC (c3)

0171 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP

0172 C.

0173 C. ***** XRT END *****

0174 . C. Stop EIS observation and temporarily disable EIS mode changes

0175 C.

0176 C.

0177 C. ***** Start EIS operation (TI set) *****

0178 C. Execute, after the success of OP upload.

0179 C. Set EIS TI-commands

0180 +. TI 2015-08-25 09:51:30.0

0181 DC 07-FC EIS_MODE_MANU

0182 BC (21 02)

0183 +. TI 2015-08-25 09:51:40.0

0184 DC 07-FC EIS_MODE_CHG_DIS

0185 BC (22)

0186 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP

0187 C. ***** End EIS operation (TI set) *****

0188 C.

0189 C.

0190 C.

0191 . C. ***** MDP İÄİİİ»ö¼Ÿ0ÈÄD0¹0èDCBC•×²è *****

0192 C. (%â°İŸÖŸÄŸÈŸPŸŸÈŸÄŸÇŸÈ0È¼00¼Ä»Û0¹0é)

0193 . S. DC-BC dcbc-402:DCBC

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```

main-336 2015-08-25 12:39:10 164 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YFYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿áÁø•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈè¼µ•íÉ;ÈøÉ¼°ÇÔø•ø¿¼i¹çøI;çÀ®, ùø¹øèøBøÇÁ+¿®ø•øÈøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-263:MDP_OBS_F
0026 ()
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-285:MDP_OBS_S
0044 ()
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 . C. < Upload DPL table >
0054 C.
0055 C. YçYÁY×Yí;¼YÉøÍÁ°øÈSTS_CHKøðOFFøÈø¹øè
0056 C.
0057 . S. RAM ram-271:MDP_DPL
0058 ()
0059 C.
0060 . C. < Dump RAMID=MDP_DPL >
0061 +. DC 07-F0 MDP_DUMP_FGTBL
0062 BC (82 07 00 38 b8 00 40)
0063 C. -----
0064 C. MDP_DPL verify = OK [ ]
0065 C. -----
0066 C.
0067 C. STS_CHKøðONøÈø¹øè
0068 C.
0069 . C. < Update MDP DSC PAR1 >
0070 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0071 BC (4c)
0072 C. MDP_CMD_CODE = F04C0700 [ ]
0073 C. MDP_CMD_CNT (count-up 1) [ ]
0074 C. -----
0075 C.
0076 . C.
0077 C. *****
0078 C. SOT TI command set
0079 C. *****
0080 C. Execute, after the success of TBL upload.
0081 +. TI 2015-08-25 09:51:18.0
0082 DC 07-F0 MDP_SOT_MODE_OBSV
0083 BC (40)
0084 . C. -----
0085 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0086 C. -----
0087 C.
0088 C.
0089 C. ***** XRT START *****
0090 C.
0091 . C. ----- Success Verify ? OK / NG_____
0092 C.
0093 C. XRT Obs. Table Upload
0094 . S. RAM ram-291:MDP_OBS_X
0095 ()

```

```

0096 C.
0097 +. DC 07-F0 MDP_DUMP_XRTTBL
0098 BC (84 07 00 00 00 3a d4)
0099 . C. ----- Comparison Check ? OK / ERR ____
0100 C.
0101 C.
0102 +. DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 01 b1 b1 04 04)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 02 b1 b1 08 08)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 03 b1 b1 08 08)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 04 b1 b1 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 05 85 83 06 06)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 06 85 83 06 06)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 07 c0 c0 10 10)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 08 80 80 20 20)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 09 40 c0 10 10)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 0a 40 40 10 10)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 0b c0 40 10 10)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 0c 80 80 20 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 0d 80 80 08 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 0e 85 83 03 04)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 0f 80 80 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 10 80 80 08 08)
0134 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0135 BC (c4 12)
0136 . C. ----- Success Verify ? OK / NG ____
0137 C.
0138 C.
0139 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0140 C.
0141 +. DC 07-F0 MDP_XRT_MODE_OBSV
0142 BC (c2)
0143 +. TI 2015-08-25 09:51:02.0
0144 DC 07-F0 MDP_XRT_MODE_OBSV
0145 BC (c2)
0146 . C. ----- Success Verify ? OK / NG ____
0147 C.
0148 C. ***** XRT END *****
0149 C.
0150 . C. ***** MDP `úÃîï»ö%ÝðÊÄð¹ðèDCBC•x²è *****
0151 C. (%á°îÿÓYÁYÈYÏYáYçYèðE%¼ðð%Á»Û¹ðé)
0152 . S. DC-BC dcbc-402:DCBC
0153 (MDP_known_event)
0154 C.
0155 C.
0156 . C. ***** ¥D¥¹•İ Daily±¿İÑðÈ´Ø¹ðèDCBC•x²è *****
0157 . S. DC-BC dcbc-153:DCBC
0158 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0159 C.
0160 C.
0161 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Á»Û;ä
0162 C.
0163 . C. ***** LOS *****
0164 C.

```

Aug 25, 15 12:39

XRT_OGLIST_0351.chk

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*** OP Sequence for XRT ***

2015/08/25	10:02:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2015/08/25	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	12:59:56.0	XRT_CTRL_MANU_417_OG [0x1a1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	13:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2015/08/25	13:02:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/25	13:02:52.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2015/08/25	13:02:54.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/25	13:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/25	13:02:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/25	13:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/25	13:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	13:09:56.0	XRT_CTRL_MANU_417_OG [0x1a1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	13:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2015/08/25	13:12:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/25	13:12:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2015/08/25	13:12:54.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/25	13:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/25	13:12:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/25	13:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/25	13:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	13:19:56.0	XRT_CTRL_MANU_417_OG [0x1a1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	13:20:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2015/08/25	13:22:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/25	13:22:52.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2015/08/25	13:22:54.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/25	13:22:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/25	13:22:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/25	13:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/25	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	13:29:56.0	XRT_CTRL_MANU_417_OG [0x1a1]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	13:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2015/08/25	13:32:32.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/08/25	13:32:52.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/08/25	13:32:54.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/25	13:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/25	13:32:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/25	13:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/25	13:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	13:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	13:39:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2015/08/25	13:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2015/08/25	13:43:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/08/25	13:44:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/25	13:44:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/25	13:44:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/25	13:44:24.0	XRT_ARS_DIS_423_OG [0x1a7]							

2015/08/25	13:44:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/08/25	13:46:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/25	13:46:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12				
2015/08/25	13:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03				
2015/08/25	14:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/25	14:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/25	14:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/25	14:14:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/25	14:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2015/08/25	14:47:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2015/08/25	14:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0					
2015/08/25	15:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/25	15:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/25	15:39:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/25	15:40:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2015/08/25	15:40:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00	00	00	00	00	
2015/08/25	15:42:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9					
2015/08/25	15:42:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2015/08/25	15:42:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/08/25	15:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e				
2015/08/25	15:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/25	15:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/25	15:49:58.0	XRT_ROI_A_437_OG [0x1b5]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
			MDP_XRT_ROI_SET	6	07-F0	cd	05	85	83	06	06
			MDP_XRT_ROI_SET	6	07-F0	cd	06	85	83	06	06
			MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80	20	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0c	80	80	20	08
			MDP_XRT_ROI_SET	6	07-F0	cd	0d	80	80	08	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0e	85	83	03	04
			MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06	06
			MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08	08
2015/08/25	15:50:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00	00	00	00	00
2015/08/25	15:50:03.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2015/08/25	15:50:23.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2015/08/25	15:50:25.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8					
2015/08/25	15:50:27.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2015/08/25	15:50:29.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0					
2015/08/25	15:50:31.0	XRT_FLD_RESET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/08/25	16:35:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/25	16:35:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12				
2015/08/25	16:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03				
2015/08/25	17:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2015/08/25	17:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/25	17:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2015/08/25	17:15:18.0	XRT_FLD_DIS_422_OG [0x1a6]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2015/08/25	17:15:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9					
2015/08/25	17:15:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2015/08/25	17:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0					
2015/08/25	17:15:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2015/08/25	17:17:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da					
2015/08/25	17:17:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03				

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2015/08/25	17:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/25	18:12:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	18:12:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/25	18:12:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/25	18:12:24.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/25	18:12:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/25	18:12:28.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/25	18:12:30.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/25	18:12:32.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/25	18:15:02.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2015/08/25	18:15:04.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/25	18:15:06.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	03:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/26	04:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/26	04:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/26	04:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/26	04:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/26	04:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/08/26	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	04:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	04:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/26	04:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/08/26	04:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/26	04:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/26	04:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/26	04:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/26	04:10:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/26	04:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2015/08/26	04:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/26	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	04:45:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	04:45:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	04:45:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/26	04:45:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/26	04:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/26	05:15:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/26	05:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	06:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	06:26:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	06:26:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/26	06:26:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/26	06:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/26	06:53:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/26	06:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	08:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							

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2015/08/26	08:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	08:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	08:06:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/26	08:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/26	08:30:00.0	AOCS_OrE-point_Start_1_OG [0x097]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/26	08:32:00.0	XRT_Custom_430_OG [0x1ae]	AOCU_NM	5	02-76	01 00 00 00 00				
2015/08/26	08:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2015/08/26	09:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/08/26	09:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	09:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	09:46:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	09:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/26	10:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/26	10:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/26	11:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_430_OG [0x1ae]							
2015/08/26	11:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/08/26	11:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	11:34:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	11:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	11:48:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/26	11:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/26	13:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/26	13:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]							
2015/08/26	13:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/08/26	13:12:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	13:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	13:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	13:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/26	14:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/26	14:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/26	14:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_Custom_430_OG [0x1ae]							
2015/08/26	14:51:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/08/26	14:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	14:51:26.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	15:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/26	15:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/26	16:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/26	16:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]							
2015/08/26	16:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/08/26	16:29:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	16:29:08.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	16:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/26	17:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/26	17:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/26	17:57:24.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]							
2015/08/26	17:57:26.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/08/26	17:57:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/26	17:57:30.0	AOCS_OrE-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/26	17:57:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/26	18:00:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/26	18:00:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
			MDP_XRT_ARS_DIS	1	07-F0	d5				

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2015/08/26	18:00:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e	
2015/08/26	18:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/26	18:07:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/26	18:07:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/26	18:07:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2015/08/26	18:07:30.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01	00 00 00 00	
2015/08/26	18:07:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/08/26	18:07:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/08/26	18:07:52.0	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/08/26	18:07:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/08/26	18:07:56.0	XRT_FLD_RESET_449_OG [0x1c1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/26	18:47:26.0	XRT_QT_PROG_SET_413_OG [0x19d]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2015/08/26	18:47:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03	
2015/08/26	18:47:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/26	19:46:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/26	19:46:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/26	19:46:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/26	19:46:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/26	19:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/26	20:23:30.0	XRT_Custom_430_OG [0x1ae]					
2015/08/26	20:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/26	21:24:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/26	21:24:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/26	21:24:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/26	21:24:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/26	21:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/26	22:00:00.0	XRT_Custom_430_OG [0x1ae]					
2015/08/26	22:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/26	23:03:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/26	23:03:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/26	23:03:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/26	23:03:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/26	23:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/26	23:30:00.0	XRT_Custom_430_OG [0x1ae]					
2015/08/26	23:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/27	00:41:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/27	00:41:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/27	00:41:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/27	00:41:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/27	00:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/27	00:58:30.5	XRT_Custom_430_OG [0x1ae]					
2015/08/27	00:59:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/08/27	02:14:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/27	02:14:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/08/27	02:14:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/08/27	02:14:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/08/27	02:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/08/27	02:34:30.0	XRT_Custom_430_OG [0x1ae]					
2015/08/27	02:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2015/08/27	03:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	03:42:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	03:42:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/27	03:42:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/27	03:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/27	04:12:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/27	04:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/27	04:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	04:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	04:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/08/27	04:20:00.0	AOCS_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/08/27	04:20:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/08/27	04:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/08/27	04:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/27	04:22:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/08/27	04:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/27	04:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	04:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	04:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/08/27	04:30:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/08/27	04:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/08/27	04:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/08/27	04:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/08/27	04:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/08/27	04:30:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/27	04:32:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2015/08/27	04:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/08/27	04:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/27	05:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	05:22:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	05:22:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/27	05:22:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/27	05:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/27	05:50:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/27	05:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/27	07:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	07:02:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	07:02:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/27	07:02:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/27	07:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/27	07:28:30.0	XRT_Custom_430_OG [0x1ae]							
2015/08/27	07:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/08/27	08:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	08:42:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/08/27	08:42:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/08/27	08:42:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/08/27	08:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/08/27	09:07:00.0	XRT_Custom_430_OG [0x1ae]							
2015/08/27	09:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

2015/08/27	10:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/08/27	10:35:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/08/27	10:36:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	00 00 00 00 00