

# XRT Timeline to be uploaded on 2015/09/05

Period: 2015/09/05 10:07:00 - 2015/09/10 10:22:00

\* \* \* \* \*

Normal mode

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XOB #1AD6: HOP257 Target A- Al/Poly - 384FOV - 1x1 - Q95 - 30s Cad - AEC1												
Term	Pointing (x, y)							Comment				
09/05 10:20:00 - 09/05 10:59:54	Track ( 255.8, -453.0) <sup>Ⓢ 09/05 10:17:00</sup>	# OP start + 10min HOP257										
09/06 08:03:00 - 09/06 10:59:54	Track ( 44.9, -71.1) <sup>Ⓢ 09/06 08:00:00</sup>	HOP257 AR12409										
09/07 08:03:00 - 09/07 10:23:30	Track ( 263.5, -66.6) <sup>Ⓢ 09/07 08:00:00</sup>	HOP257 AR12409										
09/08 08:05:06 - 09/08 09:50:55	Track ( 467.9, -55.8) <sup>Ⓢ 09/08 08:00:00</sup>	HOP257 AR12409										
<b>PROG= 15 Inf.-time(s)</b>												
└─ <b>Subr= 1 1-time(s) 2.0sec</b>												
└─┬─ <b>Seqn= 56 1-time(s) 2.0sec</b>												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─ <b>Subr= 2 120-time(s) 30.0sec</b>												
└─┬─ <b>Seqn= 20 1-time(s) 2.0sec</b>												
└─┬─┬─ Al-poly/Open Al-poly/thick-Al close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec												
Default Filter    Thicker Filter    VLS    mode    image    Exp.    CCD    Bin    ROI: size (center)    Comp.    AEC Buffer    Interval												

XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms												
Term	Pointing (x, y)							Comment				
09/05 11:03:00 - 09/05 17:48:00	Fixed ( -16.0, 874.0)	HOP206 N-pole										
09/07 10:36:08 - 09/07 18:00:54	Fixed ( -16.0, -926.0)	HOP81 S-pole										
<b>PROG= 20 Inf.-time(s)</b>												
└─ <b>Subr= 1 1-time(s) 2.0sec</b>												
└─┬─ <b>Seqn= 9 2-time(s) 2.0sec</b>												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─┬─ <b>Subr= 2 1-time(s) 2.0sec</b>												
└─┬─ <b>Seqn= 7 1-time(s) 30.0sec</b>												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─┬─ <b>Subr= 3 30-time(s) 2.0sec</b>												
└─┬─ <b>Seqn= 57 1-time(s) 60.0sec</b>												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─┬─┬─ Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Default Filter    Thicker Filter    VLS    mode    image    Exp.    CCD    Bin    ROI: size (center)    Comp.    AEC Buffer    Interval												

XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-												
Term	Pointing (x, y)							Comment				
09/05 18:18:30 - 09/05 18:21:24	Fixed ( 0.0, 0.0)	synoptic, shifted 11.5 min										
09/06 06:01:00 - 09/06 06:07:54	Fixed ( 0.0, 0.0)	synoptic, shifted -2.0 min										
09/06 18:03:00 - 09/06 18:09:54	Fixed ( 0.0, 0.0)	synoptic										
09/07 06:03:00 - 09/07 06:09:54	Fixed ( 0.0, 0.0)	synoptic										
09/07 18:04:00 - 09/07 18:10:54	Fixed ( 0.0, 0.0)	synoptic, shifted 1.0 min										
09/08 05:34:00 - 09/08 05:40:54	Fixed ( 0.0, 0.0)	synoptic, shifted -29.0 min										
<b>PROG= 14 1-time(s)</b>												
└─ <b>Subr= 1 1-time(s) 12.0sec</b>												
└─┬─ <b>Seqn= 33 1-time(s) 4.0sec</b>												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ <b>Seqn= 5 1-time(s) 2.0sec</b>												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─ <b>Seqn= 61 1-time(s) 4.0sec</b>												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ <b>Seqn= 49 1-time(s) 2.0sec</b>												
└─┬─┬─ Al-poly/Open Al-poly/Open close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ <b>Seqn= 39 1-time(s) 2.0sec</b>												
└─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ <b>Seqn= 54 1-time(s) 2.0sec</b>												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter    Thicker Filter    VLS    mode    image    Exp.    CCD    Bin    ROI: size (center)    Comp.    AEC Buffer    Interval												

**XOB #1ACA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

Term	Pointing (x, y)	Comment
09/05 18:24:30 - 09/06 05:57:54	Track ( -81.3, -70.7) @ 09/05 18:21:30	AR12409
09/06 06:11:00 - 09/06 07:59:54	Track ( 27.6, -71.1) @ 09/06 06:08:00	AR12409
09/06 11:03:00 - 09/06 17:59:54	Track ( 72.6, -70.9) @ 09/06 11:00:00	AR12409
09/06 18:13:00 - 09/07 05:59:54	Track ( 138.5, -70.0) @ 09/06 18:10:00	AR12409
09/07 06:13:00 - 09/07 07:59:54	Track ( 247.2, -67.2) @ 09/07 06:10:00	AR12409
09/07 18:14:00 - 09/07 18:58:30	Track ( 352.7, -62.8) @ 09/07 18:11:00	AR12409
09/07 22:03:00 - 09/08 03:59:54	Fixed ( 919.5, 6.5)	EIS coronal rain with IRIS AR 12406
09/08 04:03:00 - 09/08 05:30:54	Track ( 435.4, -58.0) @ 09/08 04:00:00	AR12409
09/08 05:44:00 - 09/08 07:39:00	Track ( 449.2, -57.1) @ 09/08 05:41:00	AR12409

**PROG= 18 Inf.-time(s)**

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	4-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 23	4-time(s)	240.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1AC2: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 120s cad**

Term	Pointing (x, y)	Comment
09/07 19:26:06 - 09/07 21:59:54	Fixed ( 919.5, 6.5)	EIS off-limb spectral atlas

**PROG= 09 Inf.-time(s)**

Subr=	1-time(s)	2.0sec										
Subr= 1	45-time(s)	120.0sec										
Seqn= 29	1-time(s)	4.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 26	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
09/05 10:20:00 - 09/05 10:59:54	Track ( 255.8, -453.0) @ 09/05 10:17:00	# OP start + 10min HOP257
09/05 11:03:00 - 09/05 17:48:00	Fixed ( -16.0, 874.0)	HOP206 N-pole
09/05 18:24:30 - 09/06 05:57:54	Track ( -81.3, -70.7) @ 09/05 18:21:30	AR12409
09/06 06:11:00 - 09/06 07:59:54	Track ( 27.6, -71.1) @ 09/06 06:08:00	AR12409
09/06 08:03:00 - 09/06 10:59:54	Track ( 44.9, -71.1) @ 09/06 08:00:00	HOP257 AR12409
09/06 11:03:00 - 09/06 17:59:54	Track ( 72.6, -70.9) @ 09/06 11:00:00	AR12409
09/06 18:13:00 - 09/07 05:59:54	Track ( 138.5, -70.0) @ 09/06 18:10:00	AR12409
09/07 06:13:00 - 09/07 07:59:54	Track ( 247.2, -67.2) @ 09/07 06:10:00	AR12409
09/07 08:03:00 - 09/07 10:23:30	Track ( 263.5, -66.6) @ 09/07 08:00:00	HOP257 AR12409
09/07 10:36:08 - 09/07 18:00:54	Fixed ( -16.0, -926.0)	HOP81 S-pole
09/07 18:14:00 - 09/07 18:58:30	Track ( 352.7, -62.8) @ 09/07 18:11:00	AR12409
09/07 19:26:06 - 09/07 21:59:54	Fixed ( 919.5, 6.5)	EIS off-limb spectral atlas
09/07 22:03:00 - 09/08 03:59:54	Fixed ( 919.5, 6.5)	EIS coronal rain with IRIS AR 12406
09/08 04:03:00 - 09/08 05:30:54	Track ( 435.4, -58.0) @ 09/08 04:00:00	AR12409
09/08 05:44:00 - 09/08 07:39:00	Track ( 449.2, -57.1) @ 09/08 05:41:00	AR12409
09/08 08:05:06 - 09/08 09:50:55	Track ( 467.9, -55.8) @ 09/08 08:00:00	HOP257 AR12409

**PROG= 03 30-time(s)**

Subr=	1-time(s)	2.0sec										
Subr= 1	20-time(s)	2.0sec										
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100	1-time(s)	10.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11	1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn= 53	1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

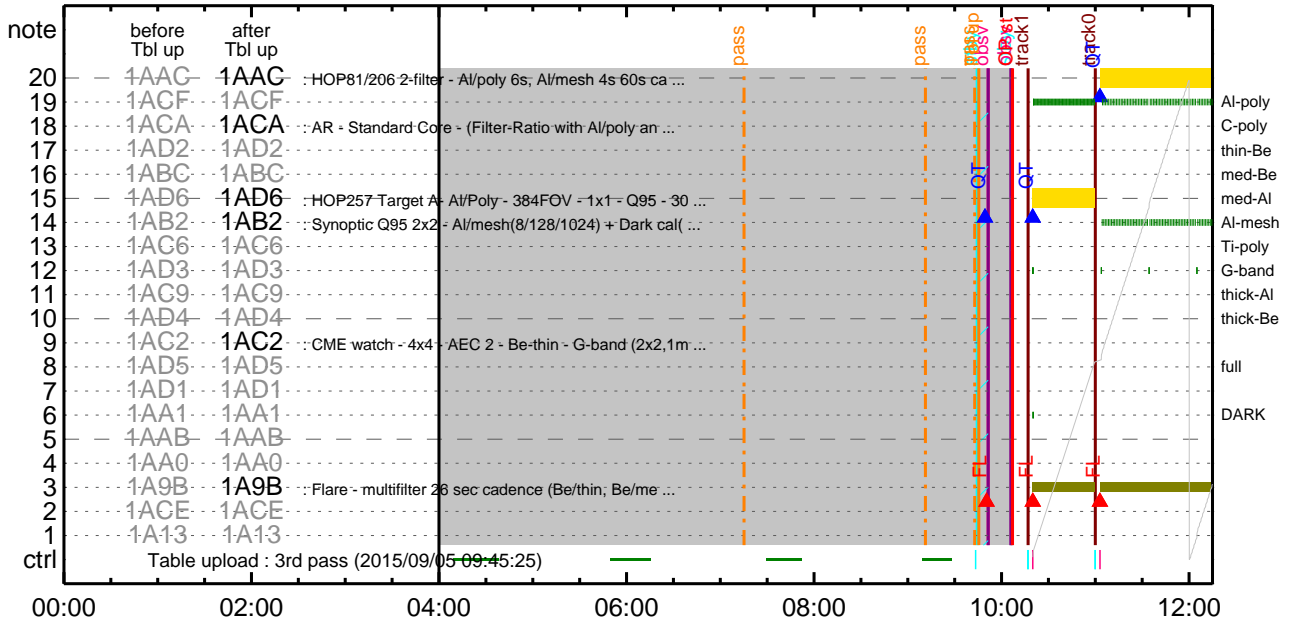
\* \* \* \* \*

### Flare Detection

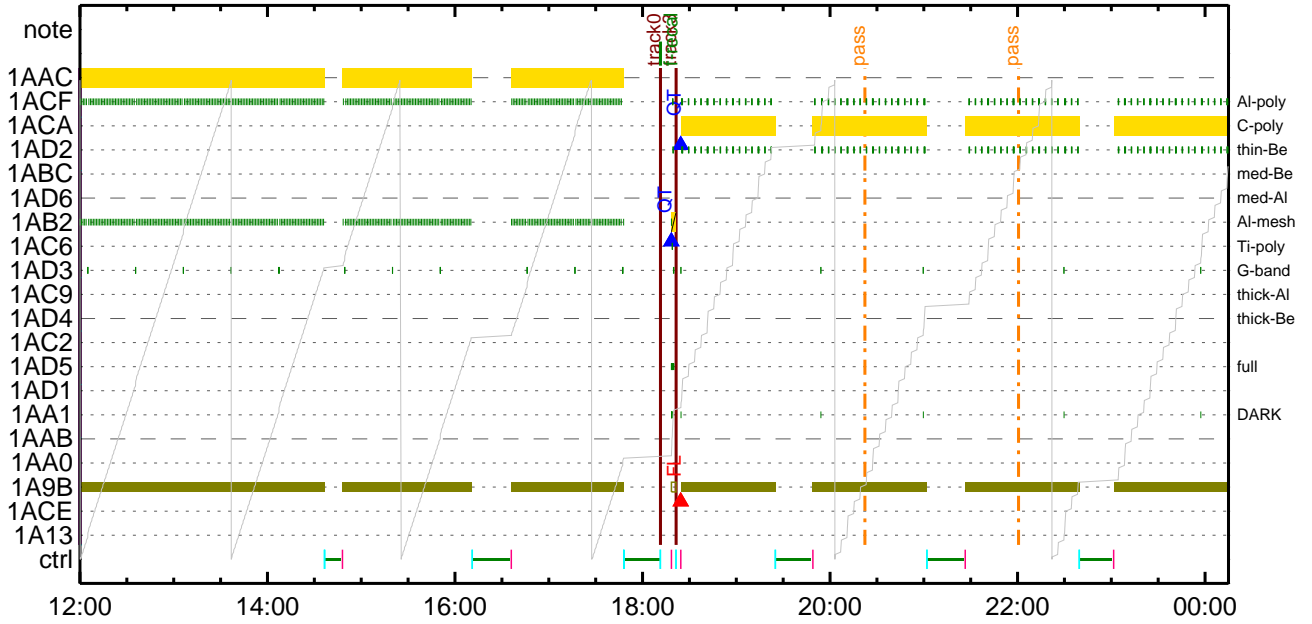
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FLD Patrol													
Term		Pointing (x, y)				Comment							
09/05 18:21:48 - 09/06 05:58:18		Track ( -81.3, -70.7) <sup>Ⓜ</sup> 09/05 18:21:30				AR12409							
09/06 06:08:18 - 09/06 18:00:18		Track ( 27.6, -71.1) <sup>Ⓜ</sup> 09/06 06:08:00				AR12409							
09/06 18:10:18 - 09/07 06:00:18		Track ( 138.5, -70.0) <sup>Ⓜ</sup> 09/06 18:10:00				AR12409							
09/07 06:10:18 - 09/07 18:01:18		Track ( 247.2, -67.2) <sup>Ⓜ</sup> 09/07 06:10:00				AR12409							
09/07 18:11:18 - 09/08 05:31:18		Track ( 352.7, -62.8) <sup>Ⓜ</sup> 09/07 18:11:00				AR12409							
09/08 05:41:18 - 09/10 10:22:00		Track ( 449.2, -57.1) <sup>Ⓜ</sup> 09/08 05:41:00				AR12409							
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

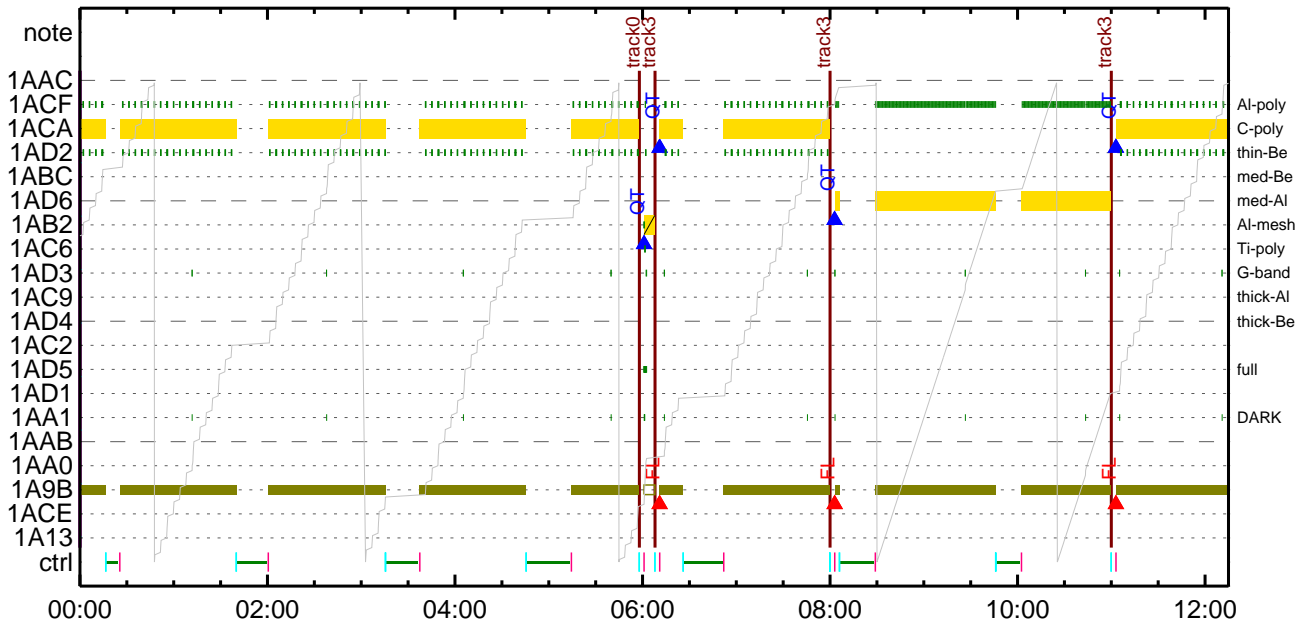
### CMDI #0378 2015/09/05



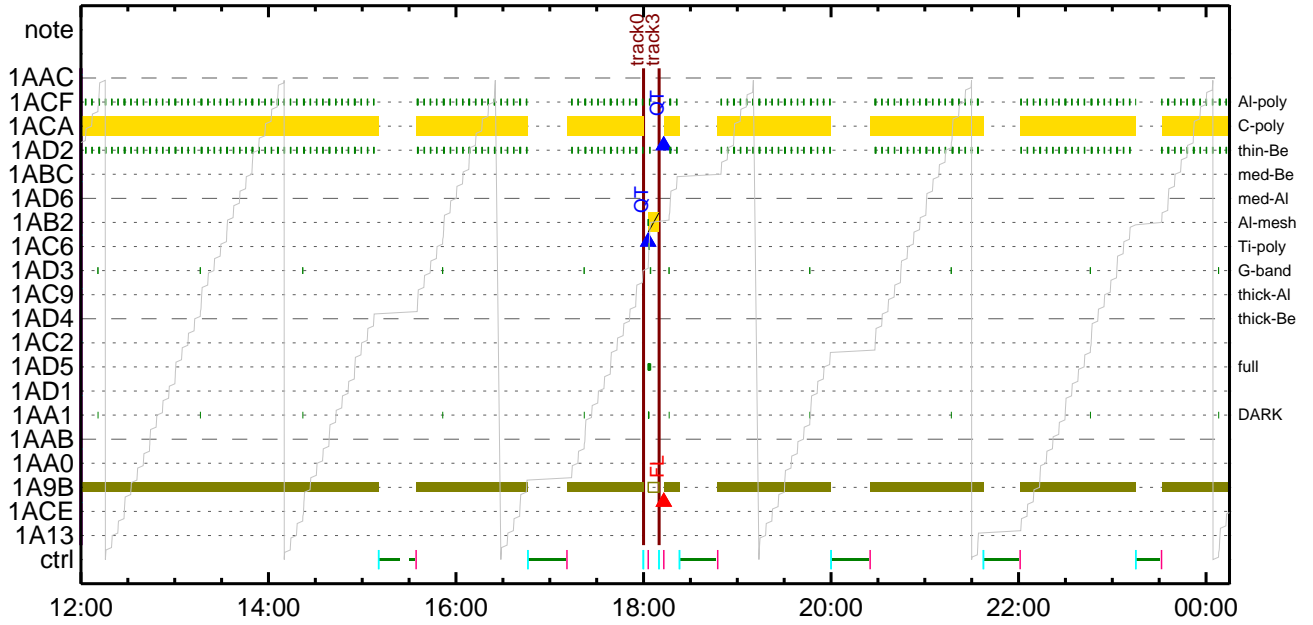
### CMDI #0378 2015/09/05



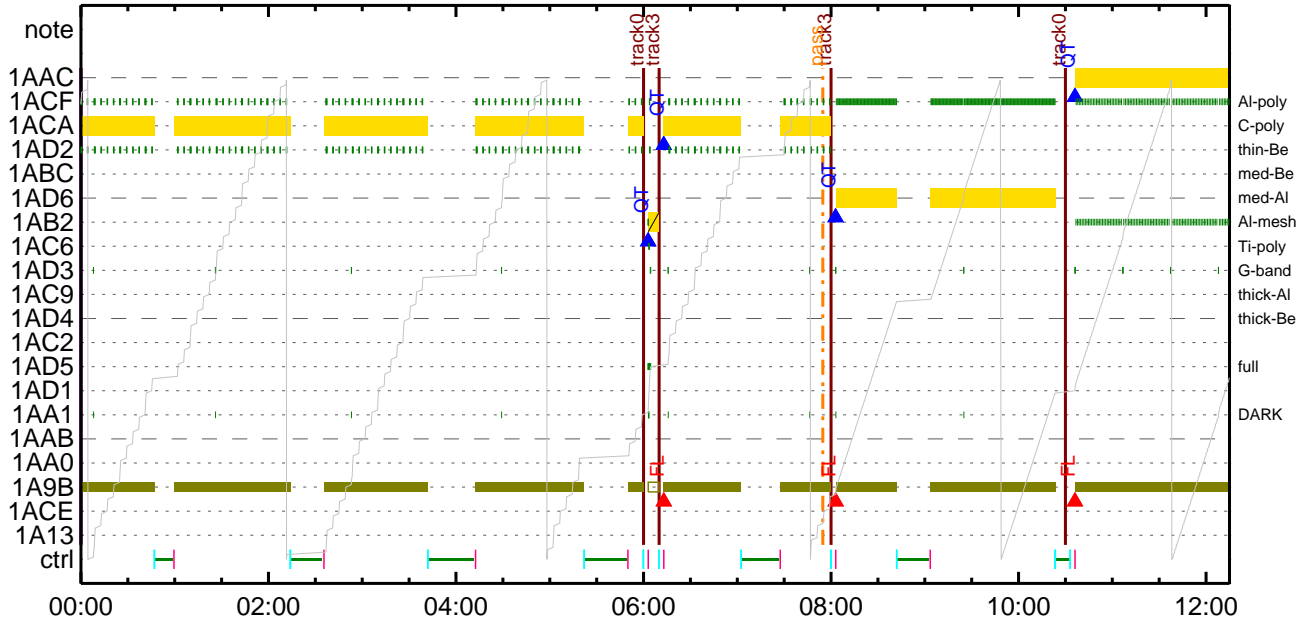
### CMDI #0378 2015/09/06



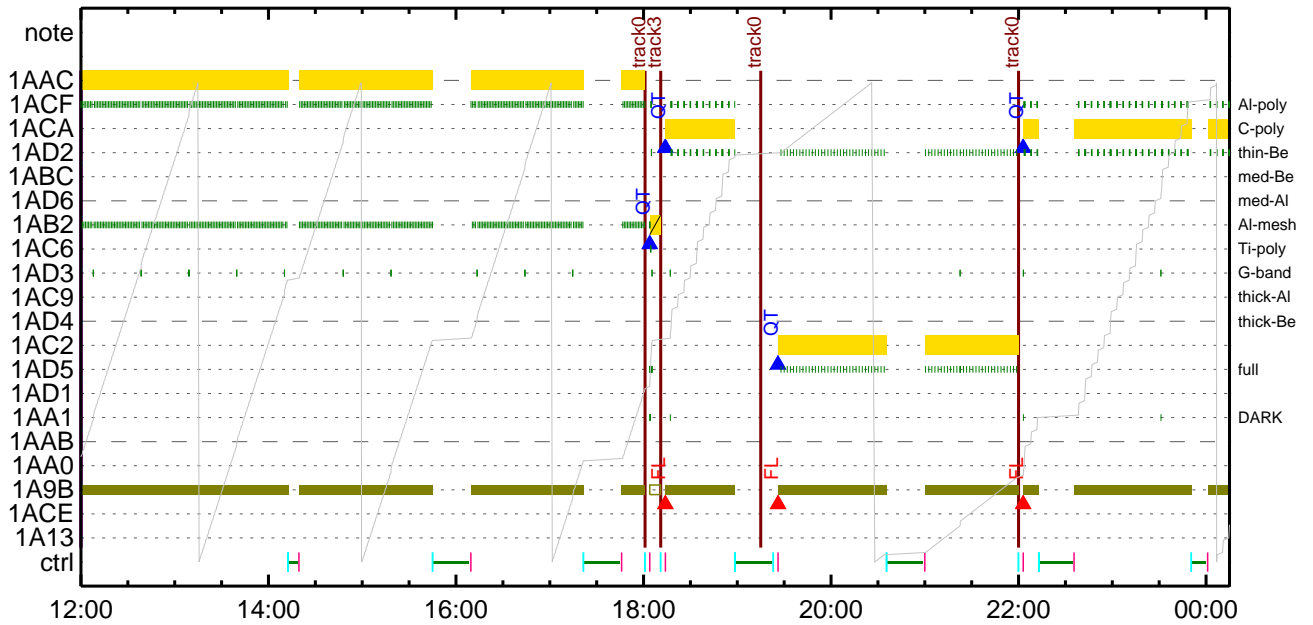
CMDI #0378 2015/09/06



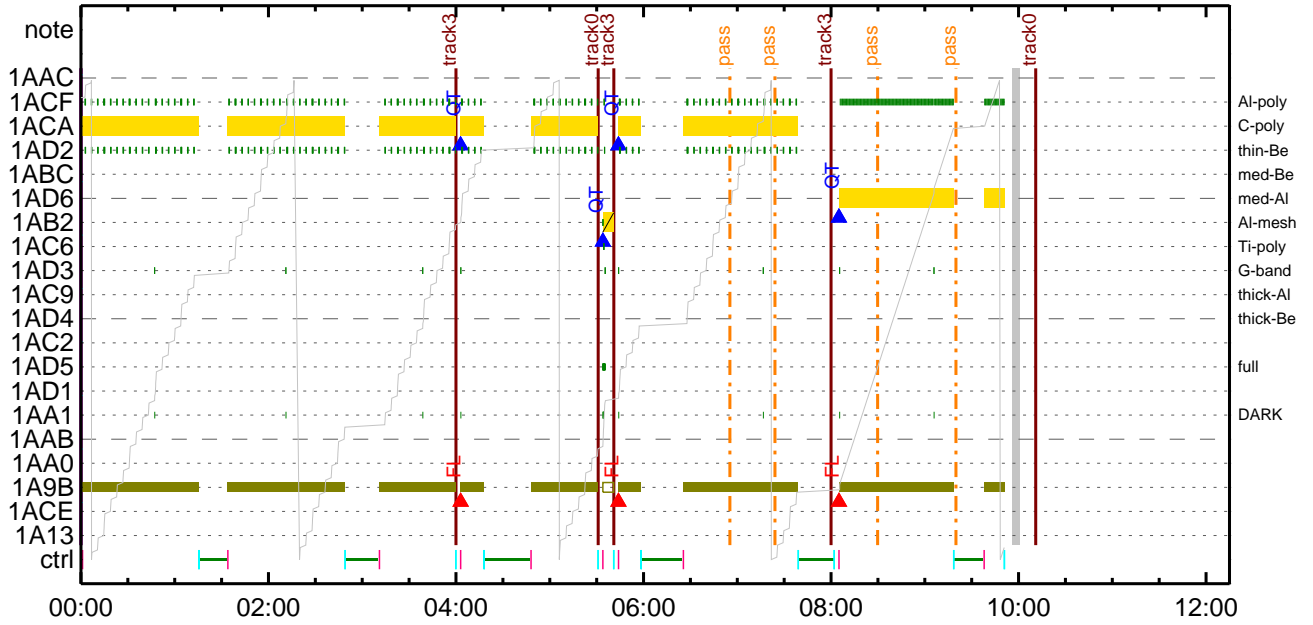
CMDI #0378 2015/09/07



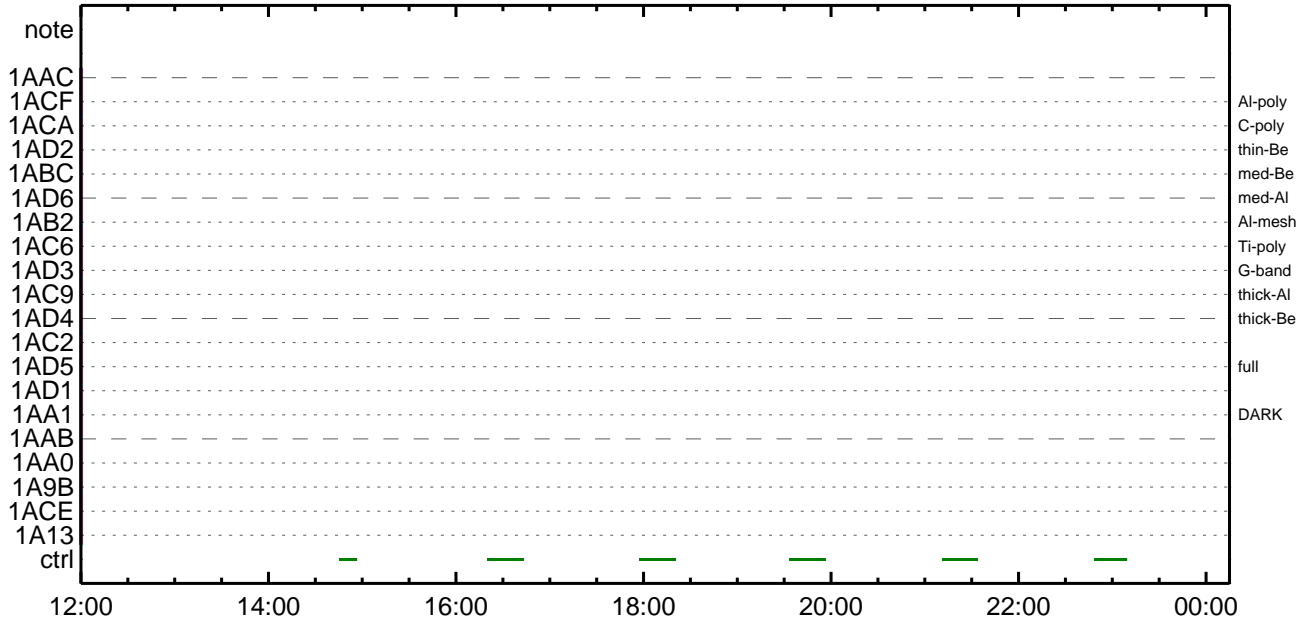
CMDI #0378 2015/09/07



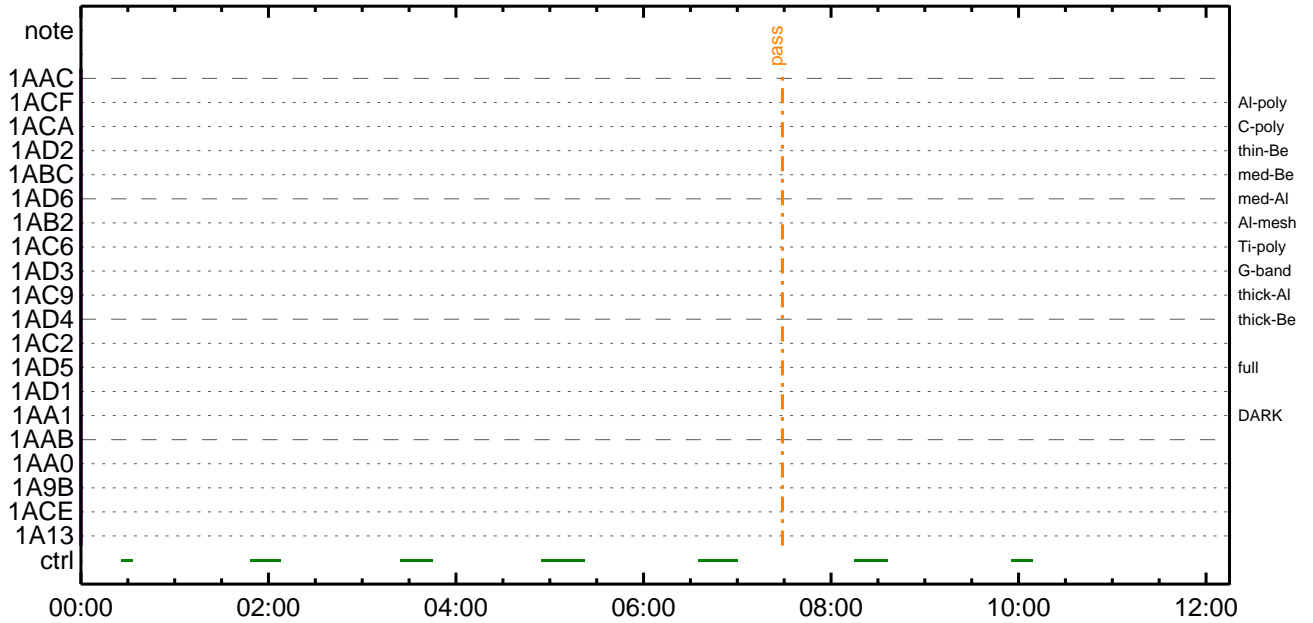
CMDI #0378 2015/09/08



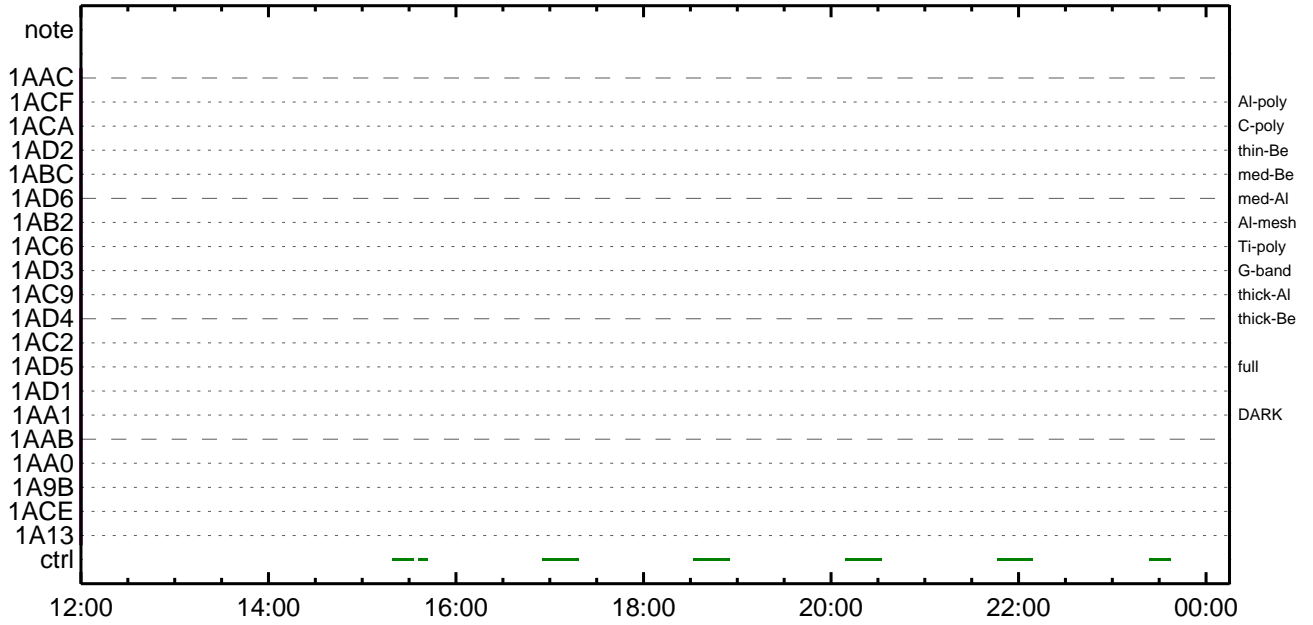
CMDI #0378 2015/09/08



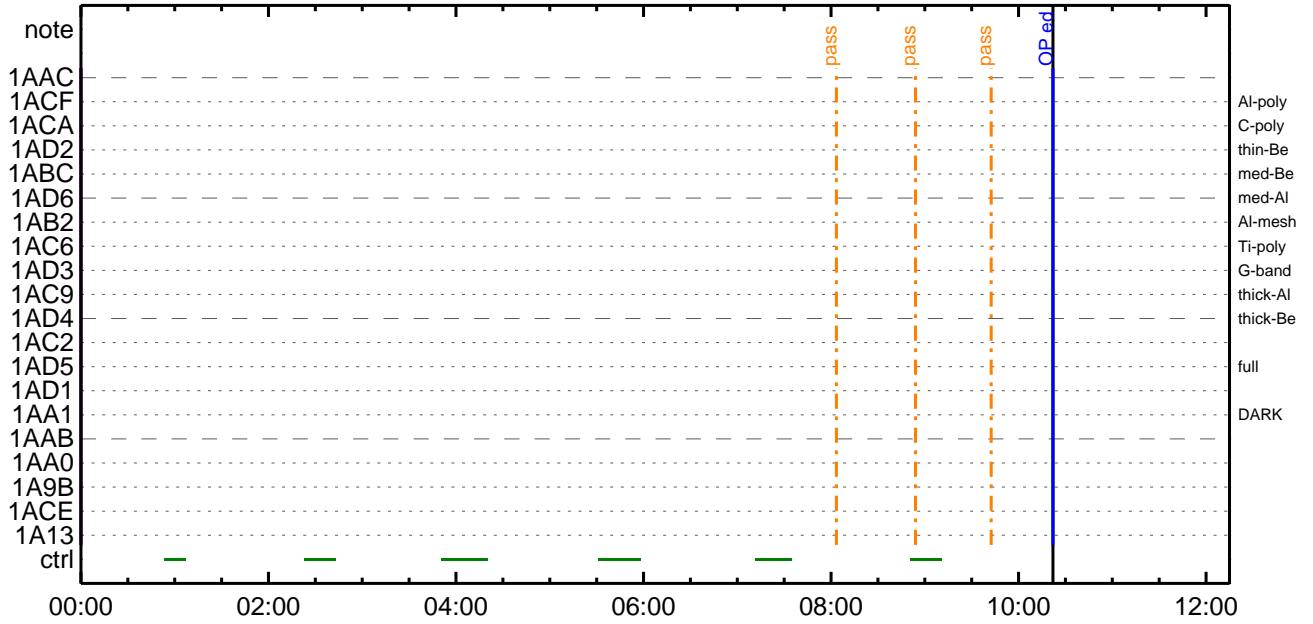
CMDI #0378 2015/09/09



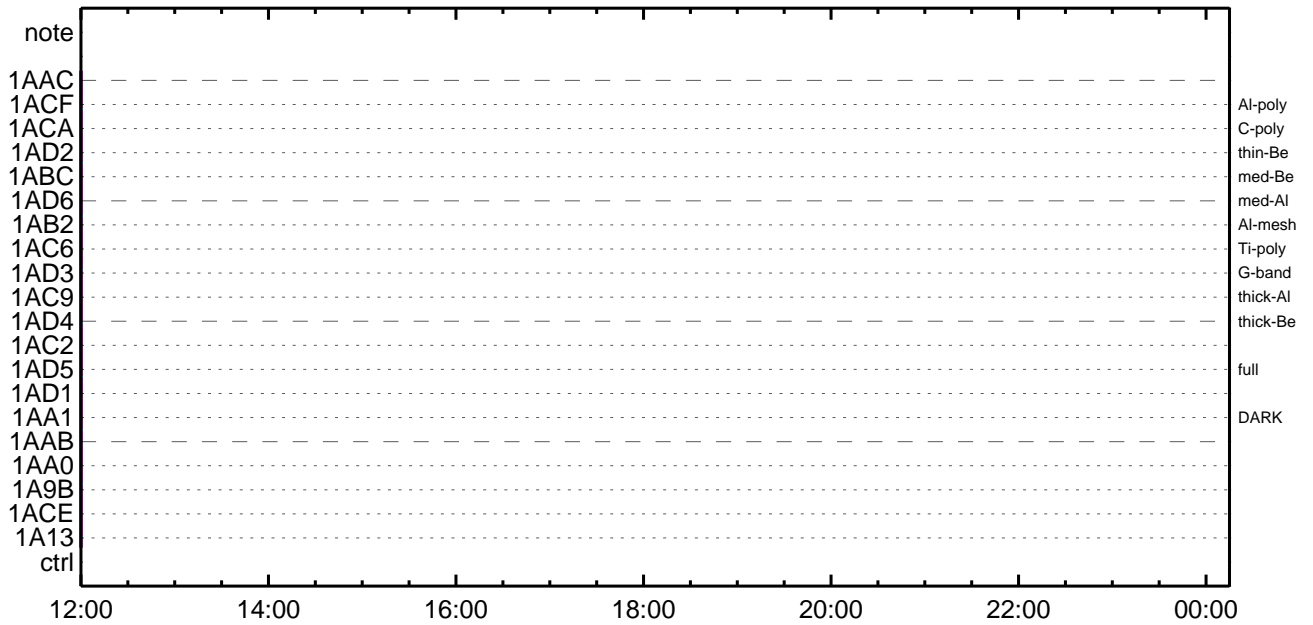
CMDI #0378 2015/09/09



CMDI #0378 2015/09/10



CMDI #0378 2015/09/10



(a) Spacecraft Operation Procedure (real-commands)

```
main-356 2015-09-05 13:30:47 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Ä»Ü;ä
0005 C.
0006 C. YÀYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿òÁò•µ°Ë»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈòÈ¼°ÇÓò•ò¿¼l¹çòí;çÀ®, ùò¹òÈòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-356:OP
0020 ()
0021 . S. OG og-356:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYóY×¼ªª î»òð³ îÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòí¼È¹ç•è² ÌOKòð³ îÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYóY×¼ªª î»òð³ îÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòí¼È¹ç•è² ÌOKòð³ îÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYóY×¼ªª î»òð³ îÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPòí¼È¹ç•è² ÌOKòð³ îÇ§
0081 C.
0082 . C. ***** °È²¼òí¼Ä´ ¶¼°òÈÈ-ò°Á+¿® (¼áµ-YÀYóY×¼¼¿çòðÁÓÃæç¼ªª °ò¼¼ì¹çòçòâ) *****
0083 C. DHUYâ;¼YÉ;È¼Y¼, Yí;¼YÉ;Èòòíáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADò-Á+¿®NGòí¼È¹ç;ç°È²¼òí¼TI-CMDÁ+¿®òí¼Á¹Ôò•òÈòòò³òÈ;f
```



```

0096 C.          01-01; SET 01EDUMP 01A±°iYÑY¹aÇ¹Ôa|a³aE; f
0097 C.
0098 C. TTY³YF¥ÖYÉaðdADİ¿(UT)
0099 +. TI 2015-09-05 10:02:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM] EQ      1COUNTUP
0102 C.
0103 +. TI 2015-09-05 10:02:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM] EQ      1COUNTUP
0106 C.
0107 +. TI 2015-09-05 10:02:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM] EQ      1COUNTUP
0110 C.
0111 +. TI 2015-09-05 10:06:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM] EQ      1COUNTUP
0114 C.
0115 C. °E²¼aİÄê%îÍÑaİYÁ¥§YÁY-¹àİÜ
0116 C.          çç[HK1_TI_CMD_ENA/DIS] EQ      ENA
0117 C.          çç[HK1_TI_CMD_NUM] EQ      4
0118 C.          çç[HK1_NEXT_EXEC_PIM] EQ      DHU
0119 C.          çç[HK1_NEXT_EXEC_DC] EQ      0xB3
0120 C.
0121 C. *****
0122 C. TIIİ°èYÁYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1] EQ      07
0129 C.          çç[HK1_DMP_TOP_ADRS_0] EQ      2B
0130 C.          çç[HK1_DMP_BLOCK_NUM] EQ      3
0131 C.          çç[HK1_DMP_REPEAT_NUM] EQ      0
0132 C.          çç[HK1_DMA_DMP_PIM] EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO] EQ      7
0136 C.          çç[HK1_PKT_GEN_TIME] EQ      0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE] EQ      32k
0138 C.          çç[HK1_X_TLM_BIT_RATE] EQ      4M
0139 C.          çç[HK1_DMP_CHK_FLG] EQ      EXEC
0140 C.
0141 C. YÁYÖY×½¹İ»að³İÇ§
0142 C.          çç[HK1_DMP_CHK_FLG] EQ      NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKað³İÇ§
0145 C.
0146 C. DHU¥â;¼YÉ;È¼Y½;¥İ;¼YÈ;Èaðİáa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO] EQ      2
0150 C.          çç[HK1_PKT_GEN_TIME] EQ      0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE] EQ      32K
0152 C.          çç[HK1_X_TLM_BIT_RATE] EQ      4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2015-09-05 10:06:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 C. -----
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2015-09-05 10:06:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2015-09-05 10:06:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 C.          [ ] [HK1_TI_CMD_NUM] EQ      2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2015-09-05 10:06:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM] EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 'ûÃİaİ»ö¼YaÈÄa¹aèDCBC•×²è *****
0192 C. (¼a°İYÖYÁYÉY¥Y¥YáYçYèaÈ¼a¼A»Üa¹aé)
0193 C. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



(a) Spacecraft Operation Procedure (real-commands)

```
main-358 2015-09-05 13:30:47 126 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁY$;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èª¿ªÁª•µ°È»Í×ÁÇªÍY¿Y×Yí;¼YÉ;ÈÈ%µ•ííÉ;ÈªÈ¼°ÇÔª•ª¿¼i¹çªí;çÁ®, ùª¹ªÈªªªçÁ+¿ªª•ªÈªªªªªÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-268:MDP_OBS_F
0026 ()
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2015-09-05 10:06:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ()
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 06 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 07 80 80 20 20)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 08 80 80 20 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 09 80 80 08 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 0f 80 80 06 06)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 10 80 80 08 08)
0084 + DC 07-F0 MDP_XRT_FLD_ENA
0085 BC (d8)
0086 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0087 BC (c8)
0088 + DC 07-F0 MDP_XRT_AEC_RESET
0089 BC (d0)
0090 + DC 07-F0 MDP_XRT_ARS_DIS
0091 BC (d5)
0092 + DC 07-F0 MDP_XRT_FLD_RESET
0093 BC (da)
0094 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0095 BC (c4 0f)
```

0096 +. DC 07-F0 MDP\_XRT\_FL\_PROG\_SET  
0097 BC (c5 03)  
0098 . C. ----- Success Verify ? OK / NG \_\_\_\_  
0099 C.  
0100 C.  
0101 . C. All OK? Yes--> Please Proceed. / No --> Stop here.  
0102 C.  
0103 +. DC 07-F0 MDP\_XRT\_MODE\_OBSV  
0104 BC (c2)  
0105 +. TI 2015-09-05 10:06:02.0  
0106 DC 07-F0 MDP\_XRT\_MODE\_OBSV  
0107 BC (c2)  
0108 . C. ----- Success Verify ? OK / NG \_\_\_\_  
0109 C.  
0110 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0111 C.  
0112 . C. \*\*\*\*\* MDP `uAîaî»ö¼YoËÂð¹æDCBC•x²è \*\*\*\*\*  
0113 C. (¼a°îÿÖYÄYËYpYËYâYçYëaE¼¼a¼Ä»Üa¹æè)  
0114 . S. DC-BC dcbc-402:DCBC  
0115 (MDP\_known\_event)  
0116 C.  
0117 C.  
0118 . C. \*\*\*\*\* YDÿ¹•ï Daily±;îÑaË'øa¹æDCBC•x²è \*\*\*\*\*  
0119 . S. DC-BC dcbc-153:DCBC  
0120 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0121 C.  
0122 C.  
0123 . C. ;äLOSŸÄYŸYÄY-¼Ä»Ü;ä  
0124 C.  
0125 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0126 C.

\*\*\* OP Sequence for XRT \*\*\*

2015/09/05	10:16:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	10:16:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	10:16:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/09/05	10:17:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2015/09/05	10:17:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/09/05	10:17:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/09/05	10:17:22.5	XRT_AEC_RESET_441_OG [0x1b9]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/09/05	10:17:24.5	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/09/05	10:17:26.5	XRT_FLD_RESET_447_OG [0x1bf]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/09/05	10:19:56.5	XRT_QT_PROG_SET_426_OG [0x1aa]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2015/09/05	10:19:58.5	XRT_FL_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2015/09/05	10:20:00.5	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/09/05	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/09/05	11:00:00.5	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 b2 4e 01 68		
2015/09/05	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/09/05	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/09/05	11:00:22.0	XRT_AEC_RESET_443_OG [0x1bb]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/09/05	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/09/05	11:00:26.0	XRT_FLD_RESET_448_OG [0x1c0]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/09/05	11:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2015/09/05	11:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2015/09/05	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/09/05	14:36:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	14:36:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	14:36:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/09/05	14:36:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/09/05	14:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/09/05	14:47:00.0	XRT_Custom_430_OG [0x1ae]					
2015/09/05	14:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/09/05	16:11:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	16:11:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	16:11:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/09/05	16:11:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/09/05	16:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/09/05	16:35:00.0	XRT_Custom_430_OG [0x1ae]					
2015/09/05	16:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/09/05	17:48:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	17:48:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	17:48:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/09/05	17:48:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/09/05	17:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/09/05	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	18:11:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/05	18:11:28.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]					
		XRT_FOCUS_RECAL	2	07-F8	78 00		
2015/09/05	18:11:30.5	AOCS_Ore-point_Start_3_OG [0x099]					

Sep 05, 15 13:31

## XRT\_OGLIST\_0378.chk

Page 2/11

2015/09/05	18:15:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCU_NM	5	02-76	00	00	00	00	00
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2015/09/05	18:15:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/09/05	18:18:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/09/05	18:18:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/05	18:18:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/09/05	18:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/05	18:21:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/05	18:21:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/05	18:21:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/09/05	18:21:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03	00	00	00	00
2015/09/05	18:21:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/05	18:21:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/05	18:21:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/05	18:21:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/05	18:21:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/05	18:24:26.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2015/09/05	18:24:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/09/05	18:24:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/05	19:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/05	19:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/05	19:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/05	19:25:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/05	19:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/05	19:48:00.0	XRT_Custom_430_OG [0x1ae]								
2015/09/05	19:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/05	21:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/05	21:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/05	21:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/05	21:02:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/05	21:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/05	21:25:30.0	XRT_Custom_430_OG [0x1ae]								
2015/09/05	21:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/05	22:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/05	22:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/05	22:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/05	22:39:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/05	22:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/05	23:00:30.0	XRT_Custom_430_OG [0x1ae]								
2015/09/05	23:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/06	00:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/06	00:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/06	00:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/06	00:16:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/06	00:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/06	00:24:30.0	XRT_Custom_430_OG [0x1ae]								
2015/09/06	00:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/06	01:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/06	01:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				

Sep 05, 15 13:31

## XRT\_OGLIST\_0378.chk

Page 3/11

2015/09/06	01:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2015/09/06	01:40:06.0	MDP_XRT_FLD_RESET			
2015/09/06	01:43:14.0	XRT_PREFLR_STRT_425_OG [0x1a9]	1	07-F0	e8
2015/09/06	01:43:14.0	MDP_XRT_PREFLR_STRT			
2015/09/06	01:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2015/09/06	01:59:30.0	MDP_XRT_PREFLR_STOP			
2015/09/06	01:59:30.0	XRT_Custom_430_OG [0x1ae]			
2015/09/06	02:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2015/09/06	02:00:30.0	MDP_XRT_CTRL_AUTO			
2015/09/06	03:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2015/09/06	03:15:30.0	MDP_XRT_CTRL_MANU			
2015/09/06	03:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2015/09/06	03:15:32.0	MDP_XRT_CTRL_MANU			
2015/09/06	03:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2015/09/06	03:15:34.0	MDP_XRT_FLD_RESET			
2015/09/06	03:15:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	1	07-F0	e8
2015/09/06	03:15:36.0	MDP_XRT_PREFLR_STRT			
2015/09/06	03:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2015/09/06	03:18:44.0	MDP_XRT_PREFLR_STOP			
2015/09/06	03:36:30.0	XRT_Custom_430_OG [0x1ae]			
2015/09/06	03:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2015/09/06	03:37:30.0	MDP_XRT_CTRL_AUTO			
2015/09/06	04:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2015/09/06	04:45:30.0	MDP_XRT_CTRL_MANU			
2015/09/06	04:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2015/09/06	04:45:32.0	MDP_XRT_CTRL_MANU			
2015/09/06	04:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2015/09/06	04:45:34.0	MDP_XRT_FLD_RESET			
2015/09/06	04:45:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	1	07-F0	e8
2015/09/06	04:45:36.0	MDP_XRT_PREFLR_STRT			
2015/09/06	04:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2015/09/06	04:48:44.0	MDP_XRT_PREFLR_STOP			
2015/09/06	05:13:30.0	XRT_Custom_430_OG [0x1ae]			
2015/09/06	05:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2015/09/06	05:14:30.0	MDP_XRT_CTRL_AUTO			
2015/09/06	05:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2015/09/06	05:57:54.0	MDP_XRT_CTRL_MANU			
2015/09/06	05:57:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2015/09/06	05:57:56.0	MDP_XRT_CTRL_MANU			
2015/09/06	05:57:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	4	07-F8	22 ff aa 00
2015/09/06	05:57:58.0	XRT_FOCUS_POSITION			
2015/09/06	05:58:00.0	XRT_FOCUS_POSITION			
2015/09/06	05:58:00.0	AOCs_Ore-point_Start_3_OG [0x099]	5	02-76	00 00 00 00 00
2015/09/06	05:58:00.0	AOCU_NM			
2015/09/06	05:58:18.0	XRT_FLD_DIS_406_OG [0x196]	1	07-F0	d9
2015/09/06	05:58:18.0	MDP_XRT_FLD_DIS			
2015/09/06	06:00:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0	c9
2015/09/06	06:00:54.0	MDP_XRT_FLRCTRL_DIS			
2015/09/06	06:00:56.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5
2015/09/06	06:00:56.0	MDP_XRT_ARS_DIS			
2015/09/06	06:00:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	2	07-F0	c4 0e
2015/09/06	06:00:58.0	MDP_XRT_QT_PROG_SET			
2015/09/06	06:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0
2015/09/06	06:01:00.0	MDP_XRT_CTRL_AUTO			
2015/09/06	06:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2015/09/06	06:07:54.0	MDP_XRT_CTRL_MANU			
2015/09/06	06:07:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2015/09/06	06:07:56.0	MDP_XRT_CTRL_MANU			
2015/09/06	06:07:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	4	07-F8	22 fe 97 00
2015/09/06	06:07:58.0	XRT_FOCUS_POSITION			
2015/09/06	06:08:00.0	XRT_FOCUS_POSITION			
2015/09/06	06:08:00.0	AOCs_Ore-point_Start_4_OG [0x09a]	5	02-76	03 00 00 00 00
2015/09/06	06:08:00.0	AOCU_NM			
2015/09/06	06:08:18.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8
2015/09/06	06:08:18.0	MDP_XRT_FLD_ENA			
2015/09/06	06:08:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8
2015/09/06	06:08:20.0	MDP_XRT_FLRCTRL_ENA			
2015/09/06	06:08:22.0	XRT_AEC_RESET_441_OG [0x1b9]	1	07-F0	d0
2015/09/06	06:08:22.0	MDP_XRT_AEC_RESET			
2015/09/06	06:08:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5
2015/09/06	06:08:24.0	MDP_XRT_ARS_DIS			
2015/09/06	06:08:26.0	XRT_FLD_RESET_447_OG [0x1bf]	1	07-F0	da
2015/09/06	06:08:26.0	MDP_XRT_FLD_RESET			
2015/09/06	06:10:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	2	07-F0	c4 12
2015/09/06	06:10:56.0	MDP_XRT_QT_PROG_SET			
2015/09/06	06:10:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	2	07-F0	c5 03
2015/09/06	06:10:58.0	MDP_XRT_FL_PROG_SET			
2015/09/06	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0
2015/09/06	06:11:00.0	MDP_XRT_CTRL_AUTO			
2015/09/06	06:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2015/09/06	06:26:00.0	MDP_XRT_CTRL_MANU			
2015/09/06	06:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2015/09/06	06:26:02.0	MDP_XRT_CTRL_MANU			
2015/09/06	06:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2015/09/06	06:26:04.0	MDP_XRT_FLD_RESET			
2015/09/06	06:26:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	1	07-F0	e8
2015/09/06	06:26:06.0	MDP_XRT_PREFLR_STRT			
2015/09/06	06:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2015/09/06	06:29:14.0	MDP_XRT_PREFLR_STOP			
2015/09/06	06:51:00.0	XRT_Custom_430_OG [0x1ae]			
2015/09/06	06:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2015/09/06	06:52:00.0	MDP_XRT_CTRL_AUTO			
2015/09/06	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2015/09/06	07:59:54.0	MDP_XRT_CTRL_MANU			
2015/09/06	07:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			



2015/09/06	07:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	08:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/09/06	08:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/09/06	08:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/09/06	08:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/09/06	08:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/09/06	08:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/06	08:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/06	08:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2015/09/06	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/09/06	08:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/06	08:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	08:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	08:06:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/06	08:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/06	08:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/06	08:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/09/06	09:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/06	09:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	09:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	09:46:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/06	09:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/06	10:01:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/06	10:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/09/06	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/06	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	11:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/09/06	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/09/06	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/09/06	11:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/09/06	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/09/06	11:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/06	11:02:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/06	11:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2015/09/06	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/09/06	15:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/06	15:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	15:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	15:10:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/06	15:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/06	15:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/06	15:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/09/06	16:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/06	16:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	16:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/06	16:46:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/06	16:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

Sep 05, 15 13:31

## XRT\_OGLIST\_0378.chk

Page 5/11

2015/09/06	17:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/06	17:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/06	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/09/06	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]				
			AOCU_NM	5	02-76	00 00 00 00 00
2015/09/06	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2015/09/06	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/09/06	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/09/06	18:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/09/06	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/06	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/09/06	18:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]				
			AOCU_NM	5	02-76	03 00 00 00 00
2015/09/06	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2015/09/06	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/09/06	18:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2015/09/06	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2015/09/06	18:10:26.0	XRT_FLD_RESET_447_OG [0x1bf]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/06	18:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2015/09/06	18:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/09/06	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/06	18:23:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	18:23:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	18:23:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/06	18:23:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/06	18:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/06	18:46:30.0	XRT_Custom_430_OG [0x1ae]				
2015/09/06	18:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/06	20:00:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	20:00:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	20:00:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/06	20:00:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/06	20:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/06	20:24:00.0	XRT_Custom_430_OG [0x1ae]				
2015/09/06	20:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/06	21:37:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	21:37:32.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	21:37:34.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/06	21:37:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/06	21:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/06	22:00:00.0	XRT_Custom_430_OG [0x1ae]				
2015/09/06	22:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/06	23:15:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	23:15:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/06	23:15:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/06	23:15:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8

2015/09/06	23:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/06	23:30:30.0	XRT_Custom_430_OG [0x1ae]							
2015/09/06	23:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/07	00:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	00:47:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	00:47:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/07	00:47:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/07	00:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/07	00:58:30.0	XRT_Custom_430_OG [0x1ae]							
2015/09/07	00:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/07	02:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	02:14:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	02:14:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/07	02:14:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/07	02:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/07	02:34:30.0	XRT_Custom_430_OG [0x1ae]							
2015/09/07	02:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/07	03:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	03:42:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	03:42:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/07	03:42:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/07	03:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/07	04:11:30.0	XRT_Custom_430_OG [0x1ae]							
2015/09/07	04:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/07	05:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	05:22:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	05:22:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/07	05:22:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/07	05:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/07	05:49:00.0	XRT_Custom_430_OG [0x1ae]							
2015/09/07	05:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/07	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/09/07	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/09/07	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/09/07	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/09/07	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/07	06:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/09/07	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/07	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/07	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/09/07	06:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/09/07	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/07	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/07	06:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/07	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/07	06:10:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

2015/09/07	06:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2015/09/07	06:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/09/07	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	07:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	07:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	07:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	07:02:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/07	07:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/07	07:26:30.0	XRT_Custom_430_OG [0x1ae]					
2015/09/07	07:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	07:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	07:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/09/07	08:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03	00 00 00 00
2015/09/07	08:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/09/07	08:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/09/07	08:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/09/07	08:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/07	08:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	08:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2015/09/07	08:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/09/07	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	08:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	08:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	08:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	08:42:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/07	08:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/07	09:02:30.0	XRT_Custom_430_OG [0x1ae]					
2015/09/07	09:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	10:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	10:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	10:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	10:23:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/07	10:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/07	10:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	52 4d 01 68
2015/09/07	10:33:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	10:33:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	10:33:06.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/09/07	10:33:26.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/09/07	10:33:28.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/09/07	10:33:30.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/09/07	10:33:32.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/07	10:33:34.0	XRT_FLD_RESET_448_OG [0x1c0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	10:36:04.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2015/09/07	10:36:06.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2015/09/07	10:36:08.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	14:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	14:12:32.0	XRT_CTRL_MANU_402_OG [0x192]					

2015/09/07	14:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	14:12:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	14:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/07	14:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/07	14:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	15:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	15:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	15:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	15:45:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	15:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/07	16:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/07	16:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	17:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	17:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	17:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	17:21:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	17:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/07	17:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/07	17:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	18:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	18:00:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	18:00:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	18:01:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/09/07	18:01:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/09/07	18:03:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/09/07	18:03:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/09/07	18:03:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/07	18:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2015/09/07	18:10:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	18:10:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	18:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	18:11:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/09/07	18:11:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2015/09/07	18:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/09/07	18:11:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/09/07	18:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/09/07	18:11:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/07	18:13:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	18:13:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2015/09/07	18:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/09/07	18:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	18:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	18:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	18:58:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	19:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/07	19:15:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/07	19:23:00.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	00 ff 6d ae 46	
2015/09/07	19:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2015/09/07	19:23:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	19:23:24.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/09/07	19:23:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/09/07	19:23:28.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/09/07	19:23:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/09/07	19:23:32.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/07	19:26:02.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	19:26:04.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2015/09/07	19:26:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/09/07	20:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	20:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	20:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	20:35:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	20:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/07	20:59:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/07	21:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/09/07	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	21:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	21:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	22:00:00.0	AOCs_Orе-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/09/07	22:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 ff 6d ae 46	
2015/09/07	22:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/09/07	22:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/09/07	22:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/09/07	22:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/07	22:02:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	22:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2015/09/07	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/09/07	22:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	22:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	22:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	22:13:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	22:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/07	22:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/07	22:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/09/07	23:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/07	23:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	23:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/07	23:50:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/07	23:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/08	00:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/08	00:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/09/08	01:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/08	01:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/08	01:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/08	01:15:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/08	01:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/08	01:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

Sep 05, 15 13:31

XRT\_OGLIST\_0378.chk

Page 10/11

2015/09/08	01:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/08	02:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	02:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	02:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/08	02:49:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/08	02:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/08	03:10:00.0	XRT_Custom_430_OG [0x1ae]							
2015/09/08	03:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/08	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	03:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/09/08	04:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/09/08	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/08	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/08	04:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/08	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/08	04:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/08	04:02:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2015/09/08	04:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/09/08	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/08	04:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	04:18:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	04:18:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/08	04:18:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/08	04:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/08	04:47:00.0	XRT_Custom_430_OG [0x1ae]							
2015/09/08	04:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/08	05:30:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	05:30:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	05:30:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/09/08	05:31:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/09/08	05:31:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/09/08	05:33:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/09/08	05:33:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/08	05:33:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/09/08	05:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/08	05:40:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	05:40:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/08	05:40:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/09/08	05:41:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/09/08	05:41:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/08	05:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/08	05:41:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/08	05:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/08	05:41:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/08	05:43:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2015/09/08	05:43:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/09/08	05:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

Sep 05, 15 13:31

## XRT\_OGLIST\_0378.chk

Page 11/11

2015/09/08	05:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/09/08	05:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	05:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	05:58:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/09/08	06:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/09/08	06:24:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/09/08	06:25:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/09/08	07:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	07:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	07:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	07:39:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/09/08	07:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/09/08	08:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/09/08	08:02:00.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	03 00 00 00 00		
2015/09/08	08:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	08:02:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	08:02:24.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/09/08	08:02:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/09/08	08:02:28.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/09/08	08:02:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/09/08	08:02:32.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/09/08	08:05:02.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/09/08	08:05:04.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2015/09/08	08:05:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2015/09/08	09:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/09/08	09:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	09:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	09:18:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/09/08	09:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/09/08	09:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/09/08	09:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/09/08	09:50:55.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	09:50:57.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/09/08	10:11:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
		AOCU_NM		5	02-76	00 00 00 00 00		