

XRT Timeline to be uploaded on 2015/09/10

Period: 2015/09/10 10:22:00 - 2015/09/15 11:08:00

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Normal mode

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XOB #1AC2: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 120s cad												
Term	Pointing (x, y)	Comment										
09/10 10:43:06 - 09/10 11:17:54	Fixed (936.0, 0.0)	# OP start + 10min, HOP 130 (EIS Full Sun Scan) 1/15										
09/10 11:21:00 - 09/10 11:33:54	Fixed (468.0, 0.0)	# 2/15										
09/10 11:53:00 - 09/10 12:05:54	Fixed (-468.0, 0.0)	# 4/15										
09/10 12:09:00 - 09/10 12:21:54	Fixed (-936.0, 0.0)	# 5/15										
09/10 12:25:00 - 09/10 12:31:54	Fixed (815.0, 470.0)	# 6/15										
09/10 12:35:00 - 09/10 12:47:54	Fixed (411.0, 847.0)	# 7/15										
09/10 12:51:00 - 09/10 13:03:54	Fixed (0.0, 941.0)	# 8/15										
09/10 13:07:00 - 09/10 13:19:54	Fixed (-411.0, 847.0)	# 9/15										
09/10 13:23:00 - 09/10 13:29:54	Fixed (-815.0, 470.0)	# 10/15										
09/10 13:33:00 - 09/10 13:39:54	Fixed (815.0, -470.0)	# 11/15										
09/10 13:43:00 - 09/10 13:55:54	Fixed (411.0, -847.0)	# 12/15										
09/10 13:59:00 - 09/10 14:20:30	Fixed (0.0, -941.0)	# 13/15										
09/10 14:37:30 - 09/10 14:50:24	Fixed (-411.0, -847.0)	# 14/15										
09/10 14:53:30 - 09/10 15:06:24	Fixed (-815.0, -471.0)	# 15/15										
PROG= 09 Inf.-time(s)												
└─ Subr= 1 45-time(s) 120.0sec												
└─ Seqn= 29 1-time(s) 4.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 26 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1ABC: Synoptic 8 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Ti-poly(16/362/2x1024), Al-poly+Ti-poly												
Term	Pointing (x, y)	Comment										
09/10 11:37:00 - 09/10 11:49:54	Fixed (0.0, 0.0)	# 3/15										
PROG= 16 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 33 1-time(s) 4.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 49 1-time(s) 4.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 77 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 1-time(s) 12.0sec												
└─ Seqn= 46 2-time(s) 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 61 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 72 2-time(s) 4.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 59 2-time(s) 2.0sec												
└─ med-Al/Open med-Al/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ med-Al/Open med-Al/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1ACA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with												
Term	Pointing (x, y)	Comment										
09/10 15:09:30 - 09/10 17:30:00	Track (-178.6, -262.1)	# Observe AR 12412										
09/10 18:08:30 - 09/11 05:39:54	Track (-151.8, -262.7)	# AR 12412										

09/11 05:53:00 - 09/11 07:48:00 Track (-45.1, -264.1) @ 09/11 05:50:00 # AR 12412
 09/11 17:55:00 - 09/11 23:59:54 Track (64.7, -264.0) @ 09/11 17:52:00 # AR 12412
 09/12 00:03:00 - 09/12 05:59:54 Fixed (920.0, 20.0) # AR 12409 - EIS/IRIS coronal rain observations
 09/12 06:13:00 - 09/12 07:59:54 Track (176.2, -262.1) @ 09/12 06:10:00 # AR 12412

PROG= 18 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
Seqn= 56 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 4-time(s) 2.0sec													
Seqn= 75 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 23 4-time(s) 240.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AB2: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/1024) + Al-

Term	Pointing (x, y)	Comment
09/10 17:58:30 - 09/10 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
09/11 05:43:00 - 09/11 05:49:54	Fixed (0.0, 0.0)	synoptic, shifted -20.0 min
09/11 17:45:00 - 09/11 17:51:54	Fixed (0.0, 0.0)	synoptic, shifted -18.0 min
09/12 06:03:00 - 09/12 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 14 1-time(s)

Subr= 1 1-time(s) 12.0sec													
Seqn= 33 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 61 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 39 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AD6: HOP257 Target A- Al/Poly - 384FOV - 1x1 - Q95 - 30s Cad - AEC1

Term	Pointing (x, y)	Comment
09/11 08:14:06 - 09/11 10:59:54	Track (593.0, -6.0) @ 09/11 08:00:00	# HOP 257 - unresolved fine structure
09/12 08:03:00 - 09/12 10:05:00	Track (748.3, 13.9) @ 09/12 08:00:00	# HOP 257 - unresolved fine structure

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
Seqn= 56 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 120-time(s) 30.0sec													
Seqn= 20 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms

Term	Pointing (x, y)	Comment
09/11 11:03:00 - 09/11 17:41:54	Fixed (-16.0, 874.0)	# HOP 81 & HOP 206 at North Pole
PROG= 20 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 9 2-time(s) 2.0sec		
Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec		
Subr= 2 1-time(s) 2.0sec		
Seqn= 7 1-time(s) 30.0sec		
Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec		
Subr= 3 30-time(s) 2.0sec		
Seqn= 57 1-time(s) 60.0sec		
Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec		
Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

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Flare mode

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Term	Pointing (x, y)	Comment
XOB #1A9B: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512		
09/10 10:43:06 - 09/10 11:17:54	Fixed (936.0, 0.0)	# OP start + 10min, HOP 130 (EIS Full Sun Scan) 1/15
09/10 11:21:00 - 09/10 11:33:54	Fixed (468.0, 0.0)	# 2/15
09/10 11:53:00 - 09/10 12:05:54	Fixed (-468.0, 0.0)	# 4/15
09/10 12:09:00 - 09/10 12:21:54	Fixed (-936.0, 0.0)	# 5/15
09/10 12:25:00 - 09/10 12:31:54	Fixed (815.0, 470.0)	# 6/15
09/10 12:35:00 - 09/10 12:47:54	Fixed (411.0, 847.0)	# 7/15
09/10 12:51:00 - 09/10 13:03:54	Fixed (0.0, 941.0)	# 8/15
09/10 13:07:00 - 09/10 13:19:54	Fixed (-411.0, 847.0)	# 9/15
09/10 13:23:00 - 09/10 13:29:54	Fixed (-815.0, 470.0)	# 10/15
09/10 13:33:00 - 09/10 13:39:54	Fixed (815.0, -470.0)	# 11/15
09/10 13:43:00 - 09/10 13:55:54	Fixed (411.0, -847.0)	# 12/15
09/10 13:59:00 - 09/10 14:20:30	Fixed (0.0, -941.0)	# 13/15
09/10 14:37:30 - 09/10 14:50:24	Fixed (-411.0, -847.0)	# 14/15
09/10 14:53:30 - 09/10 15:06:24	Fixed (-815.0, -471.0)	# 15/15
09/10 15:09:30 - 09/10 17:30:00	Track (-178.6, -262.1) @ 09/10 15:06:30	# Observe AR 12412
09/10 18:08:30 - 09/11 05:39:54	Track (-151.8, -262.7) @ 09/10 18:05:30	# AR 12412
09/11 05:53:00 - 09/11 07:48:00	Track (-45.1, -264.1) @ 09/11 05:50:00	# AR 12412
09/11 08:14:06 - 09/11 10:59:54	Track (593.0, -6.0) @ 09/11 08:00:00	# HOP 257 - unresolved fine structure
09/11 11:03:00 - 09/11 17:41:54	Fixed (-16.0, 874.0)	# HOP 81 & HOP 206 at North Pole
09/11 17:55:00 - 09/11 23:59:54	Track (64.7, -264.0) @ 09/11 17:52:00	# AR 12412
09/12 00:03:00 - 09/12 05:59:54	Fixed (920.0, 20.0)	# AR 12409 - EIS/IRIS coronal rain observations
09/12 06:13:00 - 09/12 07:59:54	Track (176.2, -262.1) @ 09/12 06:10:00	# AR 12412
09/12 08:03:00 - 09/12 10:05:00	Track (748.3, 13.9) @ 09/12 08:00:00	# HOP 257 - unresolved fine structure

Term	Pointing (x, y)	Comment
PROG= 03 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec		
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec		
med-Be/Open Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec		
Seqn= 53 1-time(s) 2.0sec		
Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec		
Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec		
Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

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Active Region Search

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NOT USED

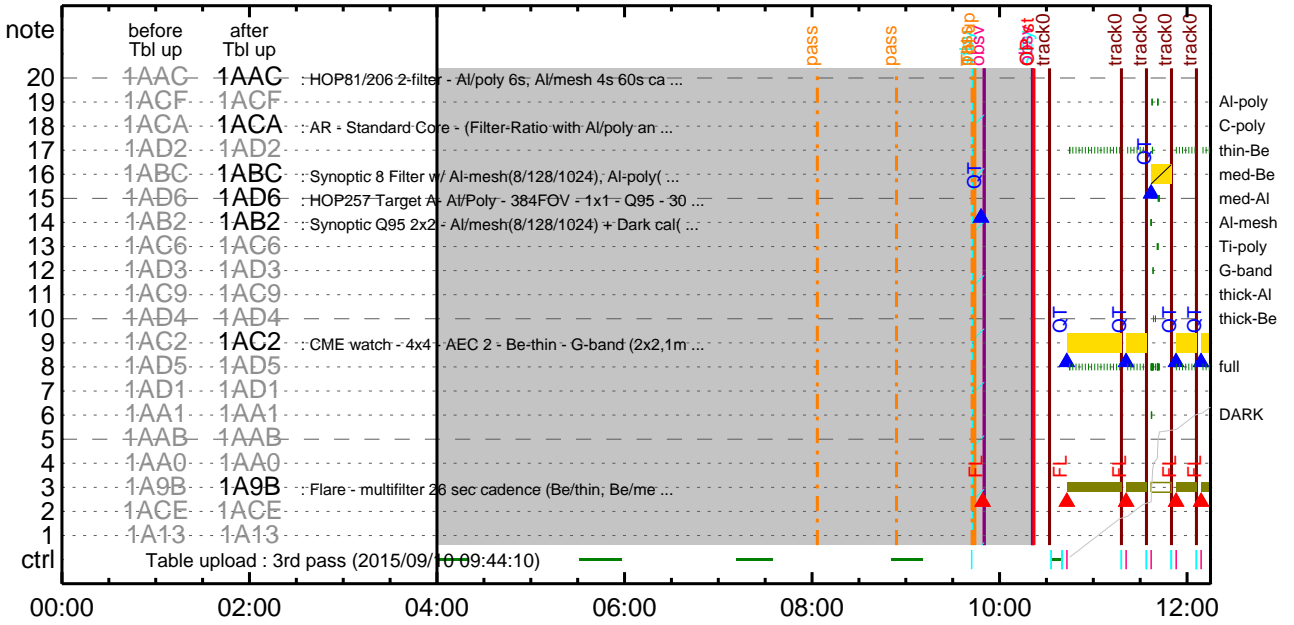
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Flare Detection

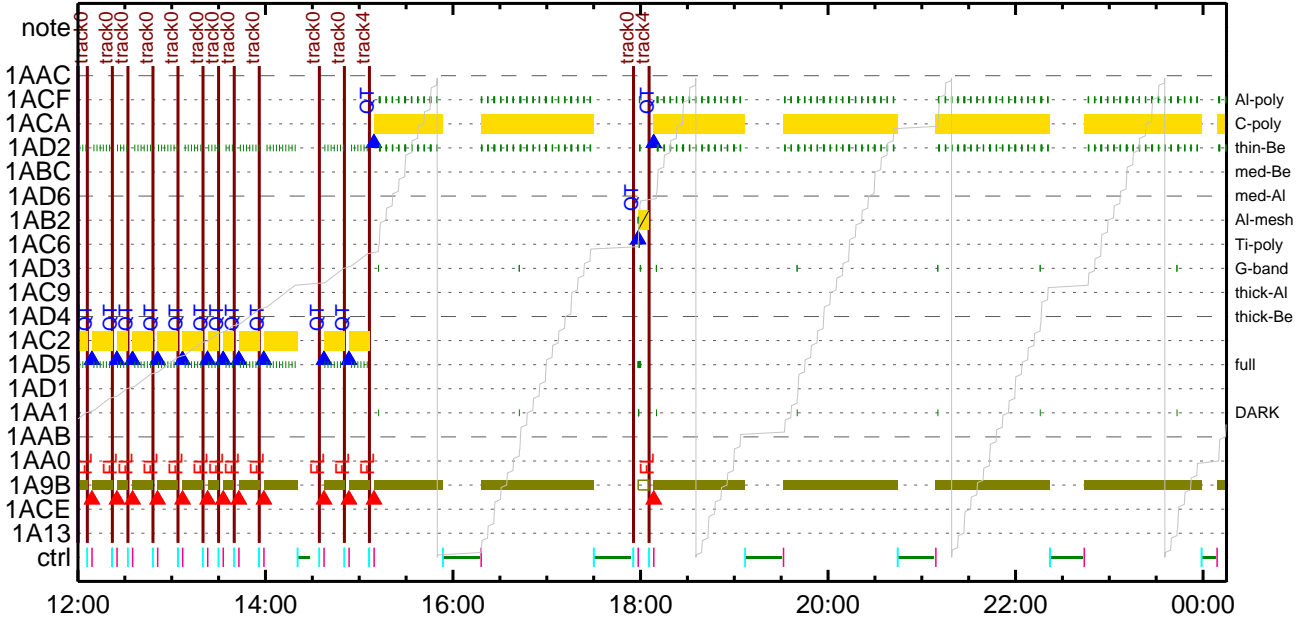
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Term	Pointing (x, y)	Comment
FLD Patrol		
09/10 11:50:18 - 09/10 17:55:48	Fixed (-468.0, 0.0)	# 4/15
09/10 18:05:48 - 09/11 05:40:18	Track (-151.8, -262.7) @ 09/10 18:05:30	# AR 12412
09/11 05:50:18 - 09/11 17:42:18	Track (-45.1, -264.1) @ 09/11 05:50:00	# AR 12412
09/11 17:52:18 - 09/12 06:00:18	Track (64.7, -264.0) @ 09/11 17:52:00	# AR 12412
09/12 06:10:18 - 09/15 11:08:00	Track (176.2, -262.1) @ 09/12 06:10:00	# AR 12412
Open/Ti-poly Open/thick-Al close Safe Norm 8ms Obs 8x8 Q=50 30sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

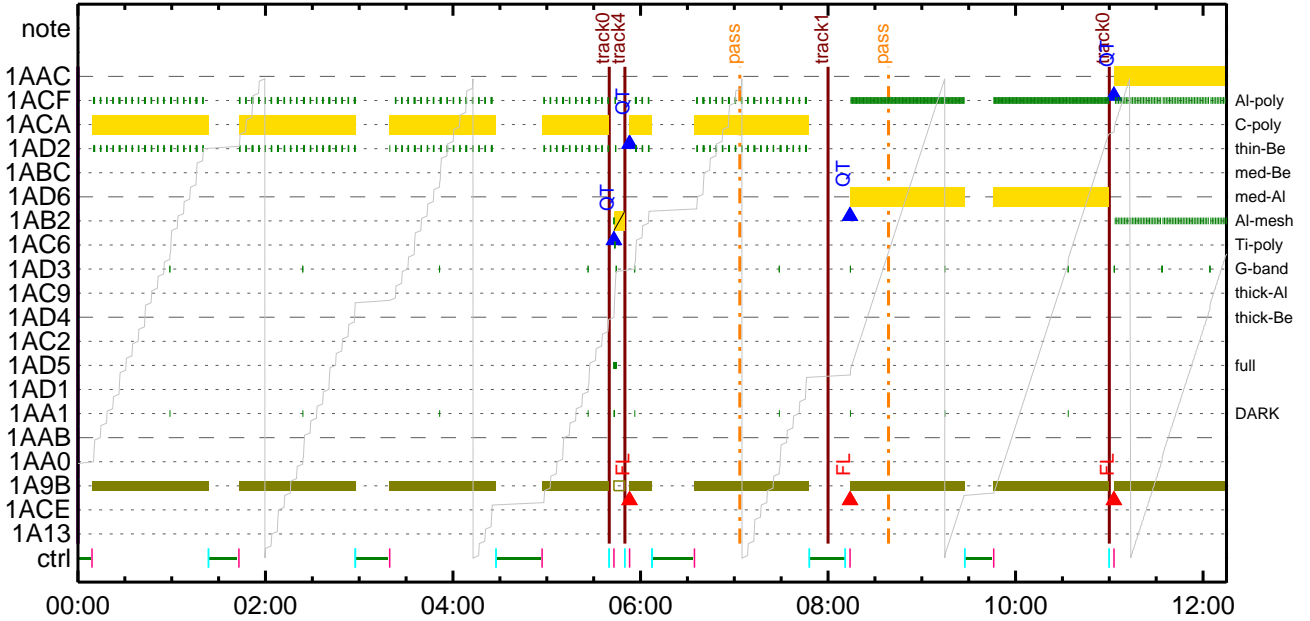
CMDI #0386 2015/09/10



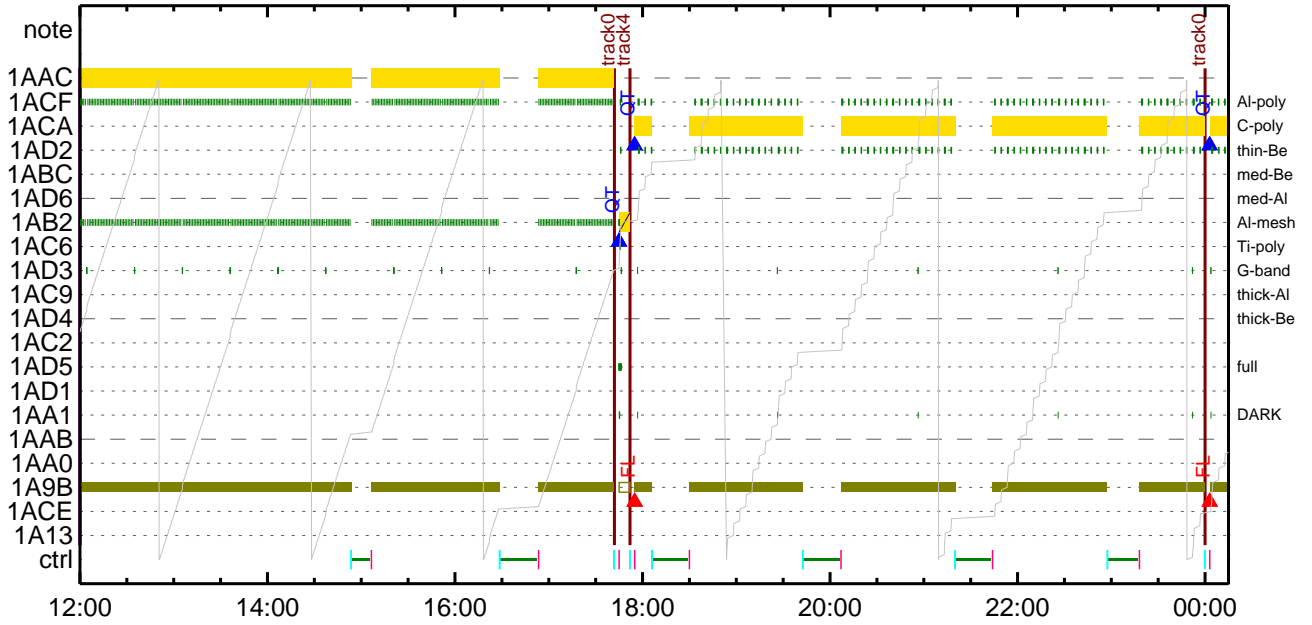
CMDI #0386 2015/09/10



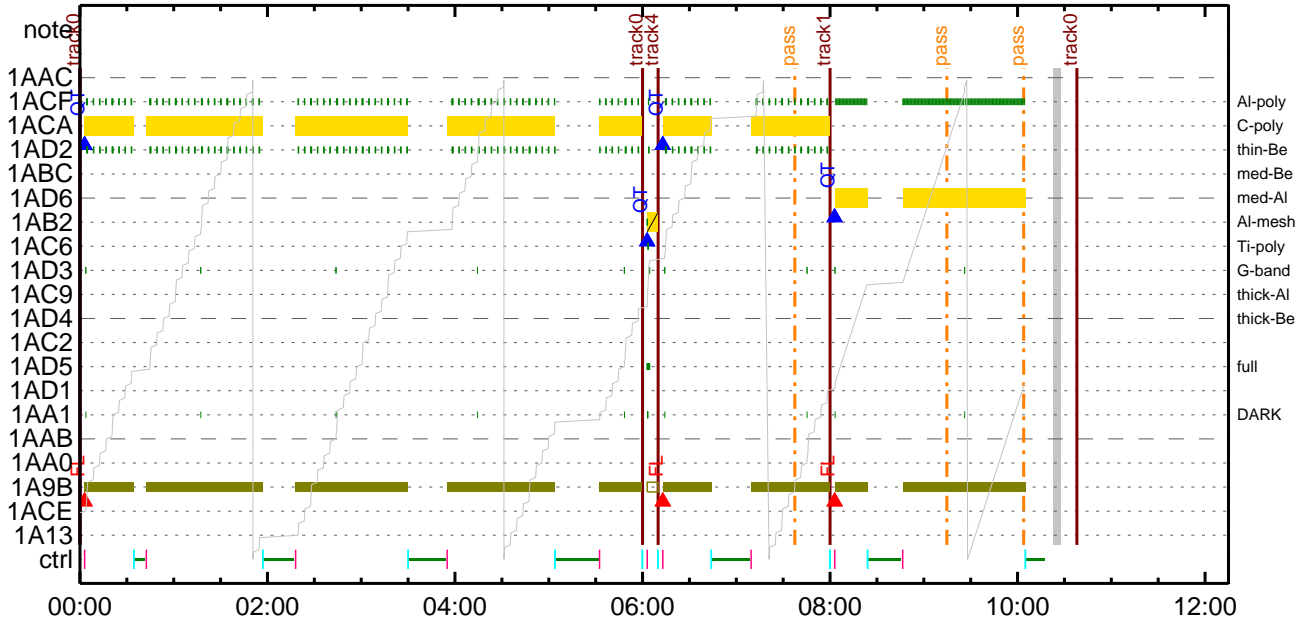
CMDI #0386 2015/09/11



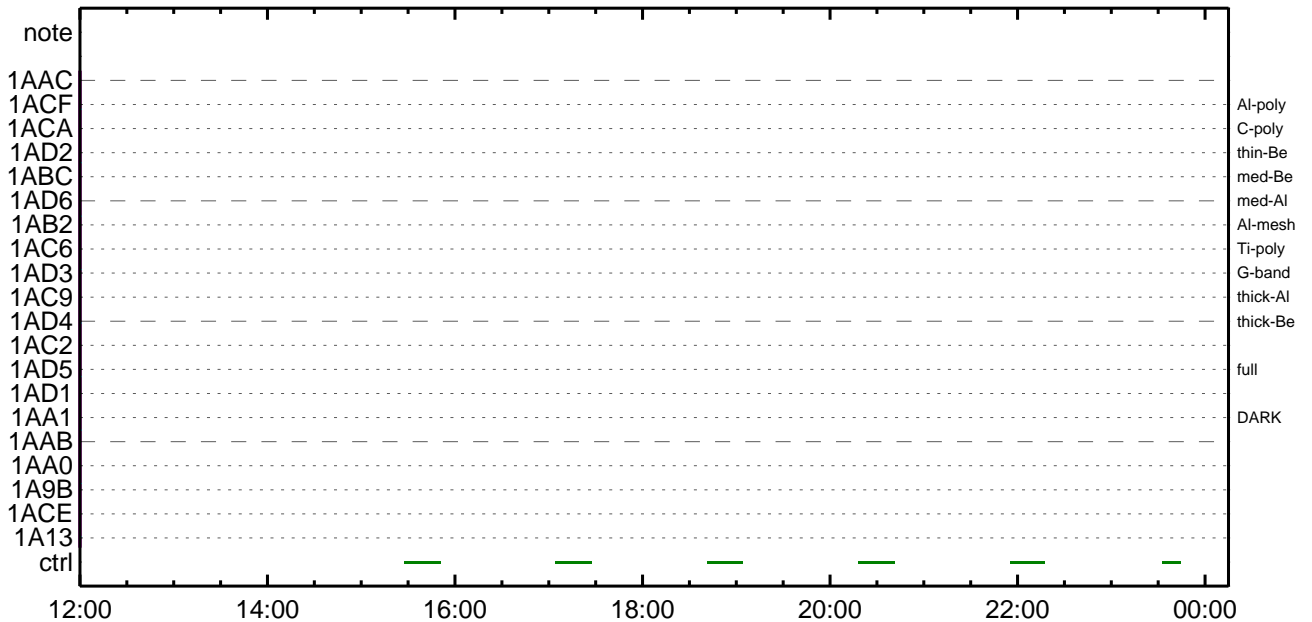
CMDI #0386 2015/09/11



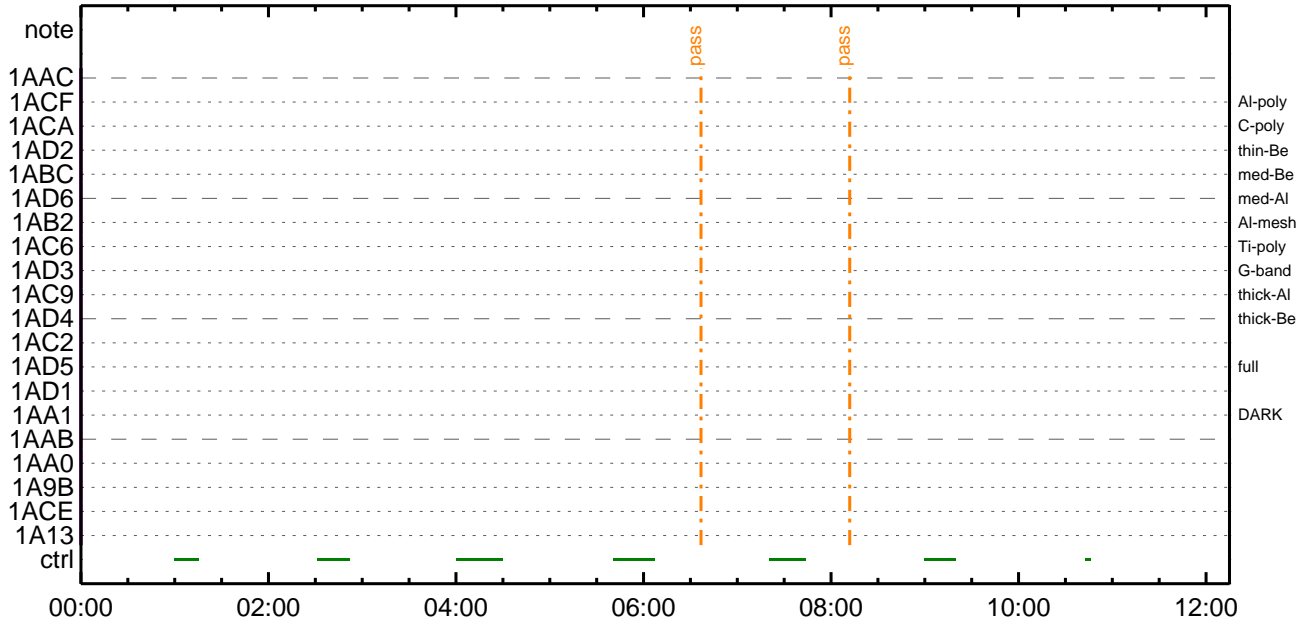
CMDI #0386 2015/09/12



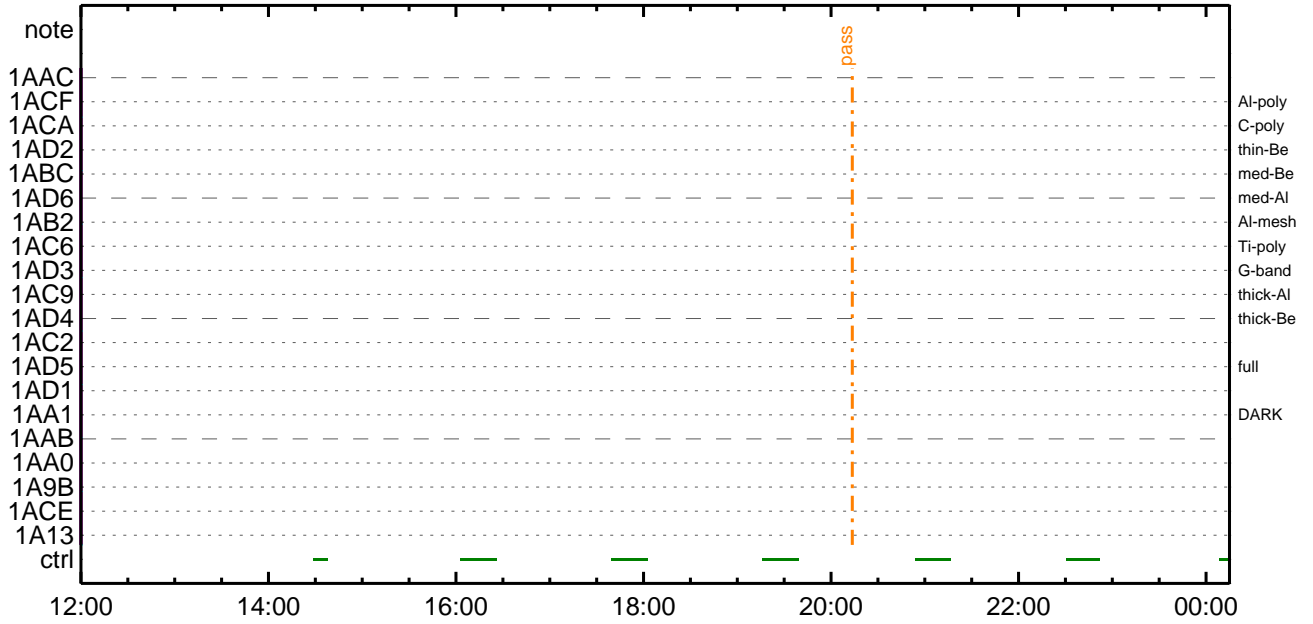
CMDI #0386 2015/09/12



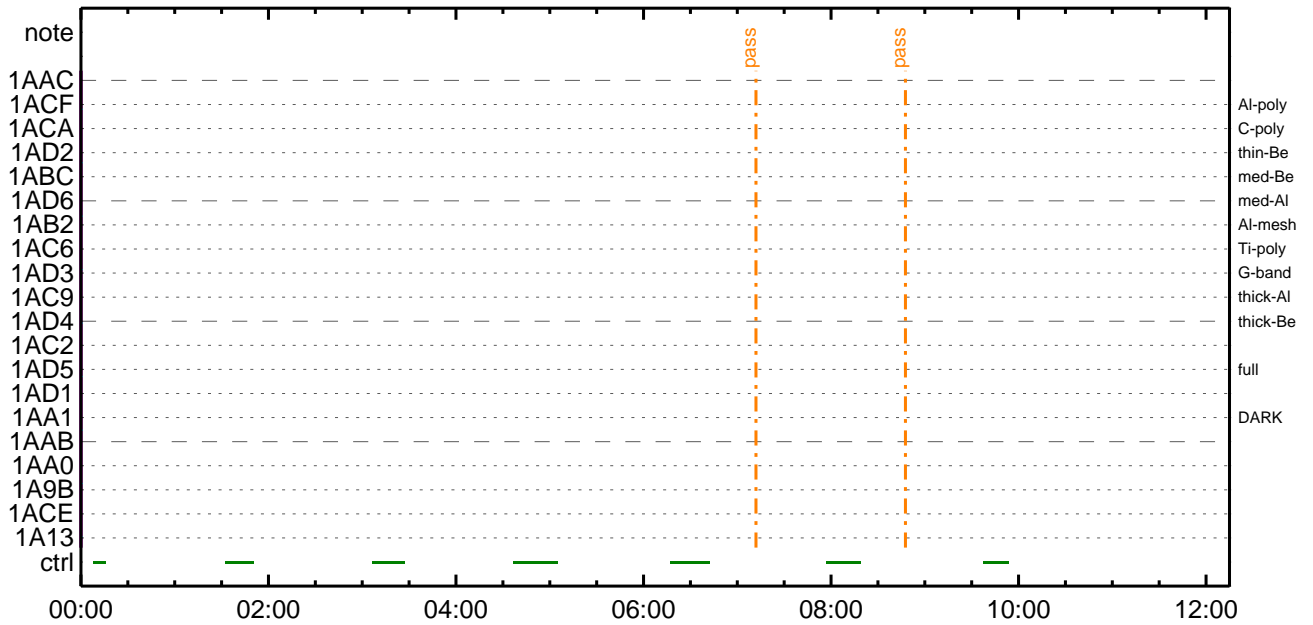
CMDI #0386 2015/09/13



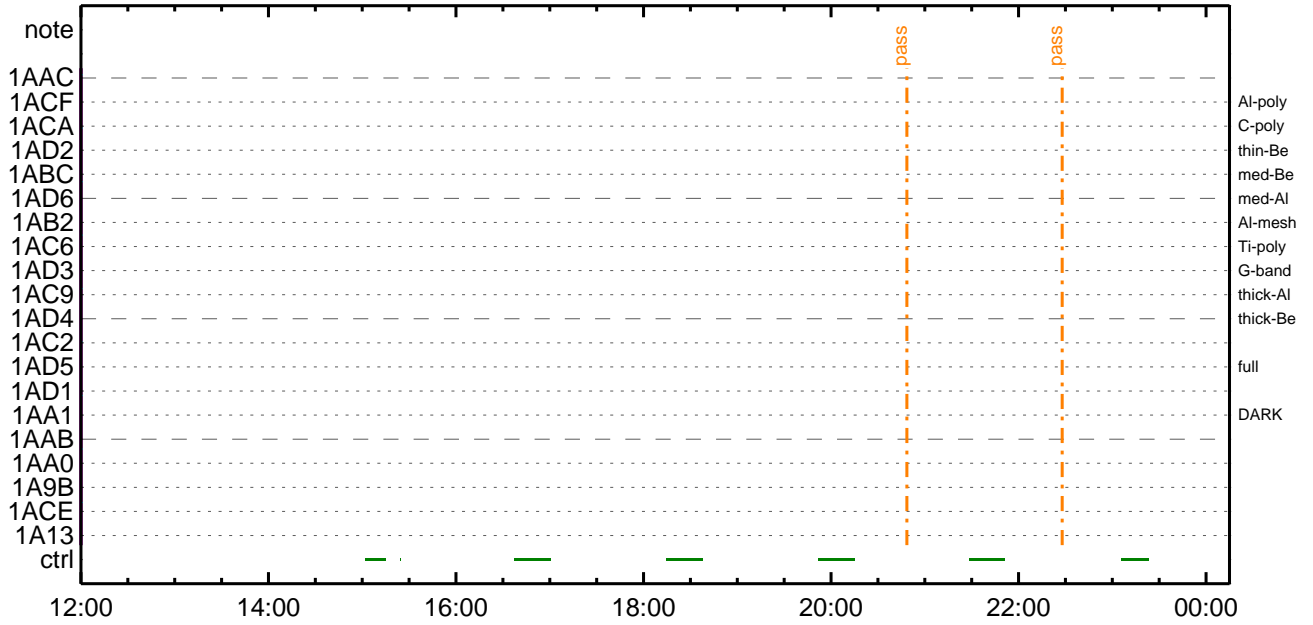
CMDI #0386 2015/09/13



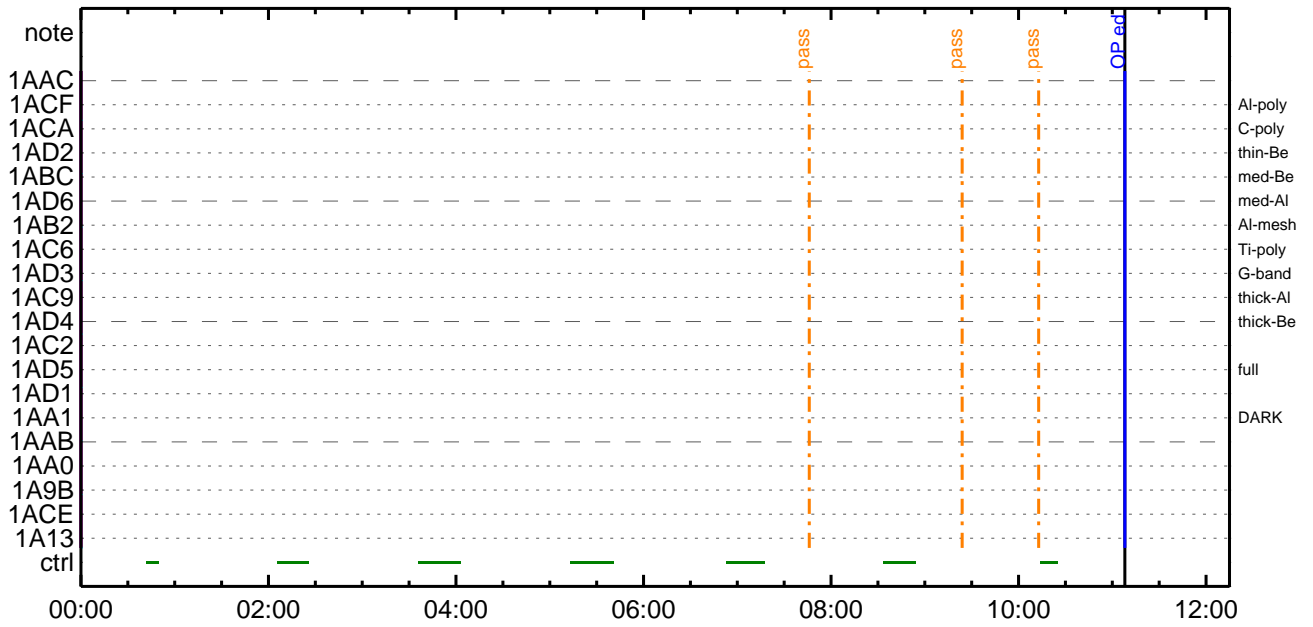
CMDI #0386 2015/09/14



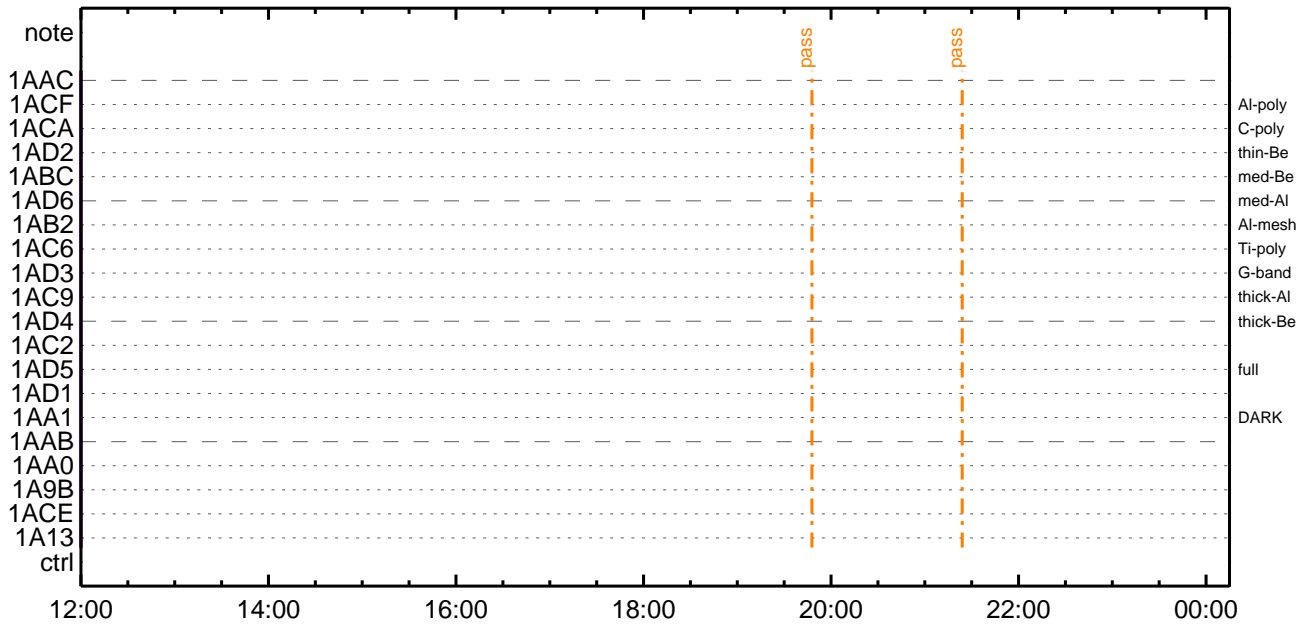
CMDI #0386 2015/09/14



CMDI #0386 2015/09/15



CMDI #0386 2015/09/15




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-364:OP
0104 ( )
0105 S. OG og-364:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î½E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;E²òîã²
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î½E¹ç•è²îOK²³îÇ§
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³YpYôYE²ò²ÁDîç(UT)
0183 +. TI 2015-09-10 10:17:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-09-10 10:17:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-09-10 10:17:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2015-09-10 10:21:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼ïÄê%îíñîîŷÄŷ§ŷÄŷ¹âiü
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TÎîî°êŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îç§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBLîî¼Ê¹çªë²îOKòò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷÉ;Êòðîâª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2015-09-10 10:21:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2015-09-10 10:21:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2015-09-10 10:21:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2015-09-10 10:21:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîñî»ö¼ŷîÊÃðª¹êDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÉŷŷŷŷÄŷçŷèè¼ªª¼ª»Ûª¹è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹.î Daily±çîñîè´øª¹êDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ¹¼ª»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-365 2015-09-10 13:33:29 117 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È±¿±Á±•µ°È»Í×ÁÇ±ÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;È±È¼°ÇÔ±•±¿¼í¹ç±Í;çÁ®, ù±¹±±±±±±±ÇÁ+¿®±•±È±±±±±±±È;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCS_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS_DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCS_ORB_UPD
0044 . C.
0045 . C. *****
0046 C. SOT table upload
0047 C. *****
0048 . C. < Stop FG table >
0049 +. DC 07-F0 MDP_FG_CTRL_MANU
0050 BC (51)
0051 . C.
0052 C. MDP_FG_CTRL_MODE = MANU [ ]
0053 C. -----
0054 C.
0055 . C. <Upload FG Observation Table>
0056 . S. RAM ram-269:MDP_OBS_F
0057 ( )
0058 C.
0059 . C. < Dump RAMID=MDP_OBS_F >
0060 +. DC 07-F0 MDP_DUMP_FGTBL
0061 BC (82 07 00 00 00 38 b8)
0062 C. -----
0063 C. MDP_OBS_F verify = OK/NG [ ]
0064 C. -----
0065 C.
0066 C. *****
0067 C. SOT TI command set
0068 C. *****
0069 C. Execute, after the success of TBL upload.
0070 +. TI 2015-09-10 10:21:18.0
0071 DC 07-F0 MDP_SOT_MODE_OBSV
0072 BC (40)
0073 . C. -----
0074 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0075 C. -----
0076 C.
0077 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0078 +. DC 07-FC EIS_MODE_CHG_ENA
0079 BC (20)
0080 . C. Verify EIS_MODE_CHG_FLG is ENA
0081 +. DC 07-FC EIS_MODE_MANU
0082 BC (21 02)
0083 . C. Verify EIS in MANUAL mode
0084 . C. Estimated OBSTBL upload time is 1m2s
0085 C. *****
0086 C. EIS START OBSTBL LOAD
0087 C. *****
0088 . S. RAM ram-820:EIS_OBSTBL
0089 ( )
0090 +. DC 07-FC EIS_DUMP_OBSTBL
0091 BC (07 07 07 00 00 70 00)
0092 C.
0093 C. Execute, after the success of OBSTBL upload.
0094 C. Set EIS TI-commands
0095 +. TI 2015-09-10 10:21:50.0
```


*** OP Sequence for XRT ***

```

2015/09/10 10:32:00.5 AOCS_ORe-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 00 00 00 ac cd
2015/09/10 10:33:00.5 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 10:33:02.5 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 10:33:04.5 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2015/09/10 10:33:06.5 XRT_PREFLR_STRT_425_OG [0x1a9]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2015/09/10 10:36:14.5 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2015/09/10 10:40:00.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 10:40:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 10:40:04.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION        4 07-F8 22 fe 97 00
2015/09/10 10:40:24.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA          1 07-F0 d8
2015/09/10 10:40:26.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA       1 07-F0 c8
2015/09/10 10:40:28.0 XRT_AEC_RESET_441_OG [0x1b9]
                        MDP_XRT_AEC_RESET         1 07-F0 d0
2015/09/10 10:40:30.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2015/09/10 10:40:32.0 XRT_FLD_RESET_447_OG [0x1bf]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2015/09/10 10:43:02.0 XRT_QT_PROG_SET_407_OG [0x197]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 09
2015/09/10 10:43:04.0 XRT_FL_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_FL_PROG_SET       2 07-F0 c5 03
2015/09/10 10:43:06.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2015/09/10 11:17:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 11:17:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 11:17:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION        4 07-F8 22 fe 97 00
2015/09/10 11:18:00.0 AOCS_ORe-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 d6 67
2015/09/10 11:18:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA          1 07-F0 d8
2015/09/10 11:18:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA       1 07-F0 c8
2015/09/10 11:18:22.0 XRT_AEC_RESET_441_OG [0x1b9]
                        MDP_XRT_AEC_RESET         1 07-F0 d0
2015/09/10 11:18:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2015/09/10 11:18:26.0 XRT_FLD_RESET_447_OG [0x1bf]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2015/09/10 11:20:56.0 XRT_QT_PROG_SET_407_OG [0x197]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 09
2015/09/10 11:20:58.0 XRT_FL_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_FL_PROG_SET       2 07-F0 c5 03
2015/09/10 11:21:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2015/09/10 11:33:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 11:33:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 11:33:58.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/09/10 11:34:00.0 AOCS_ORe-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2015/09/10 11:34:18.0 XRT_FLD_DIS_406_OG [0x196]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2015/09/10 11:36:54.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS       1 07-F0 c9
2015/09/10 11:36:56.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2015/09/10 11:36:58.0 XRT_QT_PROG_SET_414_OG [0x19e]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 10
2015/09/10 11:37:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2015/09/10 11:49:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 11:49:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2015/09/10 11:49:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION        4 07-F8 22 fe 97 00
2015/09/10 11:50:00.0 AOCS_ORe-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 00 00 29 99
2015/09/10 11:50:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA          1 07-F0 d8
2015/09/10 11:50:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA       1 07-F0 c8
2015/09/10 11:50:22.0 XRT_AEC_RESET_441_OG [0x1b9]
                        MDP_XRT_AEC_RESET         1 07-F0 d0
2015/09/10 11:50:24.0 XRT_ARS_DIS_423_OG [0x1a7]

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2015/09/10	11:50:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/10	11:52:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2015/09/10	11:52:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/09/10	11:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/10	12:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	12:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	12:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/09/10	12:06:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 00 00 53 33
2015/09/10	12:06:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/09/10	12:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/09/10	12:06:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/09/10	12:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/09/10	12:06:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/10	12:08:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2015/09/10	12:08:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/09/10	12:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/10	12:21:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	12:21:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	12:21:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/09/10	12:22:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 d6 36 b7 8e
2015/09/10	12:22:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/09/10	12:22:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/09/10	12:22:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/09/10	12:22:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/09/10	12:22:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/10	12:24:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2015/09/10	12:24:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/09/10	12:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/10	12:31:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	12:31:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	12:31:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/09/10	12:32:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 b4 b5 db 75
2015/09/10	12:32:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/09/10	12:32:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/09/10	12:32:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/09/10	12:32:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/09/10	12:32:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/10	12:34:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2015/09/10	12:34:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/09/10	12:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/10	12:47:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	12:47:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	12:47:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/09/10	12:48:00.0	AOCS_OrE-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 ac 5b 00 00
2015/09/10	12:48:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/09/10	12:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8

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2015/09/10	12:48:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/10	12:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/10	12:48:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/10	12:50:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2015/09/10	12:50:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/09/10	12:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/10	13:03:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	13:03:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	13:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/09/10	13:04:00.0	AOCS_OrE-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2015/09/10	13:04:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/10	13:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/10	13:04:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/10	13:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/10	13:04:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/10	13:06:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2015/09/10	13:06:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/09/10	13:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/10	13:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	13:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	13:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/09/10	13:20:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 d6 36 48 72				
2015/09/10	13:20:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/10	13:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/10	13:20:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/10	13:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/10	13:20:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/10	13:22:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2015/09/10	13:22:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/09/10	13:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/10	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	13:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	13:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/09/10	13:30:00.0	AOCS_OrE-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 29 ca b7 8e				
2015/09/10	13:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/10	13:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/10	13:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/10	13:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/10	13:30:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/10	13:32:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2015/09/10	13:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2015/09/10	13:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/10	13:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	13:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	13:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/09/10	13:40:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 4b 4b db 75				
2015/09/10	13:40:18.0	XRT_FLD_ENA_411_OG [0x19b]							

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2015/09/10	13:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/09/10	13:40:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/09/10	13:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/09/10	13:40:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/09/10	13:42:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2015/09/10	13:42:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/09/10	13:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/09/10	13:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/09/10	13:55:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/09/10	13:55:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/09/10	13:56:00.0	AOCS_OrE-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	53	a5	00 00
2015/09/10	13:56:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/09/10	13:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/09/10	13:56:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/09/10	13:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/09/10	13:56:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/09/10	13:58:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2015/09/10	13:58:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/09/10	13:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/09/10	14:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/09/10	14:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/09/10	14:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/09/10	14:20:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/09/10	14:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/09/10	14:34:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/09/10	14:34:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/09/10	14:34:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/09/10	14:34:30.0	AOCS_OrE-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	4b	4b	24 8b
2015/09/10	14:34:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/09/10	14:34:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/09/10	14:34:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/09/10	14:34:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/09/10	14:34:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/09/10	14:37:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2015/09/10	14:37:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2015/09/10	14:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/09/10	14:50:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/09/10	14:50:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/09/10	14:50:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/09/10	14:50:30.0	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	29	db	48 72
2015/09/10	14:50:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/09/10	14:50:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/09/10	14:50:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/09/10	14:50:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/09/10	14:50:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/09/10	14:53:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		

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2015/09/10	14:53:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/09/10	14:53:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/10	15:06:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	15:06:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	15:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/09/10	15:06:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	04	00	00	00	00
2015/09/10	15:06:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/10	15:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/10	15:06:52.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/10	15:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/10	15:06:56.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/10	15:09:26.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2015/09/10	15:09:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/09/10	15:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/10	15:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	15:53:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	15:53:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/10	15:53:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/10	15:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/10	16:17:00.0	XRT_Custom_430_OG [0x1ae]							
2015/09/10	16:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/10	17:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	17:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	17:30:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/10	17:30:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/10	17:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/10	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	17:55:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	17:55:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/09/10	17:55:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/09/10	17:55:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/09/10	17:58:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/09/10	17:58:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/10	17:58:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/09/10	17:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/10	18:05:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	18:05:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/10	18:05:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/09/10	18:05:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	04	00	00	00	00
2015/09/10	18:05:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/10	18:05:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/10	18:05:52.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/10	18:05:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/10	18:05:56.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/10	18:08:26.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2015/09/10	18:08:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/09/10	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

2015/09/10	19:07:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	19:07:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	19:07:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/10	19:07:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/10	19:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/10	19:30:30.0	XRT_Custom_430_OG [0x1ae]			
2015/09/10	19:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/10	20:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	20:44:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	20:44:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/10	20:44:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/10	20:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/10	21:08:00.0	XRT_Custom_430_OG [0x1ae]			
2015/09/10	21:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/10	22:22:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	22:22:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	22:22:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/10	22:22:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/10	22:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/10	22:43:00.0	XRT_Custom_430_OG [0x1ae]			
2015/09/10	22:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/10	23:59:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	23:59:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/10	23:59:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/10	23:59:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/11	00:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/11	00:08:00.0	XRT_Custom_430_OG [0x1ae]			
2015/09/11	00:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/11	01:23:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	01:23:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	01:23:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/11	01:23:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/11	01:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/11	01:42:00.0	XRT_Custom_430_OG [0x1ae]			
2015/09/11	01:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/11	02:57:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	02:57:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	02:57:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/11	02:57:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/11	03:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/11	03:18:30.5	XRT_Custom_430_OG [0x1ae]			
2015/09/11	03:19:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/11	04:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	04:27:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	04:27:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/11	04:27:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/11	04:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/11	04:56:00.5	XRT_Custom_430_OG [0x1ae]			
2015/09/11	04:57:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/11	05:39:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

2015/09/11	05:39:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	05:39:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/09/11	05:40:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2015/09/11	05:40:18.0	XRT_FLD_DIS_406_OG [0x196] MDP_XRT_FLD_DIS	1	07-F0	d9
2015/09/11	05:42:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/09/11	05:42:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/09/11	05:42:58.0	XRT_QT_PROG_SET_427_OG [0x1ab] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/09/11	05:43:00.5	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/11	05:49:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	05:49:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	05:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/09/11	05:50:00.0	AOCS_ORe-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	04 00 00 00 00
2015/09/11	05:50:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/09/11	05:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/09/11	05:50:22.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/09/11	05:50:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/09/11	05:50:26.0	XRT_FLD_RESET_447_OG [0x1bf] MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/11	05:52:56.0	XRT_QT_PROG_SET_413_OG [0x19d] MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2015/09/11	05:52:58.0	XRT_FL_PROG_SET_428_OG [0x1ac] MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/09/11	05:53:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/11	06:07:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	06:07:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	06:07:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/11	06:07:36.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/11	06:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/11	06:33:30.0	XRT_Custom_430_OG [0x1ae]			
2015/09/11	06:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/11	07:48:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	07:48:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	07:48:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/11	07:48:06.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/11	07:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/11	08:00:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	01 00 00 00 00
2015/09/11	08:11:00.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	08:11:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	08:11:04.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/09/11	08:11:24.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/09/11	08:11:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/09/11	08:11:28.0	XRT_AEC_RESET_441_OG [0x1b9] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/09/11	08:11:30.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/09/11	08:11:32.0	XRT_FLD_RESET_447_OG [0x1bf] MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/11	08:14:02.0	XRT_QT_PROG_SET_426_OG [0x1aa] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/09/11	08:14:04.0	XRT_FL_PROG_SET_428_OG [0x1ac] MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/09/11	08:14:06.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/11	09:27:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	09:27:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/11	09:27:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da

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2015/09/11	09:27:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/11	09:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/11	09:45:00.0	XRT_Custom_430_OG [0x1ae]					
2015/09/11	09:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/11	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/09/11	11:00:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 b2 4e 01 68	
2015/09/11	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/09/11	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/09/11	11:00:22.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/09/11	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/11	11:00:26.0	XRT_FLD_RESET_448_OG [0x1c0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/11	11:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2015/09/11	11:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2015/09/11	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/11	14:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	14:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	14:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/11	14:53:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/11	14:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/11	15:05:30.0	XRT_Custom_430_OG [0x1ae]					
2015/09/11	15:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/11	16:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	16:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	16:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/09/11	16:28:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/09/11	16:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/09/11	16:52:30.0	XRT_Custom_430_OG [0x1ae]					
2015/09/11	16:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/11	17:41:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	17:41:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	17:41:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/09/11	17:42:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/09/11	17:42:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/09/11	17:44:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/09/11	17:44:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/11	17:44:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2015/09/11	17:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/09/11	17:51:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	17:51:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/09/11	17:51:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/09/11	17:52:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	04 00 00 00 00	
2015/09/11	17:52:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/09/11	17:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/09/11	17:52:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/09/11	17:52:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/09/11	17:52:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2015/09/11	17:54:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2015/09/11	17:54:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/09/11	17:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/11	18:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	18:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	18:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/11	18:06:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/11	18:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/11	18:29:00.0	XRT_Custom_430_OG [0x1ae]							
2015/09/11	18:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/11	19:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	19:42:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	19:42:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/11	19:42:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/11	19:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/11	20:06:00.0	XRT_Custom_430_OG [0x1ae]							
2015/09/11	20:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/11	21:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	21:20:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	21:20:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/11	21:20:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/11	21:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/11	21:43:00.0	XRT_Custom_430_OG [0x1ae]							
2015/09/11	21:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/11	22:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	22:57:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	22:57:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/11	22:57:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/11	23:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/11	23:17:00.0	XRT_Custom_430_OG [0x1ae]							
2015/09/11	23:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/11	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/11	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2015/09/12	00:00:00.0	AOCS_OrE-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	fe 36 ae 36			
2015/09/12	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/09/12	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/09/12	00:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/09/12	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/09/12	00:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/12	00:02:56.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2015/09/12	00:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2015/09/12	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/09/12	00:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/12	00:34:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/09/12	00:34:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/09/12	00:34:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/09/12	00:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/09/12	00:41:30.0	XRT_Custom_430_OG [0x1ae]							

2015/09/12	00:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2015/09/12	01:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	01:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	01:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da														
2015/09/12	01:57:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8														
2015/09/12	02:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2015/09/12	02:17:00.0	XRT_Custom_430_OG [0x1ae]																		
2015/09/12	02:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2015/09/12	03:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	03:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	03:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da														
2015/09/12	03:30:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8														
2015/09/12	03:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2015/09/12	03:54:00.0	XRT_Custom_430_OG [0x1ae]																		
2015/09/12	03:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2015/09/12	05:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	05:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	05:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da														
2015/09/12	05:04:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8														
2015/09/12	05:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														
2015/09/12	05:31:30.0	XRT_Custom_430_OG [0x1ae]																		
2015/09/12	05:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2015/09/12	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00														
2015/09/12	06:00:00.0	AOCS_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00														
2015/09/12	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9														
2015/09/12	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9														
2015/09/12	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5														
2015/09/12	06:02:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e														
2015/09/12	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2015/09/12	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00														
2015/09/12	06:10:00.0	AOCS_Orе-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	04 00 00 00 00														
2015/09/12	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8														
2015/09/12	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8														
2015/09/12	06:10:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0														
2015/09/12	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5														
2015/09/12	06:10:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da														
2015/09/12	06:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12														
2015/09/12	06:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03														
2015/09/12	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0														
2015/09/12	06:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	06:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1														
2015/09/12	06:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da														
2015/09/12	06:44:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8														
2015/09/12	06:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9														

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2015/09/12	07:08:30.0	XRT_Custom_430_OG [0x1ae]			
2015/09/12	07:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/12	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/12	07:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/12	07:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/09/12	08:00:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]			
		AOCU_NM	5	02-76	01 00 00 00 00
2015/09/12	08:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/09/12	08:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/09/12	08:00:22.0	XRT_AEC_RESET_441_OG [0x1b9]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/09/12	08:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/09/12	08:00:26.0	XRT_FLD_RESET_447_OG [0x1bf]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/12	08:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/09/12	08:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2015/09/12	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/12	08:24:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/12	08:24:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/12	08:24:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/12	08:24:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/12	08:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/12	08:45:30.0	XRT_Custom_430_OG [0x1ae]			
2015/09/12	08:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/09/12	10:05:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/12	10:05:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/09/12	10:05:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/09/12	10:05:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/09/12	10:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/09/12	10:38:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00