

XRT Timeline to be uploaded on 2015/10/22

Period: 2015/10/22 10:01:00 - 2015/10/27 10:30:00

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Normal mode

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XOB #1AC3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)

Term	Pointing (x, y)	Comment
10/22 10:14:00 - 10/22 12:59:54	Track (-214.8, 35.3) @ 10/22 10:11:00	# Track AR12436
10/23 06:17:30 - 10/23 08:14:30	Track (-30.3, 34.4) @ 10/23 06:14:30	# Track AR12436
10/24 06:20:00 - 10/24 10:00:00	Track (191.9, 37.6) @ 10/24 06:17:00	# Track AR12436

PROG= 08 Inf.-time(s)

Subr= 2	1-time(s)	2.0sec
Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 42 4-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 62 8-time(s) 160.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1AE2: AR (Filter-Ratio with Al/poly and thin-Be), 128x128 at 1064, 1048 with G-band (3ms/3ms leak) 30s cad - AEC3- HOP268

Term	Pointing (x, y)	Comment
10/22 13:03:00 - 10/22 18:54:54	Track (-313.0, 48.9) @ 10/22 13:00:00	# HOP268
10/23 13:03:00 - 10/23 18:31:30	Track (-96.3, 46.0) @ 10/23 13:00:00	# HOP268

PROG= 01 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 60 120-time(s) 30.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 128x128 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 128x128 (1064, 1048) Q=95 3 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + TH

Term	Pointing (x, y)	Comment
10/22 18:58:00 - 10/22 19:04:54	Fixed (0.0, 0.0)	synoptic, shifted
10/23 06:07:30 - 10/23 06:14:24	Fixed (0.0, 0.0)	synoptic, shifted 4.5 min
10/23 18:59:00 - 10/23 19:05:54	Fixed (0.0, 0.0)	synoptic, shifted
10/24 06:10:00 - 10/24 06:16:54	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min

PROG= 05 1-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 33 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 49 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 39 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 54 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1AE9: AR Standard-B (Morphology with thin-Be only), 384FOV at 1064, 1048 with G-band (3ms/3ms leak) 20s cad - AEC3- HOP293

Term	Pointing (x, y)	Comment
10/22 19:08:00 - 10/23 01:29:54	Track (656.8, -282.3) @ 10/22 19:05:00	# HOP293
10/23 19:09:00 - 10/24 01:29:54	Track (793.2, -265.9) @ 10/23 19:06:00	# HOP293
PROG= 11 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 47 120-time(s) 20.0sec		
thin-Be/Open	med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AC6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)

Term	Pointing (x, y)	Comment
10/23 01:33:00 - 10/23 06:04:24	Track (-74.3, 34.3) @ 10/23 01:30:00	# Track AR12436
10/24 01:33:00 - 10/24 05:30:30	Track (148.1, 36.5) @ 10/24 01:30:00	# Track AR12436
PROG= 18 Inf.-time(s)		
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
└─ Seqn= 42 4-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec	
Open/thick-Al	Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
└─ Seqn= 17 8-time(s) 360.0sec		
thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 110.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 110.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 110.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AEB: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 240s cad

Term	Pointing (x, y)	Comment
10/23 08:43:00 - 10/23 08:55:54	Fixed (936.0, 0.0)	# HOP130 #1
10/23 08:59:00 - 10/23 09:11:54	Fixed (468.0, 0.0)	# HOP130 #2
10/23 09:15:00 - 10/23 09:27:54	Fixed (0.0, 0.0)	# HOP130 #3
10/23 09:31:00 - 10/23 09:55:00	Fixed (-468.0, 0.0)	# HOP130 #4
10/23 10:18:00 - 10/23 10:30:54	Fixed (-936.0, 0.0)	# HOP130 #5
10/23 10:34:00 - 10/23 10:40:54	Fixed (815.0, 470.0)	# HOP130 #6
10/23 10:44:00 - 10/23 10:56:54	Fixed (411.0, 847.0)	# HOP130 #7
10/23 11:00:00 - 10/23 11:12:54	Fixed (0.0, 941.0)	# HOP130 #8
10/23 11:16:00 - 10/23 11:28:54	Fixed (-411.0, 847.0)	# HOP130 #9
10/23 11:32:00 - 10/23 11:38:54	Fixed (-815.0, 470.0)	# HOP130 #10
10/23 11:42:00 - 10/23 11:48:54	Fixed (815.0, -470.0)	# HOP130 #11
10/23 11:52:00 - 10/23 12:04:54	Fixed (411.0, -847.0)	# HOP130 #12
10/23 12:08:00 - 10/23 12:20:54	Fixed (0.0, -941.0)	# HOP130 #13
10/23 12:24:00 - 10/23 12:36:54	Fixed (-411.0, -847.0)	# HOP130 #14
10/23 12:40:00 - 10/23 12:59:54	Fixed (-815.0, -471.0)	# HOP130 #15
PROG= 10 Inf.-time(s)		
└─ Subr= 1 45-time(s) 240.0sec		
└─ Seqn= 29 1-time(s) 4.0sec		
thin-Be/Open	med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 26 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512)

Term	Pointing (x, y)	Comment
10/22 10:14:00 - 10/22 12:59:54	Track (-214.8, 35.3) @ 10/22 10:11:00	# Track AR12436
10/22 13:03:00 - 10/22 18:54:54	Track (-313.0, 48.9) @ 10/22 13:00:00	# HOP268
10/22 19:08:00 - 10/23 01:29:54	Track (656.8, -282.3) @ 10/22 19:05:00	# HOP293
10/23 01:33:00 - 10/23 06:04:24	Track (-74.3, 34.3) @ 10/23 01:30:00	# Track AR12436
10/23 06:17:30 - 10/23 08:14:30	Track (-30.3, 34.4) @ 10/23 06:14:30	# Tracl AR12436
10/23 08:43:00 - 10/23 08:55:54	Fixed (936.0, 0.0)	# HOP130 #1
10/23 08:59:00 - 10/23 09:11:54	Fixed (468.0, 0.0)	# HOP130 #2
10/23 09:15:00 - 10/23 09:27:54	Fixed (0.0, 0.0)	# HOP130 #3

10/23 09:31:00 - 10/23 09:55:00	Fixed (-468.0, 0.0)	# HOP130 #4
10/23 10:18:00 - 10/23 10:30:54	Fixed (-936.0, 0.0)	# HOP130 #5
10/23 10:34:00 - 10/23 10:40:54	Fixed (815.0, 470.0)	# HOP130 #6
10/23 10:44:00 - 10/23 10:56:54	Fixed (411.0, 847.0)	# HOP130 #7
10/23 11:00:00 - 10/23 11:12:54	Fixed (0.0, 941.0)	# HOP130 #8
10/23 11:16:00 - 10/23 11:28:54	Fixed (-411.0, 847.0)	# HOP130 #9
10/23 11:32:00 - 10/23 11:38:54	Fixed (-815.0, 470.0)	# HOP130 #10
10/23 11:42:00 - 10/23 11:48:54	Fixed (815.0, -470.0)	# HOP130 #11
10/23 11:52:00 - 10/23 12:04:54	Fixed (411.0, -847.0)	# HOP130 #12
10/23 12:08:00 - 10/23 12:20:54	Fixed (0.0, -941.0)	# HOP130 #13
10/23 12:24:00 - 10/23 12:36:54	Fixed (-411.0, -847.0)	# HOP130 #14
10/23 12:40:00 - 10/23 12:59:54	Fixed (-815.0, -471.0)	# HOP130 #15
10/23 13:03:00 - 10/23 18:31:30	Track (-96.3, 46.0) @ 10/23 13:00:00	# HOP268
10/23 19:09:00 - 10/24 01:29:54	Track (793.2, -265.9) @ 10/23 19:06:00	# HOP293
10/24 01:33:00 - 10/24 05:30:30	Track (148.1, 36.5) @ 10/24 01:30:00	# Track AR12436
10/24 06:20:00 - 10/24 10:00:00	Track (191.9, 37.6) @ 10/24 06:17:00	# Track AR12436

PROG= 07 30-time(s)

Subr= 1 20-time(s) 2.0sec	
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec	thin-Be/Open med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
	med-Be/Open Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
	Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec	
Seqn= 10 1-time(s) 2.0sec	med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
	Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 84 1-time(s) 2.0sec	Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
	Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
	Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
	Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

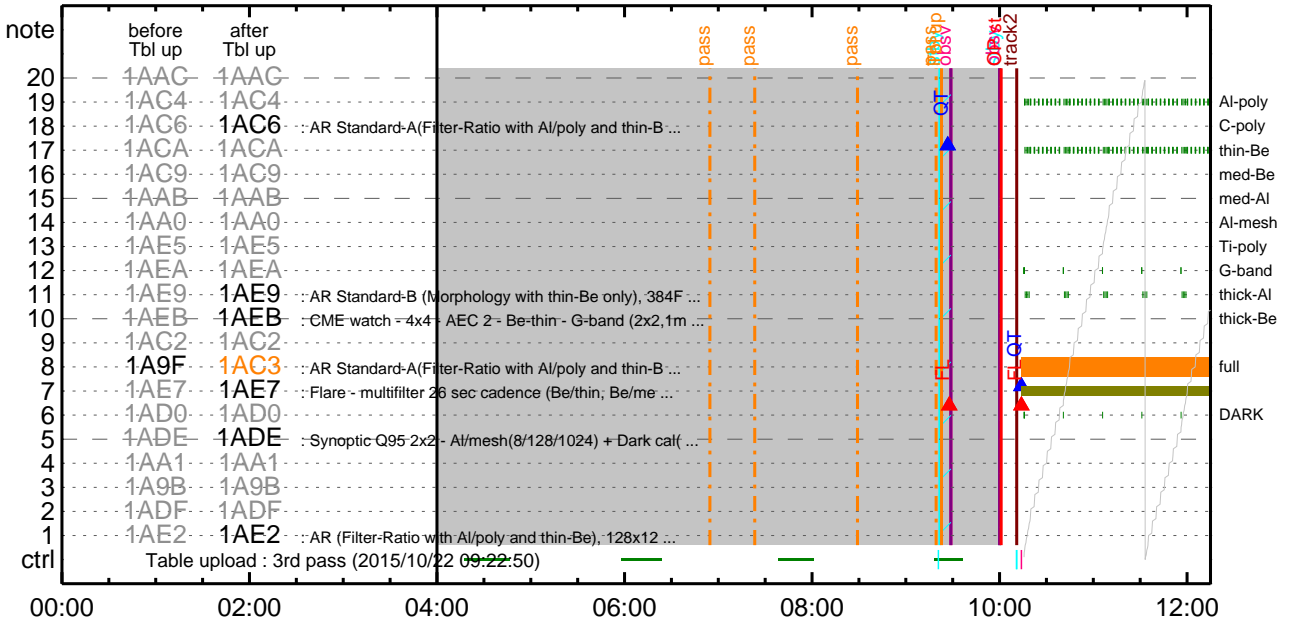
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Flare Detection

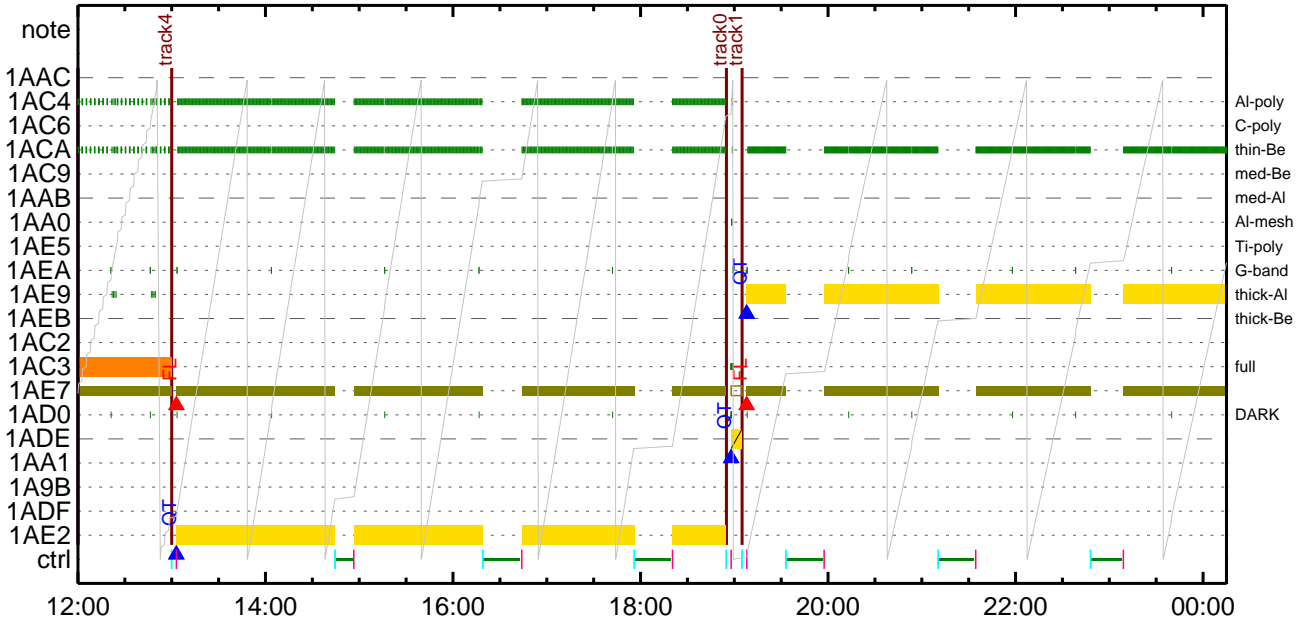
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FLD Patrol									
Term	Pointing (x, y)					Comment			
10/22 19:05:18 - 10/23 06:04:48	Track (656.8, -282.3)	@ 10/22 19:05:00	# HOP293						
10/23 06:14:48 - 10/23 18:56:18	Track (-30.3, 34.4)	@ 10/23 06:14:30	# Track AR12436						
10/23 19:06:18 - 10/24 06:07:18	Track (793.2, -265.9)	@ 10/23 19:06:00	# HOP293						
10/24 06:17:18 - 10/27 10:30:00	Track (191.9, 37.6)	@ 10/24 06:17:00	# Track AR12436						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	80sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

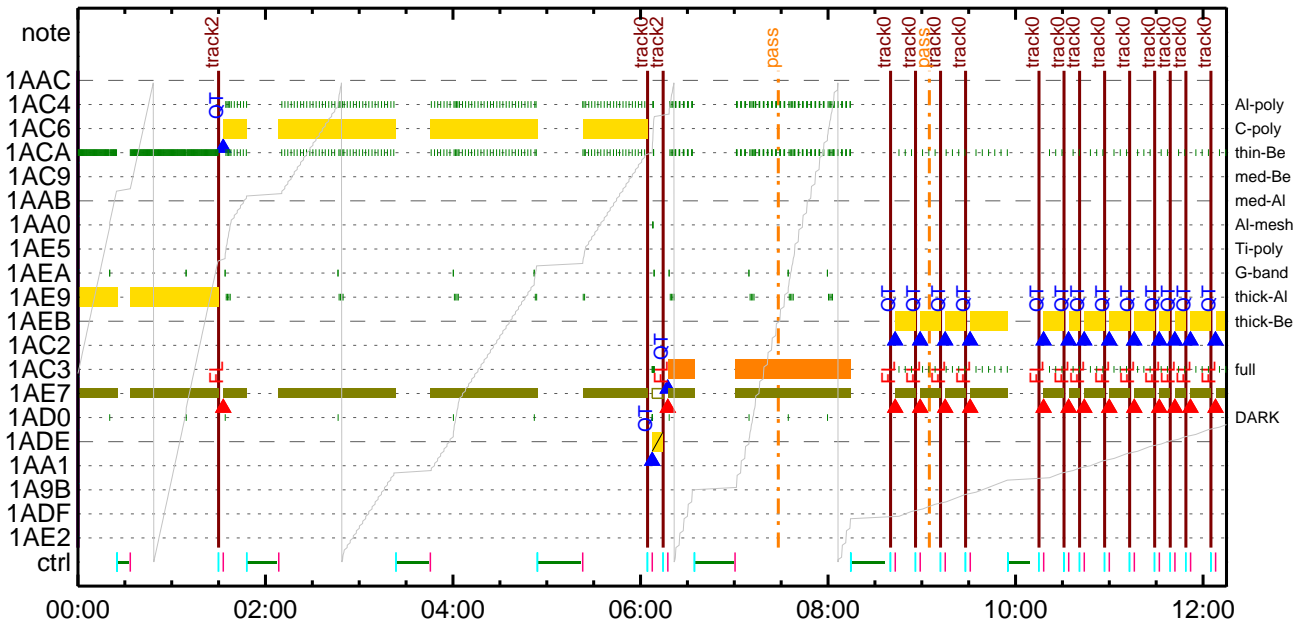
CMDI #0475 2015/10/22



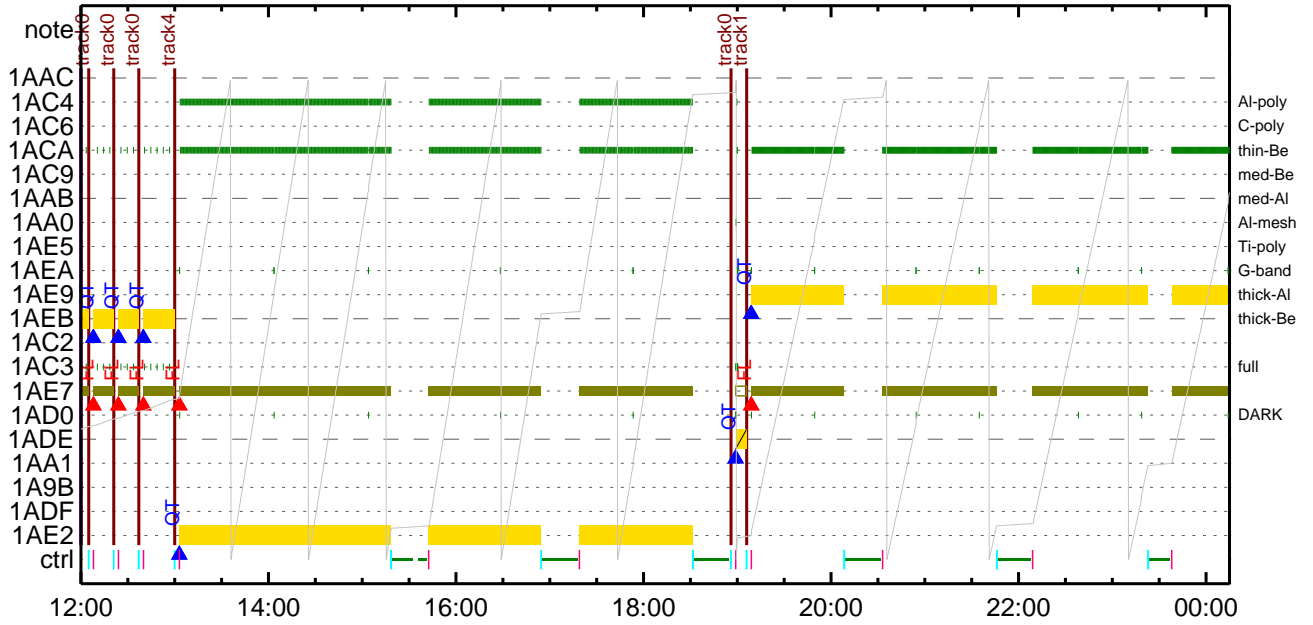
CMDI #0475 2015/10/22



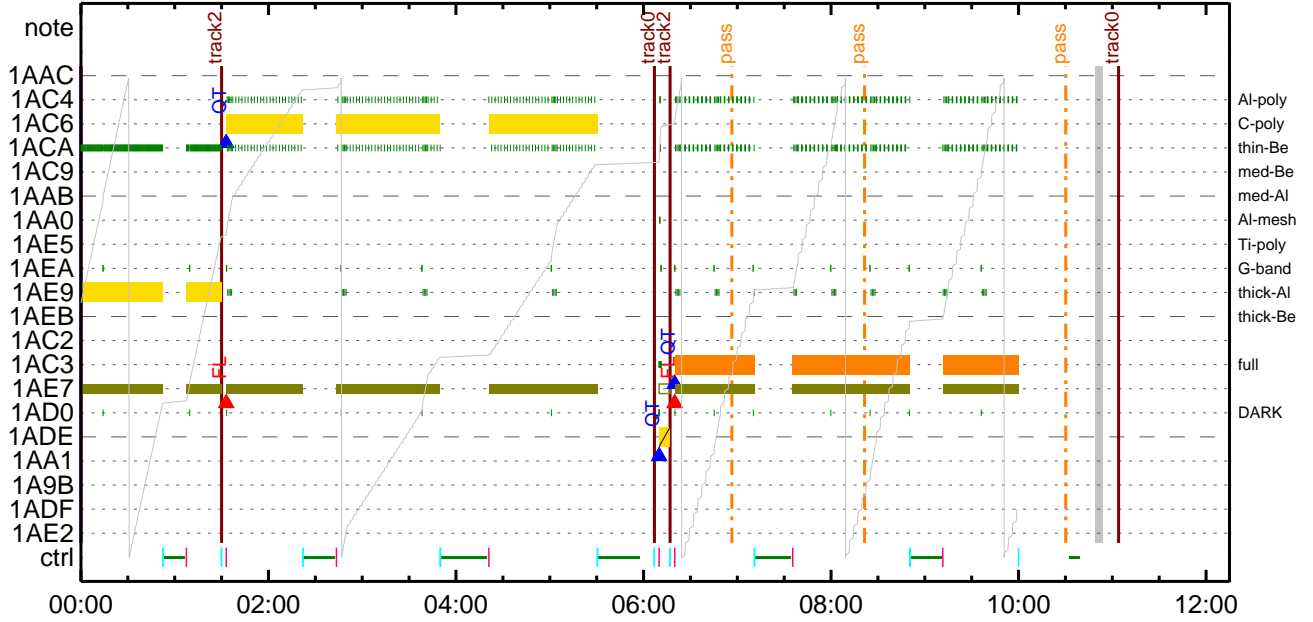
CMDI #0475 2015/10/23



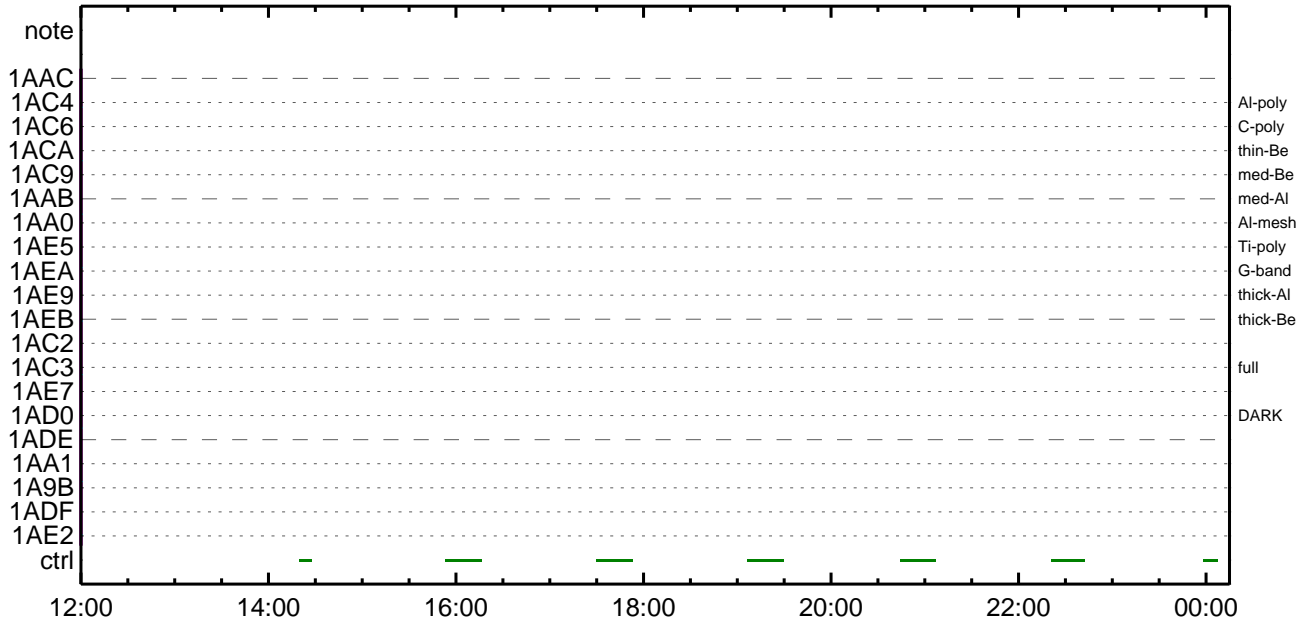
CMDI #0475 2015/10/23



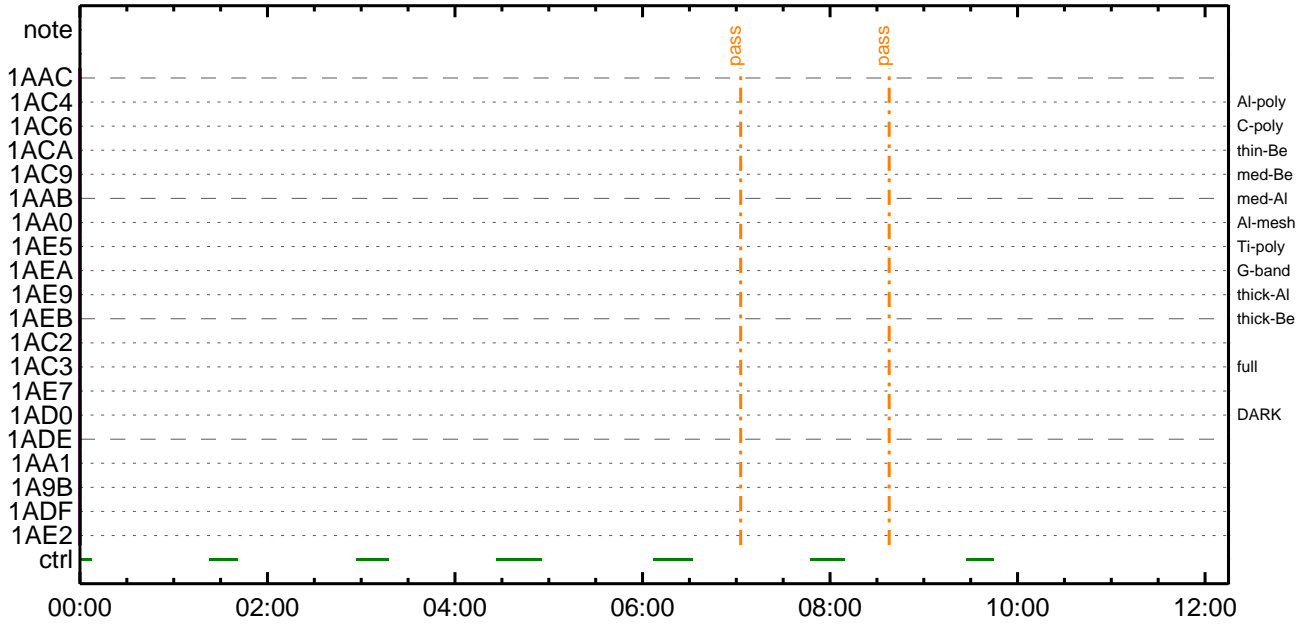
CMDI #0475 2015/10/24



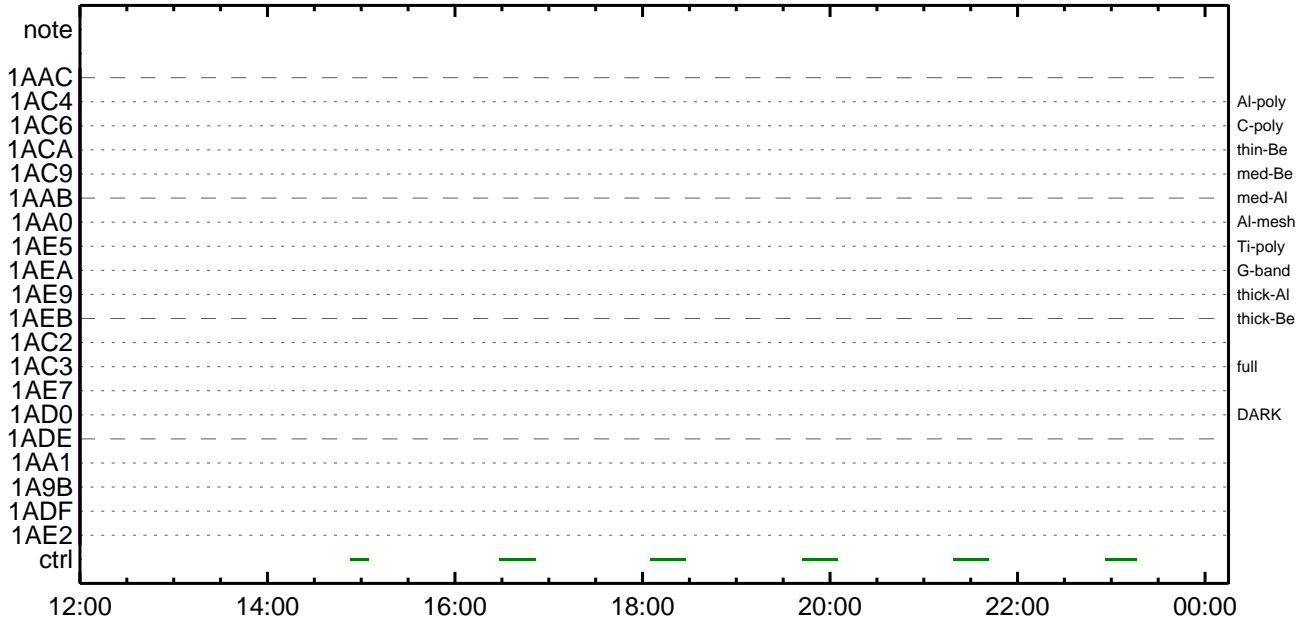
CMDI #0475 2015/10/24



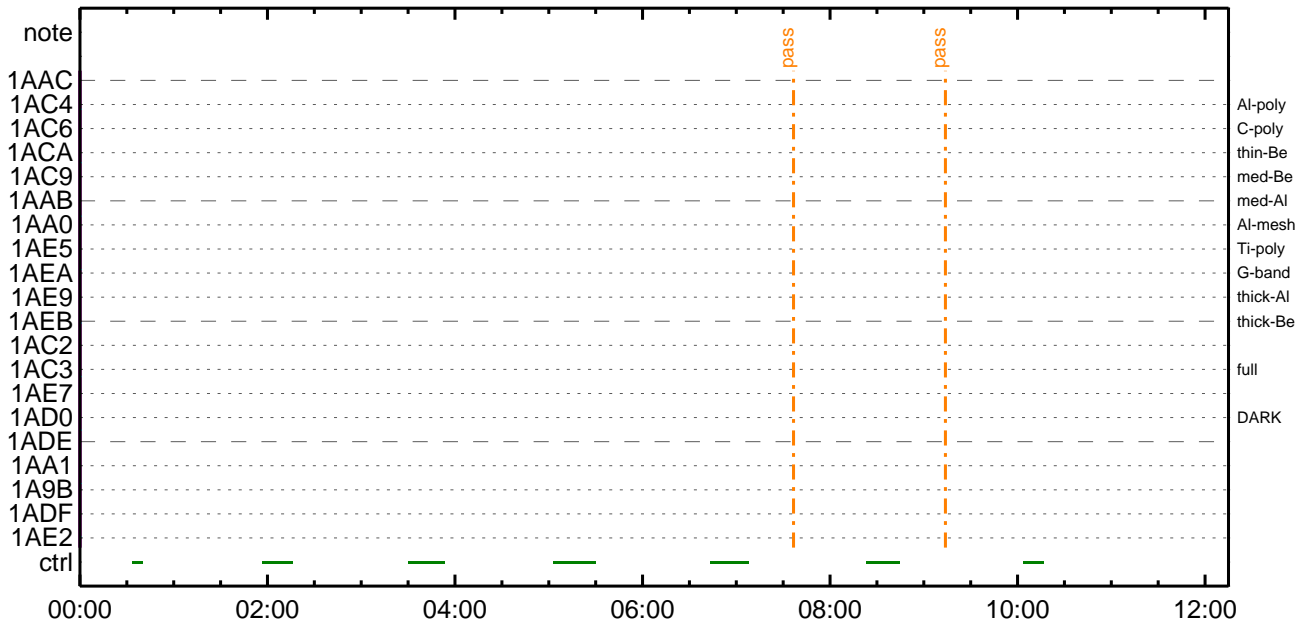
CMDI #0475 2015/10/25



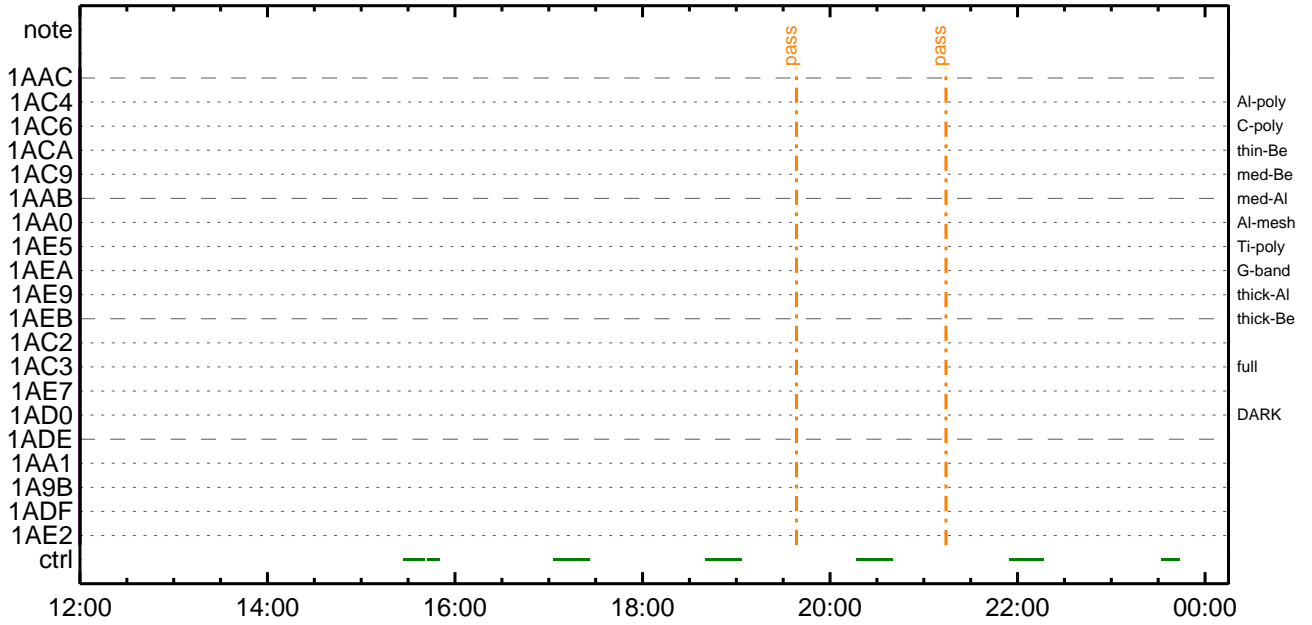
CMDI #0475 2015/10/25



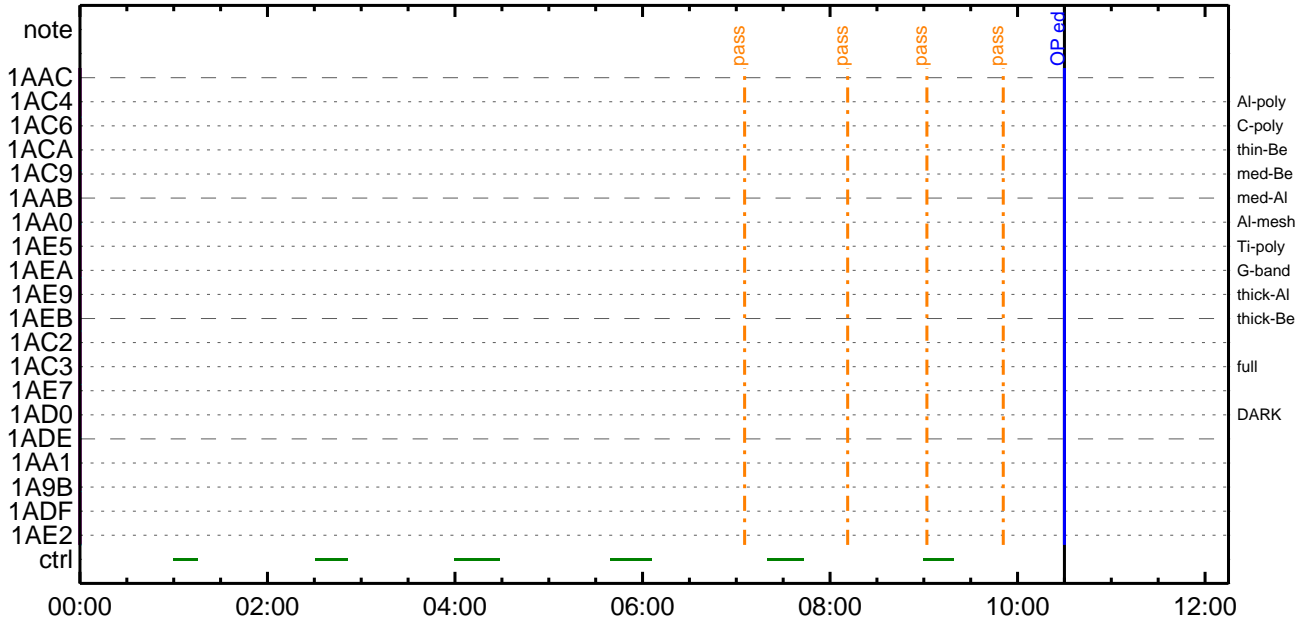
CMDI #0475 2015/10/26



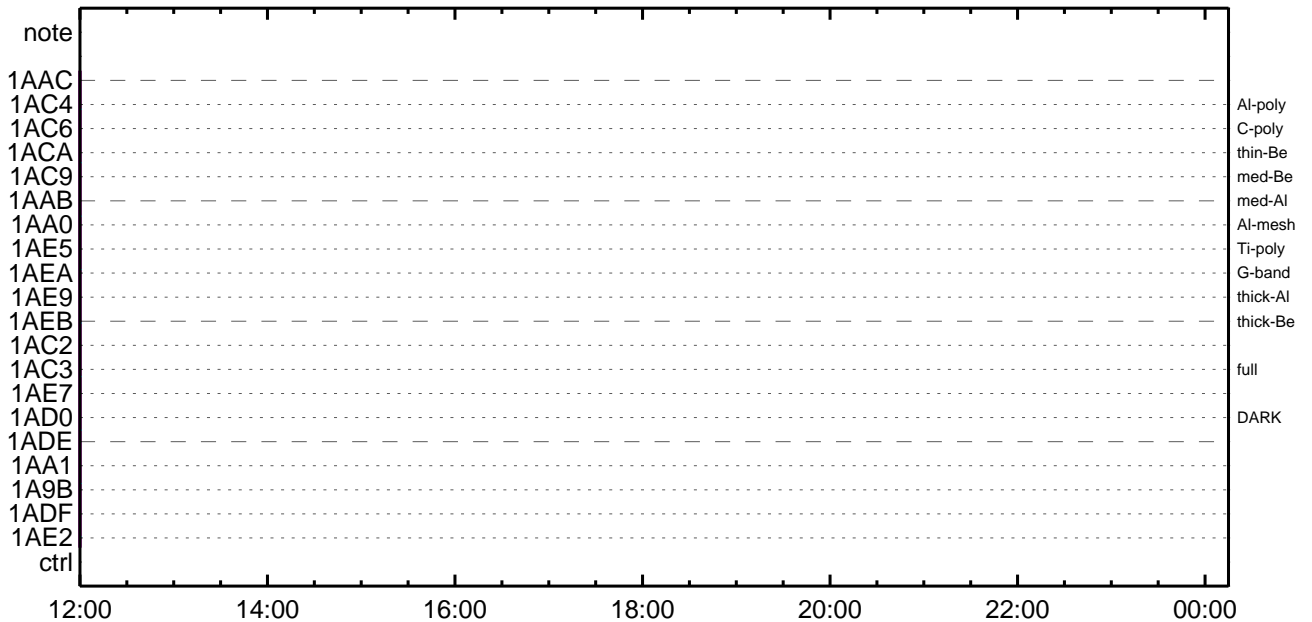
CMDI #0475 2015/10/26



CMDI #0475 2015/10/27



CMDI #0475 2015/10/27



(a) Spacecraft Operation Procedure (real-commands)

```
main-444 2015-10-22 12:28:07 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ä
0005 C.
0006 C. YÀYB;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ðÁð•µ°È»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈèÈ¼°ÇÓð•¿¿¼í¹ç¿í;çÀ®, ù¹ðèððçÁ+¿®ð•ðÈððð³ðÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ; | YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-444:OP
0020 ( )
0021 . S. OG og-444:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYÖY×¼ªª Í»ðð³ ÍÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðÍ¼È¹ç•è² ÍOKðð³ ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYÖY×¼ªª Í»ðð³ ÍÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðÍ¼È¹ç•è² ÍOKðð³ ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYÖY×¼ªª Í»ðð³ ÍÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPðÍ¼È¹ç•è² ÍOKðð³ ÍÇ§
0081 C.
0082 . C. ***** øÈ²¼øÍ¼Á´¶¼°øÈÈ-øÀ+¿® (¼áµ-YÁYÖY×¼¼ø¼çøðÁÓÁæøç¼ªª ø²¼¼í¹çøçøá) *****
0083 C. DHUYá;¼YÉ;È¼Y¼; Yí;¼YÉ;ÈøðÍáø¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADø-Á+¿®NGøÍ¼í¹ç; ç°È²¼øÍTI-CMDÁ+¿®øÍ¼Á¹Ôø•øÈøøø³øÈ; f
```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 53s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2015-10-22 10:00:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'uAiI'ö¼YðEÄð¹æDCBC·x²è *****
0156 C. (¼°iYÖYAYEYPYÈYáYçYèæE¼¼¼Ä»Ü¹æ)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** YD¥¹·I Daily±¿ÍÑæ'Ø¹æDCBC·x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥ÄY¹¼Ä»Ü;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-446 2015-10-22 12:28:07 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁ•µ°È»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¿°ÇÓã•¿¼¿l¹ç¿Í;çÁ®, ù¿¹ãÈ¿ãÇÁ+¿®ã•¿È¿ã¿ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-286:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2015-10-22 10:00:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 85 83 02 02)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 20 08)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 80 80 08 20)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0f 80 80 06 06)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 10 80 80 08 08)
0090 + DC 07-F0 MDP_XRT_FLD_ENA
0091 BC (d8)
0092 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0093 BC (c8)
0094 + DC 07-F0 MDP_XRT_AEC_RESET
0095 BC (d0)
```

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0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097   BC      (d5)
0098 + DC 07-F0 MDP_XRT_FLD_RESET
0099   BC      (da)
0100 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0101   BC      (c4 12)
0102 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0103   BC      (c5 07)
0104 . C. ----- Success Verify ?           OK / NG ____
0105   C.
0106   C.
0107 . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0108   C.
0109 +. DC 07-F0 MDP_XRT_MODE_OBSV
0110   BC      (c2)
0111 +. TI 2015-10-22 10:00:02.0
0112   DC 07-F0 MDP_XRT_MODE_OBSV
0113   BC      (c2)
0114 . C. ----- Success Verify ?           OK / NG ____
0115   C.
0116   C. ***** XRT END *****
0117   C.
0118 . C. ***** MDP 'ûÃîñî»ö%ÿñÈÂðñ¹ñèDCBC•x²è *****
0119   C. (%ã°îÿÓŸÃŸÈÿPŸÈŸãŸçŸèñ%¼ññ¼Ã»Ûñ¹ñè)
0120 . S. DC-BC dcbc-402:DCBC
0121   (MDP_known_event)
0122   C.
0123   C.
0124 . C. ***** ŸDŸ¹•î Daily±;îññÈ'Øñ¹ñèDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127   C.
0128   C.
0129 . C. ;ãLOSŸÃŸŸŸÃŸ-¼Ã»Û;ã
0130   C.
0131 . C. ***** LOS *****
0132   C.

```

*** OP Sequence for XRT ***

2015/10/22	10:10:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	10:10:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	10:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/10/22	10:11:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2015/10/22	10:11:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/10/22	10:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/10/22	10:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/10/22	10:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/10/22	10:11:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/22	10:13:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2015/10/22	10:13:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2015/10/22	10:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/10/22	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	12:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/10/22	13:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2015/10/22	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/10/22	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/10/22	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/10/22	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/10/22	13:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/22	13:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2015/10/22	13:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2015/10/22	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/10/22	14:44:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	14:44:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	14:44:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/22	14:44:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/10/22	14:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/10/22	14:55:30.0	XRT_Custom_430_OG [0x1ae]					
2015/10/22	14:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/10/22	16:19:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	16:19:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	16:19:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/22	16:19:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/10/22	16:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/10/22	16:43:00.0	XRT_Custom_430_OG [0x1ae]					
2015/10/22	16:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/10/22	17:56:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	17:56:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	17:56:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/22	17:56:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/10/22	17:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/10/22	18:19:30.0	XRT_Custom_430_OG [0x1ae]					
2015/10/22	18:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/10/22	18:54:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/22	18:54:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2015/10/22	18:54:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/10/22	18:55:00.0	AOCs_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/10/22	18:55:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/10/22	18:57:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/10/22	18:57:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/22	18:57:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2015/10/22	18:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/22	19:04:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/22	19:04:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/22	19:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/10/22	19:05:00.0	AOCs_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	01	00	00	00	00
2015/10/22	19:05:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/22	19:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/22	19:05:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/22	19:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/22	19:05:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/22	19:07:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2015/10/22	19:07:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/10/22	19:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/22	19:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/22	19:33:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/22	19:33:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/22	19:33:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/22	19:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/22	19:56:30.0	XRT_Custom_430_OG [0x1ae]							
2015/10/22	19:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/22	21:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/22	21:10:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/22	21:10:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/22	21:10:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/22	21:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/22	21:33:30.0	XRT_Custom_430_OG [0x1ae]							
2015/10/22	21:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/22	22:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/22	22:48:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/22	22:48:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/22	22:48:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/22	22:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/22	23:08:00.0	XRT_Custom_430_OG [0x1ae]							
2015/10/22	23:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/23	00:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	00:25:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	00:25:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/23	00:25:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/23	00:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/23	00:32:30.0	XRT_Custom_430_OG [0x1ae]							
2015/10/23	00:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/23	01:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	01:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							

2015/10/23	01:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2015/10/23	01:30:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00
2015/10/23	01:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/10/23	01:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/10/23	01:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/10/23	01:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/10/23	01:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/23	01:32:56.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2015/10/23	01:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/10/23	01:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/23	01:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	01:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	01:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/23	01:48:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/23	01:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/23	02:07:30.0	XRT_Custom_430_OG [0x1ae]				
2015/10/23	02:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/23	03:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	03:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	03:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/23	03:23:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/23	03:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/23	03:44:30.0	XRT_Custom_430_OG [0x1ae]				
2015/10/23	03:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/23	04:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	04:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	04:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/23	04:54:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/23	04:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/23	05:22:00.0	XRT_Custom_430_OG [0x1ae]				
2015/10/23	05:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/23	06:04:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	06:04:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	06:04:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/10/23	06:04:30.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2015/10/23	06:04:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/10/23	06:07:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/10/23	06:07:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/10/23	06:07:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/10/23	06:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/23	06:14:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	06:14:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	06:14:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/10/23	06:14:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00
2015/10/23	06:14:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/10/23	06:14:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/10/23	06:14:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/10/23	06:14:54.0	XRT_ARS_DIS_423_OG [0x1a7]				

2015/10/23	06:14:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/23	06:17:26.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	06:17:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2015/10/23	06:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/10/23	06:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/23	06:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	06:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	06:34:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	06:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/10/23	06:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/10/23	07:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2015/10/23	08:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/23	08:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	08:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	08:14:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	08:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/10/23	08:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/10/23	08:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	08:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	08:40:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2015/10/23	08:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00	ac cd		
2015/10/23	08:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/23	08:40:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/10/23	08:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/10/23	08:40:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/23	08:42:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	08:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2015/10/23	08:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/10/23	08:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/23	08:55:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	08:55:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	08:56:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2015/10/23	08:56:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00	d6 67		
2015/10/23	08:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/23	08:56:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/10/23	08:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/10/23	08:56:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/23	08:58:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	08:58:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2015/10/23	08:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/10/23	09:11:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/23	09:11:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	09:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	09:12:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2015/10/23	09:12:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00	00 00		
2015/10/23	09:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/23	09:12:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			

2015/10/23	09:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/23	09:12:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	09:14:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2015/10/23	09:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/10/23	09:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/23	09:27:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	09:27:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	09:27:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/10/23	09:28:00.0	AOCs_Or-e-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	00	00	29 99
2015/10/23	09:28:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/23	09:28:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/10/23	09:28:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/10/23	09:28:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/23	09:28:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	09:30:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2015/10/23	09:30:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/10/23	09:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/23	09:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	09:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	09:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	09:55:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/10/23	09:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/10/23	10:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	10:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	10:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/10/23	10:15:00.0	AOCs_Or-e-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	00	00	53 33
2015/10/23	10:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/23	10:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/10/23	10:15:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/10/23	10:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/23	10:15:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	10:17:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2015/10/23	10:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/10/23	10:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/23	10:30:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	10:30:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/23	10:30:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/10/23	10:31:00.0	AOCs_Or-e-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	d6	36	b7 8e
2015/10/23	10:31:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/23	10:31:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/10/23	10:31:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/10/23	10:31:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/23	10:31:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/23	10:33:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2015/10/23	10:33:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/10/23	10:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2015/10/23	10:40:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	10:40:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	10:40:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/10/23	10:41:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 b4 b5 db 75				
2015/10/23	10:41:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/23	10:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/23	10:41:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/23	10:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/23	10:41:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/23	10:43:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2015/10/23	10:43:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/10/23	10:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/23	10:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	10:56:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	10:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/10/23	10:57:00.0	AOCS_OrE-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 ac 5b 00 00				
2015/10/23	10:57:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/23	10:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/23	10:57:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/23	10:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/23	10:57:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/23	10:59:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2015/10/23	10:59:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/10/23	11:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/23	11:12:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	11:12:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	11:12:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/10/23	11:13:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2015/10/23	11:13:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/23	11:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/23	11:13:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/23	11:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/23	11:13:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/23	11:15:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2015/10/23	11:15:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/10/23	11:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/23	11:28:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	11:28:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/23	11:28:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/10/23	11:29:00.0	AOCS_OrE-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 d6 36 48 72				
2015/10/23	11:29:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/23	11:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/23	11:29:22.0	XRT_AEC_RESET_441_OG [0x1b9]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/23	11:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/23	11:29:26.0	XRT_FLD_RESET_447_OG [0x1bf]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/23	11:31:56.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2015/10/23	11:31:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							

2015/10/23	11:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/23	11:38:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	11:38:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	11:38:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2015/10/23	11:39:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 29	ca b7 8e
2015/10/23	11:39:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/23	11:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/23	11:39:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/23	11:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/23	11:39:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/23	11:41:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2015/10/23	11:41:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/23	11:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/23	11:48:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	11:48:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	11:48:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2015/10/23	11:49:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 4b	4b db 75
2015/10/23	11:49:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/23	11:49:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/23	11:49:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/23	11:49:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/23	11:49:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/23	11:51:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2015/10/23	11:51:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/23	11:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/23	12:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	12:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	12:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2015/10/23	12:05:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 53	a5 00 00
2015/10/23	12:05:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/23	12:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/23	12:05:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/23	12:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/23	12:05:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/23	12:07:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2015/10/23	12:07:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/23	12:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/23	12:20:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	12:20:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	12:20:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2015/10/23	12:21:00.0	AOCS_OrE-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 4b	4b 24 8b
2015/10/23	12:21:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/23	12:21:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/23	12:21:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/23	12:21:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/23	12:21:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	

2015/10/23	12:23:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2015/10/23	12:23:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/23	12:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/23	12:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	12:36:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	12:36:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/10/23	12:37:00.0	AOCS_OrE-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	29 db 48 72
2015/10/23	12:37:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/23	12:37:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/23	12:37:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/23	12:37:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/23	12:37:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/23	12:39:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2015/10/23	12:39:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/23	12:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/23	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	12:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/10/23	13:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	04	00 00 00 00
2015/10/23	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/23	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/23	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/23	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/23	13:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/23	13:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2015/10/23	13:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/23	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/23	15:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	15:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	15:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/23	15:18:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/10/23	15:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/10/23	15:41:30.0	XRT_Custom_430_OG [0x1ae]					
2015/10/23	15:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/23	16:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	16:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	16:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/23	16:54:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/10/23	16:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/10/23	17:18:00.0	XRT_Custom_430_OG [0x1ae]					
2015/10/23	17:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/23	18:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	18:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	18:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/23	18:31:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/10/23	18:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/10/23	18:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/23	18:55:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2015/10/23	18:55:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	18:56:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/10/23	18:56:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00
2015/10/23	18:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/10/23	18:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/10/23	18:58:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/10/23	18:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/10/23	19:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/23	19:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	19:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	19:06:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/10/23	19:06:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00
2015/10/23	19:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/10/23	19:06:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/10/23	19:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/10/23	19:06:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/10/23	19:08:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/23	19:08:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2015/10/23	19:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/10/23	20:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/23	20:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	20:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	20:08:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/23	20:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/23	20:32:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/23	20:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/10/23	21:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/23	21:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	21:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	21:46:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/23	21:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/23	22:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/23	22:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/10/23	23:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/23	23:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	23:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/23	23:23:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/23	23:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/23	23:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/23	23:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/10/24	00:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/24	00:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	00:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	00:52:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/24	00:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/24	01:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/24	01:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/10/24	01:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1

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2015/10/24	01:29:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	01:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/10/24	01:30:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02 00 00 00 00
2015/10/24	01:30:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/10/24	01:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/10/24	01:30:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/10/24	01:30:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/10/24	01:30:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/24	01:32:56.0	XRT_QT_PROG_SET_413_OG [0x19d] MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2015/10/24	01:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/10/24	01:33:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/24	02:22:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	02:22:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	02:22:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/24	02:22:06.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/24	02:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/24	02:42:30.0	XRT_Custom_430_OG [0x1ae]			
2015/10/24	02:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/24	03:50:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	03:50:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	03:50:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/24	03:50:06.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/24	03:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/24	04:20:00.0	XRT_Custom_430_OG [0x1ae]			
2015/10/24	04:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/24	05:30:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	05:30:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	05:30:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/24	05:30:36.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/24	05:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/24	06:06:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	06:06:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	06:06:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/10/24	06:07:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2015/10/24	06:07:18.0	XRT_FLD_DIS_406_OG [0x196] MDP_XRT_FLD_DIS	1	07-F0	d9
2015/10/24	06:09:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/10/24	06:09:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/10/24	06:09:58.0	XRT_QT_PROG_SET_421_OG [0x1a5] MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/10/24	06:10:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/24	06:16:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	06:16:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	06:16:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/10/24	06:17:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02 00 00 00 00
2015/10/24	06:17:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/10/24	06:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/10/24	06:17:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/10/24	06:17:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/10/24	06:17:26.0	XRT_FLD_RESET_433_OG [0x1b1]			

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2015/10/24	06:19:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/24	06:19:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2015/10/24	06:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/10/24	07:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/24	07:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	07:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	07:11:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/24	07:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/24	07:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/24	07:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/10/24	08:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/24	08:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	08:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/24	08:50:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/24	08:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/24	09:10:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/24	09:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/10/24	10:00:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/24	11:04:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00