

Period: 2015/11/10 10:05:00 - 2015/11/14 10:49:00

XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + Th														
Term		Pointing (x, y)				Comment								
11/10 18:13:00 - 11/10 18:19:54		Fixed (0.0, 0.0)				synoptic, shifted 10.0 min								
11/11 05:23:00 - 11/11 05:29:54		Fixed (0.0, 0.0)				synoptic, shifted namually								
11/11 18:01:30 - 11/11 18:08:24		Fixed (0.0, 0.0)				synoptic, shifted -1.5 min								
11/12 06:03:00 - 11/12 06:09:54		Fixed (0.0, 0.0)				synoptic								
PROG= 05 1-time(s)														
└ Subr= 1 1-time(s) 2.0sec														
└ Seqn= 5 1-time(s) 2.0sec														
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└ Seqn= 33 1-time(s) 2.0sec														
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 49 1-time(s) 2.0sec														
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 39 1-time(s) 2.0sec														
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 54 1-time(s) 2.0sec														
└ Open/G-band		Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
└ Open/G-band		Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1AE1: AR standard-B (thin-Be) with pre-flare buffer, 384 FOV at 1064x1048, thin-Be, thick-Al, and Al/Poly context, With G-band (3ms/3ms leak), 10s of														
Term		Pointing (x, y)							Comment					
11/10 23:03:00 - 11/11 03:59:54		Track (-324.1, -263.9) @ 11/10 23:00:00							HOP247					
PROG= 20 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 56		2-time(s)		2.0sec										
		Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		1-time(s)		2.0sec										

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XOB #1AF7: Coronal Cavity (2 filters) - 2x2 - 768FOV - 60s cad - AEC0														
Term			Pointing (x, y)						Comment					
11/11 07:03:00 - 11/11 09:44:30			Fixed (935.0, 8.0)						HOP237					
PROG= 08 Inf.-time(s)														
└ Subr= 1 1-time(s) 2.0sec														
└└ Seqn= 82 1-time(s) 60.0sec														
└└└ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 768x768 (1064, 1048) Q=95 0 0 2.0sec														
└└└ thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 768x768 (1064, 1048) Q=95 0 0 2.0sec														
└└└ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval														

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -																
Term			Pointing (x, y)					Comment								
11/10 14:11:00 - 11/10 17:46:30			Track (-448.0, -298.2) @ 11/10 14:08:00					AR12449								
11/10 18:23:00 - 11/10 22:38:00			Track (-414.4, -299.0) @ 11/10 18:20:00					AR12449								
11/10 23:03:00 - 11/11 03:59:54			Track (-324.1, -263.9) @ 11/10 23:00:00					HOP247								
11/11 04:03:00 - 11/11 05:19:54			Track (-334.6, -300.4) @ 11/11 04:00:00					AR12449								
11/11 07:03:00 - 11/11 09:44:30			Fixed (935.0, 8.0)					HOP237								
11/11 10:01:31 - 11/11 12:34:19			Track (15.3, 19.5) @ 11/11 09:56:30					Mosaic observation 1 of 2								
11/11 12:37:25 - 11/11 15:09:00			Track (295.7, 19.9) @ 11/11 12:34:25					Mosaic observation 2 of 2								
11/11 15:23:30 - 11/11 17:58:24			Track (-238.0, -301.5) @ 11/11 15:15:55					AR12449								
11/11 18:11:30 - 11/12 05:59:54			Track (-212.8, -301.6) @ 11/11 18:08:30					AR12449								
11/12 06:13:00 - 11/12 10:22:00			Track (-106.0, -301.8) @ 11/12 06:10:00					AR12449								
PROG= 07 30-time(s)																
└─	Subr= 1		20-time(s)		2.0sec											
	└─	Seqn= 11		1-time(s)		2.0sec										
		Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
		thin-Be/Open		med-Be/Open		close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
		med-Be/Open		Open/thick-Al		close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al		Open/thick-Be		close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
└─	Subr= 2		1-time(s)		2.0sec											
	└─	Seqn= 10		1-time(s)		2.0sec										
		med-Al/Open		med-Al/thick-Al		close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
	└─	Seqn= 11		1-time(s)		2.0sec										
		Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	└─	Seqn= 84		1-time(s)		2.0sec										
		Open/G-band		Open/G-band		open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/G-band		Open/G-band		close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval																

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Active Region Search

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NOT USED

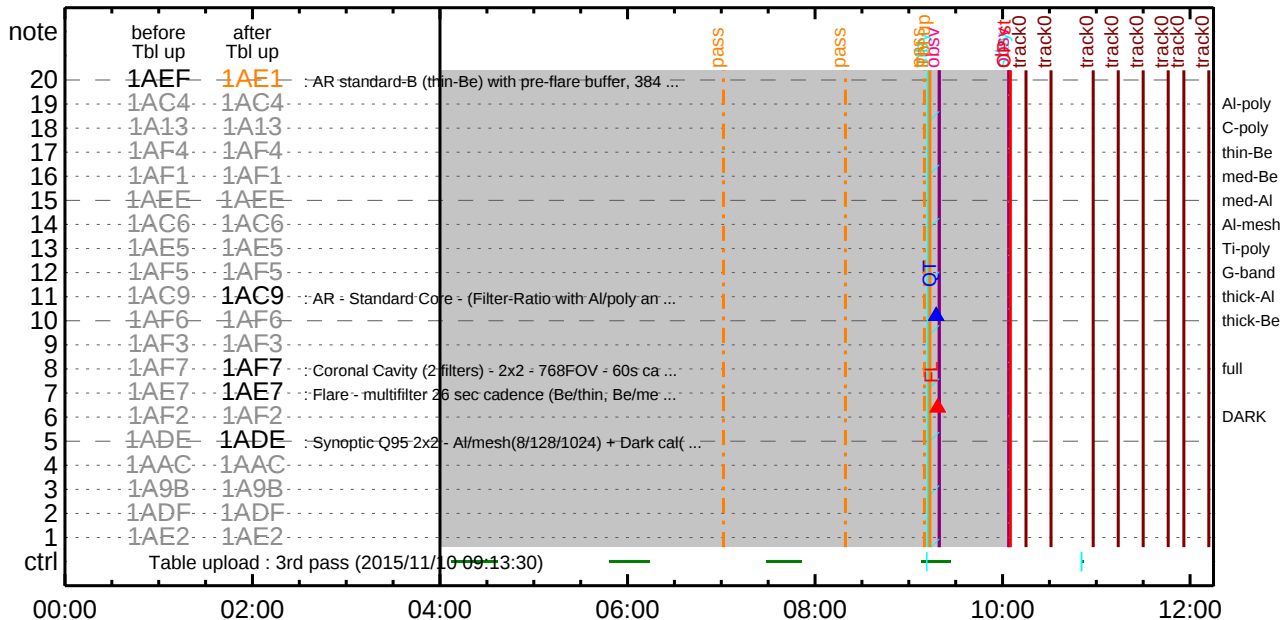
* * * * *

Flare Detection

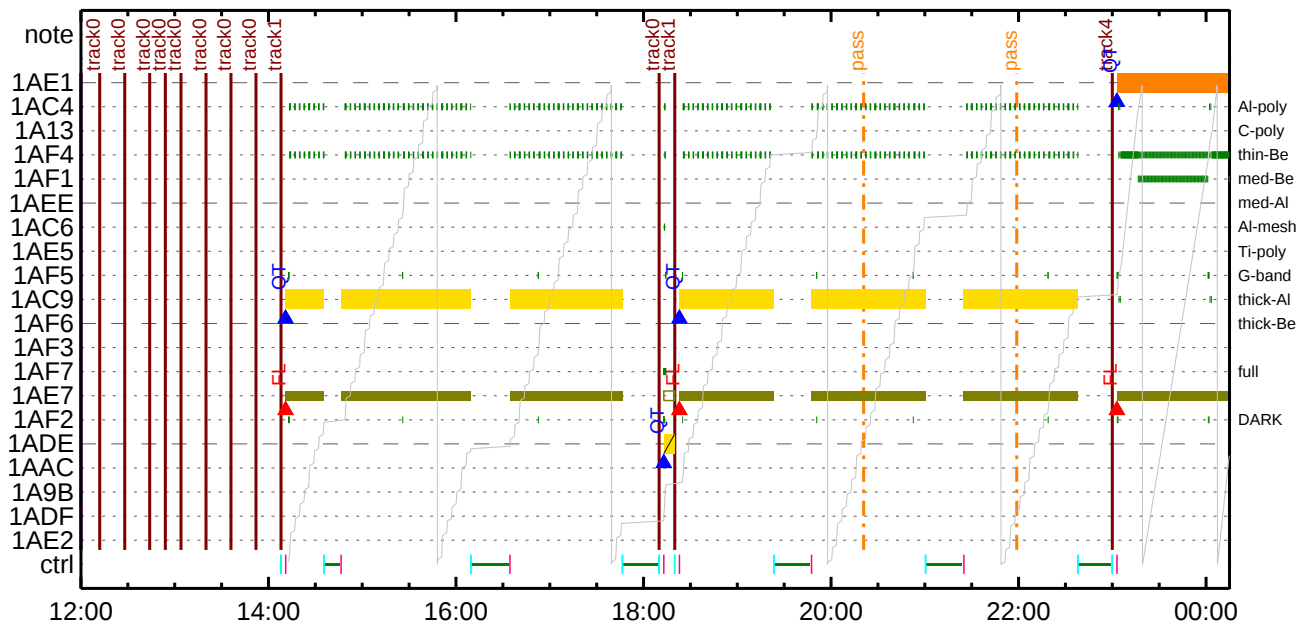
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FLD Patrol														
Term			Pointing (x, y)						Comment					
11/10 18:20:18 - 11/11 05:20:18			Track (-414.4, -299.0) @ 11/10 18:20:00						AR12449					
11/11 07:00:18 - 11/11 17:58:48			Fixed (935.0, 8.0)						HOP237					
11/11 18:08:48 - 11/12 06:00:18			Track (-212.8, -301.6) @ 11/11 18:08:30						AR12449					
11/12 06:10:18 - 11/14 10:49:00			Track (-106.0, -301.8) @ 11/12 06:10:00						AR12449					
		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		80sec	
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

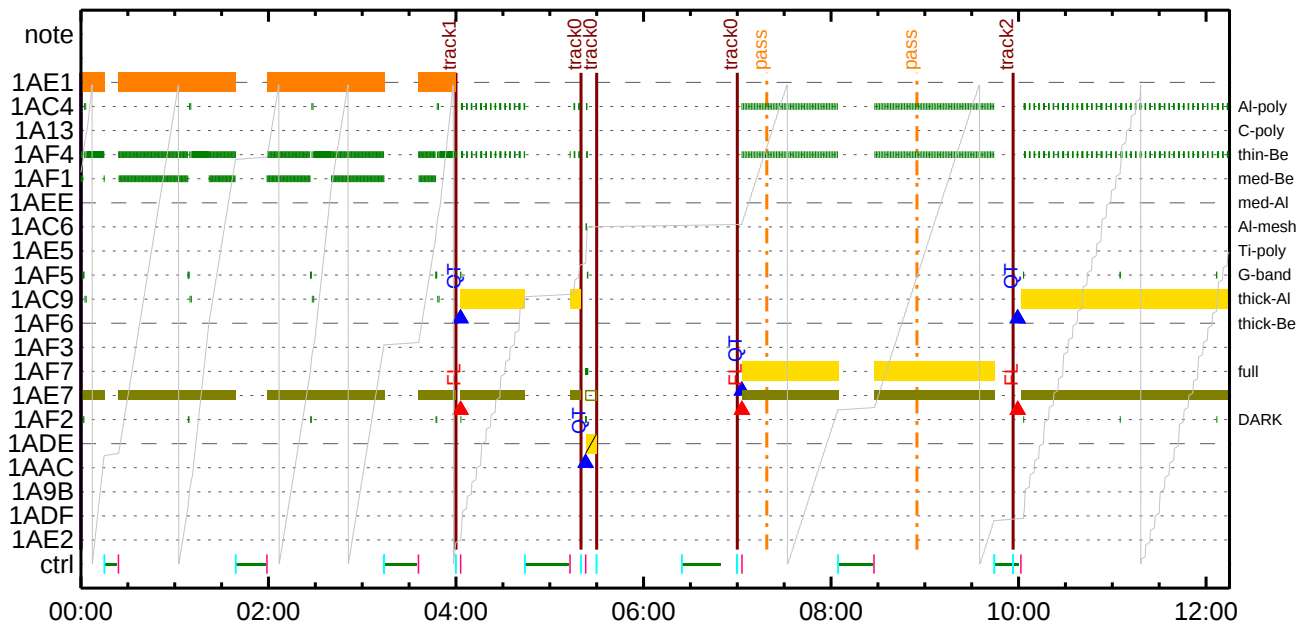
CMDI #0515 2015/11/10



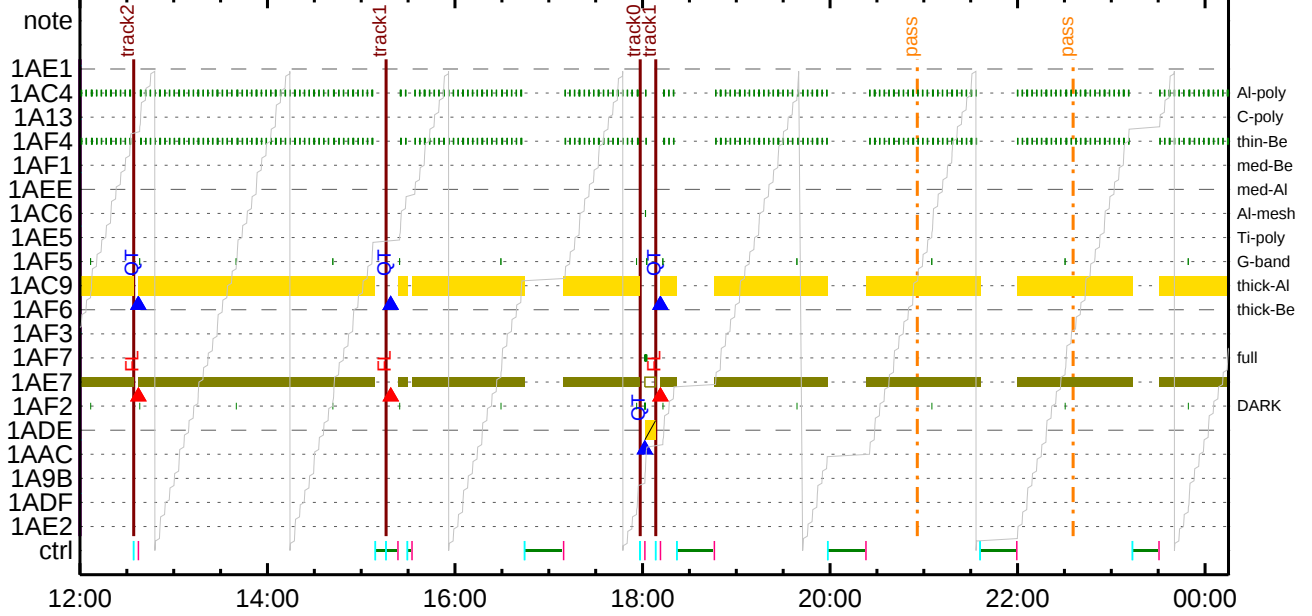
CMDI #0515 2015/11/10



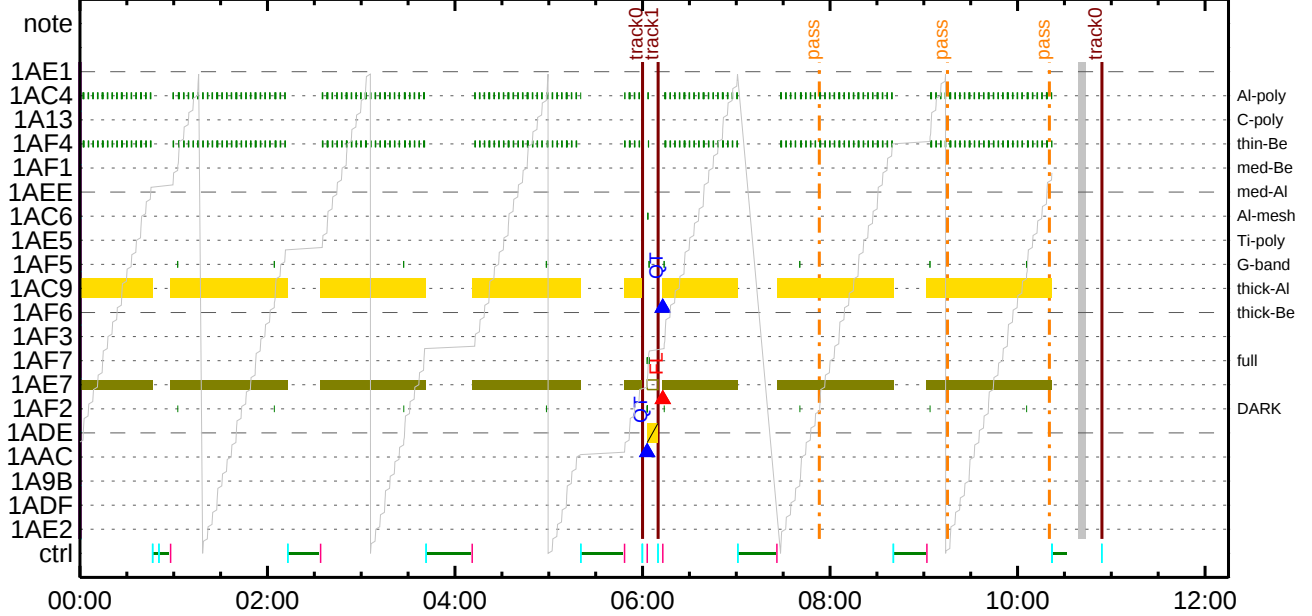
CMDI #0515 2015/11/11



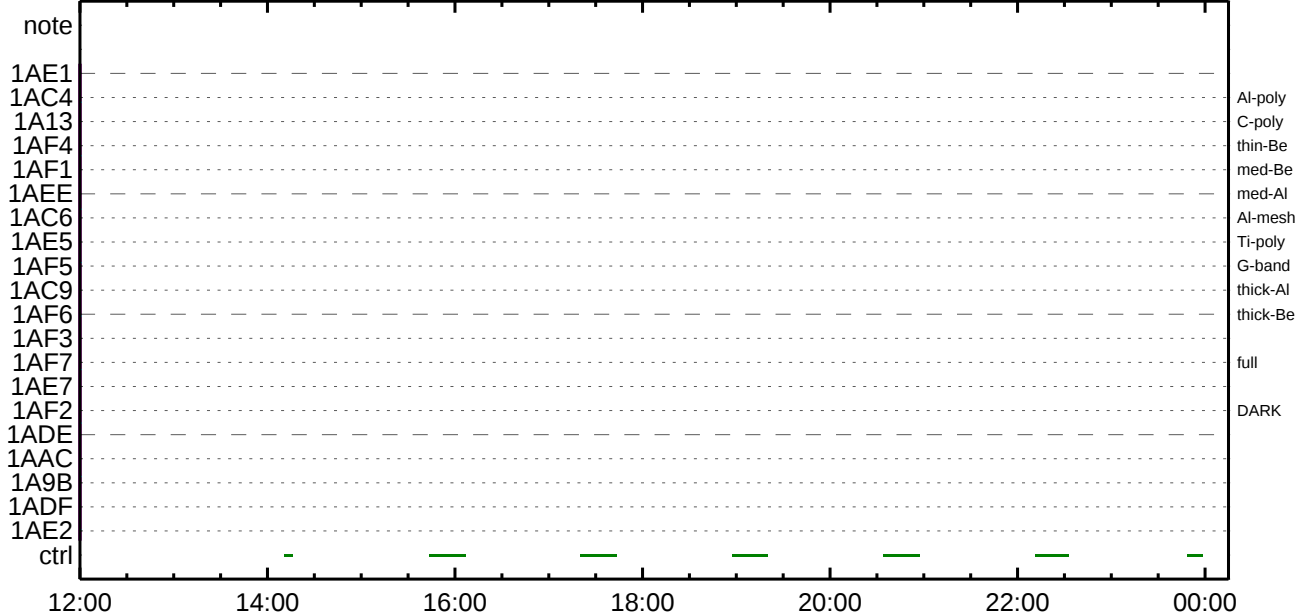
CMDI #0515 2015/11/11



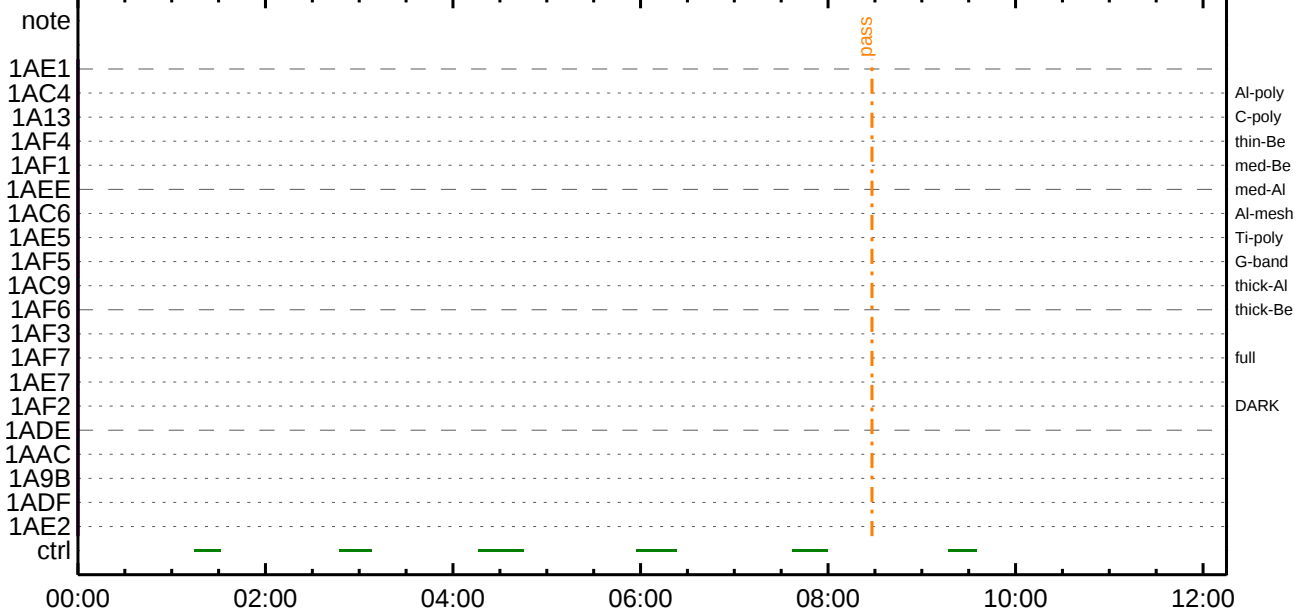
CMDI #0515 2015/11/12



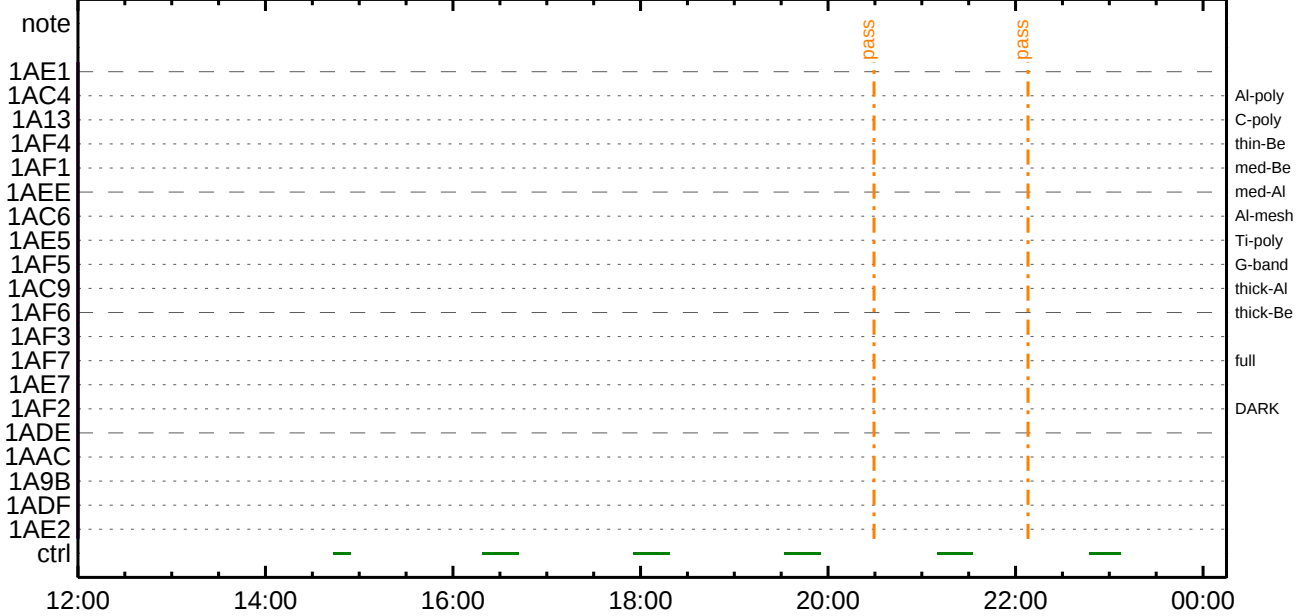
CMDI #0515 2015/11/12



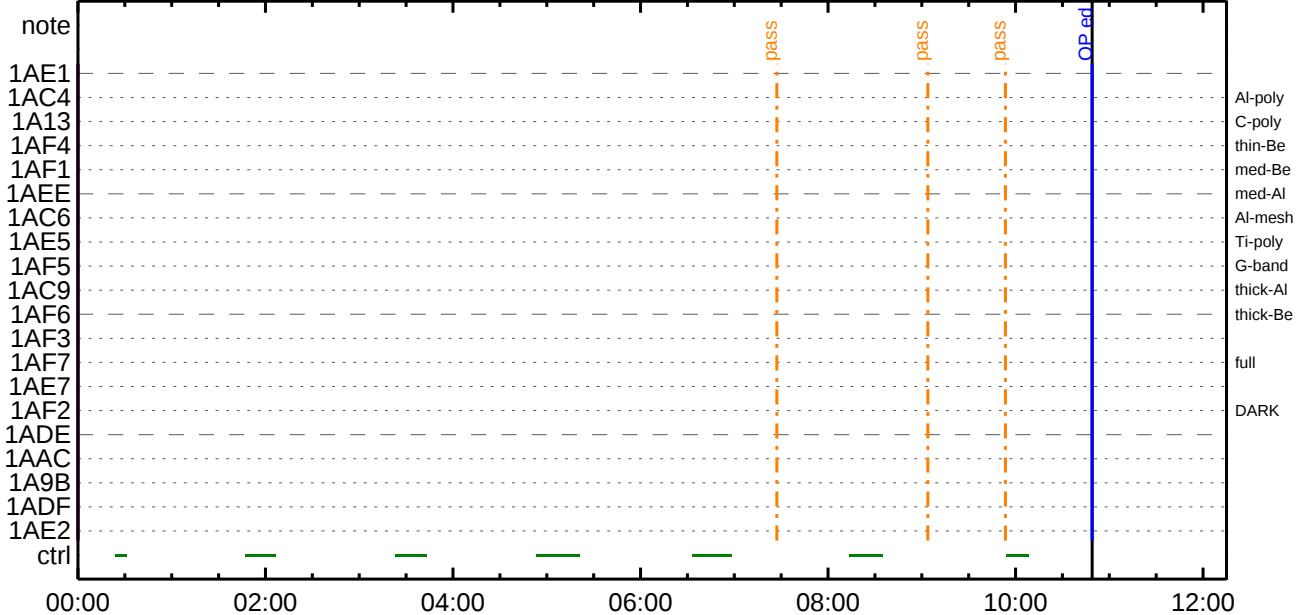
CMDI #0515 2015/11/13



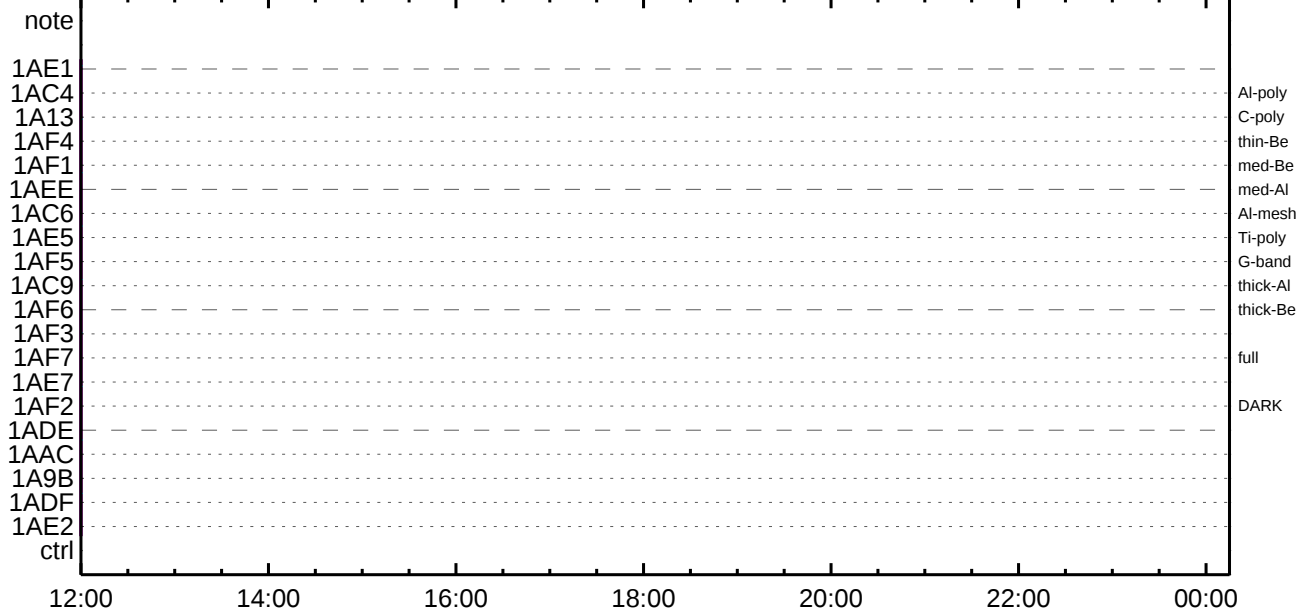
CMDI #0515 2015/11/13



CMDI #0515 2015/11/14



CMDI #0515 2015/11/14



0096	C.	0B0CjCSET0EDUMP0IÆ±°iÑ¥¹0Ç¹00 0³0ÈjE			
0097	C.				
0098	C.	TI¥³¥P¥Ó¥É00ÀDİ¿(UT)			
0099	+	TI 2015-11-10 10:00:00.0			
0100	DC	01-B3 DHU_OP_STOP			
0101	C.	00[HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0102	C.				
0103	+	TI 2015-11-10 10:00:01.0			
0104	DC	01-B4 DHU_OP_COPY			
0105	C.	00[HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0106	C.				
0107	+	TI 2015-11-10 10:00:01.0			
0108	DC	01-B5 DHU_OPOG_COPY			
0109	C.	00[HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0110	C.				
0111	+	TI 2015-11-10 10:04:59.5			
0112	DC	01-B2 DHU_OP_START			
0113	C.	00[HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0114	C.				
0115	C.	°Ê²¼0IÄê%İİÑ0I¥Ä\$¥Ä¥¹àİÜ			
0116	C.	00[HK1_TI_CMD_ENA/DIS]	EQ		ENA
0117	C.	00[HK1_TI_CMD_NUM]	EQ	4	
0118	C.	00[HK1_NEXT_EXEC_PIM]	EQ		DHU
0119	C.	00[HK1_NEXT_EXEC_DC]	EQ		0xB3
0120	C.				
0121	C.	*****			
0122	C.	TIİİ°è¥Ä¥Ó¥x			
0123	C.	*****			
0124	C.				
0125	C.	TI_TBL(0x03AB00-0x03AEFFj\$ 1024byte)			
0126	+	DC 01-23 DHU_DMA_DUMP_PRM_SET			
0127	BC	(03 ab 03 01 02)			
0128	C.	00[HK1_DMP_TOP_ADRS_1]	EQ	07	
0129	C.	00[HK1_DMP_TOP_ADRS_0]	EQ	2B	
0130	C.	00[HK1_DMP_BLOCK_NUM]	EQ	3	
0131	C.	00[HK1_DMP_REPEAT_NUM]	EQ	0	
0132	C.	00[HK1_DMA_DMP_PIM]	EQ		DHU
0133	+	DC 01-22 DHU_MODE_CHNG			
0134	BC	(07 0b f8)			
0135	C.	00[HK1_PKT_FORM_NO]	EQ	7	
0136	C.	00[HK1_PKT_GEN_TIME]	EQ	0.25	s
0137	C.	00[HK1_S_TLM_BIT_RATE]	EQ	32k	
0138	C.	00[HK1_X_TLM_BIT_RATE]	EQ	4M	
0139	C.	00[HK1_DMP_CHK_FLG]	EQ		EXEC
0140	C.				
0141	C.	¥Ä¥Ó¥x½ªİ»00³İÇ\$			
0142	C.	00[HK1_DMP_CHK_FLG]	EQ		NON
0143	C.				
0144	C.	RAM ID=TI_TBL0I%Ê¹Ç•è²İOK00³İÇ\$			
0145	C.				
0146	C.	DHU¥âj¼¥ÉjÊ¼ý½ ¥İj¼¥ËjË00İâ0¹			
0147	+	DC 01-22 DHU_MODE_CHNG			
0148	BC	(02 0a f8)			
0149	C.	00[HK1_PKT_FORM_NO]	EQ	2	
0150	C.	00[HK1_PKT_GEN_TIME]	EQ	0.5S	
0151	C.	00[HK1_S_TLM_BIT_RATE]	EQ	32K	
0152	C.	00[HK1_X_TLM_BIT_RATE]	EQ	4M	
0153	C.				
0154	C.	*****			
0155	C.	SOT TI command set			
0156	C.	*****			
0157	C.	Execute, after the success of OP upload.			
0158	+	TI 2015-11-10 10:04:16.0			
0159	DC	07-F0 MDP_SOT_MODE_STBY			
0160	BC	(41)			
0161	C.	-----			
0162	C.	HK1_TI_CMD_NUM = 1 CNTUP []			
0163	C.	-----			
0164	C.	***** SOT END *****			
0165	C.				
0166	C.	***** XRT START *****			
0167	C.	Execute, after the success of OP upload.			
0168	+	TI 2015-11-10 10:04:00.0			
0169	DC	07-F0 MDP_XRT_MODE_STBY			
0170	BC	(c3)			
0171	C.	[] [HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0172	C.				
0173	C.	***** XRT END *****			
0174	C.	Stop EIS observation and temporarily disable EIS mode changes			
0175	C.				
0176	C.				
0177	C.	***** Start EIS operation (TI set) *****			
0178	C.	Execute, after the success of OP upload.			
0179	C.	Set EIS TI-commands			
0180	+	TI 2015-11-10 10:04:30.0			
0181	DC	07-FC EIS_MODE_MANU			
0					

0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ¤Ė´Ø¤¹¤ĖDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ĩăLOS¥Á¥S¥Ā¥¼Ā»Üĭă
0203 C.
0204 . C. ***** LOS *****
0205 C.


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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 2599238.5 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 39s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2015-11-10 10:04:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP '0ÃÎï»0%ÝðËÄð¹²ëDCBC•x²è *****
0165 C. (%ã°Î¥0¥Ã¥Ë¥ð¥Ë¥ã¥Ç¥è²¼¼¼Ã»Û¹²è)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•Ï Daily±¿ÎÑ²Ë´0¹²ëDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ¡ãLOS¥Á¥S¥Ã¥´¼Ã»Û¡ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-482 2015-11-10 12:44:39 170 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Ä»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äi;Ê¿¿A¤•µ°£»I×AÇ¤I¥¢¥A¥×¥i;¼¥ÉjÊÊ½µ•iîÊjÊ¤¼°ÇÖ¤•¤¿¼i¹Ç¤IjÇÀ®, ù¤¹¤Ê¤¤¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-267:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-283:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 . C. < Upload DPL table >
0054 C.
0055 C. ¥¢¥Ã¥×¥i;¼¥É¤IÁ°¤ÊSTS_CHK¤ò¤OFF¤Ê¤¹¤Ê
0056 C.
0057 . S. RAM ram-271:MDP_DPL
0058 ( )
0059 C.
0060 . C. < Dump RAMID=MDP_DPL >
0061 +. DC 07-F0 MDP_DUMP_FGTBL
0062 BC (82 07 00 38 b8 00 40)
0063 C. -----
0064 C. MDP_DPL verify = OK [ ]
0065 C. -----
0066 C.
0067 C. STS_CHK¤ò¤ON¤Ê¤¹¤Ê
0068 C.
0069 . C. < Update MDP DSC PAR1 >
0070 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0071 BC (4c)
0072 C. MDP_CMD_CODE = F04C0700 [ ]
0073 C. MDP_CMD_CNT (count-up 1) [ ]
0074 C. -----
0075 C.
0076 . C.
0077 C. *****
0078 C. SOT TI command set
0079 C. *****
0080 C. Execute, after the success of TBL upload.
0081 +. TI 2015-11-10 10:04:18.0
0082 DC 07-F0 MDP_SOT_MODE_OBSV
0083 BC (40)
0084 . C. -----
0085 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0086 C. -----
0087 C.
0088 C.
0089 C. ***** XRT START *****
0090 C.
0091 +. DC 07-F0 MDP_XRT_CTRL_MANU
0092 BC (c1)
0093 +. DC 07-F0 MDP_XRT_MODE_STBY
0094 BC (c3)
0095 . C. ----- Success Verify ? OK / NG_____
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0096 C.
0097 C. XRT Obs. Table Upload
0098 . S. RAM ram-291:MDP_OBS_X
0099 ( )
0100 C.
0101 +. DC 07-F0 MDP_DUMP_XRTTBL
0102 BC (84 07 00 00 00 3a d4)
0103 . C. ----- Comparison Check ? OK / ERR ____
0104 C.
0105 C.
0106 +. DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 01 b1 b1 04 04)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 02 b1 b1 08 08)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 03 b1 b1 08 08)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 04 b1 b1 06 06)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 05 85 83 06 06)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 06 85 83 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 07 80 80 20 20)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 08 80 80 20 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 09 80 80 08 20)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 0a 85 83 08 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 0b 85 83 0c 0c)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 0f 80 80 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 10 80 80 08 08)
0132 + DC 07-F0 MDP_XRT_FLD_ENA
0133 BC (d8)
0134 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0135 BC (c8)
0136 + DC 07-F0 MDP_XRT_ARS_DIS
0137 BC (d5)
0138 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0139 BC (c4 0b)
0140 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0141 BC (c5 07)
0142 . C. ----- Success Verify ? OK / NG ____
0143 C.
0144 C.
0145 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0146 C.
0147 +. DC 07-F0 MDP_XRT_MODE_OBSV
0148 BC (c2)
0149 +. TI 2015-11-10 10:04:02.0
0150 DC 07-F0 MDP_XRT_MODE_OBSV
0151 BC (c2)
0152 . C. ----- Success Verify ? OK / NG ____
0153 C.
0154 C. ***** XRT END *****
0155 C.
0156 . C. ***** MDP '0AÎaÎ»0%YpÊÂð¹æðDCBC•x²è *****
0157 C. (%â°i¥0¥Ã¥Ê¥p¥Ê¥â¥C¥è¥Ê½¥ª¥Ã»Û¹æ)
0158 . S. DC-BC dcbc-402:DCBC
0159 (MDP_known_event)
0160 C.
0161 C.
0162 . C. ***** ¥Ð¥¹•İ Daily±çİÑæË´0¹æðDCBC•x²è *****
0163 . S. DC-BC dcbc-153:DCBC
0164 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0165 C.
0166 C.
0167 . C. ;ãLOS¥Ã¥S¥Ã¥¬¼Ã»Û;ä
0168 C.
0169 . C. ***** LOS *****
0170 C.

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*** OP Sequence for XRT ***

2015/11/10	10:15:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2015/11/10	10:31:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	d6	67
2015/11/10	10:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/10	10:50:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/10	10:50:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/10	10:50:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/10	10:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/10	10:58:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/11/10	11:14:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	29	99
2015/11/10	11:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	53	33
2015/11/10	11:46:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2015/11/10	11:56:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2015/11/10	12:12:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	ac	5b	00	00
2015/11/10	12:28:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	b4	b5	24	8b
2015/11/10	12:44:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2015/11/10	12:54:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2015/11/10	13:04:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2015/11/10	13:20:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2015/11/10	13:36:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2015/11/10	13:52:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	29	db	48	72
2015/11/10	14:07:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/10	14:07:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/10	14:07:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/11/10	14:08:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	01	00	00	00	00
2015/11/10	14:08:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/10	14:08:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/10	14:08:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/10	14:08:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/10	14:08:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/10	14:10:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2015/11/10	14:10:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/11/10	14:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/10	14:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/10	14:35:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/10	14:35:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/10	14:35:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/10	14:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/10	14:45:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/10	14:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/10	16:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/10	16:09:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/10	16:09:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/10	16:09:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/10	16:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/10	16:33:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/10	16:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/10	17:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							

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2015/11/10	17:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	17:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	17:46:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/10	17:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/10	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/10	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	18:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	18:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/10	18:10:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2015/11/10	18:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/11/10	18:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/10	18:12:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/10	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/11/10	18:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/10	18:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	18:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	18:20:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/10	18:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2015/11/10	18:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/10	18:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/10	18:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/10	18:20:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/10	18:22:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/10	18:22:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2015/11/10	18:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/10	19:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/10	19:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	19:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	19:23:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/10	19:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/10	19:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/10	19:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2015/11/10	21:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/10	21:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	21:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	21:00:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/10	21:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/10	21:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/10	21:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2015/11/10	22:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/10	22:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	22:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	22:38:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/10	22:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/10	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/10	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/10	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00

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2015/11/10	23:00:00.0	AOCS_0Re-point_Start_17_0G [0x0a7]		
		AOCU_NM	5	02-76 04 00 00 00 00
2015/11/10	23:00:18.0	XRT_FLD_ENA_411_0G [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/11/10	23:00:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/11/10	23:00:22.0	XRT_AEC_RESET_438_0G [0x1b6]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/11/10	23:00:24.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/11/10	23:00:26.0	XRT_FLD_RESET_439_0G [0x1b7]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/10	23:02:56.0	XRT_QT_PROG_SET_446_0G [0x1be]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 14
2015/11/10	23:02:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/11/10	23:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/11	00:15:00.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	00:15:02.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	00:15:04.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/11	00:15:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/11	00:18:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/11	00:23:00.0	XRT_Custom_430_0G [0x1ae]		
2015/11/11	00:24:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/11	01:39:00.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	01:39:02.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	01:39:04.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/11	01:39:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/11	01:42:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/11	01:58:00.0	XRT_Custom_430_0G [0x1ae]		
2015/11/11	01:59:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/11	03:14:00.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	03:14:02.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	03:14:04.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/11	03:14:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/11	03:17:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/11	03:35:00.0	XRT_Custom_430_0G [0x1ae]		
2015/11/11	03:36:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/11	03:59:54.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	03:59:56.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	03:59:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/11/11	04:00:00.0	AOCS_0Re-point_Start_16_0G [0x0a6]		
		AOCU_NM	5	02-76 01 00 00 00 00
2015/11/11	04:00:18.0	XRT_FLD_ENA_411_0G [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/11/11	04:00:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/11/11	04:00:22.0	XRT_AEC_RESET_448_0G [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/11/11	04:00:24.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/11/11	04:00:26.0	XRT_FLD_RESET_433_0G [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/11	04:02:56.0	XRT_QT_PROG_SET_440_0G [0x1b8]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0b
2015/11/11	04:02:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/11/11	04:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/11	04:44:00.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	04:44:02.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/11	04:44:04.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/11	04:44:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/11	04:47:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/11	05:12:00.0	XRT_Custom_430_0G [0x1ae]		
2015/11/11	05:13:00.5	XRT_CTRL_AUTO_424_0G [0x1a8]		

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2015/11/11	05:19:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	05:19:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	05:19:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	05:20:00.0	AOCS_ORe-point_Start_3_0G [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/11	05:20:18.0	XRT_FLD_DIS_406_0G [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2015/11/11	05:22:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/11/11	05:22:56.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/11	05:22:58.0	XRT_QT_PROG_SET_421_0G [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/11	05:23:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/11/11	05:29:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	05:29:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	05:30:00.0	AOCS_ORe-point_Start_18_0G [0x0a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	06:24:30.0	XRT_CTRL_MANU_400_0G [0x190]	AOCU_NM	5	02-76	00 3c 31 c3 cf
2015/11/11	06:24:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	06:24:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	06:24:36.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	06:27:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/11	06:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	06:59:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	06:59:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	07:00:00.0	AOCS_ORe-point_Start_19_0G [0x0a9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/11	07:00:18.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	00 ff 4c ac e6
2015/11/11	07:00:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/11	07:00:22.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/11	07:00:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/11	07:00:26.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/11	07:02:56.0	XRT_QT_PROG_SET_426_0G [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	07:02:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2015/11/11	07:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/11	08:04:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	08:04:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	08:04:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	08:04:36.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	08:07:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/11	08:26:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	08:27:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430	1	07-F0	c0
2015/11/11	09:44:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	09:44:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	09:44:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	09:44:36.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	09:47:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/11	09:56:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	09:56:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	09:56:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	09:56:30.0	AOCS_ORe-point_Start_20_0G [0x0aa]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/11	09:56:48.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	02 00 00 0b 64
2015/11/11	09:56:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8

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2015/11/11	09:56:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/11	09:56:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/11	09:56:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/11	09:59:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	09:59:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2015/11/11	10:00:31.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/11	10:01:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	12:34:19.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	12:34:21.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	12:34:23.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/11	12:34:25.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	02 00 00 f4 9c
2015/11/11	12:34:43.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/11	12:34:45.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/11	12:34:47.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/11	12:34:49.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/11	12:34:51.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	12:37:21.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2015/11/11	12:37:23.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/11	12:37:25.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	15:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	15:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	15:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	15:09:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/11	15:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	15:15:49.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	15:15:51.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	15:15:53.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/11	15:15:55.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	01 00 00 00 00
2015/11/11	15:16:13.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/11	15:16:15.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/11	15:16:17.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/11	15:16:19.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/11	15:16:21.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	15:18:51.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2015/11/11	15:18:53.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/11	15:22:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	15:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	15:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	15:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	15:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	15:29:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/11	15:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	15:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	15:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	16:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	16:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	16:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	16:44:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]				

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2015/11/11	16:47:44.0	MDP_XRT_PREFLR_STRT	1	07-F0	e8
		XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	17:08:30.0	XRT_Custom_430_0G [0x1ae]			
2015/11/11	17:09:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	17:58:24.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	17:58:26.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	17:58:28.0	XRT_FOCUS_POSITION_403_0G [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/11	17:58:30.0	AOCS_ORe-point_Start_3_0G [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/11/11	17:58:48.0	XRT_FLD_DIS_406_0G [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/11/11	18:01:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/11	18:01:26.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/11	18:01:28.0	XRT_QT_PROG_SET_421_0G [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/11/11	18:01:30.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	18:08:24.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	18:08:26.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	18:08:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/11	18:08:30.0	AOCS_ORe-point_Start_16_0G [0x0a6]			
		AOCU_NM	5	02-76	01 00 00 00 00
2015/11/11	18:08:48.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/11	18:08:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/11	18:08:52.0	XRT_AEC_RESET_448_0G [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/11	18:08:54.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/11	18:08:56.0	XRT_FLD_RESET_433_0G [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	18:11:26.0	XRT_QT_PROG_SET_440_0G [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2015/11/11	18:11:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/11	18:11:30.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	18:22:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	18:22:02.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	18:22:04.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	18:22:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/11	18:25:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	18:45:00.5	XRT_Custom_430_0G [0x1ae]			
2015/11/11	18:46:00.5	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	19:58:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	19:58:32.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	19:58:34.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	19:58:36.0	XRT_PREFLR_STRT_425_0G [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/11	20:01:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	20:22:00.0	XRT_Custom_430_0G [0x1ae]			
2015/11/11	20:23:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	21:36:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	21:36:02.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	21:36:04.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/11	21:36:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/11	21:39:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	21:58:30.0	XRT_Custom_430_0G [0x1ae]			
2015/11/11	21:59:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/11	23:13:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	23:13:32.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/11	23:13:34.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da

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2015/11/11	23:13:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/11	23:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/11	23:29:30.0	XRT_Custom_430_OG [0x1ae]			
2015/11/11	23:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/12	00:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	00:46:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	00:46:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/12	00:46:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/12	00:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/12	00:50:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	00:50:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	00:50:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/12	00:50:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/12	00:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/12	00:57:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/12	00:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/12	02:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	02:13:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	02:13:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/12	02:13:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/12	02:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/12	02:33:00.5	XRT_Custom_430_OG [0x1ae]			
2015/11/12	02:34:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/12	03:41:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	03:41:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	03:41:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/12	03:41:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/12	03:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/12	04:10:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/12	04:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/12	05:20:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	05:20:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	05:20:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/12	05:20:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/12	05:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/12	05:47:30.0	XRT_Custom_430_OG [0x1ae]			
2015/11/12	05:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/12	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/12	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/11/12	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/11/12	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/12	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/12	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/11/12	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/12	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/12	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00

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2015/11/12	06:10:00.0	AOCS_0Re-point_Start_16_0G [0x0a6]		
		AOCU_NM	5	02-76 01 00 00 00 00
2015/11/12	06:10:18.0	XRT_FLD_ENA_411_0G [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/11/12	06:10:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/11/12	06:10:22.0	XRT_AEC_RESET_448_0G [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/11/12	06:10:24.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/11/12	06:10:26.0	XRT_FLD_RESET_433_0G [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/12	06:12:56.0	XRT_QT_PROG_SET_440_0G [0x1b8]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0b
2015/11/12	06:12:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/11/12	06:13:00.0	XRT_CTRL_AUTO_408_0G [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/12	07:01:00.5	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/12	07:01:02.5	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/12	07:01:04.5	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/12	07:01:06.5	XRT_PREFLR_STRT_425_0G [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/12	07:04:14.5	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/12	07:25:00.0	XRT_Custom_430_0G [0x1ae]		
2015/11/12	07:26:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/12	08:40:30.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/12	08:40:32.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/12	08:40:34.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/12	08:40:36.0	XRT_PREFLR_STRT_425_0G [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/12	08:43:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/12	09:01:00.0	XRT_Custom_430_0G [0x1ae]		
2015/11/12	09:02:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/12	10:22:00.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/12	10:22:02.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/12	10:22:04.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/12	10:22:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/12	10:25:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/12	10:53:54.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/12	10:53:56.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/12	10:54:00.5	AOCS_0Re-point_Start_3_0G [0x099]		
		AOCU_NM	5	02-76 00 00 00 00 00