

XRT Timeline to be uploaded on 2015/11/14

Period: 2015/11/14 10:49:00 - 2015/11/19 10:00:00

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Normal mode

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XOB #1AF9: High cadence (10s thin-Be only) 384x384 at 1064 1048 AEC0												
Term		Pointing (x, y)					Comment					
11/14 11:02:00 - 11/14 13:59:54		Fixed (-889.0, -385.0)					# OP start + 10min, EIS Temperature diagnostics					
PROG= 15 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 56 1-time(s) 2.0sec												
Open/G-band Open/G-band open		Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/G-band Open/G-band close		Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 74 180-time(s) 20.0sec												
thin-Be/Open med-Be/Open close		Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	0	0	10.0sec
thin-Be/Open med-Be/Open close		Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	0	0	10.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1AFA: High cadence (10s thin-Be only) 384x384 at 1064 1048 AEC1												
Term		Pointing (x, y)					Comment					
11/14 14:03:00 - 11/14 17:59:54		Track (422.1, -279.1) ^{© 11/14 14:00:00}					AR12449					
PROG= 19 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 56 1-time(s) 2.0sec												
Open/G-band Open/G-band open		Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/G-band Open/G-band close		Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 36 180-time(s) 20.0sec												
thin-Be/Open med-Be/Open close		Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	10.0sec
thin-Be/Open med-Be/Open close		Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	10.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + TH												
Term		Pointing (x, y)					Comment					
11/14 18:03:00 - 11/14 18:09:54		Fixed (0.0, 0.0)					synoptic					
PROG= 05 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec												
Open/Al-mesh Open/Al-mesh close		Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Al-mesh Open/Al-mesh close		Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Al-mesh Open/Al-mesh close		Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 49 1-time(s) 2.0sec												
Al-poly/Open Al-poly/Open close		Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Al-poly/Open Al-poly/Open close		Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Al-poly/Open Al-poly/thick-Al close		Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 39 1-time(s) 2.0sec												
thin-Be/Open thin-Be/Open close		Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
thin-Be/Open thin-Be/Open close		Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
thin-Be/Open thin-Be/Open close		Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Open/G-band Open/G-band open		Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec
Open/G-band Open/G-band close		Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1AE5: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with												
Term		Pointing (x, y)					Comment					
11/14 18:13:00 - 11/15 06:05:54		Track (455.5, -277.9) ^{© 11/14 18:10:00}					AR12449					
PROG= 13 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 56 1-time(s) 2.0sec												
Open/G-band Open/G-band open		Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/G-band Open/G-band close		Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/Ti-poly Open/thick-Al close		Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec												
Seqn= 75 1-time(s) 2.0sec												
Al-poly/Open thin-Be/Open close		Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2	0	2.0sec
Al-poly/Open thin-Be/Open close		Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 23 2-time(s) 60.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ADF: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Med-Al(40

Term	Pointing (x, y)	Comment
11/15 06:09:00 - 11/15 06:17:55	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min
PROG= 02 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 33 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 49 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open	close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 77 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open	close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 54 1-time(s) 4.0sec		
Open/G-band	Open/G-band	open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 46 2-time(s) 2.0sec		
Open/thick-Be	Open/thick-Be	close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 72 2-time(s) 2.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 59 2-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
med-Al/Open	med-Al/Open	close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
11/14 11:02:00 - 11/14 13:59:54	Fixed (-889.0, -385.0)	# OP start + 10min, EIS Temperature diagnostics
11/14 14:03:00 - 11/14 17:59:54	Track (422.1, -279.1) @ 11/14 14:00:00	AR12449
11/14 18:13:00 - 11/15 06:05:54	Track (455.5, -277.9) @ 11/14 18:10:00	AR12449
PROG= 07 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 84 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

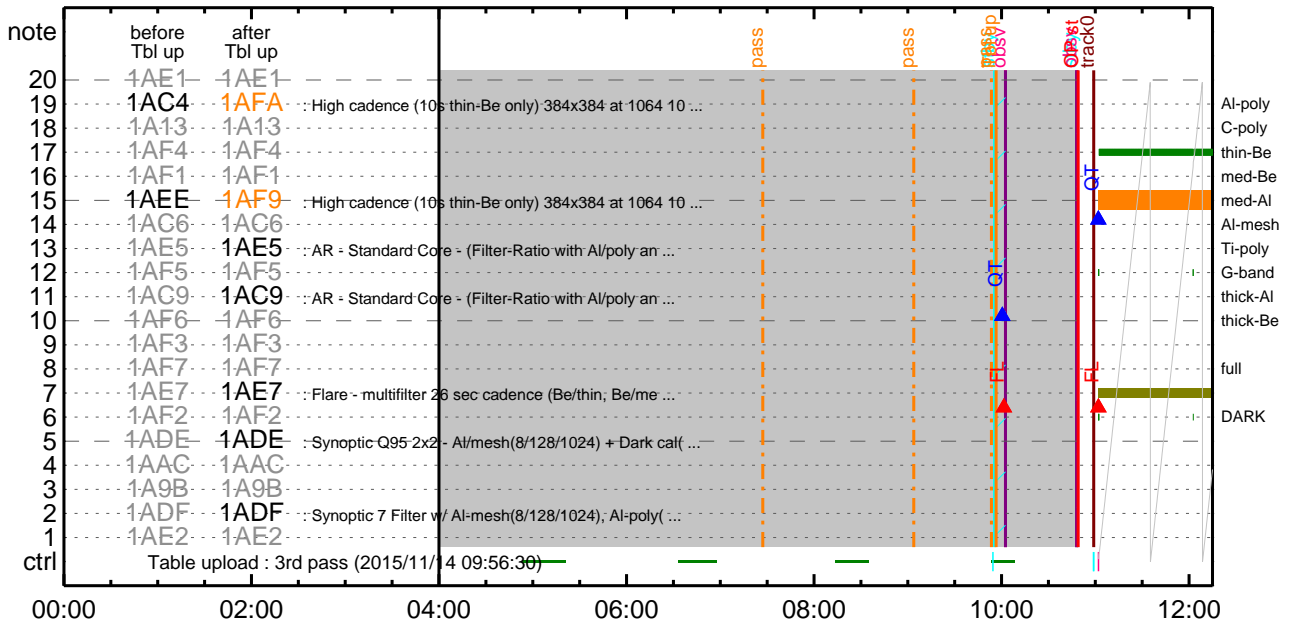
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Flare Detection

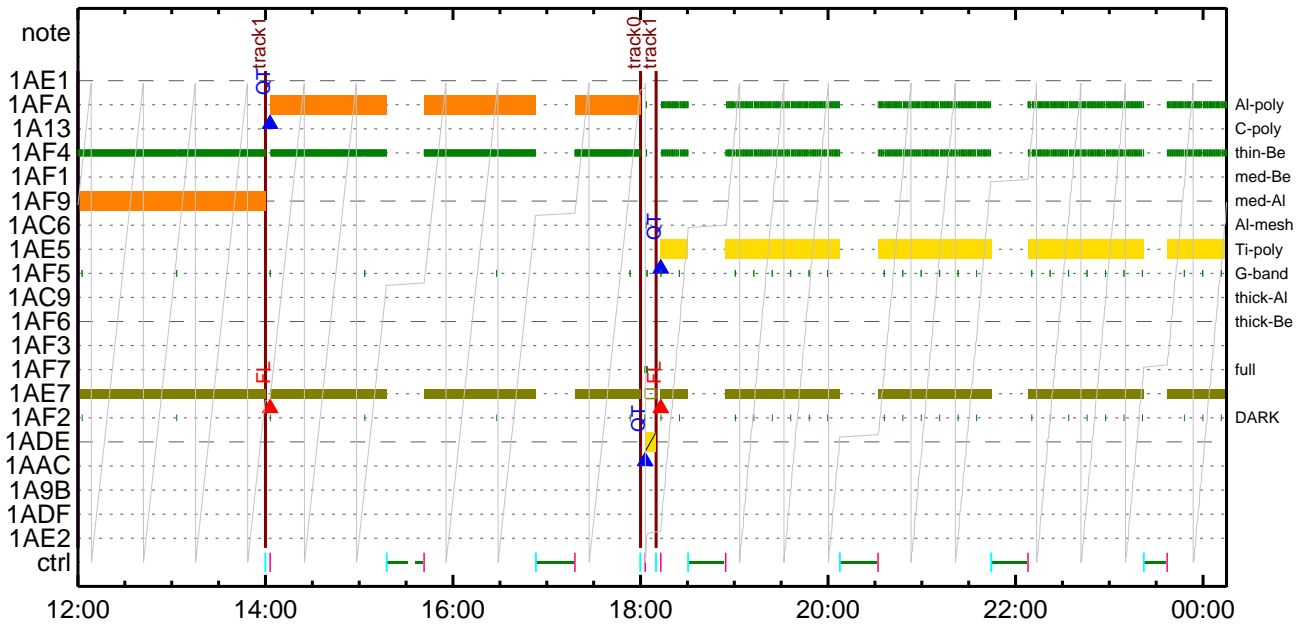
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FLD Patrol												
Term		Pointing (x, y)						Comment				
11/14 18:10:18 - 11/15 06:06:18		Track (455.5, -277.9) @ 11/14 18:10:00						AR12449				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		80sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

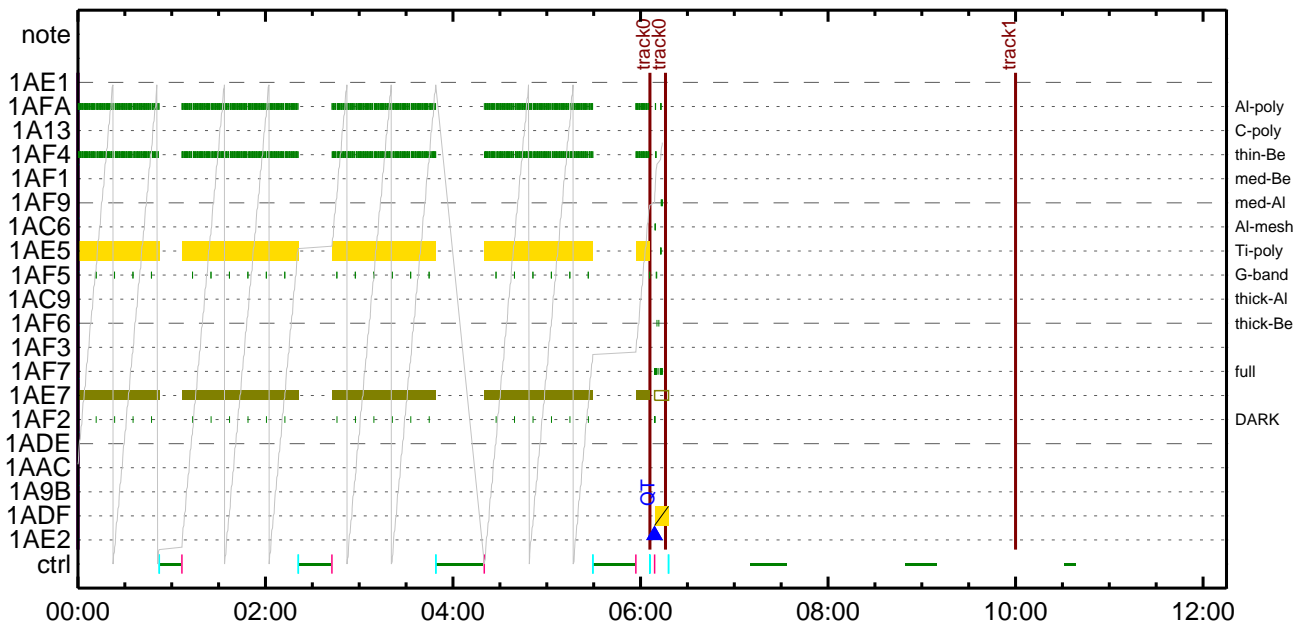
CMDI #0523 2015/11/14



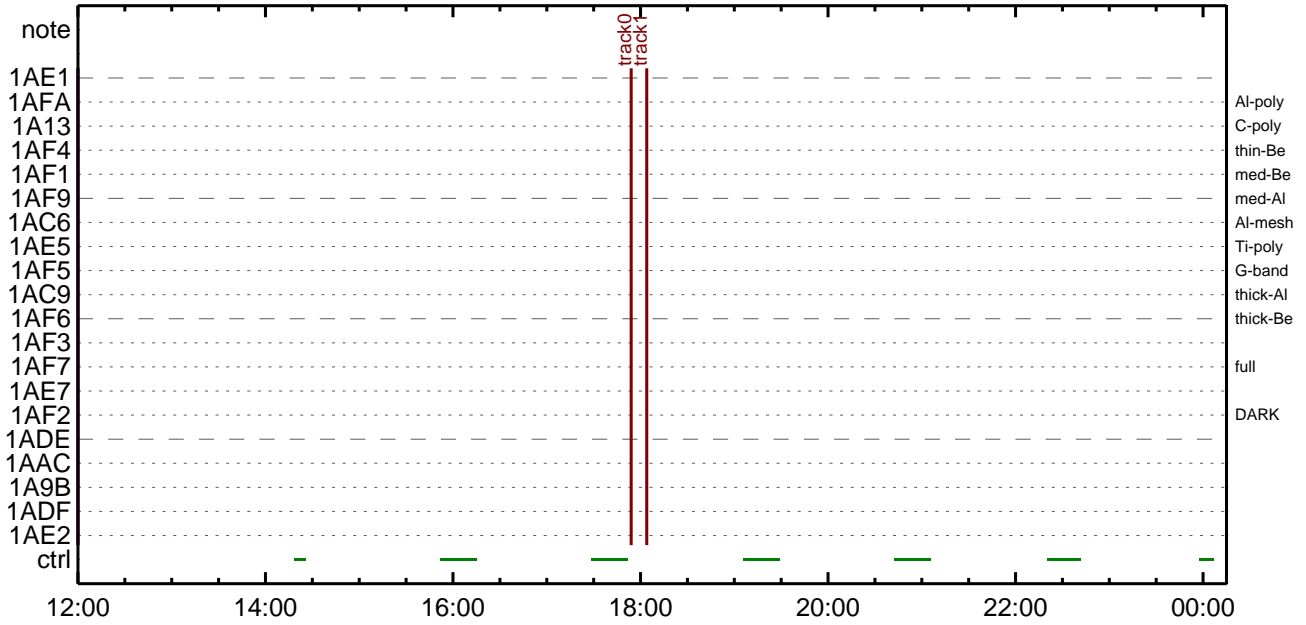
CMDI #0523 2015/11/14



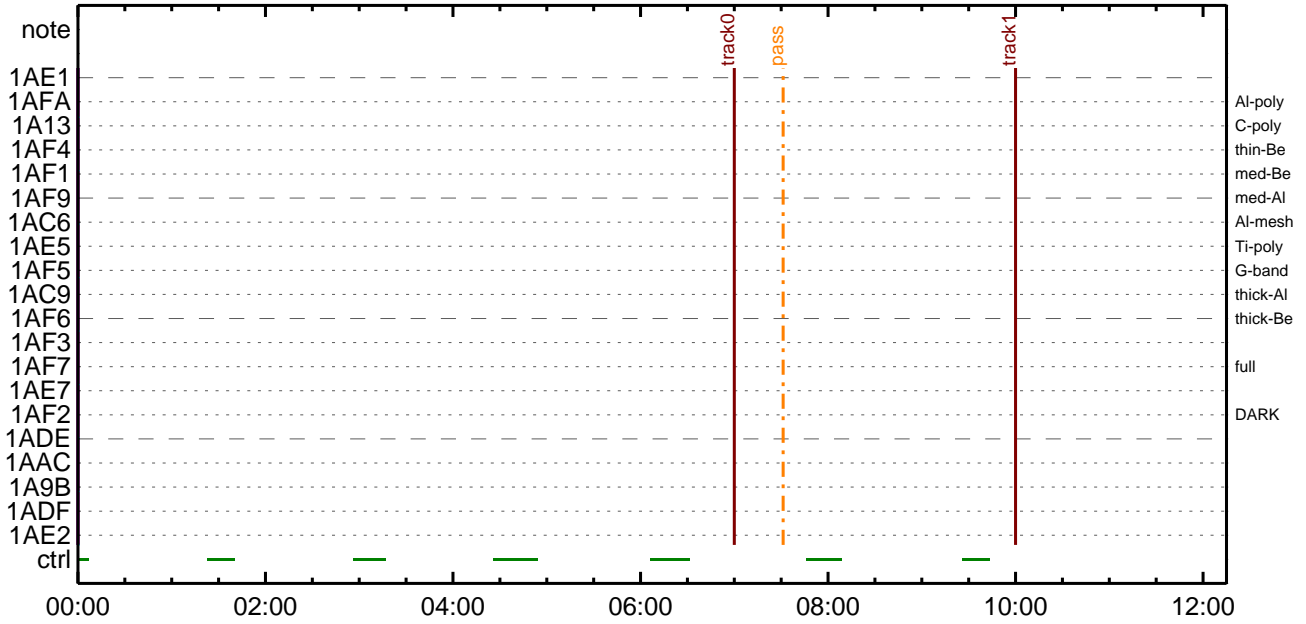
CMDI #0523 2015/11/15



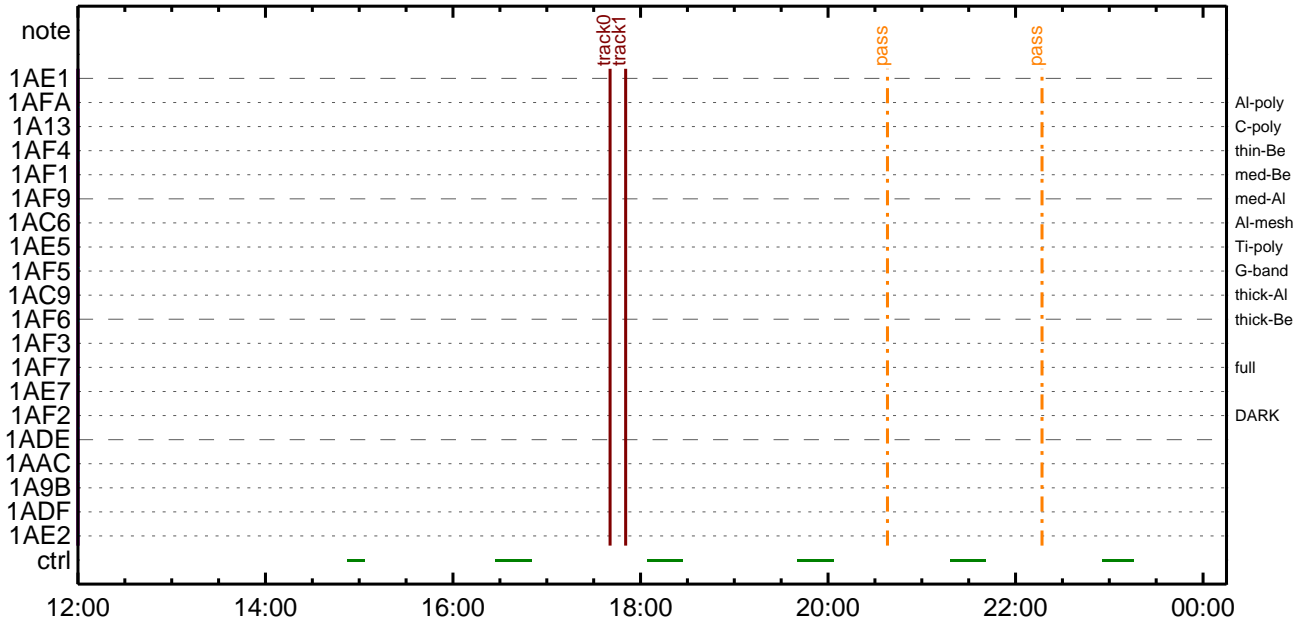
CMDI #0523 2015/11/15



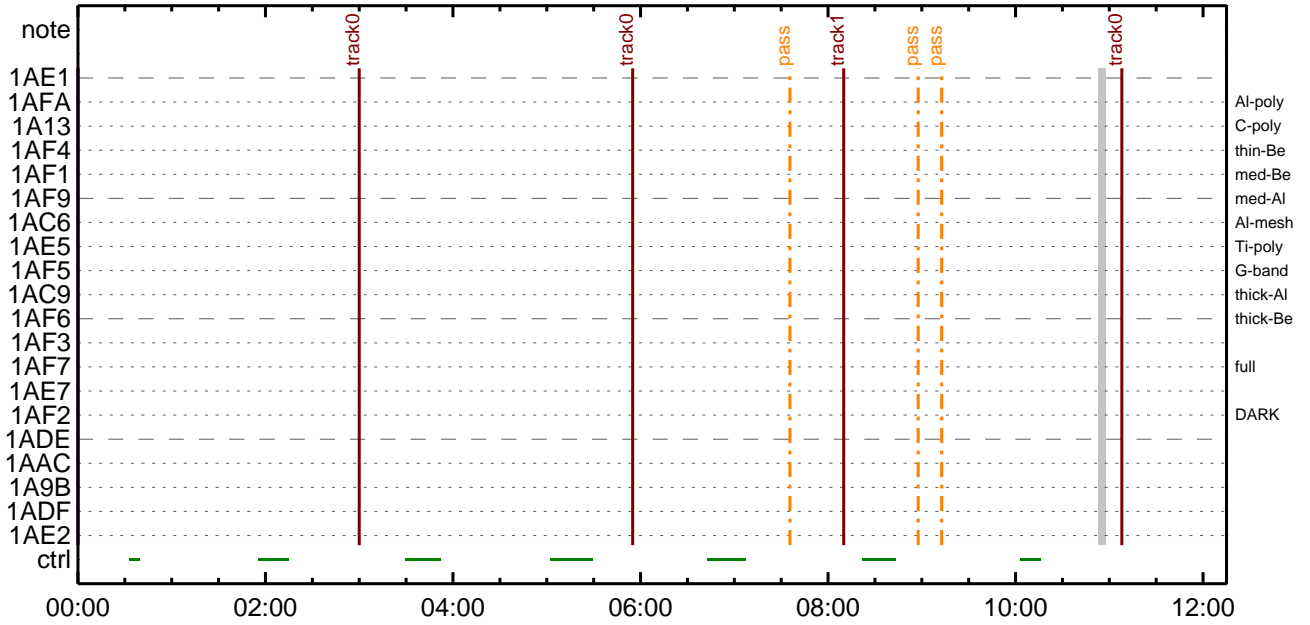
CMDI #0523 2015/11/16



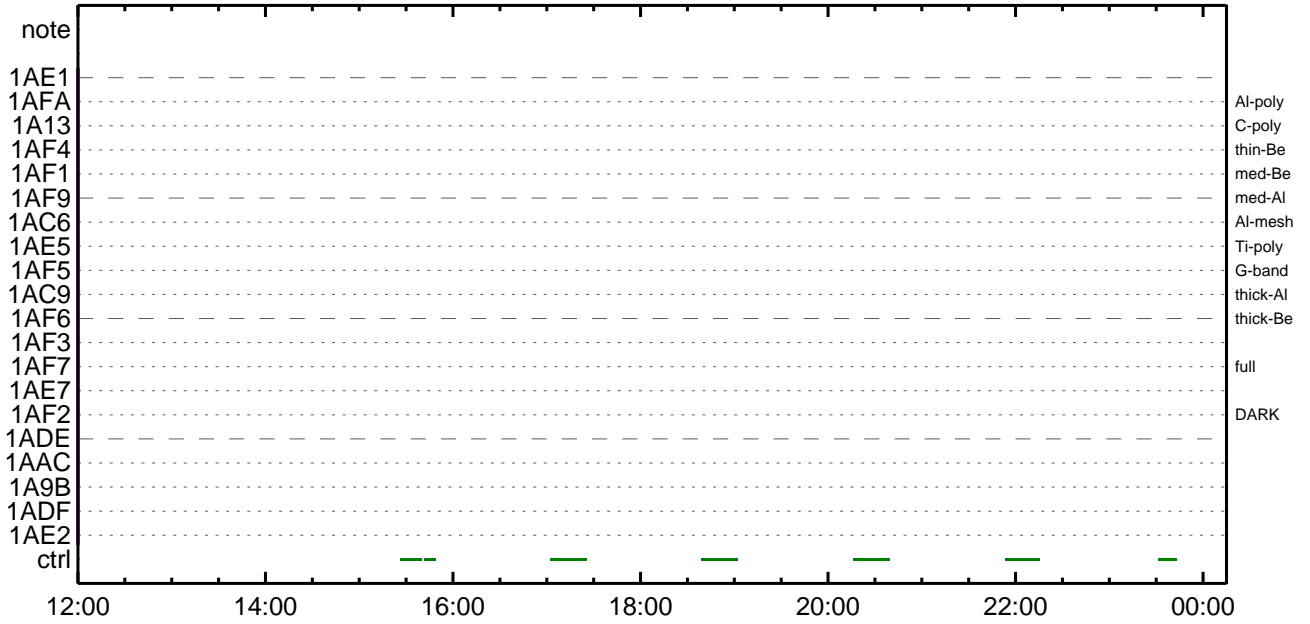
CMDI #0523 2015/11/16



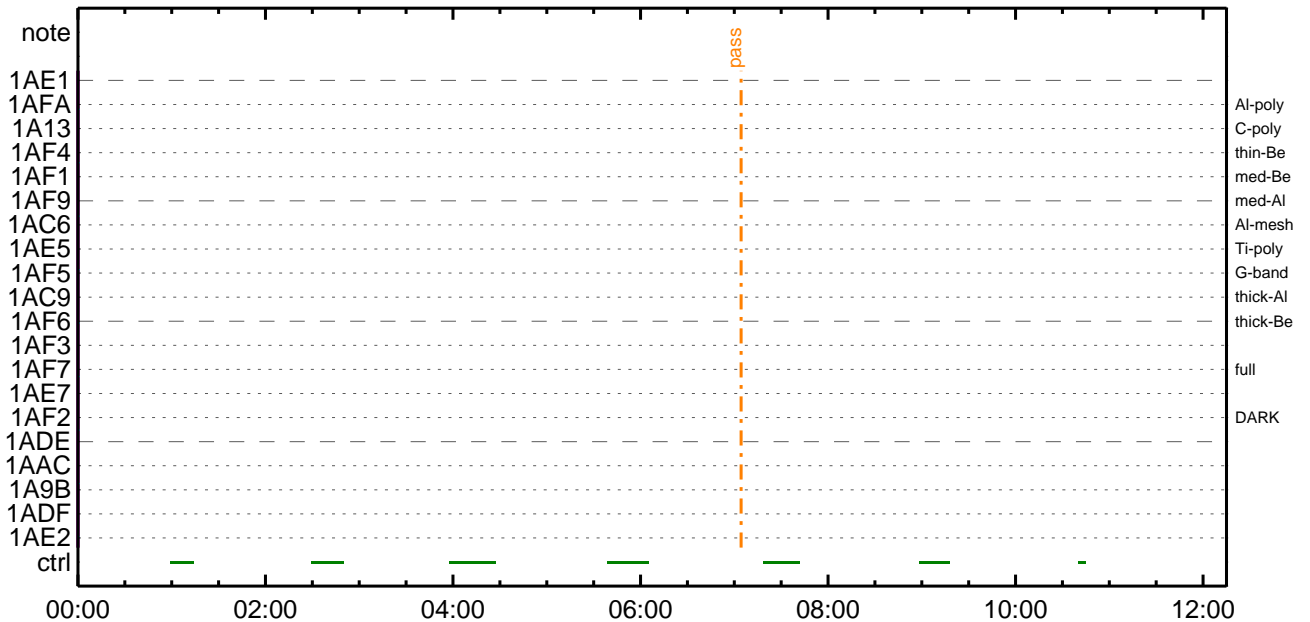
CMDI #0523 2015/11/17



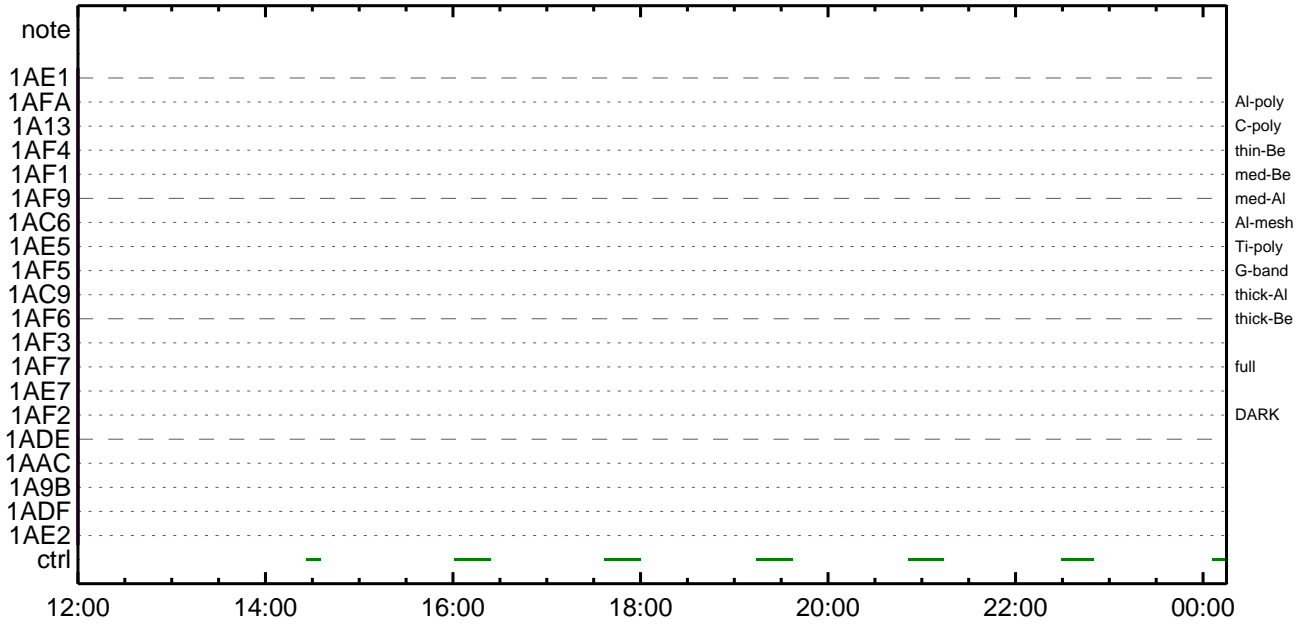
CMDI #0523 2015/11/17



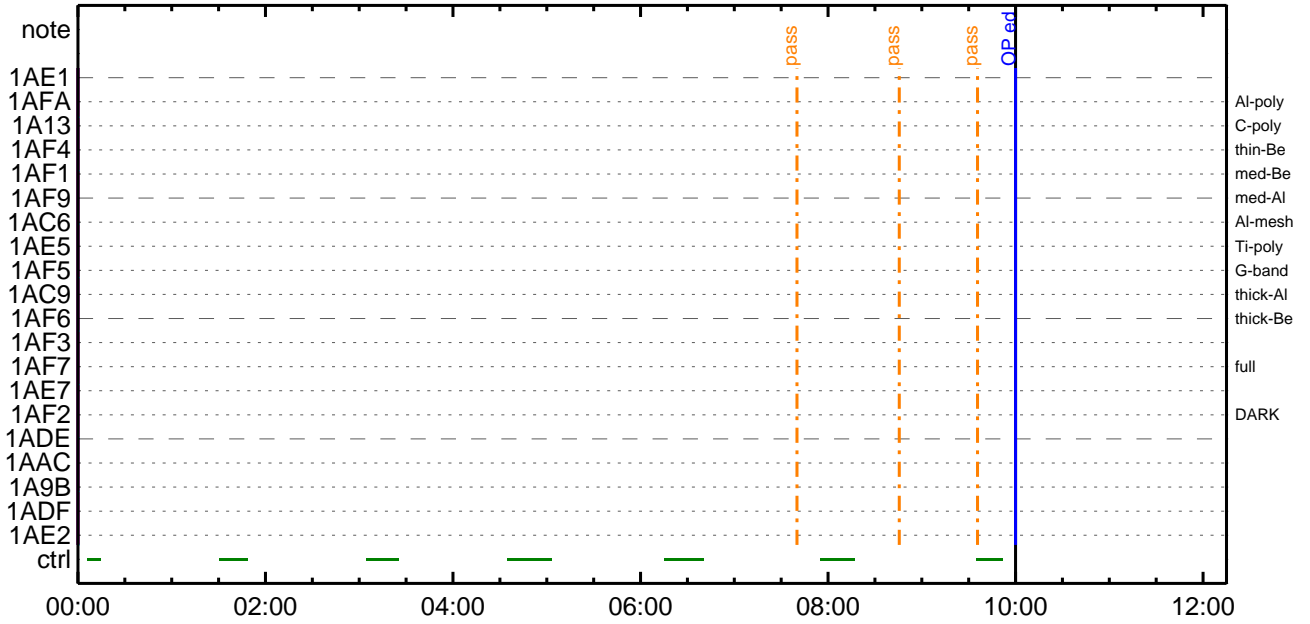
CMDI #0523 2015/11/18



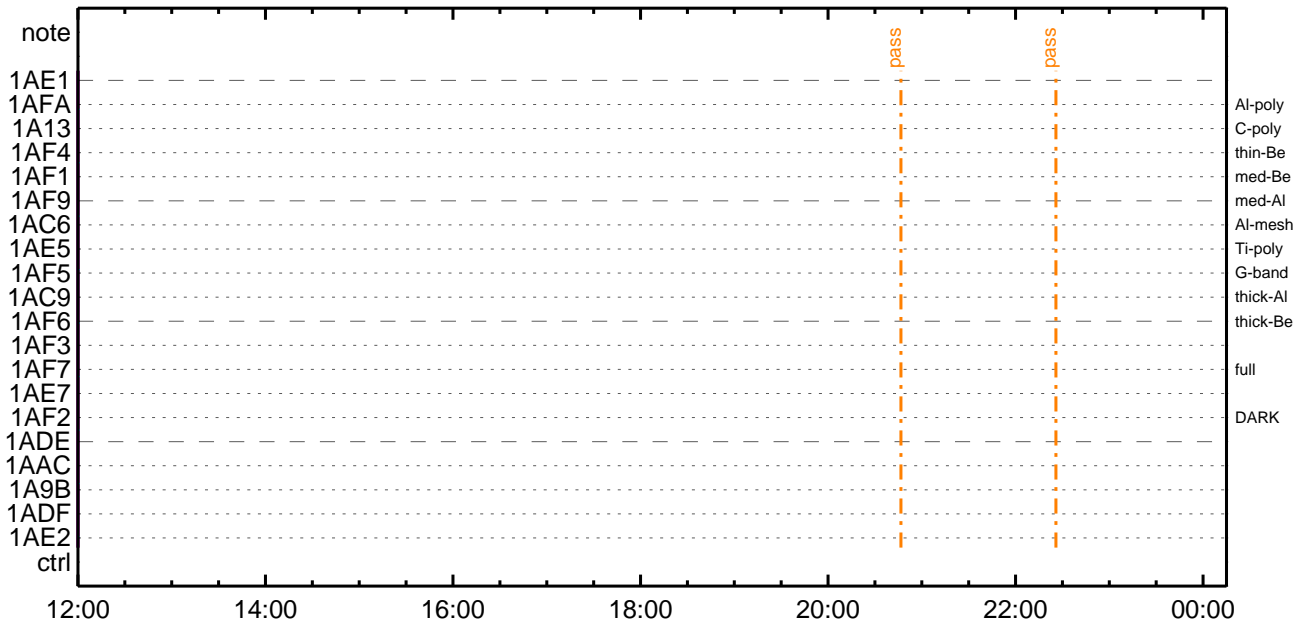
CMDI #0523 2015/11/18



CMDI #0523 2015/11/19



CMDI #0523 2015/11/19




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-489:OP
0104 ( )
0105 S. OG og-489:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼È¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼È¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼È¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °È²¼òî¼Ã´¶Á°òÈÈ¬ò°Á÷¿@ (¼âµ-YAYOYx½ªî»ò³îÇ§²¼òî¼Ã´¶Á°òÈÈ¬ò°Á÷¿@) *****
0167 C. DHUYâ;4YE;È¼Y½;Yi;4YE;Èòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼È¹ç;ç°È²¼òî¼TI-CMDÁ÷¿@²¼òî¼Ã´¶Á°òÈÈ¬ò°Á÷¿@;f
0180 C. ²¼òî¼;çSET²EDUMPA²±°iYNY¹ç¹Ò²|²³²È;f
0181 C.
0182 C. TIY³YpYóYÈ²òðÁDî¿(UT)
0183 +. TI 2015-11-14 10:44:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-11-14 10:44:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-11-14 10:44:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. *****
0130 C. SOT table upload
0131 C. *****
0132 . C. < Stop FG table >
0133 +. DC 07-F0 MDP_FG_CTRL_MANU
0134 BC (51)
0135 . C. -----
0136 C. MDP_FG_CTRL_MODE = MANU [ ]
0137 C. -----
0138 C.
0139 . C. <Upload FG Observation Table>
0140 . S. RAM ram-269:MDP_OBS_F
0141 ( )
0142 C.
0143 . C. < Dump RAMID=MDP_OBS_F >
0144 +. DC 07-F0 MDP_DUMP_FGTBL
0145 BC (82 07 00 00 00 38 b8)
0146 C. -----
0147 C. MDP_OBS_F verify = OK/NG [ ]
0148 C. -----
0149 C.
0150 C. *****
0151 C. SOT TI command set
0152 C. *****
0153 C. Execute, after the success of TBL upload.
0154 +. TI 2015-11-14 10:48:18.0
0155 DC 07-F0 MDP_SOT_MODE_OBSV
0156 BC (40)
0157 . C. -----
0158 C. HKI_TI_CMD_NUM = 1 CNTUP [ ]
0159 C. -----
0160 C.
0161 C.
0162 . C. ***** MDP `úãîï»ö%ýðéâð¹²³DCBC•x²è *****
0163 C. (%ã°îÿÓÿÄÿÈÿËÿÏÿáÿçÿèë%¼½¾»Û¹²è)
0164 . S. DC-BC dcbc-402:DCBC
0165 (MDP_known_event)
0166 C.
0167 C.
0168 . C. ***** ÿÐÿ¹•Ï Daily±¿îñðÉ´Ø²³èDCBC•x²è *****
0169 . S. DC-BC dcbc-153:DCBC
0170 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0171 C.
0172 C.
0173 . C. ¡ãLOSÿÁÿ$ÿÃÿ-¼Å»Û;ä
0174 C.
0175 . C. ***** LOS *****
0176 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-491 2015-11-14 12:52:46 115 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYŞ;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È±¿±±±•µ°È»Í×ÁÇ±ÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;È±È¼°ÇÓ±±±¼í¹ç±Í;çÀ®, ù±¹±±±±±±çÁ+¿®±±±±±±±±±±;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG ____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 80 80 20 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 09 80 80 08 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 0f 80 80 06 06)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 10 80 80 08 08)
0052 + DC 07-F0 MDP_XRT_FLD_ENA
0053 BC (d8)
0054 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0055 BC (c8)
0056 + DC 07-F0 MDP_XRT_ARS_DIS
0057 BC (d5)
0058 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0059 BC (c4 0b)
0060 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0061 BC (c5 07)
0062 . C. ----- Success Verify ? OK / NG ____
0063 C.
0064 C.
0065 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0066 C.
0067 +. DC 07-F0 MDP_XRT_MODE_OBSV
0068 BC (c2)
0069 +. TI 2015-11-14 10:48:02.0
0070 DC 07-F0 MDP_XRT_MODE_OBSV
0071 BC (c2)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C. ***** XRT END *****
0075 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0076 +. DC 07-FC EIS_MODE_CHG_ENA
0077 BC (20)
0078 . C. Verify EIS_MODE_CHG_FLG is ENA
0079 +. DC 07-FC EIS_MODE_MANU
0080 BC (21 02)
0081 . C. Verify EIS in MANUAL mode
0082 . C. Estimated OBSTBL upload time is 34s
0083 C. *****
0084 C. EIS START OBSTBL LOAD
0085 C. *****
0086 . S. RAM ram-820:EIS_OBSTBL
0087 ( )
0088 +. DC 07-FC EIS_DUMP_OBSTBL
0089 BC (07 07 07 00 00 70 00)
0090 C.
0091 . C. Execute, after the success of OBSTBL upload.
0092 . C. Set EIS TI-commands
0093 +. TI 2015-11-14 10:48:50.0
0094 DC 07-FC EIS_MODE_CHG_ENA
0095 BC (20)
```

```
0096 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0097 C. *****
0098 C. EIS END OBSTBL LOAD
0099 C. *****
0100 C.
0101 . C. ***** MDP 'úÃîñî»ô¼ÝñÊÂÐñ¹ñèDCBC•x²è *****
0102 C. (¼ã°îÝÓÝÃÝÊÝÞÝËÝáÝçÝèñ¼ññ¼Ã»Ûñ¹ñè)
0103 . S. DC-BC dcbc-402:DCBC
0104 (MDP_known_event)
0105 C.
0106 C.
0107 . C. ***** ÝÐÝ¹•î Daily±¿îññÊ´Øñ¹ñèDCBC•x²è *****
0108 . S. DC-BC dcbc-153:DCBC
0109 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0110 C.
0111 C.
0112 . C. ;ãLOSÝÁÝ§ÝÃÝ´¼Ã»Û;ã
0113 C.
0114 . C. ***** LOS *****
0115 C.
```

Nov 14, 15 12:52

XRT_OGLIST_0523.chk

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*** OP Sequence for XRT ***

2015/11/14	10:58:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	10:58:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	10:58:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/11/14	10:59:00.5	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 22 35 4f 02				
2015/11/14	10:59:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/14	10:59:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/14	10:59:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/14	10:59:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/14	10:59:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/14	11:01:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2015/11/14	11:01:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/11/14	11:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/14	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	13:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/11/14	14:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/11/14	14:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/14	14:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/14	14:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/14	14:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/14	14:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/14	14:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2015/11/14	14:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/11/14	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/14	15:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	15:17:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	15:17:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/14	15:17:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/14	15:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/14	15:40:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/14	15:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/14	16:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	16:53:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	16:53:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/14	16:53:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/14	16:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/14	17:17:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/14	17:18:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/14	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/14	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/11/14	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/11/14	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/14	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/14	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/14	18:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/11/14	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

2015/11/14	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/14	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	18:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/14	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2015/11/14	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/14	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/14	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/14	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/14	18:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/14	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2015/11/14	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/14	18:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/14	18:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	18:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	18:30:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/14	18:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/14	18:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/14	18:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/11/14	20:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/14	20:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	20:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	20:07:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/14	20:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/14	20:31:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/14	20:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/11/14	21:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/14	21:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	21:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	21:44:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/14	21:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/14	22:07:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/14	22:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/11/14	23:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/14	23:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	23:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/14	23:22:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/14	23:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/14	23:36:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/14	23:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/11/15	00:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/15	00:52:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/15	00:52:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/15	00:52:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/15	00:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/15	01:05:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/15	01:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2015/11/15	02:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/15	02:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2015/11/15	02:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	02:21:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/15	02:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/15	02:41:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/15	02:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/15	03:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	03:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	03:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	03:49:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/15	03:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/15	04:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/15	04:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/15	05:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	05:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	05:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	05:29:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/15	05:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/15	05:56:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/15	05:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/15	06:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	06:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	06:05:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	06:06:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/11/15	06:06:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/11/15	06:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/11/15	06:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/11/15	06:08:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/11/15	06:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2015/11/15	06:16:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/15	06:17:55.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	00 3d ca c5 0f	
2015/11/15	06:17:57.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	06:18:00.5	XRT_TCIB_XRT_S_HTR_A_ENA_407_OG [0x197]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/15	10:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2015/11/15	17:54:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/11/15	18:04:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/11/16	07:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/11/16	10:00:00.5	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 3d ca c5 0f	
2015/11/16	17:40:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/11/16	17:50:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/11/17	03:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/11/17	05:55:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 c1 cb 00 00	
2015/11/17	08:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 4a 4d d4 73	
2015/11/17	11:08:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00	
2015/11/17			AOCU_NM	5	02-76	00 00 00 00 00	