

XRT Timeline to be uploaded on 2015/11/17

Period: 2015/11/17 10:58:00 - 2015/11/21 09:33:00

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Normal mode

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XOB #1AF1: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Al/Poly(1443ms) - w leak image-3ms												
Term		Pointing (x, y)					Comment					
11/18 12:23:00 - 11/18 12:29:54		Fixed (-528.4, -528.4)					# XRT post bake-out quadrant pointings 1/4					
PROG= 16 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 86 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 55 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AF2: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms												
Term		Pointing (x, y)					Comment					
11/18 12:33:00 - 11/18 12:39:54		Fixed (528.4, -528.4)					# 2/4					
PROG= 06 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 15 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 55 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AF3: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms												
Term		Pointing (x, y)					Comment					
11/18 12:43:00 - 11/18 12:49:54		Fixed (528.4, 528.4)					# 3/4					
PROG= 09 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 35 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 55 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 54 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AF4: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms												
Term		Pointing (x, y)					Comment					
11/18 12:53:00 - 11/18 12:59:54		Fixed (-528.4, 528.4)					# 4/4					
PROG= 17 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 3 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												

Seqn= 55	2-time(s)	2.0sec																		
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Subr= 3	2-time(s)	2.0sec																		
Seqn= 54	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec							
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval							

XOB #1ADF: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Med-Al(40

Term	Pointing (x, y)	Comment																	
11/18 13:03:00 - 11/18 13:09:54	Fixed (0.0, 0.0)	# XRT synoptic																	
PROG= 02	1-time(s)																		
Subr= 1	1-time(s)	2.0sec																	
Seqn= 5	1-time(s)	2.0sec																	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec						
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec						
Seqn= 33	1-time(s)	2.0sec																	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
Seqn= 49	1-time(s)	2.0sec																	
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
Seqn= 77	1-time(s)	2.0sec																	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
Seqn= 54	1-time(s)	4.0sec																	
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec						
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
Subr= 2	1-time(s)	2.0sec																	
Seqn= 46	2-time(s)	2.0sec																	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
Seqn= 72	2-time(s)	2.0sec																	
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
Seqn= 59	2-time(s)	2.0sec																	
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec						
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval						

XOB #1AB8: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 80s cad

Term	Pointing (x, y)	Comment																
11/18 13:14:32 - 11/18 17:37:00	Track (-64.2, 147.7) @ 11/18 13:10:00	# AR 12454																
11/19 06:00:00 - 11/19 09:35:00	Track (90.3, 149.2) @ 11/19 05:57:00	# AR 12454																
PROG= 01	Inf.-time(s)																	
Subr= 1	45-time(s)	80.0sec																
Seqn= 29	1-time(s)	4.0sec																
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec					
Subr= 2	1-time(s)	2.0sec																
Seqn= 26	1-time(s)	2.0sec																
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec					
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec					
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval					

XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + TH

Term	Pointing (x, y)	Comment											
11/18 18:03:30 - 11/18 18:10:24	Fixed (0.0, 0.0)	synoptic, shifted 0.5 min											
11/19 05:50:00 - 11/19 05:56:54	Fixed (0.0, 0.0)	synoptic, shifted -13.0 min											
PROG= 05	1-time(s)												
Subr= 1	1-time(s)	2.0sec											
Seqn= 5	1-time(s)	2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49	1-time(s)	2.0sec											

	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 39		1-time(s)	2.0sec										
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54		1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AFB: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment											
11/18 18:13:30 - 11/19 05:46:54	Track (-18.1, 148.1) @ 11/18 18:10:30	# AR 12454 fan loops											
PROG= 12 Inf-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 56 1-time(s) 2.0sec													
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec											
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec											
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec											
Subr= 2 4-time(s) 2.0sec													
Seqn= 75 1-time(s) 2.0sec													
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec											
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec											
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec											
Seqn= 24 4-time(s) 240.0sec													
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec											
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 75.0sec											
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec											
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 75.0sec											
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec											
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec											
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Flare mode** * * * * *

XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment											
11/18 13:14:32 - 11/18 17:37:00	Track (-64.2, 147.7) @ 11/18 13:10:00	# AR 12454											
11/18 18:13:30 - 11/19 05:46:54	Track (-18.1, 148.1) @ 11/18 18:10:30	# AR 12454 fan loops											
11/19 06:00:00 - 11/19 09:35:00	Track (90.3, 149.2) @ 11/19 05:57:00	# AR 12454											
PROG= 07 30-time(s)													
Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec											
Seqn=100 1-time(s) 10.0sec													
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec											
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec											
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec											
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec											
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec											
Seqn= 11 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec											
Seqn= 84 1-time(s) 2.0sec													
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec											
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec											
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec											
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec											
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Active Region Search** * * * * *

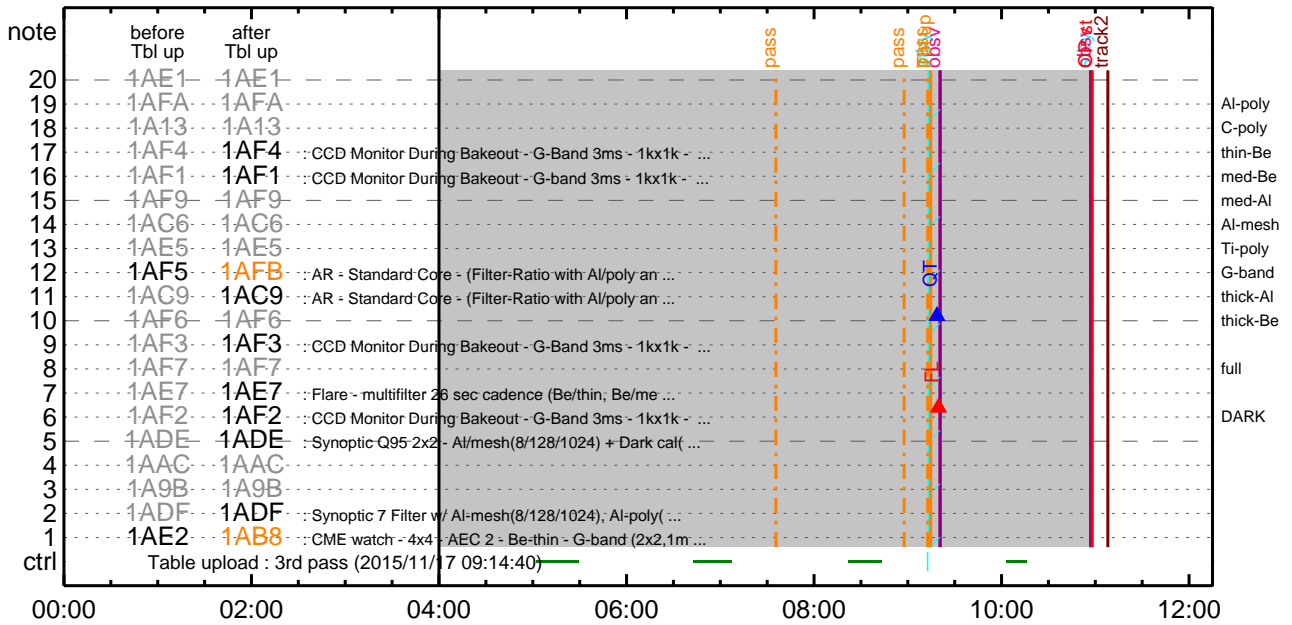
NOT USED

* * * * * **Flare Detection** * * * * *

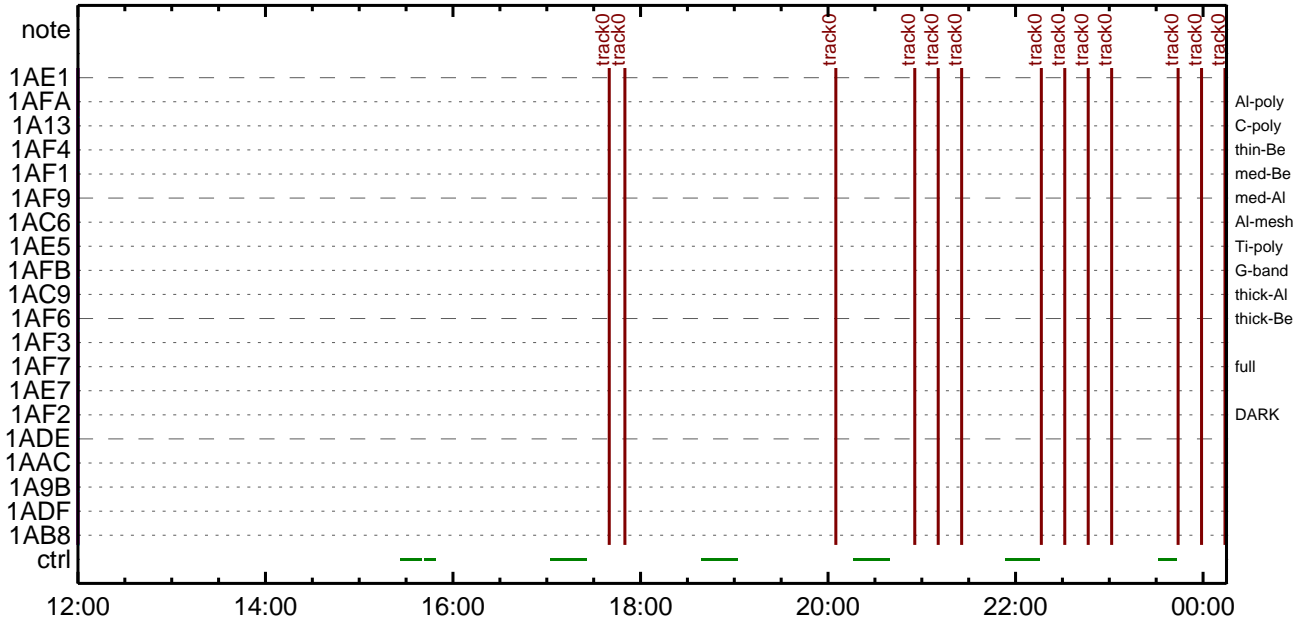
FLD Patrol

Term	Pointing (x, y)	Comment											
11/18 13:14:18 - 11/18 18:00:48	Track (-64.2, 147.7) @ 11/18 13:10:00	# AR 12454											
11/18 18:10:48 - 11/19 05:47:18	Track (-18.1, 148.1) @ 11/18 18:10:30	# AR 12454 fan loops											
11/19 05:57:18 - 11/21 09:33:00	Track (90.3, 149.2) @ 11/19 05:57:00	# AR 12454											
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec											
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

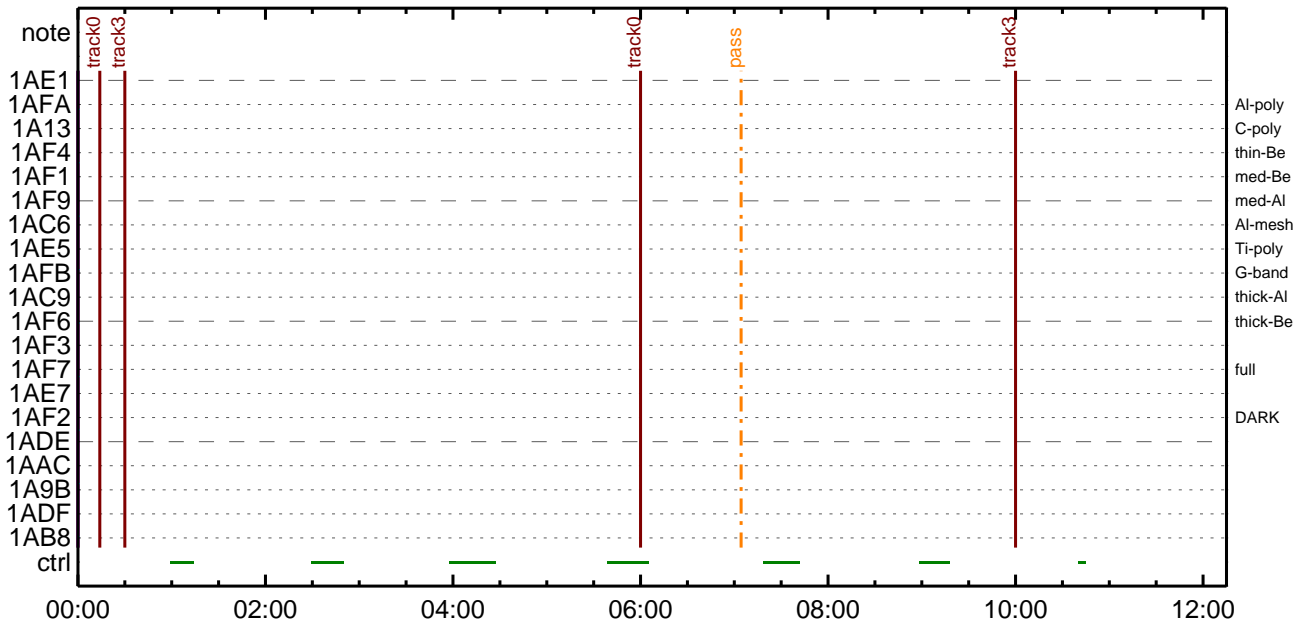
CMDI #0527 2015/11/17



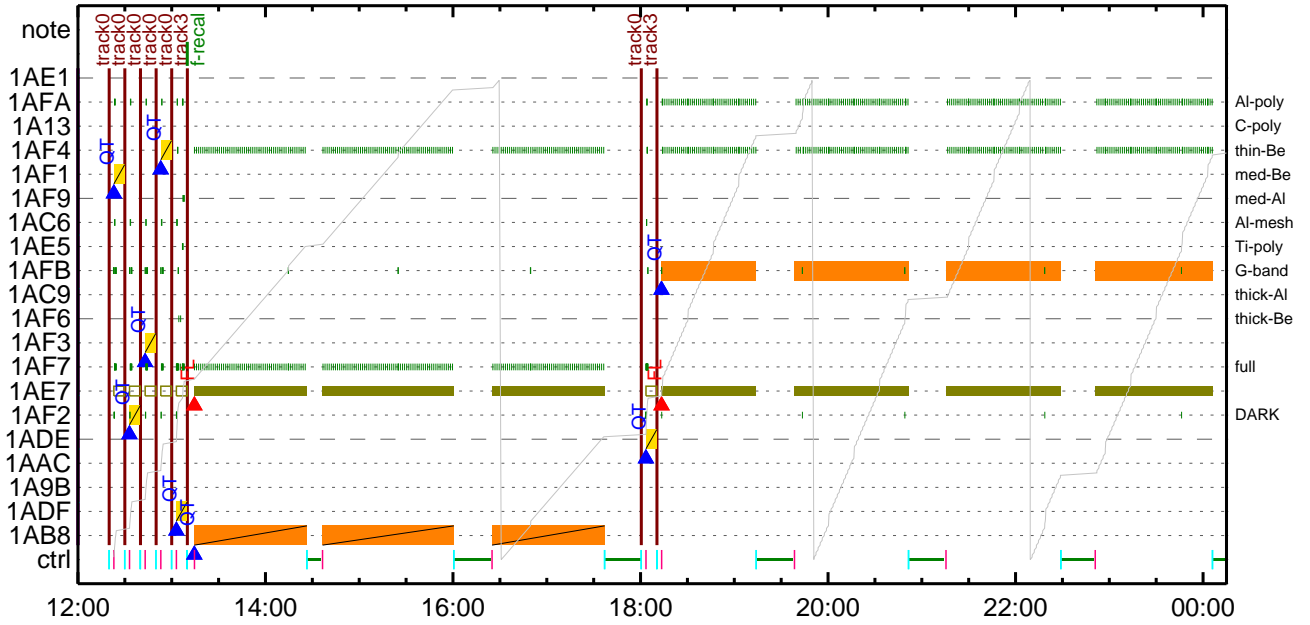
CMDI #0527 2015/11/17



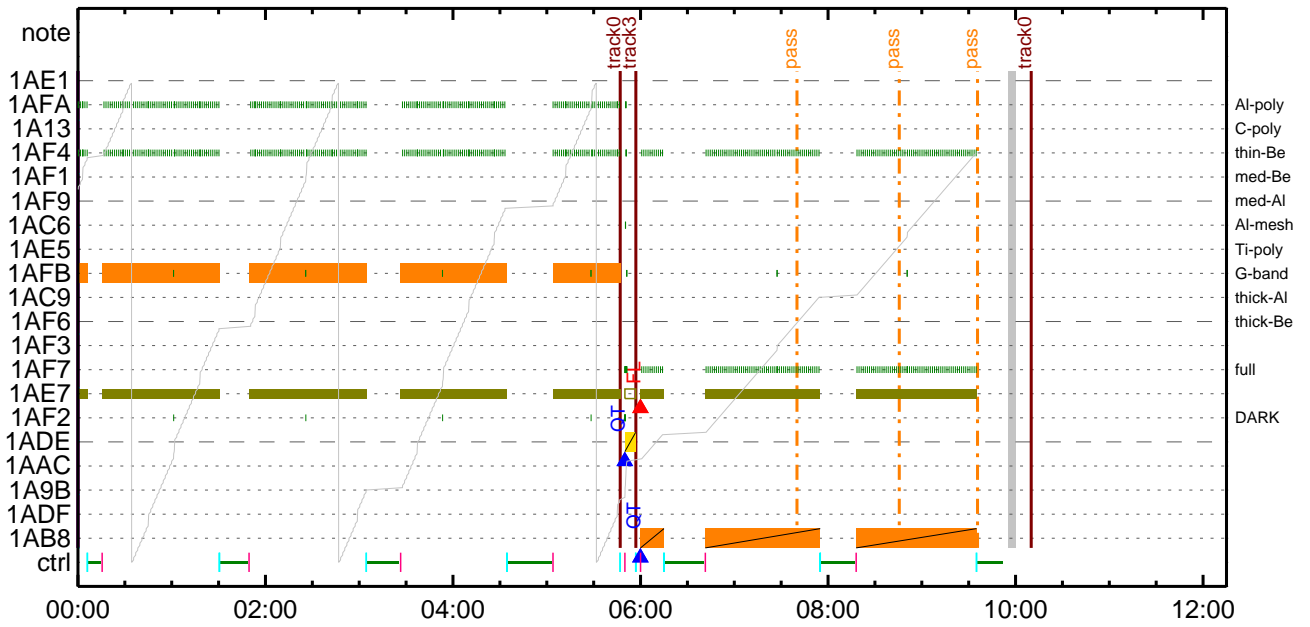
CMDI #0527 2015/11/18



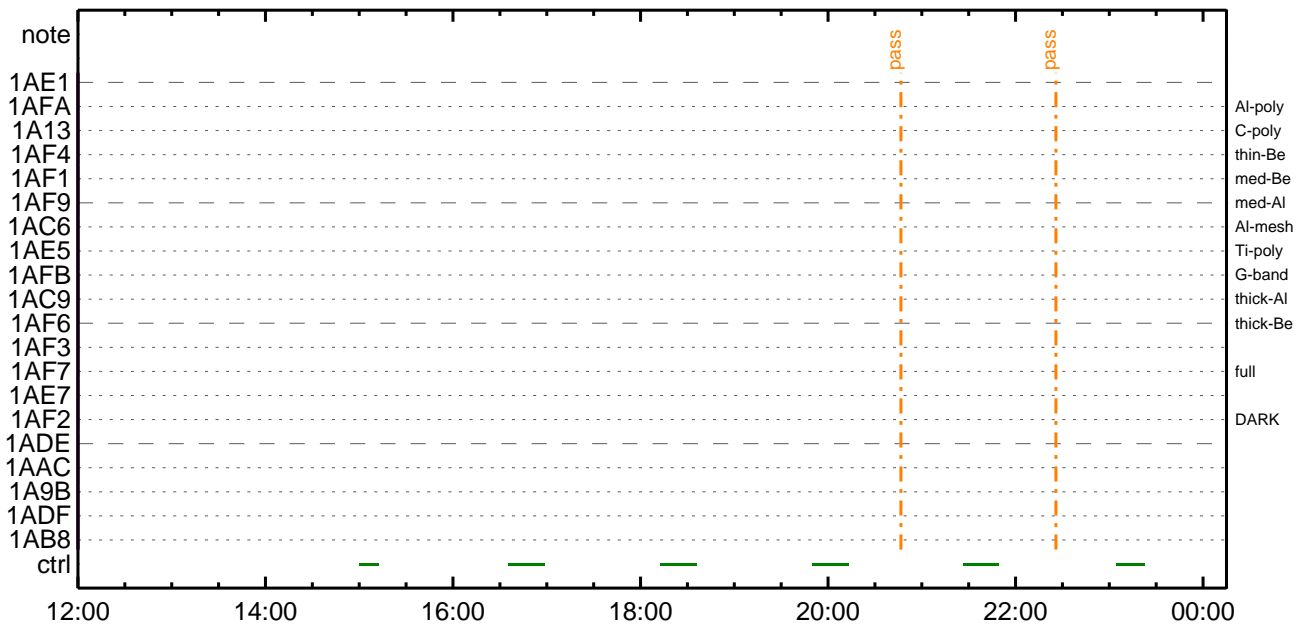
CMDI #0527 2015/11/18



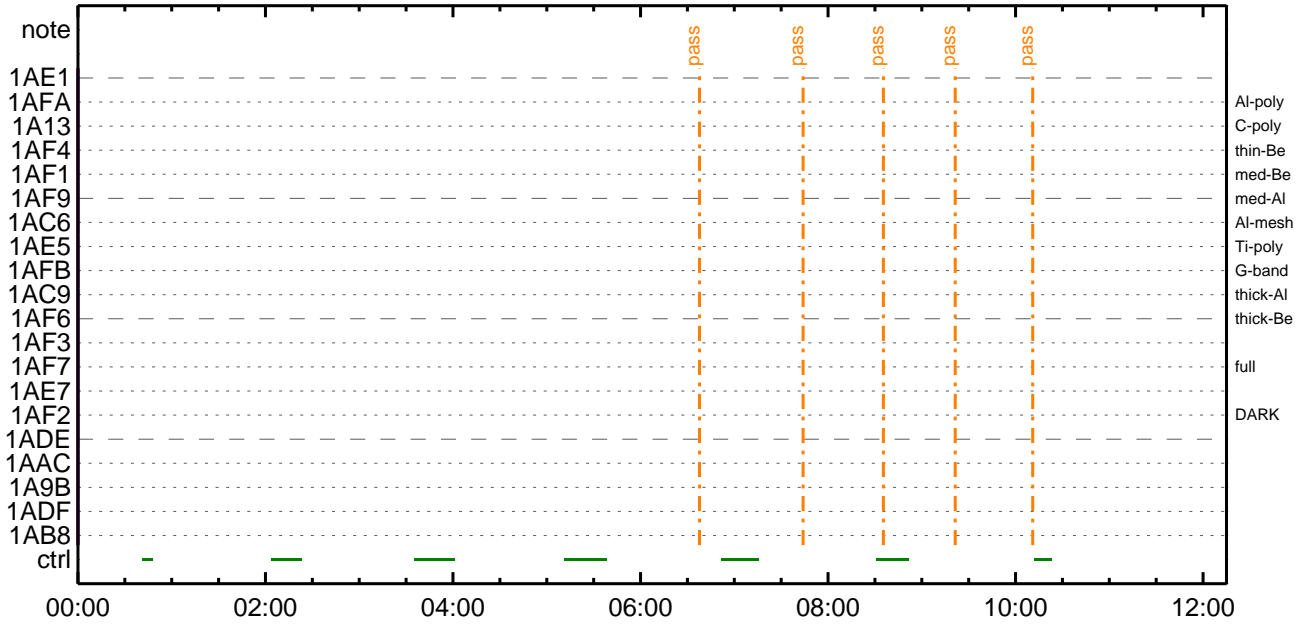
CMDI #0527 2015/11/19



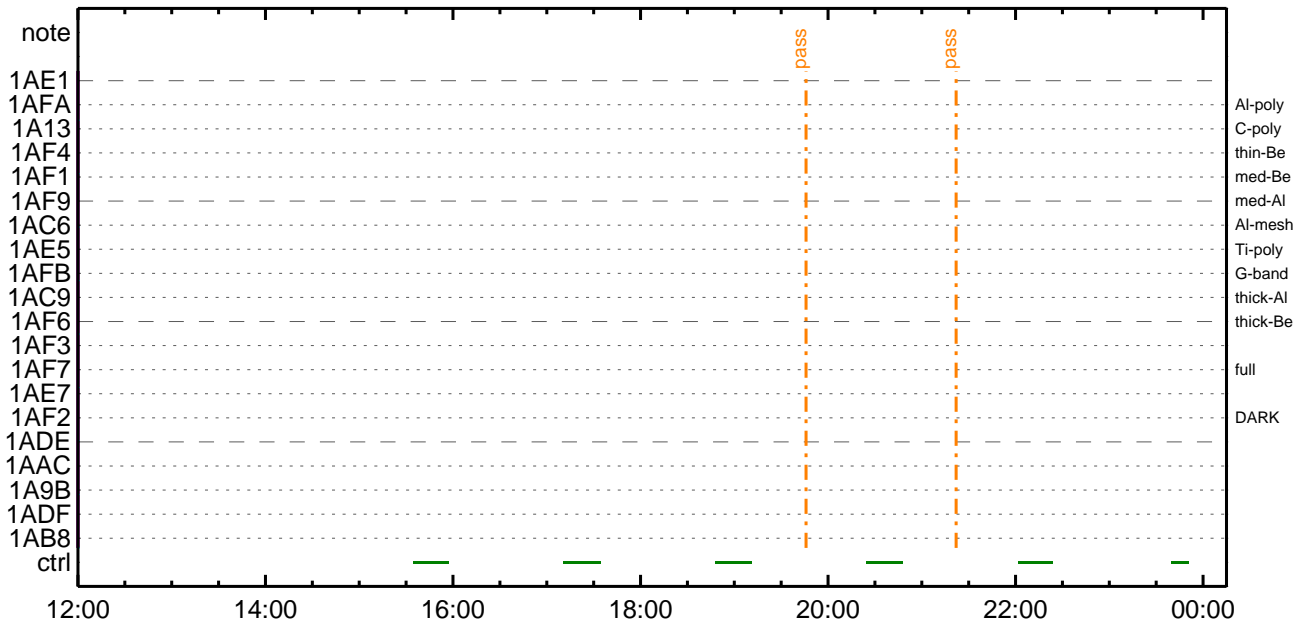
CMDI #0527 2015/11/19



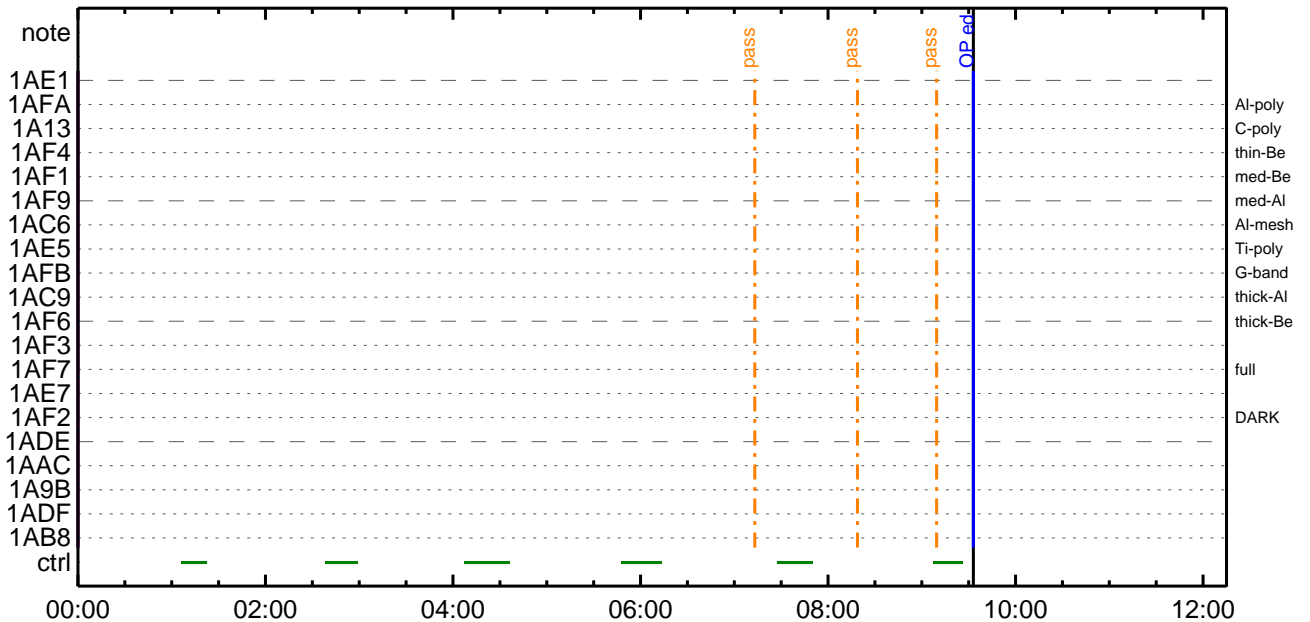
CMDI #0527 2015/11/20



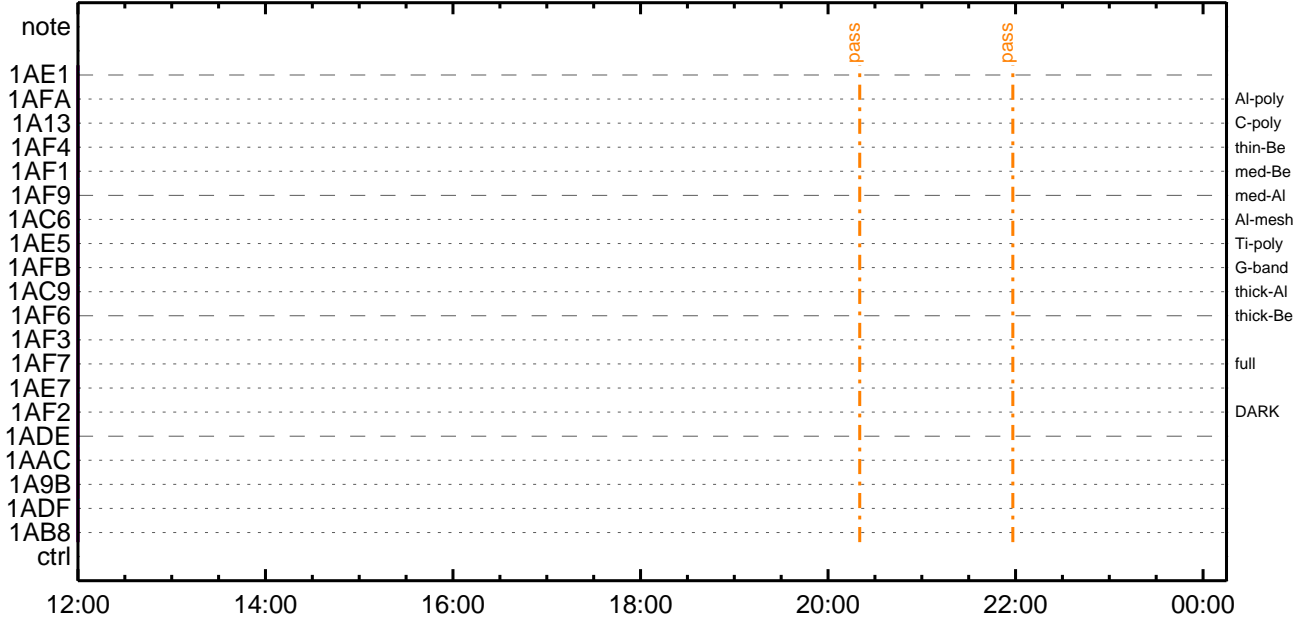
CMDI #0527 2015/11/20



CMDI #0527 2015/11/21



CMDI #0527 2015/11/21




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0096 C.
0097 C.
0098 C.
0099 C. *****
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;¼YÉ;ä
0103 S. OP op-493:OP
0104 ( )
0105 S. OG og-493:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPíî°èYÄYóYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÄYóYx½ªî»ðð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGñî¼È¹ç•è²îOKñð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÄYóYx½ªî»ðð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGñî¼È¹ç•è²îOKñð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÄYóYx½ªî»ðð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPñî¼È¹ç•è²îOKñð³îÇ§
0165 C.
0166 C. ***** òÈ²¼ñî¼Ä´¶ÁºñÈÈ-ñºÁ÷¿@ (¼áµ-YÄYóYx½ê¼çñðÄÓÄæñç¼ªªñºðè¼i¹çñçñâ) *****
0167 C. DHUYâ;¼YÉ;È¼Y½;Yî;¼YÉ;ÈñðîÄñ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADñ-Á÷¿@NGñî¼î¹ç;çºÈ²¼ñî¼TI-CMDÁ÷¿@ñî¼Ä¹ÔñºñÈñññ³ñÈ;f
0180 C. ñðñ¿;çSETñÈDUMPñî¼±ºñYñ¹ñç¹Ôñ|ñ³ñÈ;f
0181 C.
0182 C. TIY³YþYóYÉñððÄî¿¿(UT)
0183 +. TI 2015-11-17 10:53:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-11-17 10:53:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-11-17 10:53:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2015-11-17 10:57:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïÄë%îíñòîî¥Á¥§¥Á¥-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»ò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî¼È¹ç•è²îOKò³îç§
0229 C.
0230 C. DHU¥â;¼¥È;È¼¥½. ¥î;¼¥È;Èòðîäò¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2015-11-17 10:57:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2015-11-17 10:57:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2015-11-17 10:57:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2015-11-17 10:57:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîî»ö¼ÝòÈÄò¹òèDCBC•x²è *****
0276 C. (¼á°î¥Ö¥Á¥È¥Ë¥Ë¥á¥ç¥èòÈ¼òò¼Á»Ûò¹òè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñòÈ´òò¹òèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥-¼Á»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-261:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Upload DPL table >
0120 C.
0121 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°ªÈSTS_CHKªðOFFªÈª¹ªë
0122 C.
0123 . S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 . C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHKªðONªÈª¹ªë
0134 C.
0135 . C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700[ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 . C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2015-11-17 10:57:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 . C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 + DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 . C. ----- Success Verify ? OK / NG_____
0162 C.
0163 C. XRT Obs. Table Upload
0164 . S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 . C. ----- Comparison Check ? OK / ERR _____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 06 85 83 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 07 c0 c0 10 10)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 08 80 80 20 20)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 09 40 c0 10 10)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 0a 40 40 10 10)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0b c0 40 10 10)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0c 80 80 20 08)

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0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 0d 80 80 08 20)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 0f 80 80 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 10 80 80 08 08)
0200 + DC 07-F0 MDP_XRT_FLD_ENA
0201 BC (d8)
0202 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0203 BC (c8)
0204 + DC 07-F0 MDP_XRT_ARS_DIS
0205 BC (d5)
0206 + DC 07-F0 MDP_XRT_QT_PROG_SET
0207 BC (c4 0b)
0208 + DC 07-F0 MDP_XRT_FL_PROG_SET
0209 BC (c5 07)
0210 . C. ----- Success Verify ? OK / NG ____
0211 C.
0212 C.
0213 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0214 C.
0215 + DC 07-F0 MDP_XRT_MODE_OBSV
0216 BC (c2)
0217 + TI 2015-11-17 10:57:02.0
0218 DC 07-F0 MDP_XRT_MODE_OBSV
0219 BC (c2)
0220 . C. ----- Success Verify ? OK / NG ____
0221 C.
0222 C. ***** XRT END *****
0223 C.
0224 . C. ***** MDP `úÃîñî»ö%ÝñÊÃð¹ñèDCBC•x²è *****
0225 C. (%á°îÿÓÿÃÿÈÿPÿËÿáÿçÿèñÊ%¼ñ¼Ã»Ûñ¹ñè)
0226 . S. DC-BC dcbc-402:DCBC
0227 (MDP_known_event)
0228 C.
0229 C.
0230 . C. ***** ÿDÿ¹•Ï Daily±¿îññè´Øñ¹ñèDCBC•x²è *****
0231 . S. DC-BC dcbc-153:DCBC
0232 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0233 C.
0234 C.
0235 . C. ¡ãLOSÿÁÿ§ÿÃÿ-¼Ã»Û;ã
0236 C.
0237 . C. ***** LOS *****
0238 C.
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Nov 17, 15 12:05

XRT_OGLIST_0527.chk

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*** OP Sequence for XRT ***

2015/11/17	11:08:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2015/11/17	17:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/11/17	17:50:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	4f	a5	24	8b
2015/11/17	20:05:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	16	66	52	7e
2015/11/17	20:55:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	1b	1a	41	db
2015/11/17	21:10:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	1b	ce	31	99
2015/11/17	21:25:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	1c	41	20	fd
2015/11/17	22:16:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	1c	8b	10	00
2015/11/17	22:31:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	1c	9b	fe	a8
2015/11/17	22:46:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	1c	8b	f2	da
2015/11/17	23:01:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	1c	41	e1	dc
2015/11/17	23:44:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	1b	ce	d1	40
2015/11/17	23:59:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	1b	1a	c0	fe
2015/11/18	00:14:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	16	66	b0	5b
2015/11/18	00:30:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	03	00	00	00	00
2015/11/18	06:00:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	d0	e6	bc	73
2015/11/18	06:18:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_417_OG [0x1a1]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2015/11/18	10:00:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	03	00	00	00	00
2015/11/18	12:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	12:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	12:19:58.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/11/18	12:20:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2015/11/18	12:20:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/18	12:20:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/18	12:22:56.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/18	12:22:58.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2015/11/18	12:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	12:29:58.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/11/18	12:30:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2015/11/18	12:30:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/18	12:30:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/18	12:32:56.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/18	12:32:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2015/11/18	12:33:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	12:39:58.0	XRT_FOCUS_POSITION_420_OG [0x1a4]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/11/18	12:40:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2015/11/18	12:40:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/18	12:40:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/18	12:42:56.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/18	12:42:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2015/11/18	12:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	12:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							

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2015/11/18	12:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	12:49:58.0	XRT_FOCUS_POSITION_420_OG [0x1a4]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	12:50:00.0	AOCS_Ore-point_Start_20_OG [0x0aa]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/11/18	12:50:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00	d1	07	2e	f9
2015/11/18	12:50:20.0	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/18	12:52:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/18	12:52:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/18	12:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2015/11/18	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	12:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	13:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/11/18	13:00:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00	00	00	00	00
2015/11/18	13:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/18	13:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/18	13:02:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/18	13:03:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2015/11/18	13:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	13:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	13:09:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	13:10:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]	XRT_FOCUS_RECAL	2	07-F8	78	00			
2015/11/18	13:13:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCU_NM	5	02-76	03	00	00	00	00
2015/11/18	13:14:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/11/18	13:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/18	13:14:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/18	13:14:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/18	13:14:26.0	XRT_FLD_RESET_437_OG [0x1b5]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/18	13:14:28.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/18	13:14:30.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2015/11/18	13:14:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/11/18	14:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	14:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	14:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	14:26:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/18	14:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/18	14:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/18	14:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2015/11/18	16:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	16:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	16:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	16:00:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/18	16:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/18	16:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/18	16:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]							
2015/11/18	17:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	17:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	17:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_FLD_RESET	1	07-F0	da				

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2015/11/18	17:37:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/18	17:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/18	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	18:00:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	18:00:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/11/18	18:00:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/11/18	18:00:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/18	18:03:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/18	18:03:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/18	18:03:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/11/18	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	18:10:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	18:10:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/11/18	18:10:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/11/18	18:10:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/18	18:10:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/18	18:10:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/18	18:10:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/18	18:10:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/18	18:13:26.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2015/11/18	18:13:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/11/18	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	19:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	19:14:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	19:14:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/18	19:14:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/18	19:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/18	19:37:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/18	19:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	20:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	20:51:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	20:51:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/18	20:51:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/18	20:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/18	21:14:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/18	21:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/18	22:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	22:29:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/18	22:29:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/18	22:29:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/18	22:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/18	22:50:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/18	22:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/19	00:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	00:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	00:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/19	00:06:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2015/11/19	00:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/19	00:14:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/19	00:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/19	01:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	01:30:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	01:30:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/19	01:30:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/19	01:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/19	01:48:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/19	01:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/19	03:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	03:04:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	03:04:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/19	03:04:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/19	03:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/19	03:25:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/19	03:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/19	04:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	04:34:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	04:34:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/19	04:34:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/19	04:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/19	05:03:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/19	05:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/19	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	05:46:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	05:46:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/11/19	05:47:00.5	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/11/19	05:47:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/19	05:49:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/19	05:49:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/19	05:49:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/11/19	05:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/19	05:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	05:56:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	05:56:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/11/19	05:57:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/11/19	05:57:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/19	05:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/19	05:57:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/19	05:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/19	05:57:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/19	05:59:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2015/11/19	05:59:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/11/19	06:00:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/19	06:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	06:15:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/19	06:15:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/19	06:15:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							

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2015/11/19	06:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/19	06:40:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/19	06:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	07:55:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	07:55:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	07:55:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	07:55:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/19	07:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/19	08:17:00.0	XRT_Custom_430_OG [0x1ae]				
2015/11/19	08:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	09:35:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	09:35:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	09:35:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	09:35:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/19	09:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/19	10:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]				
			AOCU_NM	5	02-76	00 00 00 00 00