

XRT Timeline to be uploaded on 2015/11/19

Period: 2015/11/19 10:00:00 - 2015/11/24 09:42:00

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Normal mode

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XOB #1ADA: CME watch - 4x4 - AEC 2/3 - Be-thin (Be-Med) - Long/Short Pairs - 300s cad													
Term		Pointing (x, y)				Comment							
11/19 10:13:00 - 11/19 11:11:54		Fixed (-971.0, 0.0)				# OP start + 10min EIS calibration studies							
11/19 11:15:00 - 11/19 13:22:54		Track (-189.5, -96.8) @ 11/19 11:12:00				QS near disk center							
11/19 15:16:30 - 11/19 15:38:24		Fixed (-16.0, -971.0)				HOP79 1/20							
11/19 15:41:30 - 11/19 16:03:24		Fixed (-16.0, -871.0)				2/20							
11/19 16:06:30 - 11/19 16:35:30		Fixed (-16.0, -771.0)				3/20							
11/19 17:03:00 - 11/19 17:24:54		Fixed (-16.0, -671.0)				4/20							
11/19 17:28:00 - 11/19 17:48:54		Fixed (-16.0, -571.0)				5/20							
11/19 18:02:00 - 11/19 19:01:54		Fixed (-16.0, -471.0)				6/20							
11/19 19:05:00 - 11/19 19:49:30		Fixed (-16.0, -371.0)				7/20							
11/19 20:17:00 - 11/19 20:38:54		Fixed (-16.0, -271.0)				8/20							
11/19 20:42:00 - 11/19 21:26:30		Fixed (-16.0, -171.0)				9/20							
11/19 21:53:30 - 11/19 22:15:24		Fixed (-16.0, -71.0)				10/20							
11/19 22:18:30 - 11/19 23:04:00		Fixed (-16.0, 19.0)				11/20							
11/19 23:26:30 - 11/19 23:48:24		Fixed (-16.0, 119.0)				12/20							
11/19 23:51:30 - 11/20 00:13:24		Fixed (-16.0, 219.0)				13/20							
11/20 00:16:30 - 11/20 00:41:00		Fixed (-16.0, 319.0)				14/20							
11/20 00:52:00 - 11/20 01:13:54		Fixed (-16.0, 419.0)				15/20							
11/20 01:17:00 - 11/20 02:04:00		Fixed (-16.0, 519.0)				16/20							
11/20 02:27:30 - 11/20 02:49:24		Fixed (-16.0, 619.0)				17/20							
11/20 02:52:30 - 11/20 03:35:00		Fixed (-16.0, 719.0)				18/20							
11/20 04:05:00 - 11/20 04:26:54		Fixed (-16.0, 819.0)				19/20							
11/20 04:30:00 - 11/20 04:51:54		Fixed (-16.0, 919.0)				20/20							
PROG= 10 Inf.-time(s)													
└ Subr= 1 12-time(s) 300.0sec													
└ Seqn= 12 1-time(s) 4.0sec													
└ thin-Be/Open		med-Be/Open		close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2 0 2.0sec
└ thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term		Pointing (x, y)				Comment									
11/19 13:26:00 - 11/19 15:00:00		Track (112.0, 32.3) @ 11/19 13:23:00				AR 12454									
PROG= 18 Inf.-time(s)															
Subr= 1		1-time(s)		2.0sec											
Seqn= 56		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0 0 2.0sec		
Open/G-band		Open/G-band		close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0 0 2.0sec		
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0 0 2.0sec		
Subr= 2		5-time(s)		2.0sec											
Seqn= 75		1-time(s)		2.0sec											
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2 0 2.0sec		
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3 0 2.0sec		
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2 0 2.0sec		
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3 0 2.0sec		
Seqn= 96		4-time(s)		60.0sec											
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1 0 2.0sec		
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1 0 15.0sec		
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1 1 2.0sec		
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1 1 15.0sec		
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1 2 2.0sec		
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1 2 2.0sec		
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval		

XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + TH

Term		Pointing (x, y)				Comment									
11/19 17:52:00 - 11/19 17:58:54		Fixed (0.0, 0.0)				synoptic, shifted -11.0 min									
11/20 06:03:00 - 11/20 06:09:54		Fixed (0.0, 0.0)				synoptic									
11/20 18:08:30 - 11/20 18:15:24		Fixed (0.0, 0.0)				synoptic, shifted 5.5 min									
11/21 06:26:30 - 11/21 06:33:24		Fixed (0.0, 0.0)				synoptic, shifted 23.5 min									
PROG= 05 1-time(s)															
└ Subr= 1 1-time(s) 2.0sec															
└└ Seqn= 5 1-time(s) 2.0sec															
└└└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0 0	2.0sec		
└└└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0 0	2.0sec		
└└└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0 0	2.0sec		
└└└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0 0	2.0sec		
└└└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0 0	2.0sec		
└└ Seqn= 33 1-time(s) 2.0sec															
└└└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0	2.0sec		
└└└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0	2.0sec		

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 39 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term			Pointing (x, y)					Comment						
11/20 04:55:00 - 11/20 05:59:54			Track (255.1, 34.7) @ 11/20 04:52:00					AR 12454						
PROG= 04 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 56		1-time(s)		2.0sec										
Open/G-band		Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Subr= 2		5-time(s)		2.0sec										
Seqn= 75		1-time(s)		2.0sec										
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2	0	2.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Seqn= 2		4-time(s)		180.0sec										
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	55.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	1	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	1	55.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	2	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	2	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1AFB: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term			Pointing (x, y)					Comment						
11/20 06:13:00 - 11/20 18:05:24			Track (266.9, 35.0) @ 11/20 06:10:00					AR 12454						
PROG= 12 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 56		1-time(s)		2.0sec										
Open/G-band		Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Subr= 2		4-time(s)		2.0sec										
Seqn= 75		1-time(s)		2.0sec										
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2	0	2.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Seqn= 24		4-time(s)		240.0sec										
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	75.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	1	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	1	75.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	2	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	2	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1AFC: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms

Term			Pointing (x, y)						Comment							
11/20 18:18:30 - 11/21 06:23:24			Track (374.4, 37.4) @ 11/20 18:15:30						AR 12454							
PROG= 14 Inf.-time(s)																
Subr= 2		1-time(s)		2.0sec												
Seqn= 56		1-time(s)		2.0sec												
└ Open/G-band		Open/G-band		open		Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
└ Open/G-band		Open/G-band		close		Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al		close		Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Seqn= 42		3-time(s)		2.0sec												
└ Al-poly/Open		thin-Be/Open		close		Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
└ thin-Be/Open		med-Be/Open		close		Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└ Open/thick-Al		Open/thick-Al		close		Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Seqn= 8		20-time(s)		160.0sec												
└ thin-Be/Open		med-Be/Open		close		Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	46.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	47.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AFD: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (3ms/3ms)

Term			Pointing (x, y)						Comment					
11/21 06:36:30 - 11/21 09:07:30			Track (		478.3, 40.3)		@ 11/21 06:33:30		AR 12454					
PROG= 03 Inf-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 56		1-time(s)		2.0sec										
└ Open/G-band		Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
└ Open/G-band		Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Seqn= 42		3-time(s)		2.0sec										
└ Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec
└ Open/thick-Al		Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Seqn= 32		30-time(s)		120.0sec										
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
└ Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	34.0sec
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec
└ Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	34.0sec
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec
└ Al-poly/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)

Term	Pointing (x, y)	Comment
11/19 10:13:00 - 11/19 11:11:54	Fixed (-971.0, 0.0)	# OP start + 10min EIS calibration studies
11/19 11:15:00 - 11/19 13:22:54	Track (-189.5, -96.8) @ 11/19 11:12:00	QS near disk center
11/19 13:26:00 - 11/19 15:00:00	Track (112.0, 32.3) @ 11/19 13:23:00	AR 12454
11/19 15:16:30 - 11/19 15:38:24	Fixed (-16.0, -971.0)	HOP79 1/20
11/19 15:41:30 - 11/19 16:03:24	Fixed (-16.0, -871.0)	2/20
11/19 16:06:30 - 11/19 16:35:30	Fixed (-16.0, -771.0)	3/20
11/19 17:03:00 - 11/19 17:24:54	Fixed (-16.0, -671.0)	4/20
11/19 17:28:00 - 11/19 17:48:54	Fixed (-16.0, -571.0)	5/20
11/19 18:02:00 - 11/19 19:01:54	Fixed (-16.0, -471.0)	6/20
11/19 19:05:00 - 11/19 19:49:30	Fixed (-16.0, -371.0)	7/20
11/19 20:17:00 - 11/19 20:38:54	Fixed (-16.0, -271.0)	8/20
11/19 20:42:00 - 11/19 21:26:30	Fixed (-16.0, -171.0)	9/20
11/19 21:53:30 - 11/19 22:15:24	Fixed (-16.0, -71.0)	10/20
11/19 22:18:30 - 11/19 23:04:00	Fixed (-16.0, 19.0)	11/20
11/19 23:26:30 - 11/19 23:48:24	Fixed (-16.0, 119.0)	12/20
11/19 23:51:30 - 11/20 00:13:24	Fixed (-16.0, 219.0)	13/20
11/20 00:16:30 - 11/20 00:41:00	Fixed (-16.0, 319.0)	14/20
11/20 00:52:00 - 11/20 01:13:54	Fixed (-16.0, 419.0)	15/20
11/20 01:17:00 - 11/20 02:04:00	Fixed (-16.0, 519.0)	16/20
11/20 02:27:30 - 11/20 02:49:24	Fixed (-16.0, 619.0)	17/20
11/20 02:52:30 - 11/20 03:35:00	Fixed (-16.0, 719.0)	18/20
11/20 04:05:00 - 11/20 04:26:54	Fixed (-16.0, 819.0)	19/20
11/20 04:30:00 - 11/20 04:51:54	Fixed (-16.0, 919.0)	20/20
11/20 04:55:00 - 11/20 05:59:54	Track (255.1, 34.7) @ 11/20 04:52:00	AR 12454
11/20 06:13:00 - 11/20 18:05:24	Track (266.9, 35.0) @ 11/20 06:10:00	AR 12454
11/20 18:18:30 - 11/21 06:23:24	Track (374.4, 37.4) @ 11/20 18:15:30	AR 12454
11/21 06:36:30 - 11/21 09:07:30	Track (478.3, 40.3) @ 11/21 06:33:30	AR 12454

PROG= 07 30-time(s)

Subr= 1		20-time(s)	2.0sec									
Seqn= 11		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100		1-time(s)	10.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 10		1-time(s)	2.0sec									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

L

Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

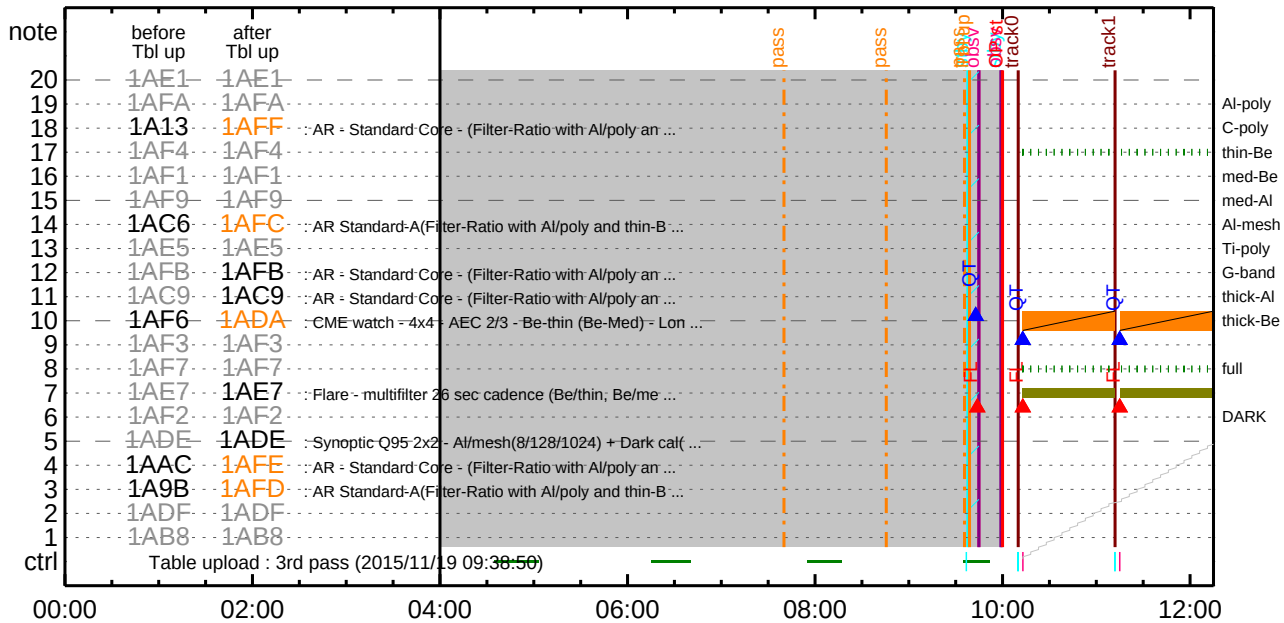
* * * * *

Flare Detection

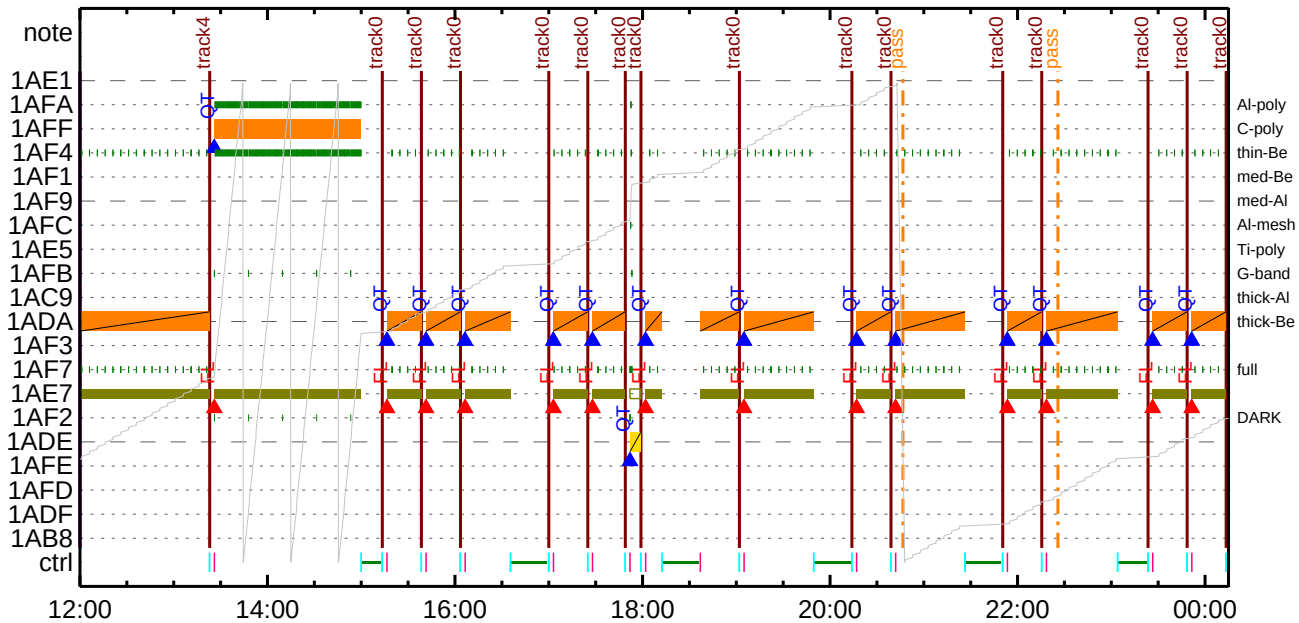
* * * * *

FLD Patrol												
Term			Pointing (x, y)					Comment				
11/19 17:59:18 - 11/20 06:00:18			Fixed (-16.0, -471.0)					6/20				
11/20 06:10:18 - 11/20 18:05:48			Track (266.9, 35.0) @ 11/20 06:10:00					AR 12454				
11/20 18:15:48 - 11/21 06:23:48			Track (374.4, 37.4) @ 11/20 18:15:30					AR 12454				
11/21 06:33:48 - 11/24 09:42:00			Track (478.3, 40.3) @ 11/21 06:33:30					AR 12454				
Open/Ti-poly		Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

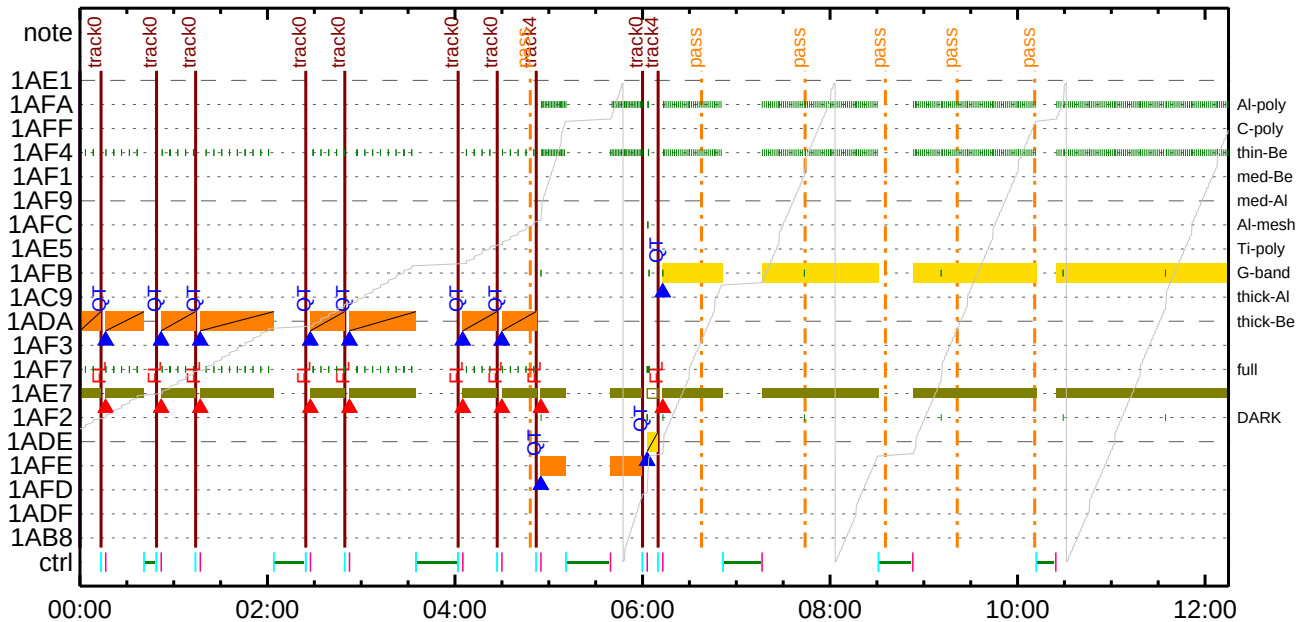
CMDI #0534 2015/11/19



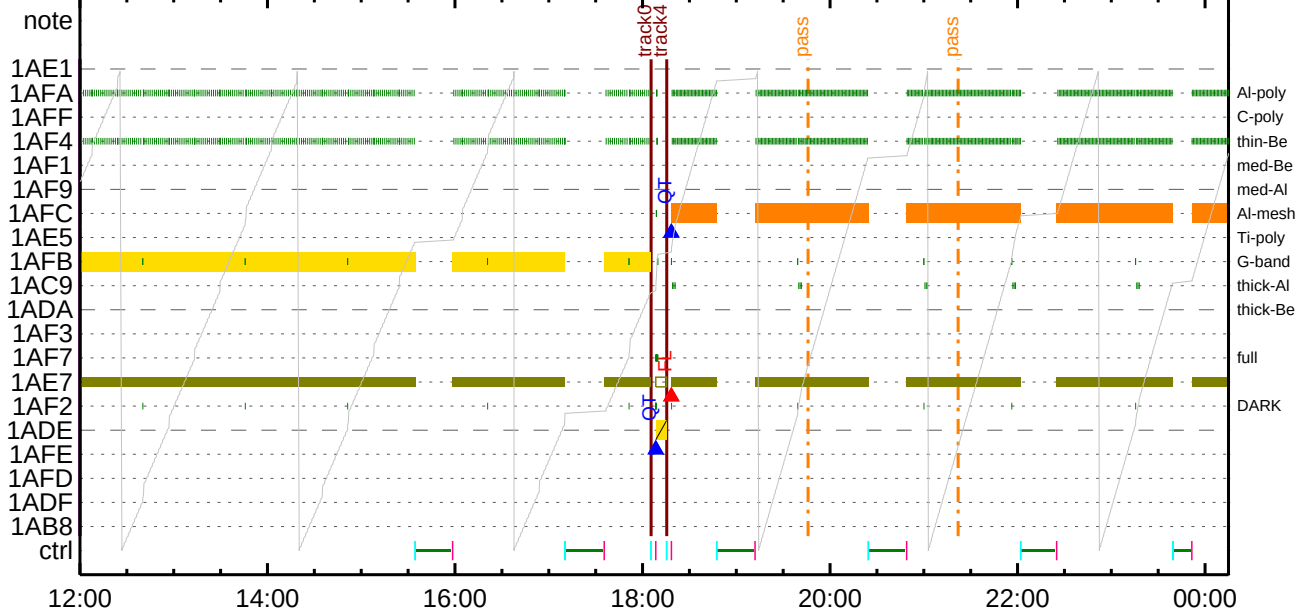
CMDI #0534 2015/11/19



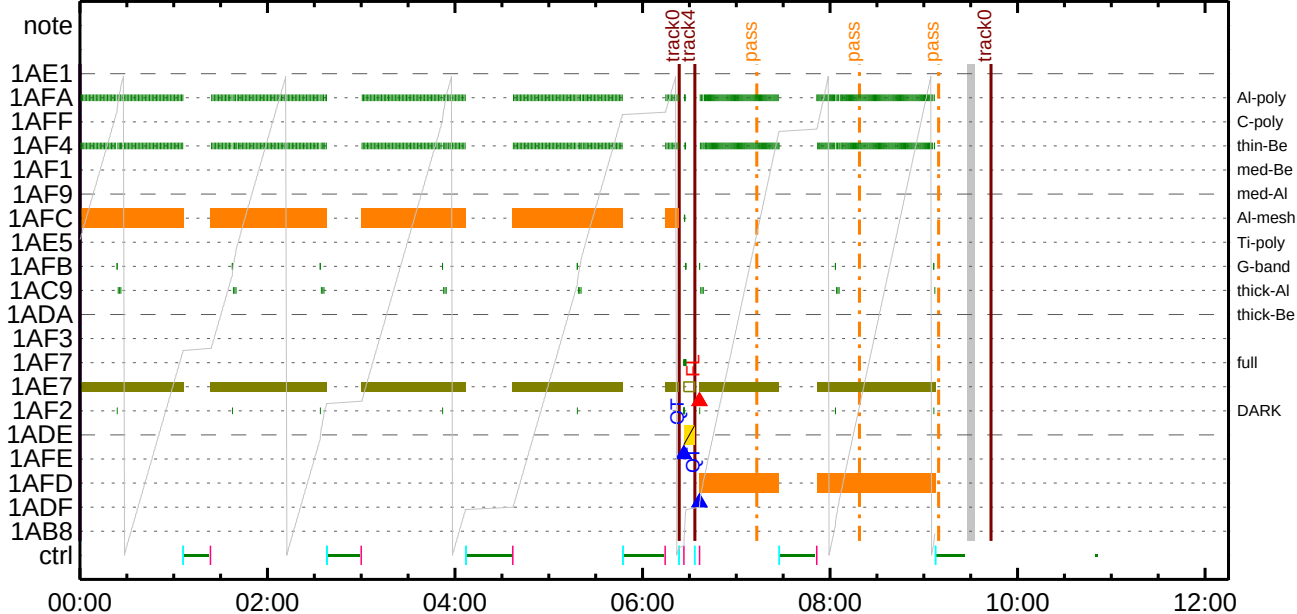
CMDI #0534 2015/11/20



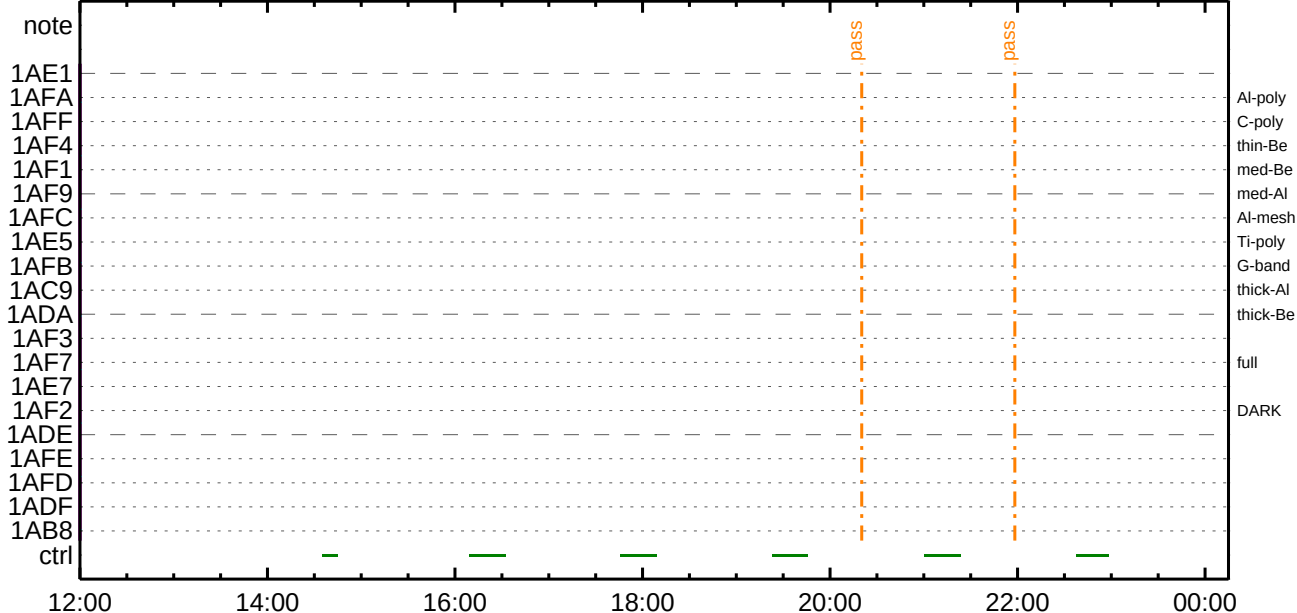
CMDI #0534 2015/11/20



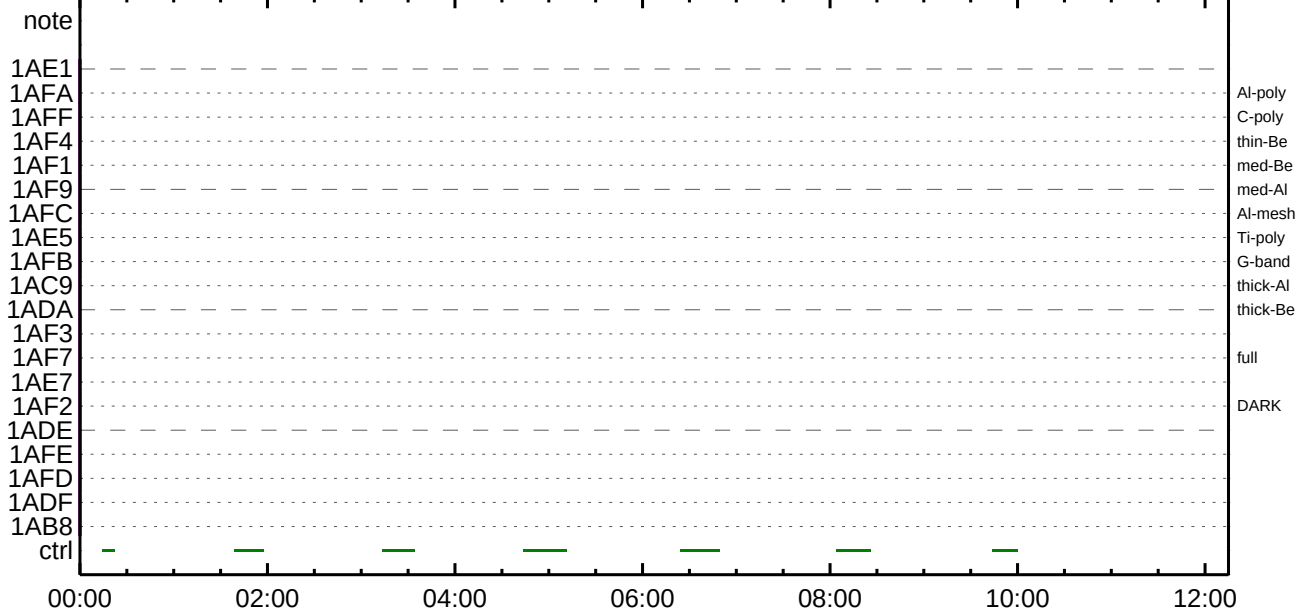
CMDI #0534 2015/11/21



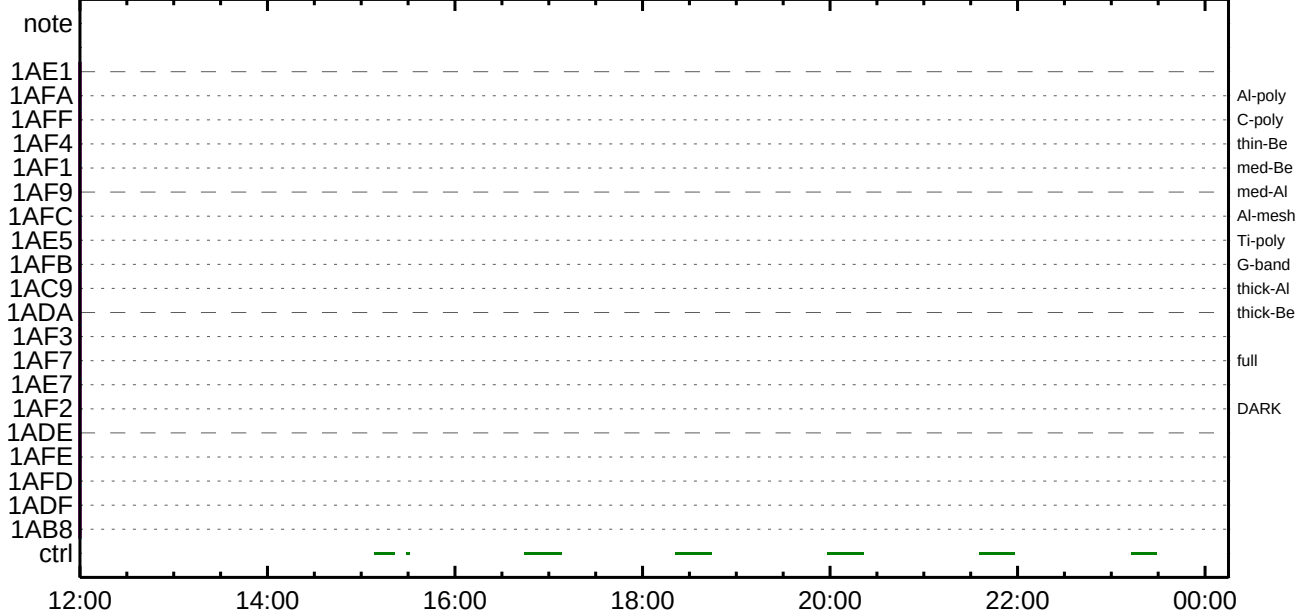
CMDI #0534 2015/11/21



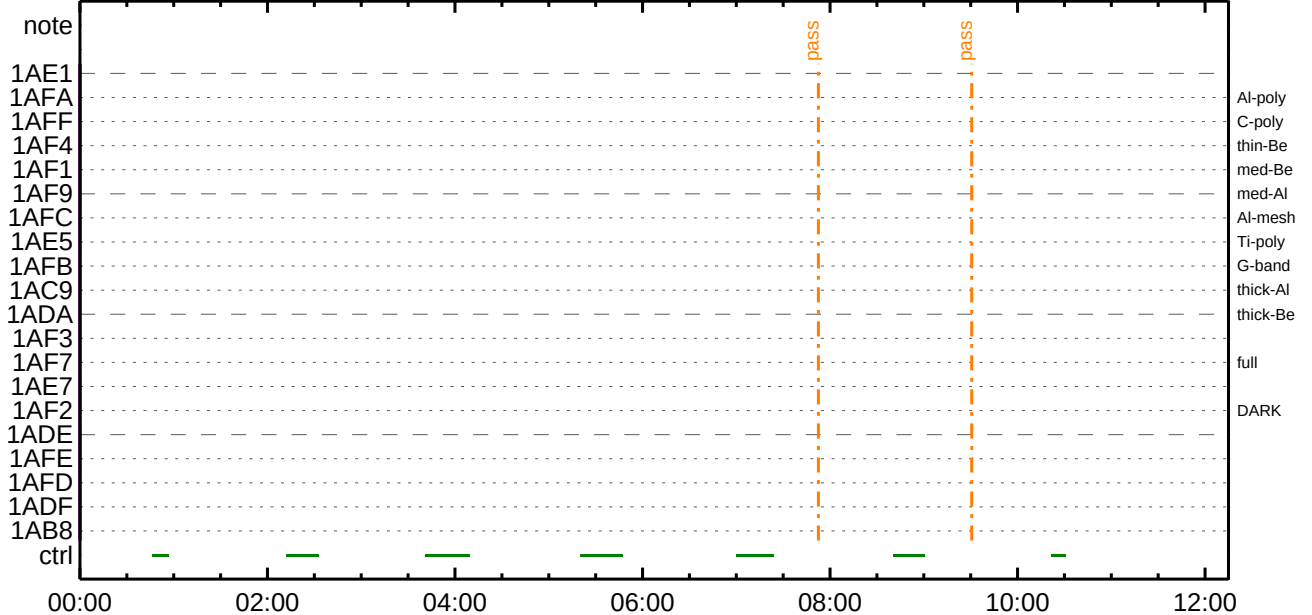
CMDI #0534 2015/11/22



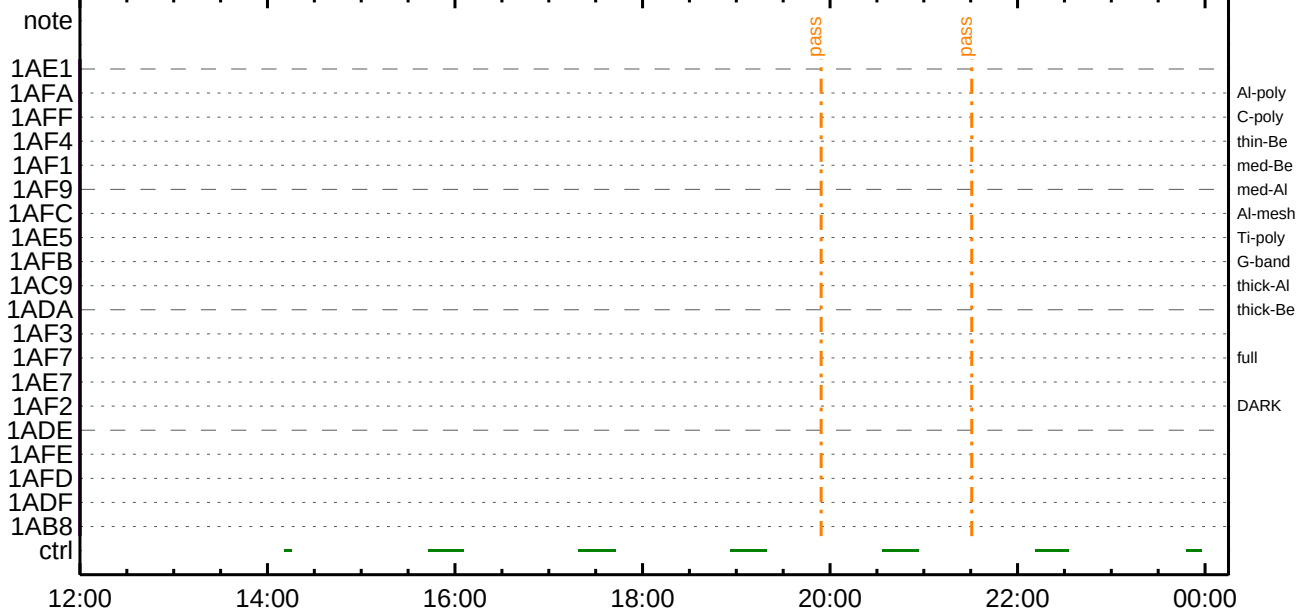
CMDI #0534 2015/11/22



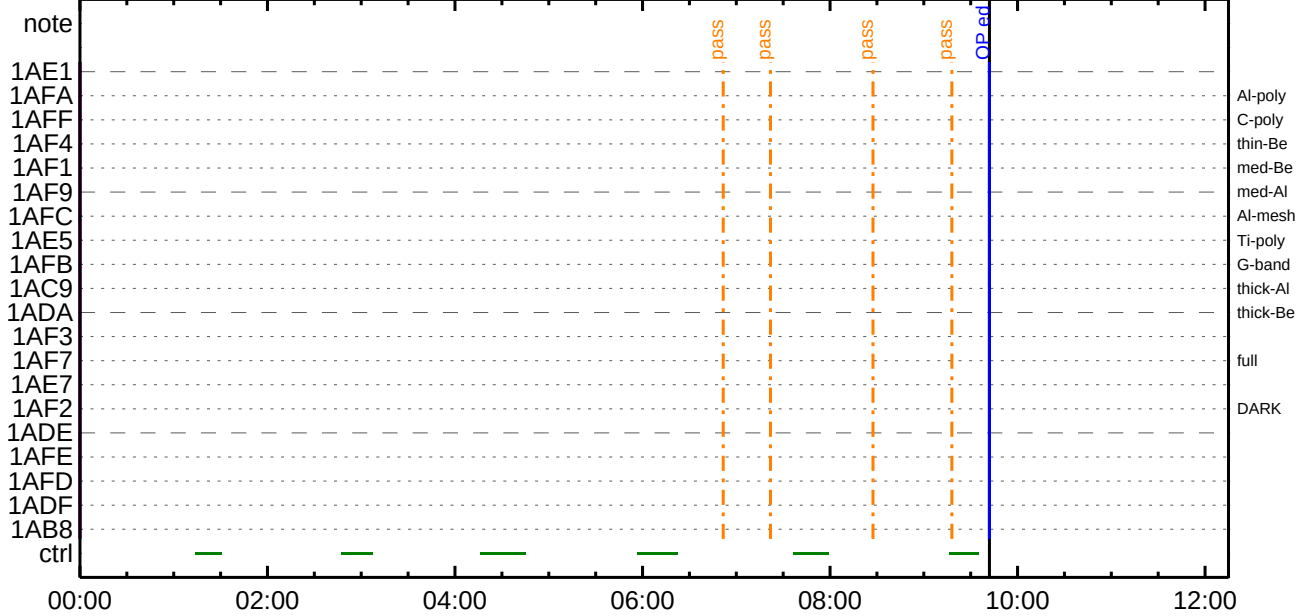
CMDI #0534 2015/11/23



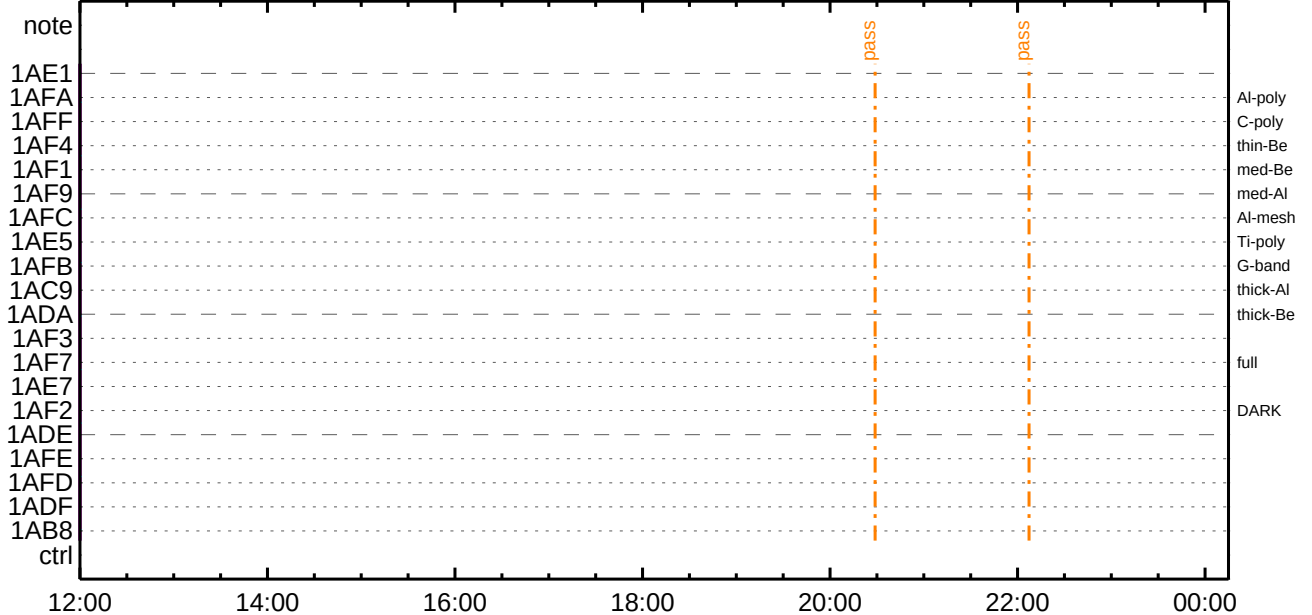
CMDI #0534 2015/11/23



CMDI #0534 2015/11/24



CMDI #0534 2015/11/24



(a) Spacecraft Operation Procedure (real-commands)

```
main-497 2015-11-19 17:57:07 209 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Ä»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äi;Ê¿¿A¤•µ°£»I×AÇ¤I¥C¥A¥×¥i;¼¥ÉjÊÊ¿µ•iîÊjÊ¿¼°Ç¿¤•¤¿¼i¹Ç¤IjÇÀ®, Ü¤¹¤Ê¤¤¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤Èj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OG¥i;¼¥Éj;¼¥Á¥Ó¥×
0016 C. *****
0017 C.
0018 . C. iãOP/OG¥i;¼¥Éjã
0019 . S. OP op-497:OP
0020 ()
0021 . S. OG og-497:OG
0022 ()
0023 C.
0024 . C. iãNMOG&OPîî°è¥Á¥Ó¥×jã
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0036 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ¥À¥Ó¥×½ªi»¤ò³îÇS
0041 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOG¤î¼Ê¹Ç•è²îOK¤ò³îÇS
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0055 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ¥À¥Ó¥×½ªi»¤ò³îÇS
0060 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOG¤î¼Ê¹Ç•è²îOK¤ò³îÇS
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0074 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ¥À¥Ó¥×½ªi»¤ò³îÇS
0079 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OP¤î¼Ê¹Ç•è²îOK¤ò³îÇS
0081 C.
0082 . C. ***** °Ê²¼¤î¼Ä~¶¹¤°¤ÊÊ¬¤°Á÷¿® (¼¤µ-¥À¥Ó¥×½ªè½Ç¤òÄÓÃ¤¤Ç½¤°¬¤è¼i¹Ç¤Ç¤â) *****
0083 C. DHU¥âj¼¥ÉjÊ¼ý½ ¥i;¼¥ÉjÊ¤òîÄ¤¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0087 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE jS OPOG UPLOAD¤¬Á÷¿®NG¤î¼i¹ÇjÇ°Ê²¼¤îîTI-CMDÁ÷¿®¤î¼Ä¹Ô¤•¤Ê¤¤¤³¤Èj£
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0096 C.                ρρρζ;ϕSETρÈDUMPρİÆ±°ıŸŸ±¹ρÇ¹Ôρ|ρ³ρÈ;Æ
0097 C.
0098 C. TIŸ³ŸρŸóŸÉρòðÀðİζ(UT)
0099 +. TI 2015-11-19 09:55:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                ϕϕ[HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2015-11-19 09:55:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                ϕϕ[HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2015-11-19 09:55:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                ϕϕ[HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2015-11-19 09:59:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                ϕϕ[HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C. °Ê²¼ρİÄê%İİŸİİŸİŸÄŸ$ŸÄŸ⁻¹àİÜ
0116 C.                ϕϕ[HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C.                ϕϕ[HK1_TI_CMD_NUM] EQ 4
0118 C.                ϕϕ[HK1_NEXT_EXEC_PIM] EQ DHU
0119 C.                ϕϕ[HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 C. *****
0122 C. TIİİİ°èŸÄŸóŸ×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                ϕϕ[HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.                ϕϕ[HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.                ϕϕ[HK1_DMP_BLOCK_NUM] EQ 3
0131 C.                ϕϕ[HK1_DMP_REPEAT_NUM] EQ 0
0132 C.                ϕϕ[HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                ϕϕ[HK1_PKT_FORM_NO] EQ 7
0136 C.                ϕϕ[HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.                ϕϕ[HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.                ϕϕ[HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.                ϕϕ[HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C. ŸÄŸóŸ×½ªİ»ρò³İÇ$
0142 C.                ϕϕ[HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 C. RAM ID=TI_TBLρİ¼È¹Ç•ê²İOKρò³İÇ$
0145 C.
0146 C. DHUŸâ;¼ŸÉ;Ê¼Ÿ½, Ÿİ;¼ŸÊ;Ëρòİáρ¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                ϕϕ[HK1_PKT_FORM_NO] EQ 2
0150 C.                ϕϕ[HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.                ϕϕ[HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.                ϕϕ[HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2015-11-19 09:59:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2015-11-19 09:59:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.                [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. MANUAL mode
0171 +. DC 07-FC EIS_MODE_MANU
0172 BC (21 02)
0173 C. Check EIS_MODE = MANU
0174 C. STANBY mode
0175 +. DC 07-FC EIS_MODE_STBY
0176 BC (21 01)
0177 C. Check EIS_MODE = STBY
0178 C. Clear ICU errors
0179 +. DC 07-FC EIS_CLR_ICU_ERR
0180 BC (23)
0181 C. BAKE-OUT mode
0182 +. DC 07-FC EIS_MODE_BAKE
0183 BC (21 04)
0184 C. Check EIS_mode = BAKE
0185 C.
0186 C. ***** XRT START *****
0187 C. Execute, after the success of OP upload.
0188 +. TI 2015-11-19 09:59:00.0
0189 DC 07-F0 MDP_XRT_MODE_STBY
0190 BC (c3)
0191 C.                [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0192 C.
0193 C. ***** XRT END *****

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```
00194 C.  
00195 . C. ***** MDP ´ÚÃÎ±İ»Ö¾Ý±ĖÂÐ±¹±ēDCBC•×²è *****  
00196 . C. (%ā°ī¥Ō¥Ā¥Ĕ¥P¥ĕ¥ā¥Ç¥ē±Ė½¼±¼¼Ä»Û±¹±ē)  
00197 . S. DC-BC dcbc-402:DCBC  
00198 (MDP_known_event)  
00199 C.  
00200 C.  
00201 . C. ***** ¥Đ¥¹·İ Daily±¿İÑ±Ė´Ø±¹±ēDCBC•×²è *****  
00202 . S. DC-BC dcbc-153:DCBC  
00203 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
00204 C.  
00205 C.  
00206 . C. ¡ãŁS¥Á¥S¥Ā¥¬¼Ä»Û¡ä  
00207 C.  
00208 . C. ***** LOS *****  
00209 C.
```

```
main-498 2015-11-19 17:57:07 246 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ¡ãAOS¥Á¥$¥Ã¥¬¼Å»Ü¡ä
0005 C.
0006 C. ¥Ä¥ß¡¼¥³¥Ð¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Ä¡¡Ë¤¿¤À¤•µ°Æ»Î×Ã¤¤¡¤¢¥Ä¥×¥¡¡¼¥É¡ËËë½µ•îËË¡Ë¤Ê¼°Ç¤¤•¤¿¼¡¹Ç¤¡¡¡Ç¤®, ù¤¹¤è¤¤¤Ç¤Ä÷¿®¤¤•¤Ê¤¤¤³¤È¡£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷¿®µ¡ON
0016 C. *****
0017 C. ¢´ °ÆÄ, Î×ËÝ¤àLOS¤¤¤Ç¤¤¡»þ´Ô¤ð¹ÍÎ, ¤•¡¢ÉÔÎ×¤EXÄÓ¤¤¤¹¹Ô¤È¤¡¤Ê¤¤¤³¤È¡£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ¢¢[HK1_XPA_ON/OFF] EQ ON
0025 C. ¢¢[HK1_XPA_PWR_HI/L0] EQ HI
0026 C. ¢¢[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¢¢[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥Ó¥É¥¡¥Ã¥¬¼ÖÄÔ¤¤¬¼Äê¤¤•¤¿¤¤è¡¢°Ê²¼¤¤Î°ÆÄ, ¼ê½Ç¤¤ð¼Ä¹Ô¤¤¹¤è¡£
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄÎ¼¹°ÆÄ,
0033 C. *****
0034 C. ¢´ RESTART¡ÊPT1¡Ê¤¤•¤¿¤¤¼¼¹Ç¤¤¡¡¢°Ê²¼¤¤Î¼Ä¹Ô¤¤¤¤¤¡¢DCBC-150¤Ø¿Ê¤¤à¡£
0035 C.
0036 . C. ¡ãPT1°ÆÄ, ³«»Î¡ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¡¼Ú)
0043 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¼Ú)
0044 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¼Ú)
0045 C.
0046 . C. ¡ã¥¢¥ó¥Æ¥ÊÄÙÄØ¡ÊÄ•Äº²öÈò¡Ë, ä¤Î°ÆÄ, °Æ³«¡ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¡¼Ú)
0050 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¼Ú)
0051 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ¤¬¼«Æ°Ää»ß¤¤•¤¿, ä¡¢°Ê²¼¤¤ð¼Ä¹Ô¤¤¹¤è¡£
0055 C. ¥¢¥ó¥Æ¥ÊÄÙÄØ¤¤Ä•Äº²öÈò¤¬¼á¤¤¼¼¹Ç¤¤¡¹°Î»¤¤¹¤è¤¤¤Ç¤¤Ô¤¤Ä¡£
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄÎ¼¹°ÆÄ,
0059 C. *****
0060 C. ¢´ RESTART¡ÊPT2¡Ê¤¤•¤¿¤¤¼¼¹Ç¤¤¡¡¢°Ê²¼¤¤Î¼Ä¹Ô¤¤¤¤¤¡¢DCBC-151¤Ø¿Ê¤¤à¡£
0061 C.
0062 . C. ¡ãPT2°ÆÄ, ³«»Î¡ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¡¼Ú)
0069 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¼Ú)
0070 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¼Ú)
0071 C.
0072 . C. ¡ã¥¢¥ó¥Æ¥ÊÄÙÄØ¡ÊÄ•Äº²öÈò¡Ë, ä¤Î°ÆÄ, °Æ³«¡ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¡¼Ú)
0076 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¼Ú)
0077 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ß¡¢XÄ÷¿®µ¡OFF
0081 C. *****
0082 C.
0083 . C. ¡ãDR°ÆÄ, Ää»ß¡ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¢¢[HK1_REP_STA/STP] EQ STOP
0087 C. ¢¢[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¢¢[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ¡ãXÄ÷¿®µ¡OFF¡ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ¢¢[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¢¢[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOUC_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOUC_ORB_UPD
0128 C.
0129 C. Change EIS mode to STBY
0130 +. DC 07-FC EIS_MODE_STBY
0131 BC (21 01)
0132 C. Wait for 10 sec / Confirm EIS_MODE = STBY
0133 C. Enable mode change command, just in case it is disabled - No harm if already enabled
0134 +. DC 07-FC EIS_MODE_CHG_ENA
0135 BC (20)
0136 C. Confirm EIS_MODE_CHG_FLG = ENA
0137 C. Change mode to Manual
0138 +. DC 07-FC EIS_MODE_MANU
0139 BC (21 02)
0140 C. Confirm EIS_MODE = MANU
0141 C. Exit CAM default mode
0142 +. DC 07-FC EIS_CAM_IDLE
0143 BC (41)
0144 C. Confirm EIS_CAM_VLD_FLG = VLD
0145 C.
0146 C. MHC in PROM mode
0147 +. DC 07-FC EIS_MHC_WATDOG_ENA
0148 BC (76 c0 a9 00 02 00 01)
0149 C.
0150 C. LOAD MHC code from EEPROM bank 3d to MHC RAM
0151 +. DC 07-FC EIS_LOAD_MHC_SOFT3
0152 BC (2c 03)
0153 C. New: Confirm EIS_MHC_LOAD_STS = LOAD
0154 C. It takes 2 min to complete.
0155 C. New: Confirm EIS_MHC_LOAD_STS = IDLE
0156 C.
0157 +. DC 07-FC EIS_DUMP_MHCRAM
0158 BC (0a 07 00 00 00 80 00)
0159 C. New: Confirm EIS_MEM_DUMP_STS = DUMP
0160 C. It takes 2 min to complete.
0161 C. New: Confirm EIS_MEM_DUMP_STS = IDLE
0162 C.
0163 +. DC 07-FC EIS_MHC_MODE_RAM
0164 BC (6b 88 11 00 02 ff ff)
0165 C. Wait 10sec: 10sec interval is required after EIS_MHC_MODE_RAM.
0166 +. DC 07-FC EIS_MHC_ABORT
0167 BC (51 e8 81)
0168 C. New: Wait 10sec, then confirm EIS_MHC_VLD_FLG = VLD, EIS_MHC_MEM_MODE = RAM
0169 C.
0170 +. DC 07-FC EIS_CLR_ICU_ERR
0171 BC (23)
0172 C. Confirm EIS_MHC_IF_ERR= 0x0000
0173 C.
0174 C. Confirm EIS_CAM_IF_ERR= 0x0000
0175 C.
0176 +. DC 07-FC EIS_MHC_WATDOG_ENA
0177 BC (76 c0 a9 00 02 00 01)
0178 C. Config MHC heaters - Default setting -
0179 C.
0180 +. DC 07-FC EIS_UPLOAD_MHCPAR
0181 BC (6f c0 95 00 04 21 2e 24)
0182 BC (92)
0183 +. DC 07-FC EIS_UPLOAD_MHCPAR
0184 BC (6f c0 95 00 04 c9 af 36)
0185 BC (db)
0186 +. DC 07-FC EIS_UPLOAD_MHCPAR
0187 BC (6f c0 95 00 04 a9 30 24)
0188 BC (92)
0189 +. DC 07-FC EIS_UPLOAD_MHCPAR
0190 BC (6f c0 95 00 04 41 b1 36)
0191 BC (db)
0192 +. DC 07-FC EIS_UPLOAD_MHCPAR
0193 BC (6f c0 95 00 04 81 b2 12)

```

```

0194      BC          (49)
0195 + DC 07-FC EIS_UPLOAD_MHCPAR
0196      BC          (6f c0 95 00 04 69 33 24)
0197      BC          (92)
0198 + DC 07-FC EIS_UPLOAD_MHCPAR
0199      BC          (6f c0 95 00 04 e1 b4 12)
0200      BC          (49)
0201 + DC 07-FC EIS_UPLOAD_MHCPAR
0202      BC          (6f c0 95 00 04 09 35 1b)
0203      BC          (6d)
0204 + DC 07-FC EIS_UPLOAD_MHCPAR
0205      BC          (6f c0 95 00 04 c9 36 09)
0206      BC          (24)
0207 + DC 07-FC EIS_UPLOAD_MHCPAR
0208      BC          (6f c0 95 00 04 21 b7 09)
0209      BC          (24)
0210 + DC 07-FC EIS_UPLOAD_MHCPAR
0211      BC          (6f c0 95 00 04 21 b8 24)
0212      BC          (92)
0213 + DC 07-FC EIS_UPLOAD_MHCPAR
0214      BC          (6f c0 95 00 04 c9 39 00)
0215      BC          (00)
0216      C.    Set heaters duty cycle
0217      C.
0218 + DC 07-FC EIS_UPLOAD_MHCPAR
0219      BC          (6f c0 95 00 04 09 3a 03)
0220      BC          (e8)
0221      . C. Dump MHC parameter table
0222 + DC 07-FC EIS_DUMP_MHCPAR
0223      BC          (0c 07 02 00 00 00 cc)
0224 + DC 07-FC EIS_OPE_HTR_ALL_ON
0225      BC          (6a 28 8d 00 02 0f ff)
0226      C. Confirm EIS_OPE_HTR##_PWR = sometimes ON
0227      C. New: Enable MHC safe mode (auto-safe)
0228 + DC 07-FC EIS_MHC_SAFE_ENA
0229      BC          (5f c0 9a 00 02 00 01)
0230      C. The next daily plan MUST start with mhc_init, default_htrs1_6, default_htrs7_12 and trace dump
0231      C.
0232      . C. ***** MDP `0ÃĬı»0%ŸıEÂĐı¹ëDCBC•x²è *****
0233      C. (%ã°İ¥0¥Á¥É¥ı¥É¥ä¥ç¥ëE½%ı¼A»Üı¹ë)
0234      . S. DC-BC dcbc-402:DCBC
0235      (MDP_known_event)
0236      C.
0237      C.
0238      . C. ***** ¥D¥¹•İ Daily±¿İÑıÉ´Đı¹ëDCBC•x²è *****
0239      . S. DC-BC dcbc-153:DCBC
0240      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0241      C.
0242      C.
0243      . C. ĵăLOS¥Á¥$¥Ă¥¬¼Ä»Üĵă
0244      C.
0245      . C. ***** LOS *****
0246      C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-499 2015-11-19 17:57:07 119 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀiĲ¿²A²•µ°£»İ×AÇ²İ¥Ç¥A¥×¥İj¼¥ÉjĒĒē½µ•İİĒjĒĒĒ°ÇÖ²•²¿¼i¹Ç²İjÇÀ®, ū²²ē²²²²ÇÁ÷¿®²•²Ē²²²²²ĒĒj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 85 83 08 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 + DC 07-F0 MDP_XRT_ARS_DIS
0061 BC (d5)
0062 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0063 BC (c4 0b)
0064 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0065 BC (c5 07)
0066 . C. ----- Success Verify ? OK / NG ____
0067 C.
0068 C.
0069 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0070 C.
0071 +. DC 07-F0 MDP_XRT_MODE_OBSV
0072 BC (c2)
0073 +. TI 2015-11-19 09:59:02.0
0074 DC 07-F0 MDP_XRT_MODE_OBSV
0075 BC (c2)
0076 . C. ----- Success Verify ? OK / NG ____
0077 C.
0078 C. ***** XRT END *****
0079 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0080 +. DC 07-FC EIS_MODE_CHG_ENA
0081 BC (20)
0082 . C. Verify EIS_MODE_CHG_FLG is ENA
0083 +. DC 07-FC EIS_MODE_MANU
0084 BC (21 02)
0085 . C. Verify EIS in MANUAL mode
0086 . C. Estimated OBSTBL upload time is 27s
0087 C. *****
0088 C. EIS START OBSTBL LOAD
0089 C. *****
0090 . S. RAM ram-820:EIS_OBSTBL
0091 ( )
0092 +. DC 07-FC EIS_DUMP_OBSTBL
0093 BC (07 07 07 00 00 70 00)
0094 C.
0095 C. Execute, after the success of OBSTBL upload.
```

```
0096 C. Set EIS TI-commands
0097 +. TI 2015-11-19 09:59:50.0
0098 DC 07-FC EIS_MODE_CHG_ENA
0099 BC (20)
0100 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0101 C. *****
0102 C. EIS END OBSTBL LOAD
0103 C. *****
0104 C.
0105 . C. ***** MDP ´ûÃîï»ö%ýðËÂð¹ðËDCBC•x²è *****
0106 C. (¾å°ï¥0¥Ã¥Ë¥P¥Ë¥â¥C¥ËðË½¼ð¼Ã»Û¹ðË)
0107 . S. DC-BC dcbc-402:DCBC
0108 (MDP_known_event)
0109 C.
0110 C.
0111 . C. ***** ¥Ð¥¹•ï Daily±¿îñðË´0¹ðËDCBC•x²è *****
0112 . S. DC-BC dcbc-153:DCBC
0113 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0114 C.
0115 C.
0116 . C. ¡ãLOS¥Á¥S¥Ã¥¬¼Ã»Û¡ä
0117 C.
0118 . C. ***** LOS *****
0119 C.
```


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*** OP Sequence for XRT ***

2015/11/19	10:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	10:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	10:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	10:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 56 4d
2015/11/19	10:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	10:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	10:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	10:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	10:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	10:12:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	10:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	10:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	11:11:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	11:11:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	11:11:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	11:12:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 00 00 00 00
2015/11/19	11:12:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	11:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	11:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	11:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	11:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	11:14:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	11:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	11:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	13:22:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	13:22:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	13:22:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/19	13:23:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/11/19	13:23:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	13:23:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	13:23:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	13:23:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	13:23:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	13:25:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2015/11/19	13:25:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	13:26:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	15:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	15:00:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	15:00:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	15:00:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/19	15:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/19	15:13:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	15:13:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	15:13:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	15:13:30.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 56 4d 01 68
2015/11/19	15:13:48.0	XRT_FLD_ENA_411_OG [0x19b]			

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2015/11/19	15:13:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	15:13:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	15:13:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	15:13:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	15:16:26.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	15:16:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	15:16:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	15:38:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	15:38:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	15:38:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	15:38:30.0	AOCS_0Re-point_Start_5_0G [0x09b]	AOCU_NM	5	02-76	00 4d 68 01 68
2015/11/19	15:38:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	15:38:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	15:38:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	15:38:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	15:38:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	15:41:26.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	15:41:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	15:41:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	16:03:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	16:03:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	16:03:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	16:03:30.0	AOCS_0Re-point_Start_6_0G [0x09c]	AOCU_NM	5	02-76	00 44 8b 01 68
2015/11/19	16:03:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	16:03:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	16:03:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	16:03:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	16:03:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	16:06:26.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	16:06:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	16:06:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	16:35:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	16:35:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	16:35:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	16:35:36.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/19	16:38:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/19	16:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	16:59:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	16:59:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	17:00:00.0	AOCS_0Re-point_Start_7_0G [0x09d]	AOCU_NM	5	02-76	00 3b a5 01 68
2015/11/19	17:00:18.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	17:00:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	17:00:22.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	17:00:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	17:00:26.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	17:02:56.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a

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2015/11/19	17:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07	
2015/11/19	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2015/11/19	17:24:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	17:24:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	17:24:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2015/11/19	17:25:00.0	AOCS_ORe-point_Start_8_OG [0x09e]			
		AOCU_NM	5	02-76 00 32 c0 01 68	
2015/11/19	17:25:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2015/11/19	17:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2015/11/19	17:25:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2015/11/19	17:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2015/11/19	17:25:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2015/11/19	17:27:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a	
2015/11/19	17:27:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07	
2015/11/19	17:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2015/11/19	17:48:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	17:48:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	17:48:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2015/11/19	17:49:00.0	AOCS_ORe-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76 00 00 00 00 00	
2015/11/19	17:49:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2015/11/19	17:51:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2015/11/19	17:51:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2015/11/19	17:51:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05	
2015/11/19	17:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2015/11/19	17:58:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	17:58:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	17:58:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2015/11/19	17:59:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]			
		AOCU_NM	5	02-76 00 29 db 01 68	
2015/11/19	17:59:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2015/11/19	17:59:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2015/11/19	17:59:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2015/11/19	17:59:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2015/11/19	17:59:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2015/11/19	18:01:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a	
2015/11/19	18:01:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07	
2015/11/19	18:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2015/11/19	18:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	18:12:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	18:12:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2015/11/19	18:12:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2015/11/19	18:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2015/11/19	18:36:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/19	18:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2015/11/19	19:01:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	19:01:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2015/11/19	19:01:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2015/11/19	19:02:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76 00 20 fd 01 68	
2015/11/19	19:02:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	

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2015/11/19	19:02:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	19:02:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	19:02:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	19:02:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	19:04:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	19:04:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	19:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	19:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	19:49:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	19:49:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	19:49:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/19	19:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/19	20:13:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	20:13:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	20:13:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	20:14:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]			
		AOCU_NM	5	02-76	00 18 18 01 68
2015/11/19	20:14:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	20:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	20:14:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	20:14:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	20:14:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	20:16:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	20:16:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	20:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	20:38:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	20:38:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	20:38:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	20:39:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]			
		AOCU_NM	5	02-76	00 0f 33 01 68
2015/11/19	20:39:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	20:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	20:39:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	20:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	20:39:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	20:41:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	20:41:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	20:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	21:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	21:26:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	21:26:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	21:26:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/19	21:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/19	21:50:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	21:50:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	21:50:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	21:50:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]			
		AOCU_NM	5	02-76	00 06 4d 01 68
2015/11/19	21:50:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	21:50:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			

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2015/11/19	21:50:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	21:50:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	21:50:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	21:53:26.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	21:53:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	21:53:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	22:15:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	22:15:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	22:15:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	22:15:30.0	AOCS_ORe-point_Start_15_0G [0x0a5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	22:15:48.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	00 fe 4e 01 68
2015/11/19	22:15:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	22:15:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	22:15:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	22:15:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	22:18:26.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	22:18:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	22:18:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	23:04:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	23:04:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	23:04:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	23:04:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	23:07:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/19	23:23:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/19	23:23:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	23:23:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	23:23:30.0	AOCS_ORe-point_Start_16_0G [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	23:23:48.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	00 f5 69 01 68
2015/11/19	23:23:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	23:23:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	23:23:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	23:23:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	23:26:26.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	23:26:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/19	23:26:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/19	23:48:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/19	23:48:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	23:48:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/19	23:48:30.0	AOCS_ORe-point_Start_17_0G [0x0a7]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/19	23:48:48.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	00 ec 8c 01 68
2015/11/19	23:48:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/19	23:48:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/19	23:48:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/19	23:48:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/19	23:51:26.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/19	23:51:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07

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2015/11/19	23:51:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/20	00:13:24.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	00:13:26.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	00:13:28.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/11/20	00:13:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]		
		AOCU_NM	5	02-76 00 e3 a6 01 68
2015/11/20	00:13:48.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/11/20	00:13:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/11/20	00:13:52.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/11/20	00:13:54.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/11/20	00:13:56.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	00:16:26.0	XRT_QT_PROG_SET_418_OG [0x1a2]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2015/11/20	00:16:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/11/20	00:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/20	00:41:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	00:41:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	00:41:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	00:41:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/20	00:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/20	00:48:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	00:48:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	00:48:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/11/20	00:49:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]		
		AOCU_NM	5	02-76 00 da c1 01 68
2015/11/20	00:49:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/11/20	00:49:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/11/20	00:49:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/11/20	00:49:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/11/20	00:49:26.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	00:51:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2015/11/20	00:51:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/11/20	00:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/20	01:13:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	01:13:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	01:13:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/11/20	01:14:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]		
		AOCU_NM	5	02-76 00 d1 dc 01 68
2015/11/20	01:14:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/11/20	01:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/11/20	01:14:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/11/20	01:14:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/11/20	01:14:26.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	01:16:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2015/11/20	01:16:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/11/20	01:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/20	02:04:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	02:04:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	02:04:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	02:04:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/20	02:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		

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2015/11/20	02:24:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	02:24:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	02:24:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/20	02:24:30.5	AOCS_ORe-point_Start_21_0G [0x0ab]	AOCU_NM	5	02-76	00 c8 fe 01 68
2015/11/20	02:24:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/20	02:24:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/20	02:24:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/20	02:24:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/20	02:24:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	02:27:26.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/20	02:27:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/20	02:27:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	02:49:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	02:49:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	02:49:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/20	02:49:30.0	AOCS_ORe-point_Start_22_0G [0x0ac]	AOCU_NM	5	02-76	00 c0 19 01 68
2015/11/20	02:49:48.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/20	02:49:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/20	02:49:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/20	02:49:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/20	02:49:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	02:52:26.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/20	02:52:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/20	02:52:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	03:35:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	03:35:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	03:35:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	03:35:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/20	03:38:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/20	04:01:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	04:01:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	04:01:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/20	04:02:00.0	AOCS_ORe-point_Start_23_0G [0x0ad]	AOCU_NM	5	02-76	00 b7 34 01 68
2015/11/20	04:02:18.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/20	04:02:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/20	04:02:22.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/20	04:02:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/20	04:02:26.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	04:04:56.0	XRT_QT_PROG_SET_418_0G [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/20	04:04:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/20	04:05:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	04:26:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	04:26:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	04:26:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/20	04:27:00.0	AOCS_ORe-point_Start_24_0G [0x0ae]	AOCU_NM	5	02-76	00 ae 4e 01 68
2015/11/20	04:27:18.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8

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2015/11/20	04:27:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/20	04:27:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/20	04:27:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/20	04:27:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	04:29:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2015/11/20	04:29:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/20	04:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	04:51:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	04:51:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	04:51:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/20	04:52:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/11/20	04:52:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/20	04:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/20	04:52:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/20	04:52:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/20	04:52:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	04:54:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/11/20	04:54:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/20	04:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	05:11:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	05:11:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	05:11:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	05:11:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/20	05:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/20	05:38:30.0	XRT_Custom_430_OG [0x1ae]			
2015/11/20	05:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/20	06:00:00.0	AOCS_ORe-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/11/20	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/11/20	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/20	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/20	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/11/20	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/20	06:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/11/20	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/20	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/20	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/20	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/20	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	06:12:56.0	XRT_QT_PROG_SET_413_OG [0x19d]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2015/11/20	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/20	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2015/11/20	06:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	06:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	06:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	06:51:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/20	06:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/20	07:15:30.0	XRT_Custom_430_OG [0x1ae]			
2015/11/20	07:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	08:31:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	08:31:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	08:31:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	08:31:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/20	08:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/20	08:52:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/20	08:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	10:12:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	10:12:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	10:12:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	10:12:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/20	10:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/20	10:23:30.0	XRT_Custom_430_OG [0x1ae]			
2015/11/20	10:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	15:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	15:34:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	15:34:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	15:34:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/20	15:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/20	15:57:30.0	XRT_Custom_430_OG [0x1ae]			
2015/11/20	15:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	17:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	17:10:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	17:10:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/20	17:10:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/20	17:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/20	17:34:30.0	XRT_Custom_430_OG [0x1ae]			
2015/11/20	17:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	18:05:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	18:05:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	18:05:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/20	18:05:30.0	AOCS_Ore-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/11/20	18:05:48.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/11/20	18:08:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/20	18:08:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/20	18:08:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/11/20	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/20	18:15:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	18:15:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/20	18:15:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/20	18:15:30.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/11/20	18:15:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8

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2015/11/20	18:15:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/11/20	18:15:52.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/11/20	18:15:54.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/11/20	18:15:56.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	18:18:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0e
2015/11/20	18:18:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/11/20	18:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/20	18:47:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	18:47:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	18:47:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	18:47:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/20	18:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/20	19:11:00.0	XRT_Custom_430_OG [0x1ae]		
2015/11/20	19:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/20	20:24:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	20:24:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	20:24:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	20:24:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/20	20:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/20	20:48:00.0	XRT_Custom_430_OG [0x1ae]		
2015/11/20	20:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/20	22:02:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	22:02:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	22:02:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	22:02:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/20	22:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/20	22:24:00.0	XRT_Custom_430_OG [0x1ae]		
2015/11/20	22:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/20	23:39:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	23:39:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/20	23:39:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/20	23:39:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/20	23:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/20	23:50:30.0	XRT_Custom_430_OG [0x1ae]		
2015/11/20	23:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/21	01:06:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/21	01:06:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/21	01:06:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/21	01:06:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/21	01:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/21	01:22:30.0	XRT_Custom_430_OG [0x1ae]		
2015/11/21	01:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/21	02:38:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/21	02:38:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/11/21	02:38:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/11/21	02:38:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/11/21	02:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/11/21	02:59:00.0	XRT_Custom_430_OG [0x1ae]		
2015/11/21	03:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/11/21	04:07:00.0	XRT_CTRL_MANU_400_OG [0x190]		

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2015/11/21	04:07:02.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	04:07:04.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	04:07:06.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/21	04:10:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/21	04:36:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/21	04:37:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/21	05:47:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	05:47:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	05:47:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	05:47:36.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/21	05:50:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/21	06:13:30.5	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/21	06:14:30.5	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/21	06:23:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	06:23:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	06:23:28.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	06:23:30.0	AOCS_ORe-point_Start_9_0G [0x09f]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/11/21	06:23:48.0	XRT_FLD_DIS_406_0G [0x196]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/11/21	06:26:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/11/21	06:26:26.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/11/21	06:26:28.0	XRT_QT_PROG_SET_421_0G [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/11/21	06:26:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2015/11/21	06:33:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/21	06:33:26.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	06:33:28.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	06:33:30.0	AOCS_ORe-point_Start_3_0G [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/11/21	06:33:48.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00	
2015/11/21	06:33:50.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/11/21	06:33:52.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/11/21	06:33:54.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/11/21	06:33:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/11/21	06:36:26.0	XRT_QT_PROG_SET_409_0G [0x199]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/21	06:36:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2015/11/21	06:36:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/11/21	07:27:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/21	07:27:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	07:27:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	07:27:36.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/21	07:30:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/21	07:50:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/21	07:51:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/21	09:07:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	09:07:32.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	09:07:34.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/21	09:07:36.0	XRT_PREFLR_STRT_425_0G [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/21	09:10:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/21	09:43:00.0	AOCS_ORe-point_Start_9_0G [0x09f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
		AOCU_NM		5	02-76	00 00 00 00 00	