

# XRT Timeline to be uploaded on 2015/11/24

Period: 2015/11/24 09:42:00 - 2015/12/02 09:32:00

\* \* \* \* \*

Normal mode

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**XOB #1B02: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

Term	Pointing (x, y)	Comment
11/24 09:59:00 - 11/24 17:54:30	Track ( -330.7, 163.6) @ 11/24 09:52:00	# OP start + 10min, AR 12457
11/24 18:31:00 - 11/25 06:02:54	Track ( -255.3, 163.6) @ 11/24 18:28:00	# AR 12457
11/25 06:16:00 - 11/25 17:59:54	Track ( -149.5, 164.0) @ 11/25 06:13:00	# AR 12457
11/25 18:13:00 - 11/26 00:51:30	Track ( -39.8, 164.7) @ 11/25 18:10:00	# AR 12457
11/26 02:42:00 - 11/26 06:05:24	Track ( 37.0, 165.4) @ 11/26 02:30:00	# AR 12457
11/26 06:18:30 - 11/26 17:53:24	Track ( 71.7, 165.8) @ 11/26 06:15:30	# AR 12457
11/26 18:06:30 - 11/27 05:37:24	Track ( 179.6, 167.2) @ 11/26 18:03:30	# AR 12457
11/27 05:50:30 - 11/27 17:39:24	Track ( 284.5, 168.9) @ 11/27 05:47:30	# AR 12457
11/27 17:52:30 - 11/28 05:57:24	Track ( 388.3, 170.8) @ 11/27 17:49:30	# AR 12457
11/28 06:10:30 - 11/28 17:59:54	Track ( 489.0, 172.9) @ 11/28 06:07:30	# AR 12457
11/28 18:13:00 - 11/28 23:29:54	Track ( 580.9, 175.1) @ 11/28 18:10:00	# AR 12457
11/30 02:33:00 - 11/30 05:46:24	Track ( 785.4, 181.2) @ 11/30 02:30:00	# AR 12457
11/30 05:59:30 - 11/30 17:48:24	Track ( 802.9, 181.9) @ 11/30 05:56:30	# AR 12457
11/30 18:01:30 - 12/01 05:59:54	Track ( 857.2, 184.0) @ 11/30 17:58:30	# AR 12457
12/01 06:13:00 - 12/01 10:11:30	Track ( 900.4, 186.1) @ 12/01 06:10:00	# AR 12457

**PROG= 15 Inf.-time(s)**

Subr=	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 79	4-time(s)	120.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	34.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	34.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1ADF: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Med-Al(40**

Term	Pointing (x, y)	Comment
11/24 18:21:00 - 11/24 18:27:54	Fixed ( 0.0, 0.0)	synoptic, shifted 18.0 min

**PROG= 02 1-time(s)**

Subr=	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	4.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 46	2-time(s)	2.0sec										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 72	2-time(s)	2.0sec										
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 59	2-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

**XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + Th**

Term	Pointing (x, y)	Comment
11/25 06:06:00 - 11/25 06:12:54	Fixed ( 0.0, 0.0)	synoptic, shifted 3.0 min
11/25 18:03:00 - 11/25 18:09:54	Fixed ( 0.0, 0.0)	synoptic
11/26 06:08:30 - 11/26 06:15:24	Fixed ( 0.0, 0.0)	synoptic, shifted 5.5 min
11/26 17:56:30 - 11/26 18:03:24	Fixed ( 0.0, 0.0)	synoptic, shifted -6.5 min
11/27 05:40:30 - 11/27 05:47:24	Fixed ( 0.0, 0.0)	synoptic, shifted -22.5 min
11/27 17:42:30 - 11/27 17:49:24	Fixed ( 0.0, 0.0)	synoptic, shifted -20.5 min
11/28 06:00:30 - 11/28 06:07:24	Fixed ( 0.0, 0.0)	synoptic, shifted -2.5 min
11/28 18:03:00 - 11/28 18:09:54	Fixed ( 0.0, 0.0)	synoptic
11/29 06:17:00 - 11/29 06:23:54	Fixed ( 0.0, 0.0)	synoptic, shifted 14.0 min
11/29 18:03:00 - 11/29 18:09:54	Fixed ( 0.0, 0.0)	synoptic
11/30 05:49:30 - 11/30 05:56:24	Fixed ( 0.0, 0.0)	synoptic, shifted -13.5 min
11/30 17:51:30 - 11/30 17:58:24	Fixed ( 0.0, 0.0)	synoptic, shifted -11.5 min
12/01 06:03:00 - 12/01 06:09:54	Fixed ( 0.0, 0.0)	synoptic

**PROG= 05 1-time(s)**

└ Subr= 1 1-time(s) 2.0sec

└ Seqn= 5 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

└ Seqn= 33 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

└ Seqn= 49 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

└ Seqn= 39 1-time(s) 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

└ Seqn= 54 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

**XOB #1AF6: CME watch - 4x4 - AEC 2/3 - Be-thin (Be-Med) - Long/Short Pairs - 180s cad**

Term	Pointing (x, y)	Comment
11/26 01:06:00 - 11/26 02:20:30	Fixed ( 810.0, -500.0)	# Prominence
11/28 23:43:30 - 11/29 06:13:54	Track ( -115.4, -200.9) @ 11/28 23:30:00	# HOP 243 and QS obs
11/29 06:27:00 - 11/29 17:36:30	Track ( -51.8, -200.5) @ 11/29 06:24:00	# QS obs
11/29 18:13:00 - 11/30 02:29:54	Track ( 57.1, -199.5) @ 11/29 18:10:00	# QS obs and HOP 243

**PROG= 19 Inf.-time(s)**

└ Subr= 1 20-time(s) 180.0sec

└ Seqn= 12 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec

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**Flare mode**

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**XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
11/24 09:59:00 - 11/24 17:54:30	Track ( -330.7, 163.6) @ 11/24 09:52:00	# OP start + 10min, AR 12457
11/24 18:31:00 - 11/25 06:02:54	Track ( -255.3, 163.6) @ 11/24 18:28:00	# AR 12457
11/25 06:16:00 - 11/25 17:59:54	Track ( -149.5, 164.0) @ 11/25 06:13:00	# AR 12457
11/25 18:13:00 - 11/26 00:51:30	Track ( -39.8, 164.7) @ 11/25 18:10:00	# AR 12457
11/26 01:06:00 - 11/26 02:20:30	Fixed ( 810.0, -500.0)	# Prominence
11/26 02:42:00 - 11/26 06:05:24	Track ( 37.0, 165.4) @ 11/26 02:30:00	# AR 12457
11/26 06:18:30 - 11/26 17:53:24	Track ( 71.7, 165.8) @ 11/26 06:15:30	# AR 12457
11/26 18:06:30 - 11/27 05:37:24	Track ( 179.6, 167.2) @ 11/26 18:03:30	# AR 12457
11/27 05:50:30 - 11/27 17:39:24	Track ( 284.5, 168.9) @ 11/27 05:47:30	# AR 12457
11/27 17:52:30 - 11/28 05:57:24	Track ( 388.3, 170.8) @ 11/27 17:49:30	# AR 12457
11/28 06:10:30 - 11/28 17:59:54	Track ( 489.0, 172.9) @ 11/28 06:07:30	# AR 12457
11/28 18:13:00 - 11/28 23:29:54	Track ( 580.9, 175.1) @ 11/28 18:10:00	# AR 12457
11/28 23:43:30 - 11/29 06:13:54	Track ( -115.4, -200.9) @ 11/28 23:30:00	# HOP 243 and QS obs
11/29 06:27:00 - 11/29 17:36:30	Track ( -51.8, -200.5) @ 11/29 06:24:00	# QS obs
11/29 18:13:00 - 11/30 02:29:54	Track ( 57.1, -199.5) @ 11/29 18:10:00	# QS obs and HOP 243
11/30 02:33:00 - 11/30 05:46:24	Track ( 785.4, 181.2) @ 11/30 02:30:00	# AR 12457
11/30 05:59:30 - 11/30 17:48:24	Track ( 802.9, 181.9) @ 11/30 05:56:30	# AR 12457
11/30 18:01:30 - 12/01 05:59:54	Track ( 857.2, 184.0) @ 11/30 17:58:30	# AR 12457

PROG= 07 30-time(s)												
Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

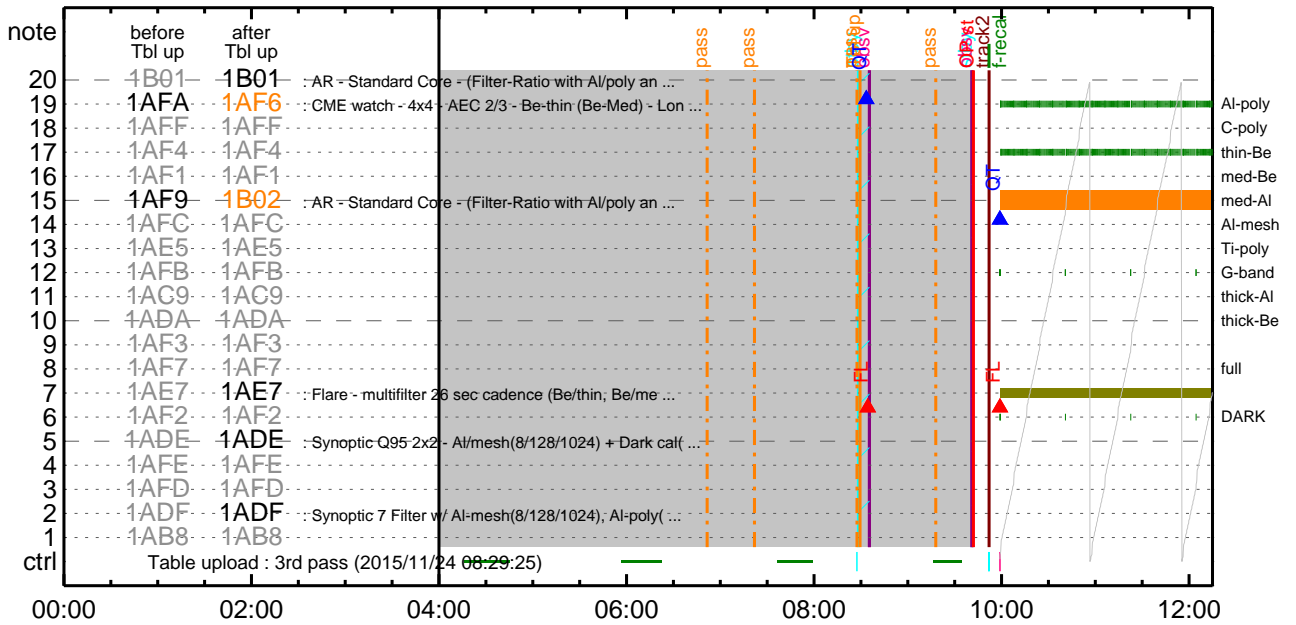
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### Flare Detection

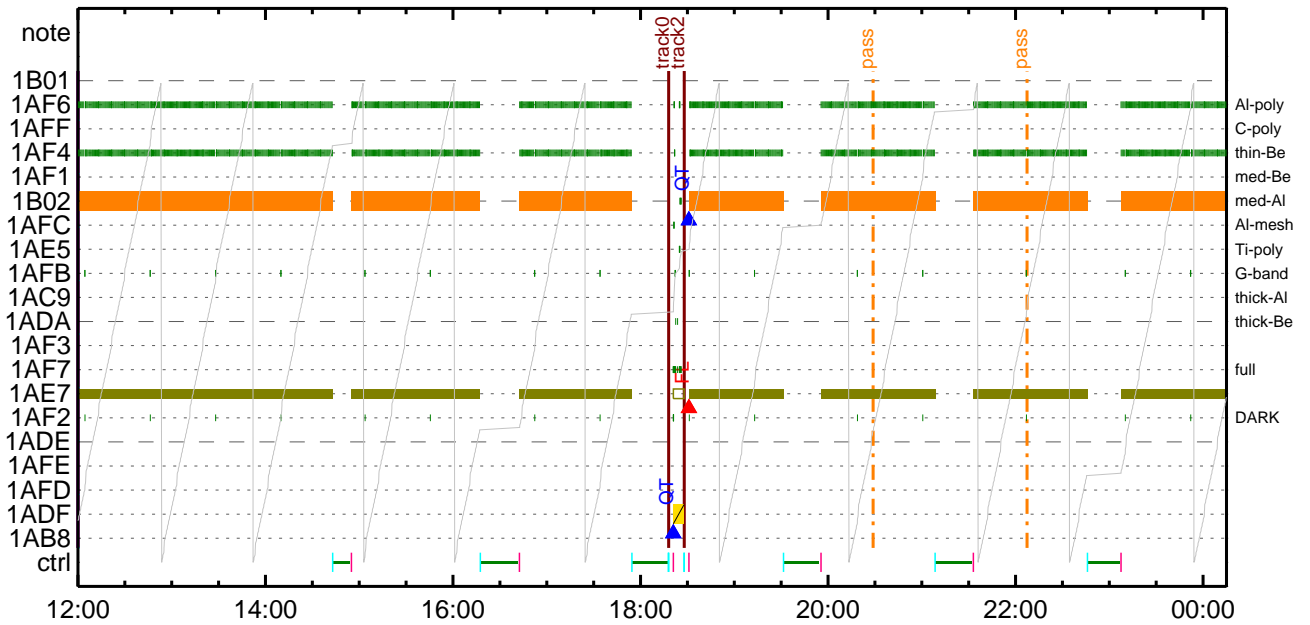
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FLD Patrol												
Term		Pointing (x, y)						Comment				
11/24 18:28:18 - 11/25 06:03:18	Track ( -255.3, 163.6)	@ 11/24 18:28:00						# AR 12457				
11/25 06:13:18 - 11/25 18:00:18	Track ( -149.5, 164.0)	@ 11/25 06:13:00						# AR 12457				
11/25 18:10:18 - 11/26 06:05:48	Track ( -39.8, 164.7)	@ 11/25 18:10:00						# AR 12457				
11/26 06:15:48 - 11/26 17:53:48	Track ( 71.7, 165.8)	@ 11/26 06:15:30						# AR 12457				
11/26 18:03:48 - 11/27 05:37:48	Track ( 179.6, 167.2)	@ 11/26 18:03:30						# AR 12457				
11/27 05:47:48 - 11/27 17:39:48	Track ( 284.5, 168.9)	@ 11/27 05:47:30						# AR 12457				
11/27 17:49:48 - 11/28 05:57:48	Track ( 388.3, 170.8)	@ 11/27 17:49:30						# AR 12457				
11/28 06:07:48 - 11/28 18:00:18	Track ( 489.0, 172.9)	@ 11/28 06:07:30						# AR 12457				
11/28 18:10:18 - 11/29 06:14:18	Track ( 580.9, 175.1)	@ 11/28 18:10:00						# AR 12457				
11/29 06:24:18 - 11/29 18:00:18	Track ( -51.8, -200.5)	@ 11/29 06:24:00						# QS obs				
11/29 18:10:18 - 11/30 05:46:48	Track ( 57.1, -199.5)	@ 11/29 18:10:00						# QS obs and HOP 243				
11/30 05:56:48 - 11/30 17:48:48	Track ( 802.9, 181.9)	@ 11/30 05:56:30						# AR 12457				
11/30 17:58:48 - 12/01 06:00:18	Track ( 857.2, 184.0)	@ 11/30 17:58:30						# AR 12457				
12/01 06:10:18 - 12/02 09:32:00	Track ( 900.4, 186.1)	@ 12/01 06:10:00						# AR 12457				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

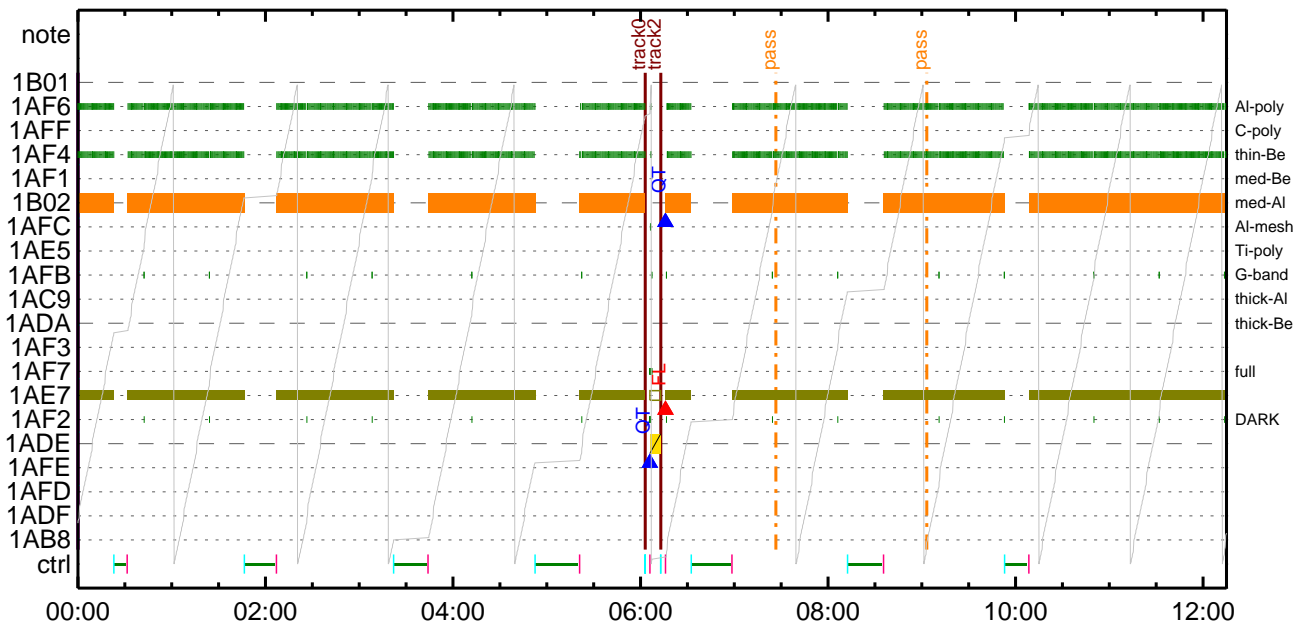
### CMDI #0546 2015/11/24



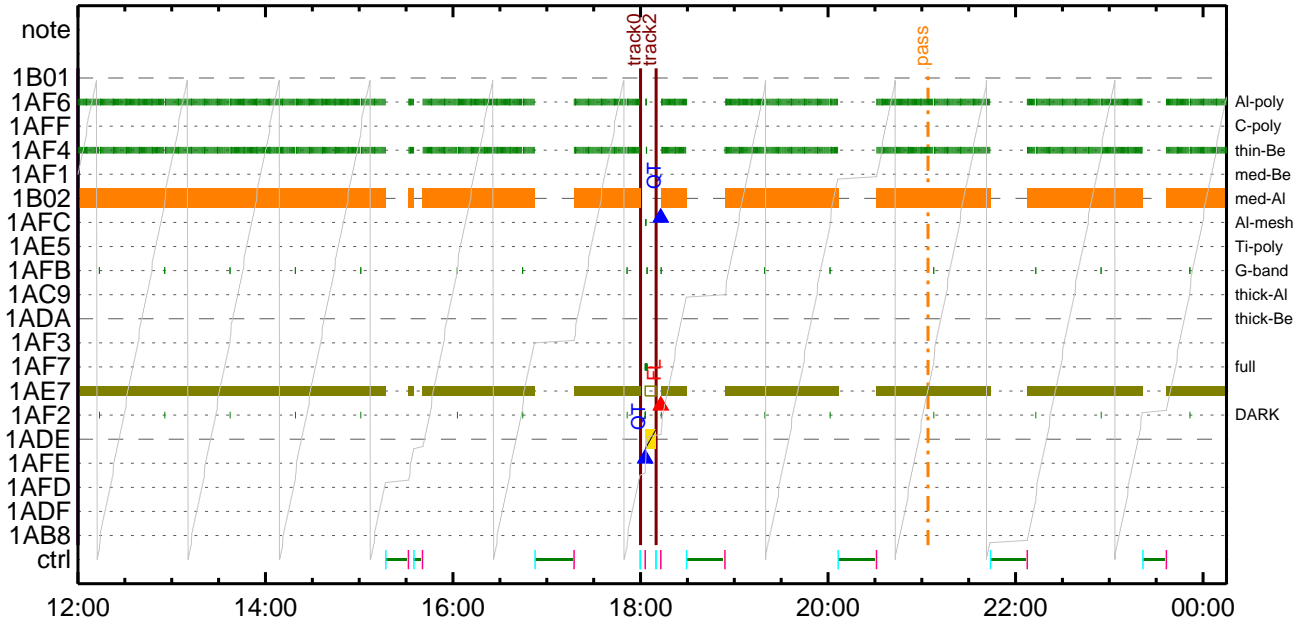
### CMDI #0546 2015/11/24



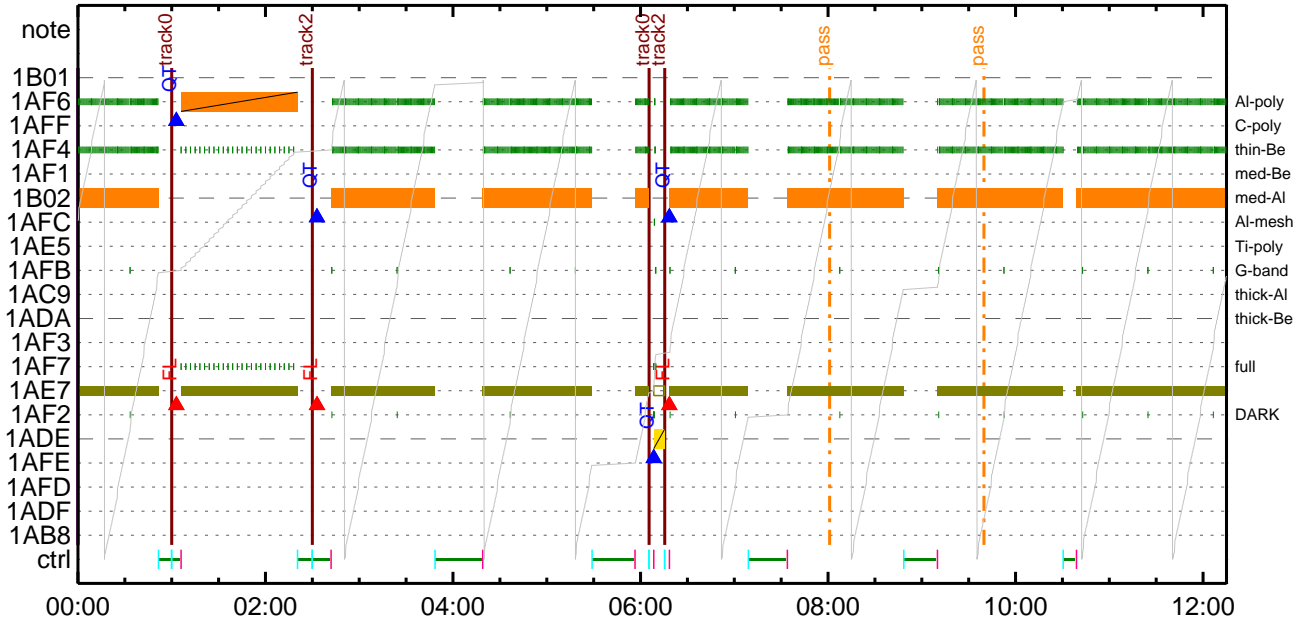
### CMDI #0546 2015/11/25



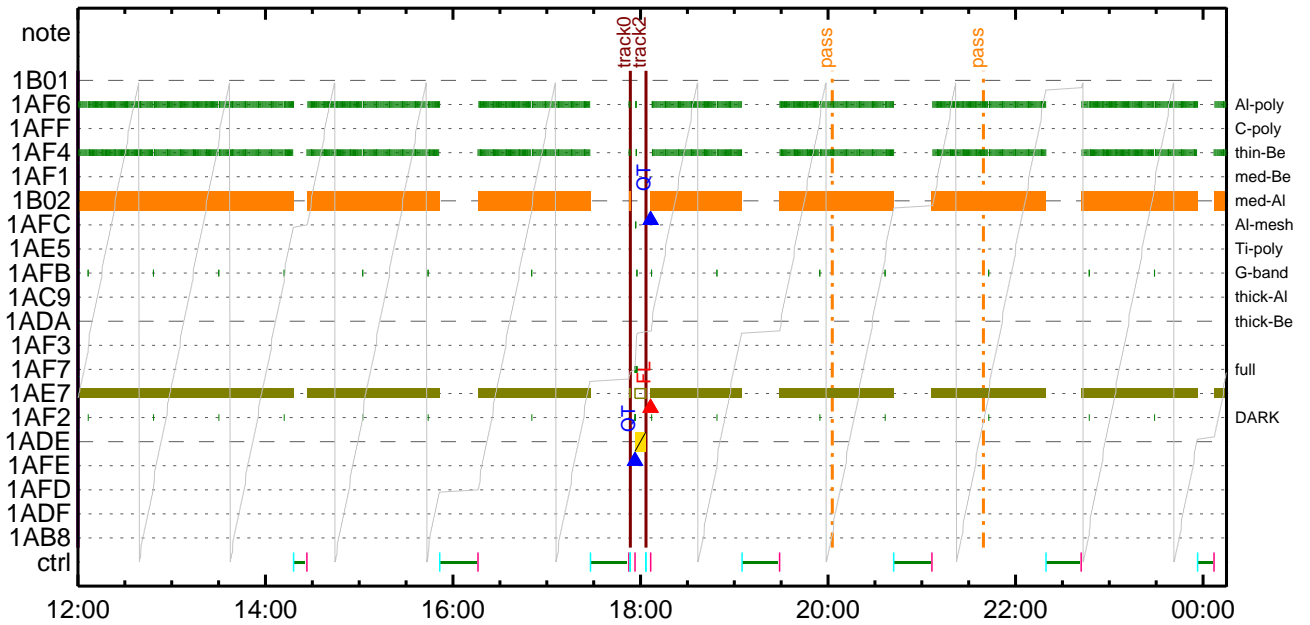
CMDI #0546 2015/11/25



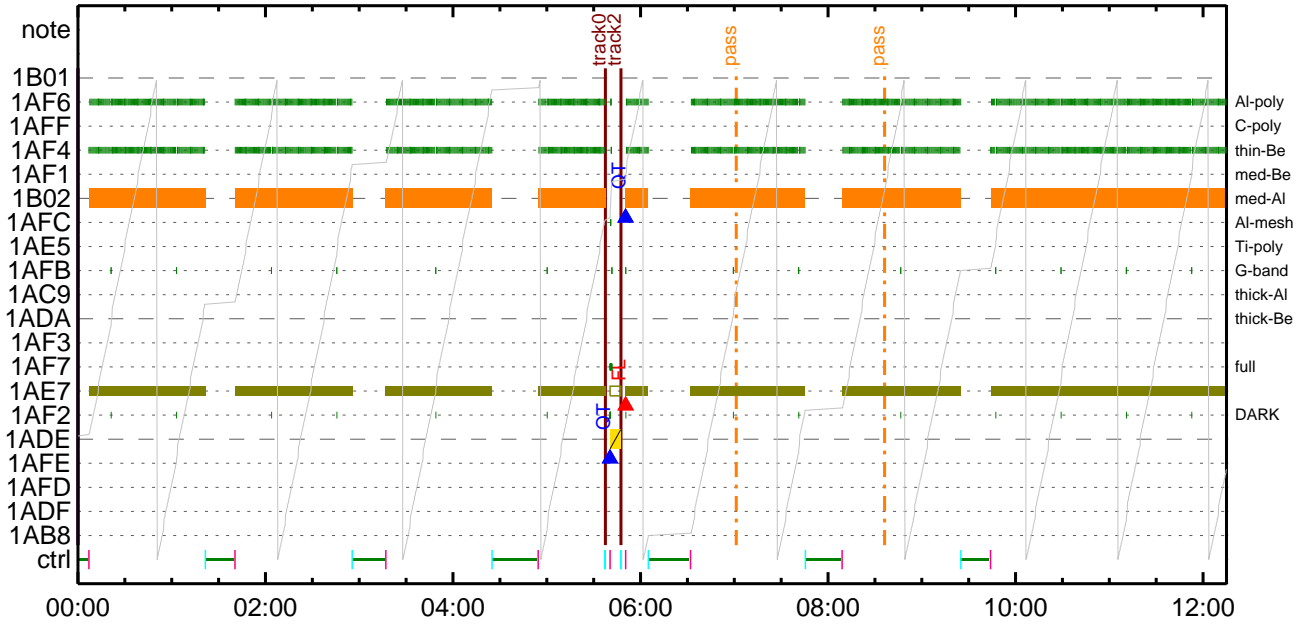
CMDI #0546 2015/11/26



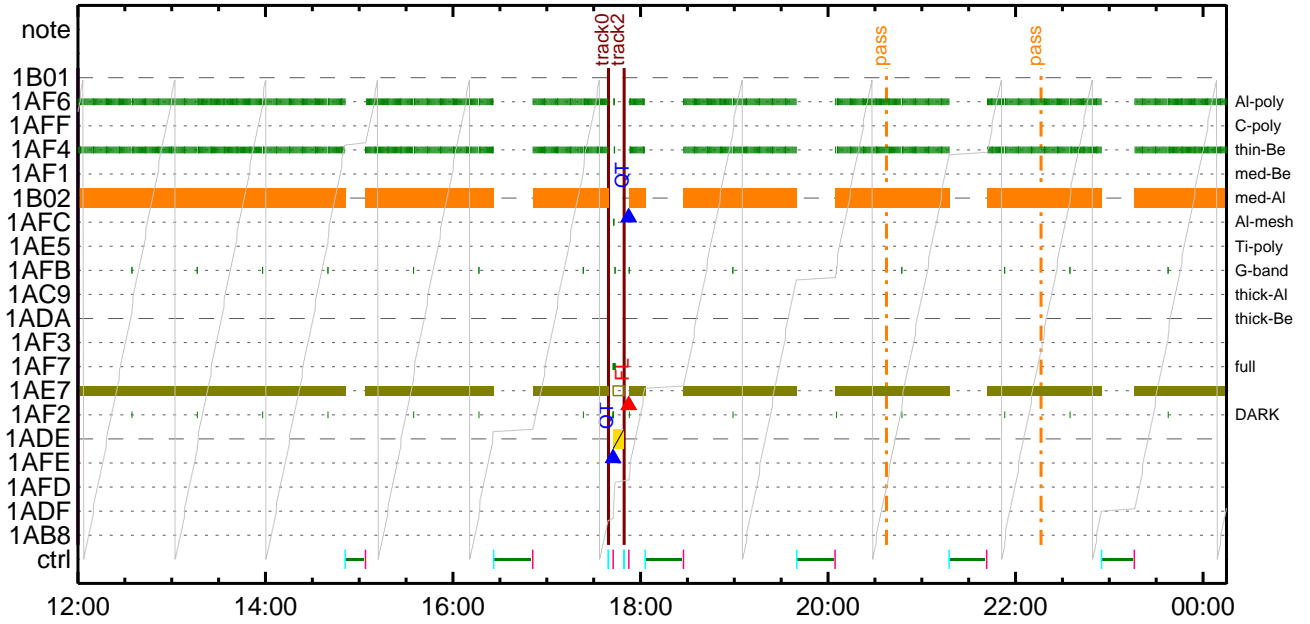
CMDI #0546 2015/11/26



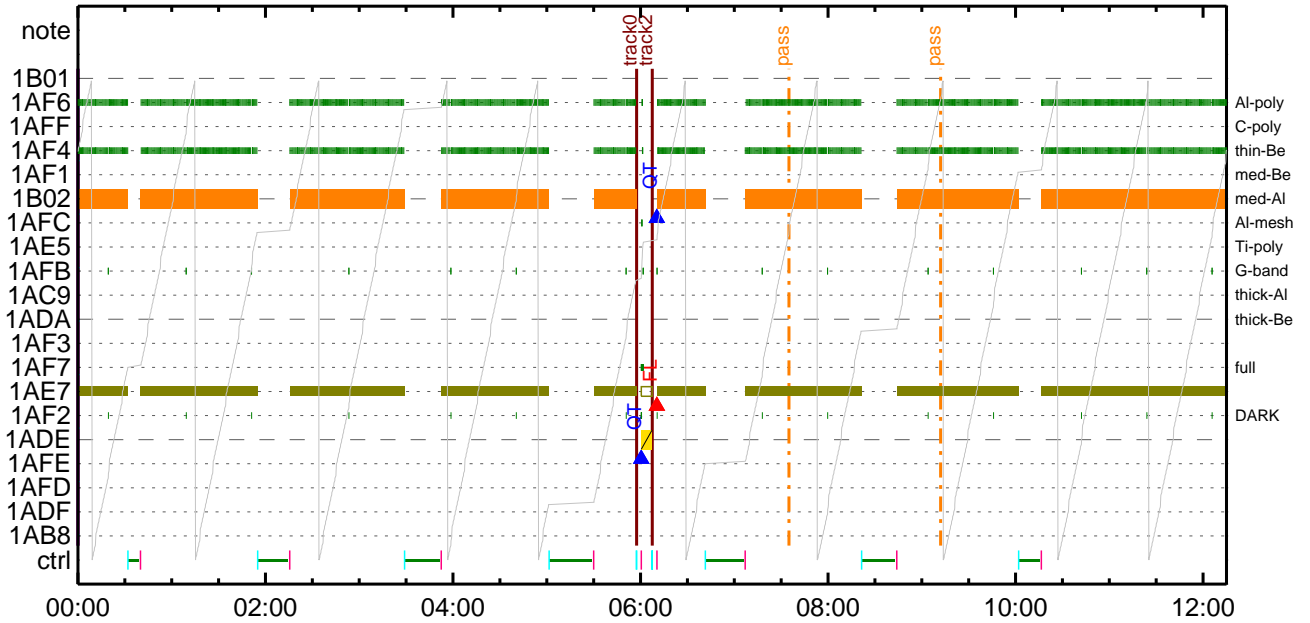
CMDI #0546 2015/11/27



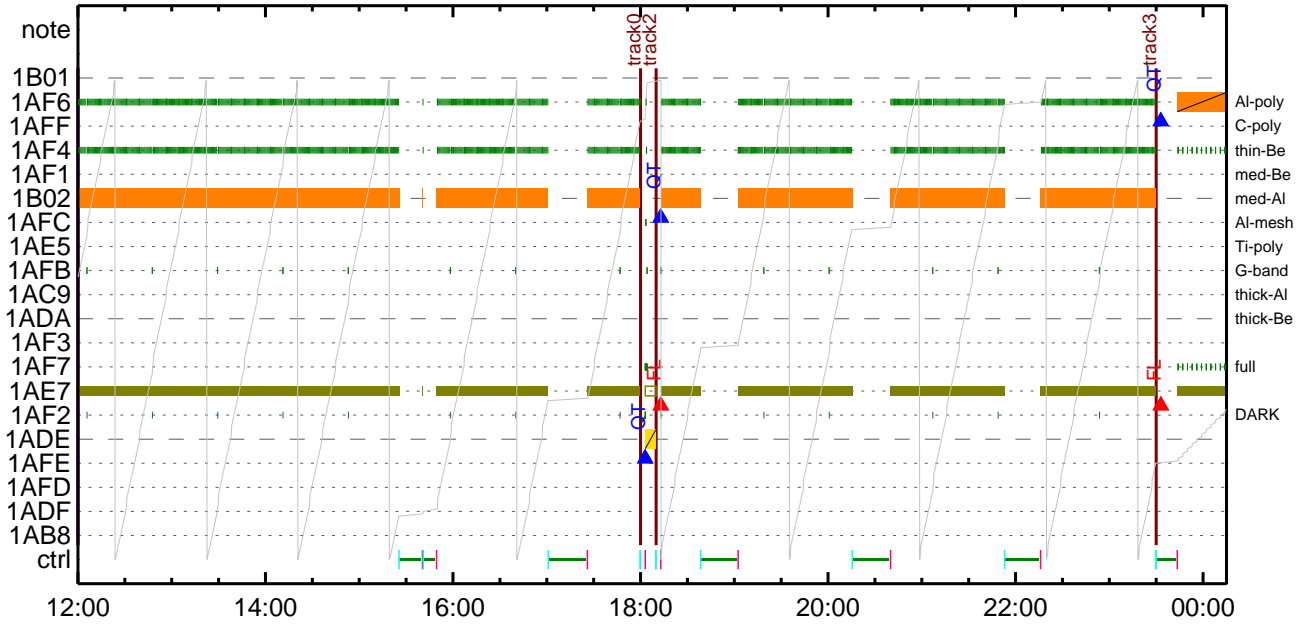
CMDI #0546 2015/11/27



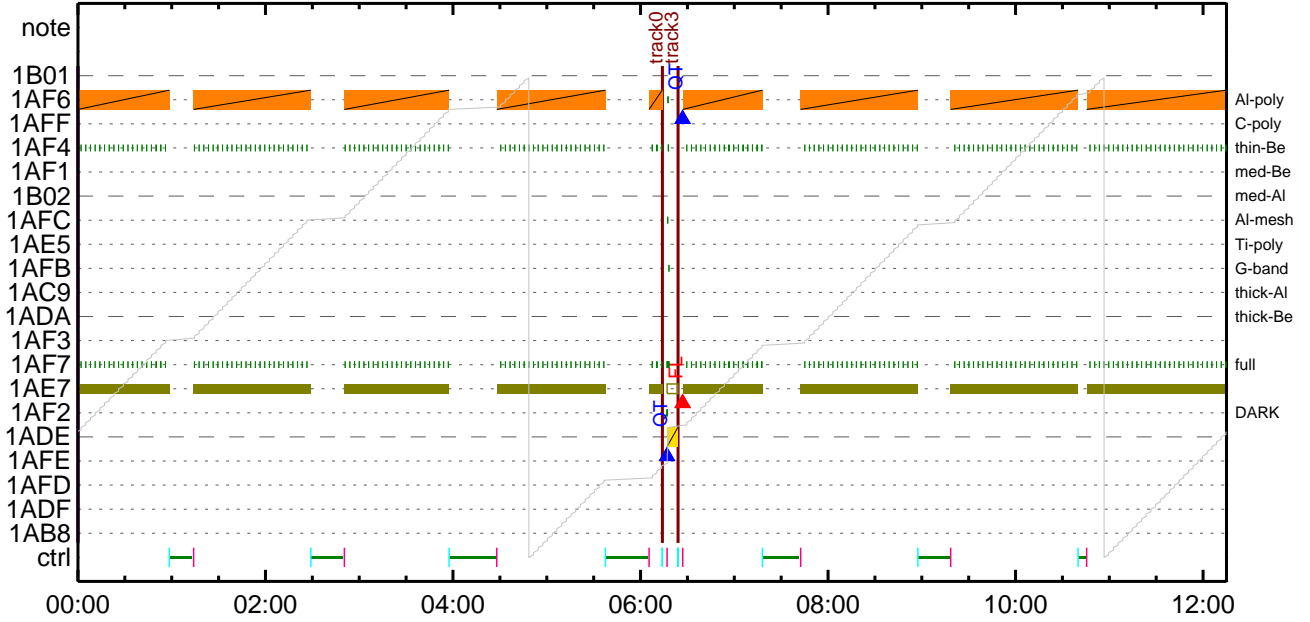
CMDI #0546 2015/11/28



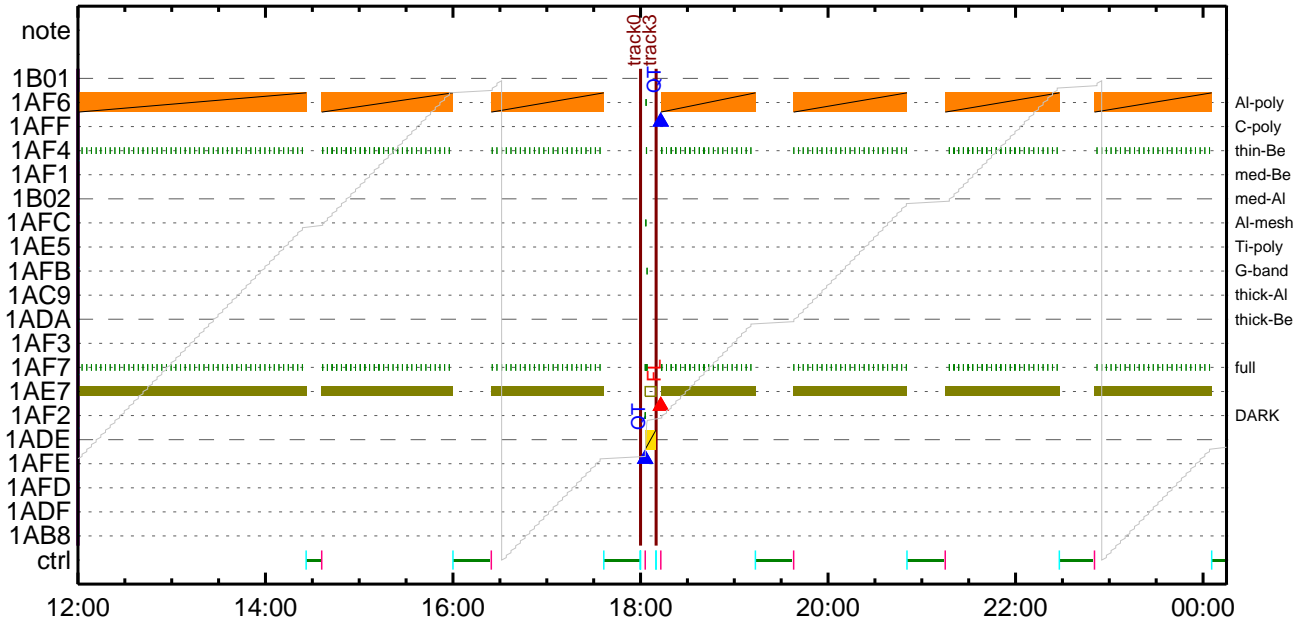
CMDI #0546 2015/11/28



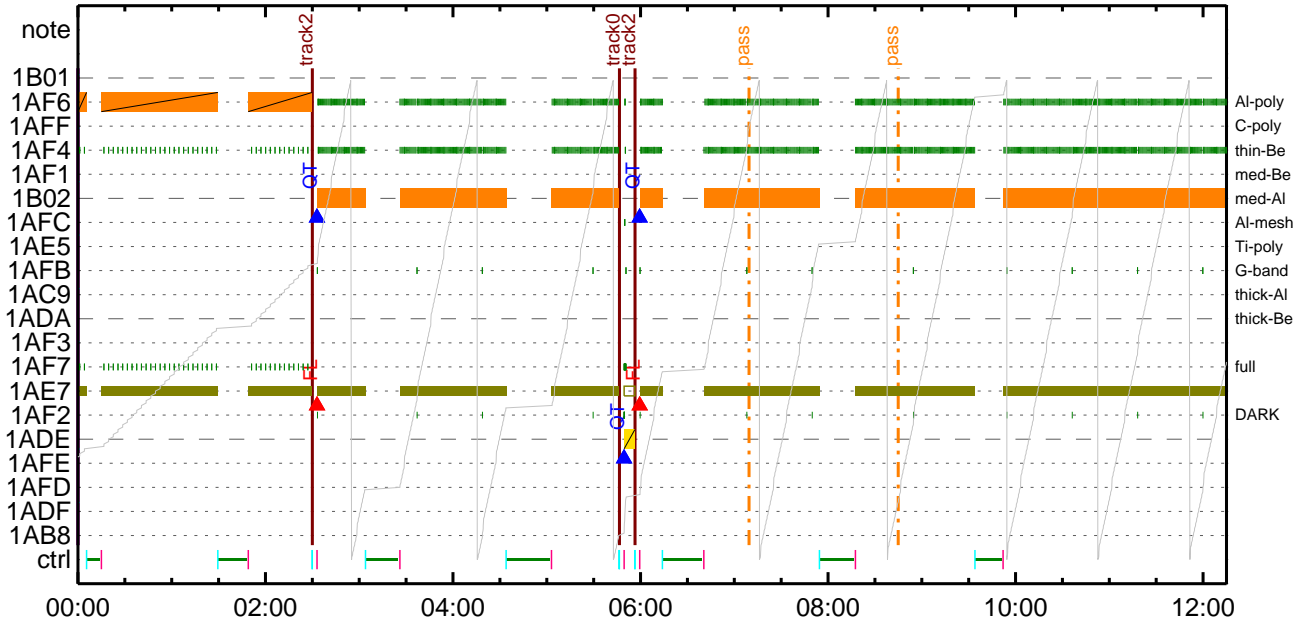
CMDI #0546 2015/11/29



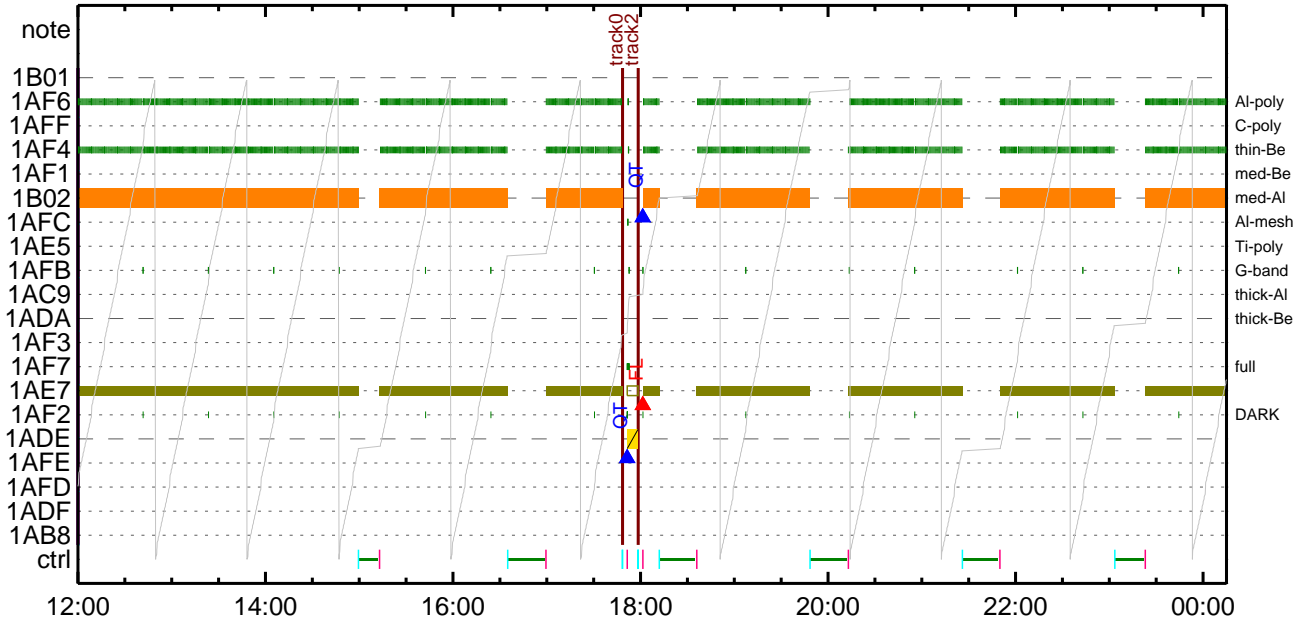
CMDI #0546 2015/11/29



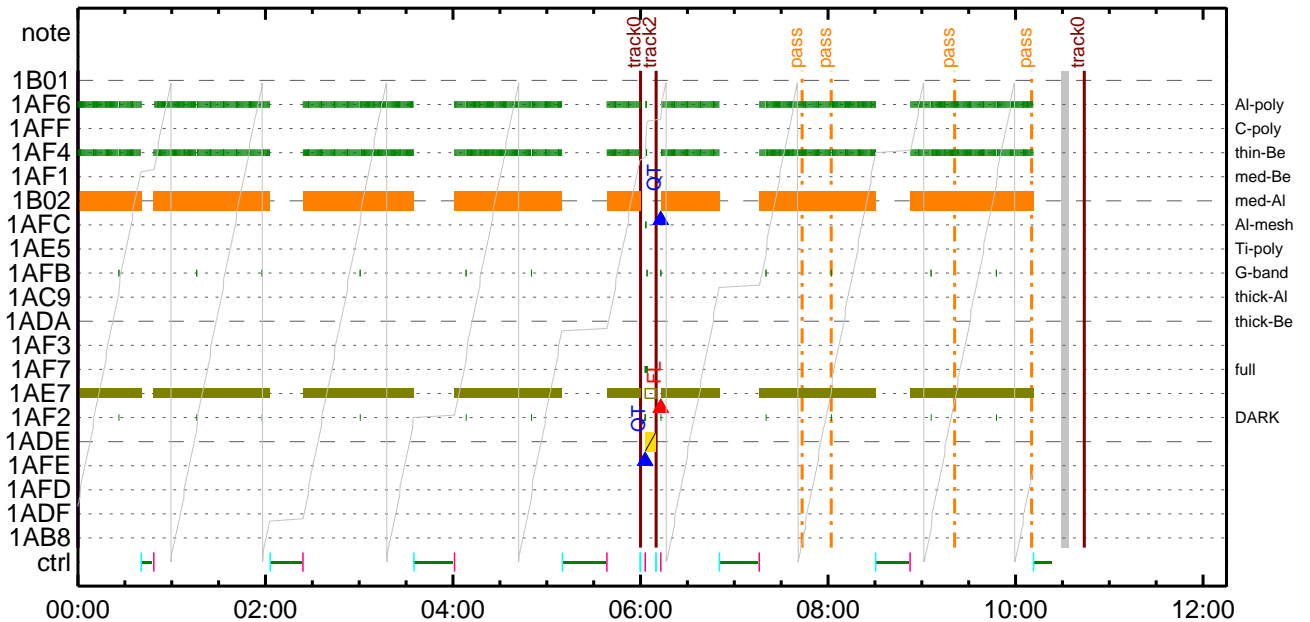
CMDI #0546 2015/11/30



CMDI #0546 2015/11/30

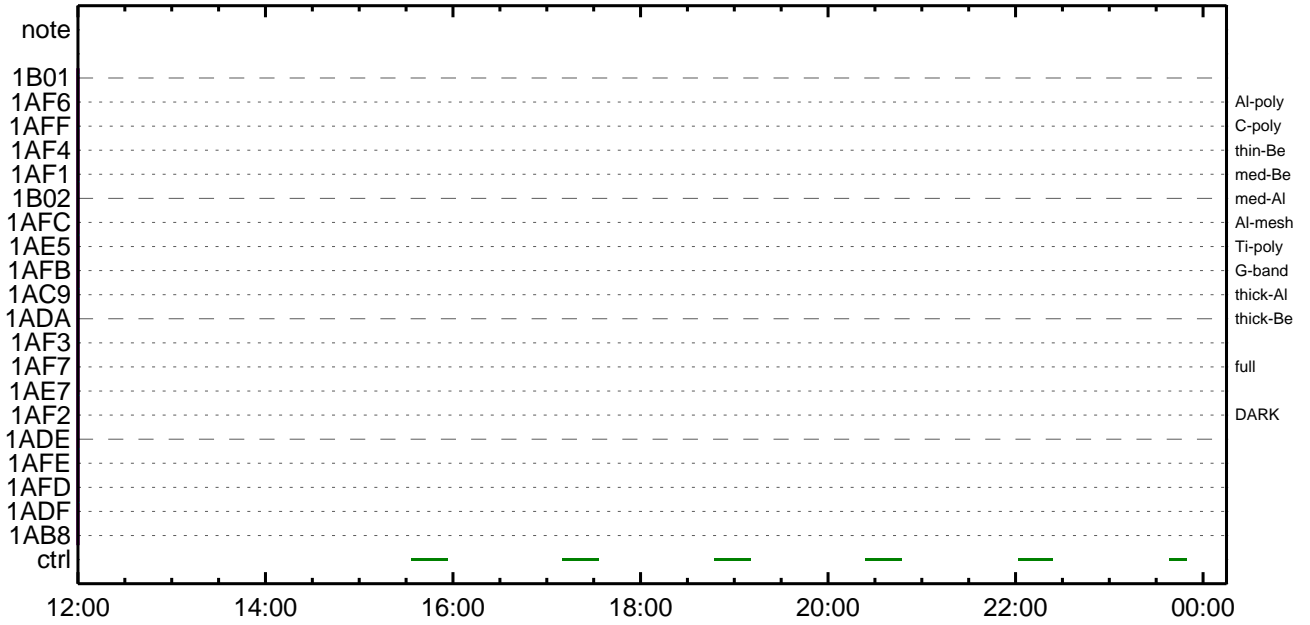


CMDI #0546 2015/12/01

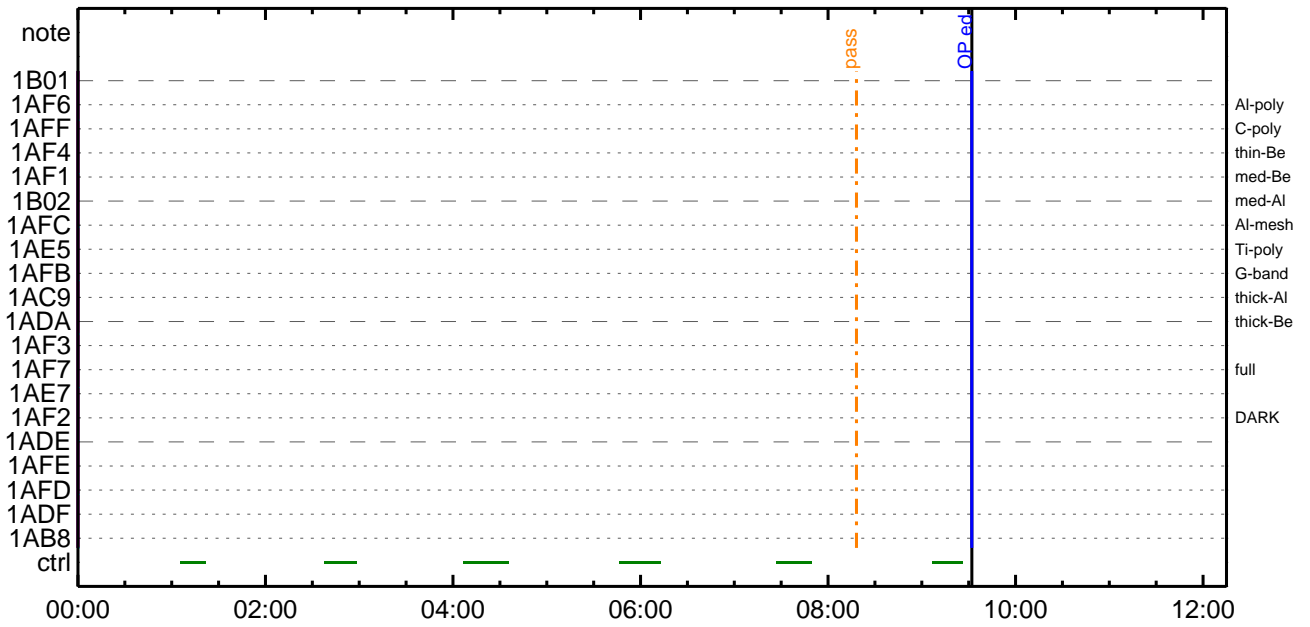




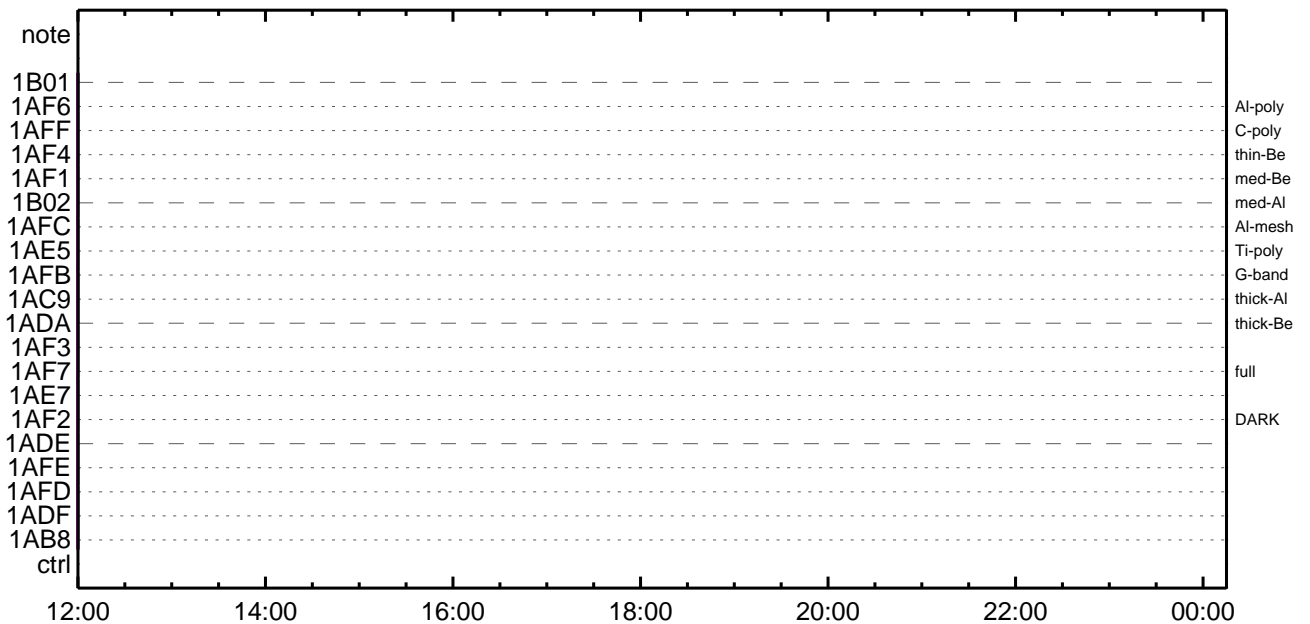
CMDI #0546 2015/12/01



CMDI #0546 2015/12/02



CMDI #0546 2015/12/02





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0096 C.                0300;çSET0EDUMP01Æ±°iYÑY¹0Ç¹Ô0|0³0E;£
0097 C.
0098 . C. TIY³YF¥ÖYÉ00ðÄDİ¿(UT)
0099 +. TI 2015-11-24 09:37:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2015-11-24 09:37:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2015-11-24 09:37:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2015-11-24 09:41:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. °E²¼0İÄè%îİÑ0İYÁY§YÄY-¹àİÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]              EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]              EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]              EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]              EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]              EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]               EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]             EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]               EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                 EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]              EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]             EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C. YÄYÖY×½¹İ»0ð³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0ð³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y½.Yİ;¼YÈ;È0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                 EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]              EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]             EQ        4M
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C. Execute, after the success of OP upload.
0157 +. TI 2015-11-24 09:41:00.0
0158 DC 07-F0 MDP_XRT_MODE_STBY
0159 BC (c3)
0160 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0161 C.
0162 C. ***** XRT END *****
0163 C. *****
0164 C. SOT TI command set
0165 C. *****
0166 C. Execute, after the success of OP upload.
0167 +. TI 2015-11-24 09:41:16.0
0168 DC 07-F0 MDP_SOT_MODE_STBY
0169 BC (41)
0170 . C. -----
0171 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0172 C. -----
0173 C. ***** SOT END *****
0174 C.
0175 . C. ***** MDP `ûÄİ0İ»0¼Y0ÈÄ0¹0èDCBC•x²è *****
0176 C. (¼á°İYÖYÄYÈYbYÈYáYçYè0E¼00¼Ä»Ü0¹0è)
0177 . S. DC-BC dcbc-402:DCBC
0178 (MDP_known_event)
0179 C.
0180 C.
0181 . C. ***** YD¥¹.İ Daily±;İÑ0È`00¹0èDCBC•x²è *****
0182 . S. DC-BC dcbc-153:DCBC
0183 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0184 C.
0185 C.
0186 . C. ;äLOS¥ÄY§YÄY-¼Ä»Ü;ä
0187 C.
0188 . C. ***** LOS *****
0189 C.

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0096 C.
0097 C.
0098 . C. Check EIS_mode = BAKE
0099 . C. Verify: EIS_CCDH_CNTRL_STS = RUN, EIS_CCDH_DUTY = 10
0100 . C. EIS_CCDA_HTR_PWR = ON/OFF, EIS_CCDB_HTR_PWR = ON/OFF
0101 . C. decrease heater power to 5%: at the beginning of uplink
0102 +. DC 07-FC EIS_CHG_CCDH_DUTY-5
0103 BC (2a)
0104 . C. Verify: EIS_CCDH_CNTRL_STS = RUN, EIS_CCDH_DUTY = 5
0105 . C. EIS_CCDA_HTR_PWR = OFF, EIS_CCDB_HTR_PWR = OFF
0106 C.
0107 C. ***** XRT START *****
0108 C.
0109 +. DC 07-F0 MDP_XRT_CTRL_MANU
0110 BC (c1)
0111 + DC 07-F0 MDP_XRT_MODE_STBY
0112 BC (c3)
0113 . C. ----- Success Verify ? OK / NG ____
0114 C.
0115 C. XRT Obs. Table Upload
0116 . S. RAM ram-291:MDP_OBS_X
0117 ( )
0118 C.
0119 +. DC 07-F0 MDP_DUMP_XRTTBL
0120 BC (84 07 00 00 00 3a d4)
0121 . C. ----- Comparison Check ? OK / ERR ____
0122 C.
0123 C.
0124 +. DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 01 b1 b1 04 04)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 02 b1 b1 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 03 b1 b1 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 04 b1 b1 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 06 85 83 06 06)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 07 80 80 20 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 08 80 80 20 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 09 80 80 08 20)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 14)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 07)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2015-11-24 09:41:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 . C. *****
0168 C. SOT table upload
0169 C. *****
0170 . C. < Stop FG table >
0171 +. DC 07-F0 MDP_FG_CTRL_MANU
0172 BC (51)
0173 . C. -----
0174 C. MDP_FG_CTRL_MODE = MANU [ ]
0175 C. -----
0176 C.
0177 . C. <Upload FG Observation Table>
0178 . S. RAM ram-263:MDP_OBS_F
0179 ( )
0180 C.
0181 . C. < Dump RAMID=MDP_OBS_F >
0182 +. DC 07-F0 MDP_DUMP_FGTBL
0183 BC (82 07 00 00 00 38 b8)
0184 C. -----
0185 C. MDP_OBS_F verify = OK/NG [ ]
0186 C. -----
0187 C.
0188 . C. < Upload DPL table >
0189 C.
0190 C. ¥ç¥Ã¥×¥í;¼¥É°îÃ°¤ÈSTS_CHK¤òOFF¤È¤¹¤è
0191 C.
0192 . S. RAM ram-271:MDP_DPL
0193 ( )

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\*\*\* OP Sequence for XRT \*\*\*

2015/11/24	09:51:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/24	09:51:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/24	09:51:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]					
		XRT_FOCUS_RECAL	2	07-F8	78 00		
2015/11/24	09:52:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2015/11/24	09:55:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/11/24	09:56:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/11/24	09:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/11/24	09:56:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/11/24	09:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/11/24	09:56:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/11/24	09:58:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2015/11/24	09:58:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2015/11/24	09:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/11/24	14:43:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/24	14:43:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/11/24	14:43:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/11/24	14:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/11/24	14:54:00.0	XRT_Custom_430_OG [0x1ae]					
2015/11/24	14:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/11/24	16:17:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/24	16:17:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/11/24	16:17:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/11/24	16:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/11/24	16:41:30.0	XRT_Custom_430_OG [0x1ae]					
2015/11/24	16:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/11/24	17:54:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/24	17:54:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2015/11/24	17:54:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/11/24	17:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/11/24	18:17:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/24	18:17:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/24	18:17:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/11/24	18:18:00.5	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2015/11/24	18:18:18.0	XRT_FLD_DIS_406_OG [0x196]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/11/24	18:20:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/11/24	18:20:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/11/24	18:20:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02		
2015/11/24	18:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/11/24	18:27:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/24	18:27:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/24	18:27:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/11/24	18:28:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2015/11/24	18:28:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/11/24	18:28:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/11/24	18:28:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/11/24	18:28:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/11/24	18:28:26.0	XRT_FLD_RESET_433_OG [0x1b1]					

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 2/19

2015/11/24	18:30:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/11/24	18:30:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/24	18:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/24	19:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/24	19:31:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/24	19:31:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/24	19:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/24	19:54:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/24	19:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/24	21:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/24	21:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/24	21:08:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/24	21:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/24	21:32:00.0	XRT_Custom_430_OG [0x1ae]				
2015/11/24	21:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/24	22:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/24	22:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/24	22:46:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/24	22:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/24	23:06:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/24	23:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	00:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	00:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	00:23:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/25	00:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/25	00:30:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/25	00:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	01:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	01:46:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	01:46:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/25	01:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/25	02:06:00.0	XRT_Custom_430_OG [0x1ae]				
2015/11/25	02:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	03:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	03:22:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	03:22:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/25	03:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/25	03:43:00.0	XRT_Custom_430_OG [0x1ae]				
2015/11/25	03:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	04:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	04:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	04:52:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/25	04:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/25	05:20:00.0	XRT_Custom_430_OG [0x1ae]				
2015/11/25	05:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	06:02:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	06:02:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/25	06:03:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/11/25	06:03:18.0	XRT_FLD_DIS_406_OG [0x196]				

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 3/19

2015/11/25	06:05:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/25	06:05:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/25	06:05:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/11/25	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	06:12:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	06:12:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	06:12:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/25	06:13:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00
2015/11/25	06:13:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/25	06:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/25	06:13:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/25	06:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/25	06:13:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	06:15:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/11/25	06:15:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/25	06:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	06:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	06:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	06:32:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/25	06:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/25	06:57:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/25	06:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	08:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	08:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	08:12:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/25	08:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/25	08:34:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/25	08:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	09:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	09:53:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	09:53:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/25	09:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/25	10:07:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/25	10:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	15:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	15:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	15:17:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/25	15:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/25	15:30:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/25	15:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	15:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	15:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	15:35:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/25	15:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/25	15:39:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/25	15:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/25	16:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/25	16:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/25	16:52:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]				

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 4/19

2015/11/25	16:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2015/11/25	17:16:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/25	17:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/25	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/25	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/25	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/11/25	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/11/25	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/11/25	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/11/25	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/11/25	18:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2015/11/25	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/25	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/25	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/25	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/11/25	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00	
2015/11/25	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/11/25	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/11/25	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/11/25	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/11/25	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/25	18:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2015/11/25	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/11/25	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/25	18:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/25	18:29:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/25	18:29:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/25	18:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/25	18:53:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/25	18:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/25	20:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/25	20:06:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/25	20:06:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/25	20:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/25	20:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/25	20:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/25	21:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/25	21:44:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/25	21:44:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/25	21:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/25	22:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/25	22:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/25	23:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/25	23:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/25	23:21:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/25	23:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/25	23:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/25	23:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/26	00:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

2015/11/26	00:51:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26	00:51:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/26	00:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/26	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/26	00:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26	00:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26	01:00:00.5	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/11/26	01:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 2c 72 b8 00	
2015/11/26	01:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/11/26	01:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/11/26	01:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/11/26	01:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/11/26	01:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/26	01:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2015/11/26	01:05:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/11/26	01:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/11/26	02:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/26	02:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26	02:20:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/26	02:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/26	02:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/26	02:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26	02:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26	02:30:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/11/26	02:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00	
2015/11/26	02:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/11/26	02:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/11/26	02:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/11/26	02:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/11/26	02:32:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/26	02:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2015/11/26	02:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/11/26	02:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/11/26	03:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/26	03:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26	03:48:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/26	03:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/26	04:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/26	04:19:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/11/26	05:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/26	05:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26	05:29:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/26	05:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/26	05:55:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/26	05:56:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2015/11/26	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/26	06:05:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26	06:05:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/26		XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 6/19

2015/11/26	06:05:30.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2015/11/26	06:05:48.0	XRT_FLD_DIS_406_OG [0x196] MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/26	06:08:24.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/26	06:08:26.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/26	06:08:28.0	XRT_QT_PROG_SET_421_OG [0x1a5] MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2015/11/26	06:08:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/26	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/26	06:15:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/26	06:15:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/11/26	06:15:30.0	AOCS_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02	00	00	00	00
2015/11/26	06:15:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/26	06:15:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/26	06:15:52.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/26	06:15:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/26	06:15:56.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/26	06:18:26.0	XRT_QT_PROG_SET_449_OG [0x1c1] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2015/11/26	06:18:28.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/11/26	06:18:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/26	07:09:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/26	07:09:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/26	07:09:04.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/26	07:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/26	07:33:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/26	07:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/26	08:48:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/26	08:48:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/26	08:48:34.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/26	08:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/26	09:09:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/26	09:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/26	10:30:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/26	10:30:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/26	10:30:34.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/26	10:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/26	10:38:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/26	10:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/26	14:18:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/26	14:18:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/26	14:18:04.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/26	14:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/26	14:25:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/26	14:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/26	15:51:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/26	15:51:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/26	15:51:34.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/26	15:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/26	16:15:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/26	16:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/26	17:28:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 7/19

2015/11/26	17:28:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/26	17:28:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/26	17:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/26	17:51:30.5	XRT_Custom_430_OG [0x1ae]				
2015/11/26	17:52:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/26	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/26	17:53:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/26	17:53:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/26	17:53:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/11/26	17:53:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/11/26	17:56:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/26	17:56:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/26	17:56:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/11/26	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/26	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/26	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/26	18:03:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/26	18:03:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00
2015/11/26	18:03:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/26	18:03:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/26	18:03:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/26	18:03:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/26	18:03:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/26	18:06:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/11/26	18:06:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/26	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/26	19:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/26	19:05:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/26	19:05:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/26	19:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/26	19:28:00.0	XRT_Custom_430_OG [0x1ae]				
2015/11/26	19:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/26	20:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/26	20:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/26	20:42:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/26	20:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/26	21:05:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/26	21:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/26	22:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/26	22:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/26	22:19:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/26	22:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/26	22:41:00.0	XRT_Custom_430_OG [0x1ae]				
2015/11/26	22:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/26	23:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/26	23:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/26	23:56:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/26	23:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/27	00:06:00.0	XRT_Custom_430_OG [0x1ae]				

2015/11/27	00:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/27	01:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/27	01:21:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/27	01:21:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/27	01:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/27	01:39:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/27	01:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/27	02:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/27	02:55:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/27	02:55:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/27	02:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/27	03:16:00.5	XRT_Custom_430_OG [0x1ae]							
2015/11/27	03:17:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/27	04:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/27	04:25:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/27	04:25:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/27	04:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/27	04:53:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/27	04:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/27	05:37:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/27	05:37:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/27	05:37:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/11/27	05:37:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/11/27	05:37:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/27	05:40:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/27	05:40:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/27	05:40:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/11/27	05:40:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/27	05:47:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/27	05:47:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/27	05:47:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/11/27	05:47:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/11/27	05:47:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/27	05:47:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/27	05:47:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/27	05:47:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/27	05:47:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/27	05:50:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2015/11/27	05:50:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/11/27	05:50:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/27	06:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/27	06:05:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/27	06:05:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/27	06:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/27	06:31:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/27	06:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/27	07:45:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/27	07:45:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/27	07:45:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							



2015/11/27	07:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2015/11/27	08:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/27	08:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/27	09:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/27	09:25:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/27	09:25:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/27	09:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/27	09:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/27	09:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/27	14:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/27	14:51:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/27	14:51:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/27	14:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/27	15:03:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/27	15:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/27	16:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/27	16:26:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/27	16:26:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/27	16:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/27	16:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/27	16:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/27	17:39:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/27	17:39:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/27	17:39:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/11/27	17:39:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/11/27	17:39:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/11/27	17:42:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/11/27	17:42:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/11/27	17:42:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2015/11/27	17:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/27	17:49:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/27	17:49:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/27	17:49:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/11/27	17:49:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00	
2015/11/27	17:49:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/11/27	17:49:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/11/27	17:49:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/11/27	17:49:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/11/27	17:49:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/27	17:52:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2015/11/27	17:52:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/11/27	17:52:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/27	18:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/11/27	18:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/11/27	18:03:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/11/27	18:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/11/27	18:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/27	18:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/11/27	19:40:00.0	XRT_CTRL_MANU_400_OG [0x190]					

2015/11/27	19:40:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/27	19:40:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/27	19:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/27	20:03:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/27	20:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/27	21:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/27	21:17:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/27	21:17:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/27	21:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/27	21:40:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/27	21:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/27	22:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/27	22:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/27	22:55:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/27	22:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/27	23:15:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/27	23:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	00:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	00:32:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	00:32:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	00:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	00:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	00:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	01:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	01:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	01:55:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	01:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	02:14:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	02:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	03:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	03:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	03:29:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	03:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	03:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	03:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	05:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	05:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	05:01:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	05:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	05:29:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	05:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	05:57:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	05:57:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	05:57:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	05:57:30.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/28	05:57:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2015/11/28	06:00:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/11/28	06:00:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/28	06:00:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/28	06:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 11/19

2015/11/28	06:07:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	06:07:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	06:07:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/28	06:07:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00
2015/11/28	06:07:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/28	06:07:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/28	06:07:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/28	06:07:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/28	06:07:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	06:10:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/11/28	06:10:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/28	06:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	06:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	06:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	06:41:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	06:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	07:06:00.5	XRT_Custom_430_OG [0x1ae]				
2015/11/28	07:07:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	08:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	08:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	08:21:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	08:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	08:43:00.0	XRT_Custom_430_OG [0x1ae]				
2015/11/28	08:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	10:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	10:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	10:02:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	10:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	10:15:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/28	10:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	15:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	15:25:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	15:25:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	15:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	15:39:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/28	15:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	15:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	15:41:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	15:41:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	15:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	15:48:30.0	XRT_Custom_430_OG [0x1ae]				
2015/11/28	15:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	17:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	17:01:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	17:01:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	17:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	17:25:00.0	XRT_Custom_430_OG [0x1ae]				
2015/11/28	17:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 12/19

2015/11/28	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/28	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2015/11/28	18:00:18.0	XRT_FLD_DIS_406_OG [0x196] MDP_XRT_FLD_DIS	1	07-F0	d9
2015/11/28	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/11/28	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/28	18:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5] MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/11/28	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/28	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02 00 00 00 00
2015/11/28	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/28	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/28	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/28	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/28	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	18:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/11/28	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/28	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	18:38:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	18:38:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	18:38:34.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	18:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	19:01:30.0	XRT_Custom_430_OG [0x1ae]			
2015/11/28	19:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	20:15:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	20:15:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	20:15:34.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	20:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	20:39:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/28	20:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	21:53:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	21:53:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	21:53:04.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/28	21:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/28	22:15:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/28	22:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/28	23:29:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	23:29:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/28	23:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/11/28	23:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	03 00 00 00 00
2015/11/28	23:30:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/28	23:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/28	23:30:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/28	23:30:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/28	23:30:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/28	23:32:56.0	XRT_QT_PROG_SET_401_OG [0x191] MDP_XRT_QT_PROG_SET	2	07-F0	c4 13

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 13/19

2015/11/28	23:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/11/28	23:42:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/28	23:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	00:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	00:58:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	00:58:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	01:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	01:13:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/29	01:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	02:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	02:29:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	02:29:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	02:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	02:49:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/29	02:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	03:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	03:57:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	03:57:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	04:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	04:27:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/29	04:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	05:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	05:37:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	05:37:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	05:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	06:04:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/29	06:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	06:13:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	06:13:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	06:13:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/11/29	06:14:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2015/11/29	06:14:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/29	06:16:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/29	06:16:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/29	06:16:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2015/11/29	06:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	06:23:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	06:23:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	06:23:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/11/29	06:24:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03	00	00	00	00
2015/11/29	06:24:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/29	06:24:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/29	06:24:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/29	06:24:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/29	06:24:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	06:26:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2015/11/29	06:26:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/11/29	06:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	07:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 14/19

2015/11/29	07:18:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	07:18:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	07:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	07:41:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/29	07:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	08:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	08:57:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	08:57:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	09:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	09:17:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/29	09:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	10:40:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	10:40:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	10:40:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	10:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	10:44:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/29	10:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	14:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	14:26:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	14:26:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	14:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	14:35:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/29	14:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	16:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	16:00:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	16:00:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	16:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	16:23:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/29	16:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	17:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	17:36:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/29	17:36:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/29	17:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/29	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/11/29	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/11/29	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/29	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/29	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/29	18:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/11/29	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/29	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/29	18:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/11/29	18:10:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/11/29	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/29	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/29	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 15/19

2015/11/29	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/29	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/29	18:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2015/11/29	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/29	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/29	19:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/29	19:13:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/29	19:13:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/29	19:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/29	19:37:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/29	19:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/29	20:50:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/29	20:50:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/29	20:50:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/29	20:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/29	21:14:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/29	21:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/29	22:28:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/29	22:28:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/29	22:28:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/29	22:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/29	22:49:30.0	XRT_Custom_430_OG [0x1ae]			
2015/11/29	22:50:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/30	00:05:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/30	00:05:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/30	00:05:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/30	00:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/30	00:14:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/30	00:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/30	01:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/30	01:29:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/30	01:29:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/11/30	01:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/11/30	01:48:00.0	XRT_Custom_430_OG [0x1ae]			
2015/11/30	01:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/30	02:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/30	02:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/30	02:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/11/30	02:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2015/11/30	02:30:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/11/30	02:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/11/30	02:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/11/30	02:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/11/30	02:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/11/30	02:32:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/11/30	02:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/11/30	02:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/11/30	03:04:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/11/30	03:04:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 16/19

2015/11/30	03:04:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	03:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/30	03:25:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/30	03:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	04:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	04:34:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	04:34:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	04:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/30	05:02:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/30	05:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	05:46:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	05:46:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	05:46:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/11/30	05:46:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/11/30	05:46:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/30	05:49:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/30	05:49:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/30	05:49:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/11/30	05:49:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	05:56:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	05:56:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	05:56:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/11/30	05:56:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/11/30	05:56:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/30	05:56:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/30	05:56:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/30	05:56:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/30	05:56:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	05:59:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2015/11/30	05:59:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/11/30	05:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	06:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	06:14:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	06:14:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	06:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/30	06:39:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/30	06:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	07:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	07:54:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	07:54:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	07:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/30	08:16:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/30	08:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	09:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	09:34:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	09:34:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	09:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/30	09:51:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/30	09:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				



Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 17/19

2015/11/30	14:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	14:59:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	14:59:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	15:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/30	15:12:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/30	15:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	16:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	16:35:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	16:35:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	16:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/30	16:58:30.0	XRT_Custom_430_OG [0x1ae]							
2015/11/30	16:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	17:48:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	17:48:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	17:48:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/11/30	17:48:30.0	AOCS_Or-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/11/30	17:48:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/11/30	17:51:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/11/30	17:51:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/30	17:51:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/11/30	17:51:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	17:58:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	17:58:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	17:58:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/11/30	17:58:30.0	AOCS_Or-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/11/30	17:58:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/11/30	17:58:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/11/30	17:58:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/11/30	17:58:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/11/30	17:58:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	18:01:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2015/11/30	18:01:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/11/30	18:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	18:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	18:12:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	18:12:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	18:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/30	18:35:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/30	18:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	19:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	19:48:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	19:48:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	19:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/11/30	20:12:00.0	XRT_Custom_430_OG [0x1ae]							
2015/11/30	20:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/11/30	21:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/11/30	21:26:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/11/30	21:26:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/11/30	21:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2015/11/30	21:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/11/30	21:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/11/30	23:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/11/30	23:03:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/11/30	23:03:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/11/30	23:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/11/30	23:22:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/11/30	23:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/12/01	00:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/01	00:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/01	00:40:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/01	00:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/01	00:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/01	00:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/12/01	02:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/01	02:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/01	02:03:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/01	02:03:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/01	02:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/01	02:23:00.0	XRT_Custom_430_OG [0x1ae]						
2015/12/01	02:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/12/01	03:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/01	03:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/01	03:35:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/01	03:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/01	04:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/01	04:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/12/01	05:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/01	05:10:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/01	05:10:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/01	05:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/01	05:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/01	05:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2015/12/01	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/01	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/01	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/01	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/12/01	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/12/01	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/12/01	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/12/01	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/12/01	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2015/12/01	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/01	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/01	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/01	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/12/01	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00		
2015/12/01	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/12/01	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/12/01	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/12/01			MDP_XRT_ARS_DIS	1	07-F0	d5		

Nov 24, 15 13:38

## XRT\_OGLIST\_0546.chk

Page 19/19

2015/12/01	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/01	06:12:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2015/12/01	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/12/01	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/01	06:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/01	06:50:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/01	06:50:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/01	06:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/01	07:15:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/01	07:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/01	08:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/01	08:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/01	08:30:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/01	08:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/01	08:51:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/01	08:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/01	10:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/01	10:11:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/01	10:11:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/01	10:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/01	10:44:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				