

Period: 2015/12/01 10:34:00 - 2015/12/09 10:25:00

PROG= 04 Inf.-time(s)

XOB #1AF6: CME watch - 4x4 - AEC 2/3 - Be-thin (Be-Med) - Long/Short Pairs - 180s cad

PROG= 19 Inf.-time(s)

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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PROG= 02	1-time(s)
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Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	4.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 46	2-time(s)	2.0sec										

Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 72 2-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 59 2-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + TH

Term	Pointing (x, y)	Comment
12/02 06:26:00 - 12/02 06:32:54	Fixed (0.0, 0.0)	synoptic, shifted 23.0 min
12/02 18:11:30 - 12/02 18:18:24	Fixed (0.0, 0.0)	synoptic, shifted 8.5 min
12/03 05:58:00 - 12/03 06:04:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min
12/03 18:00:00 - 12/03 18:06:54	Fixed (0.0, 0.0)	synoptic, shifted -3.0 min
12/04 06:03:00 - 12/04 06:09:54	Fixed (0.0, 0.0)	synoptic
12/04 18:02:30 - 12/04 18:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min
12/05 06:34:30 - 12/05 06:41:24	Fixed (0.0, 0.0)	synoptic, shifted 31.5 min
12/05 17:33:00 - 12/05 17:39:54	Fixed (0.0, 0.0)	synoptic, shifted 30 min
12/06 06:05:30 - 12/06 06:12:35	Fixed (0.0, 0.0)	synoptic, shifted 2.5 min

PROG= 05 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 39 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B02: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
12/02 18:21:30 - 12/03 05:54:54	Track (467.9, 36.2) @ 12/02 18:18:30	cont.
12/03 06:08:00 - 12/03 17:56:54	Track (562.3, 37.8) @ 12/03 06:05:00	cont.

PROG= 15 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 56 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec												
Seqn= 75 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 79 4-time(s) 120.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	34.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	34.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms

Term	Pointing (x, y)	Comment
12/05 09:35:00 - 12/05 15:29:54	Fixed (-16.0, 874.0)	HOP 81, N-pole

PROG= 13 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 9 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

✱ ✱ ✱ ✱ ✱

NOT USED

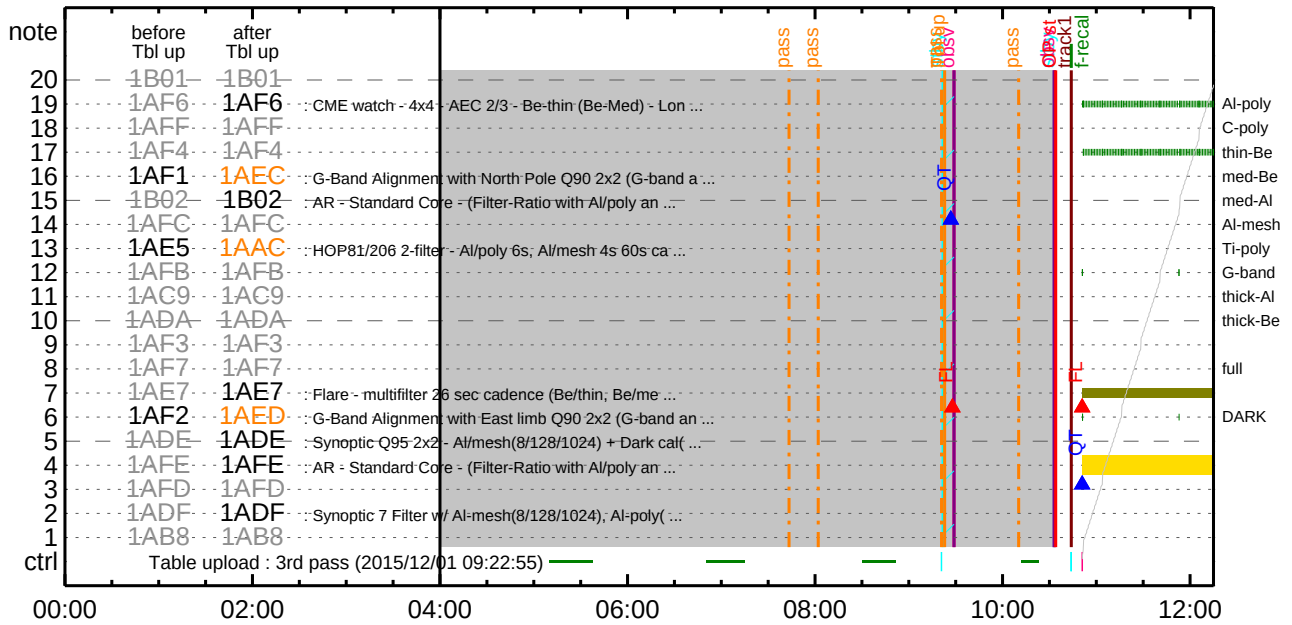
* * * * *

Flare Detection

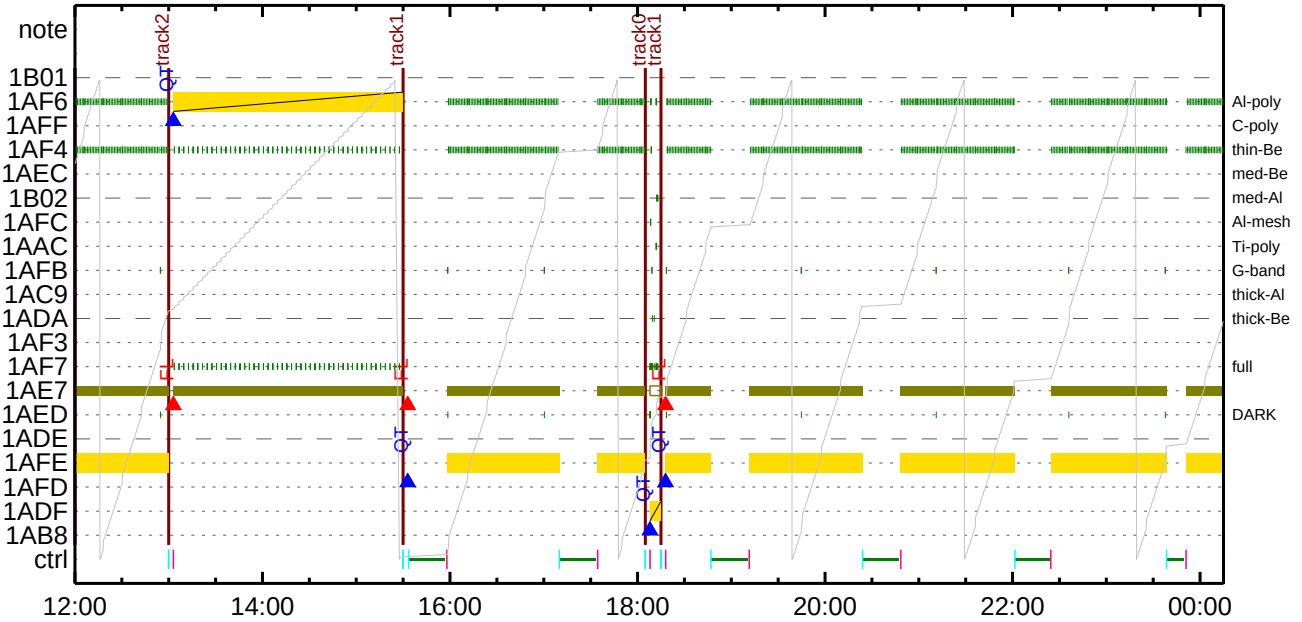
* * * * *

FLD Patrol												
Term			Pointing (x, y)					Comment				
12/01 18:15:18 - 12/02 06:23:18			Track (	257.4,	33.1)	@ 12/01 18:15:00		cont.				
12/02 06:33:18 - 12/02 18:08:48			Track (	367.5,	34.6)	@ 12/02 06:33:00		cont.				
12/02 18:18:48 - 12/03 05:55:18			Track (	467.9,	36.2)	@ 12/02 18:18:30		cont.				
12/03 06:05:18 - 12/03 17:57:18			Track (	562.3,	37.8)	@ 12/03 06:05:00		cont.				
12/03 18:07:18 - 12/04 06:00:18			Track (	651.1,	39.4)	@ 12/03 18:07:00		cont.				
12/04 06:10:18 - 12/04 17:59:48			Track (	731.1,	41.0)	@ 12/04 06:10:00		cont.				
12/04 18:09:48 - 12/05 06:31:48			Track (	800.7,	42.4)	@ 12/04 18:09:30		cont.				
12/05 06:41:48 - 12/05 15:30:18			Track (	861.7,	43.8)	@ 12/05 06:41:30		cont.				
12/05 19:40:18 - 12/06 06:02:48			Track (	911.5,	44.9)	@ 12/05 19:40:00	AR 12459					
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

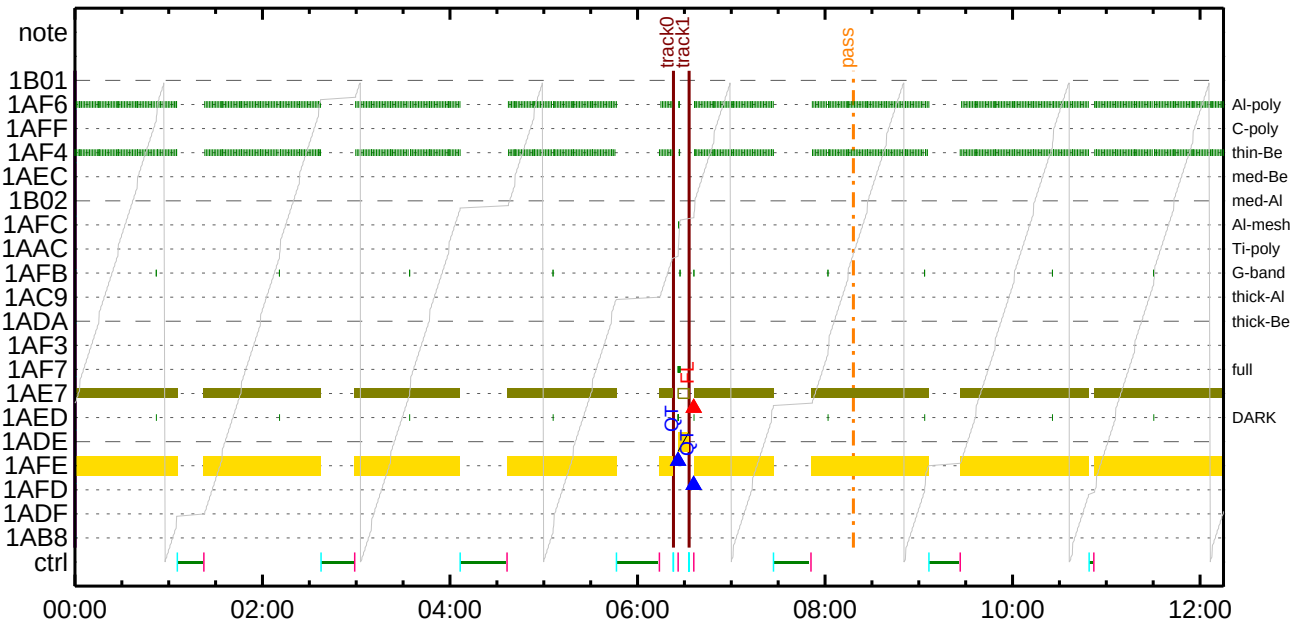
CMDI #0562 2015/12/01



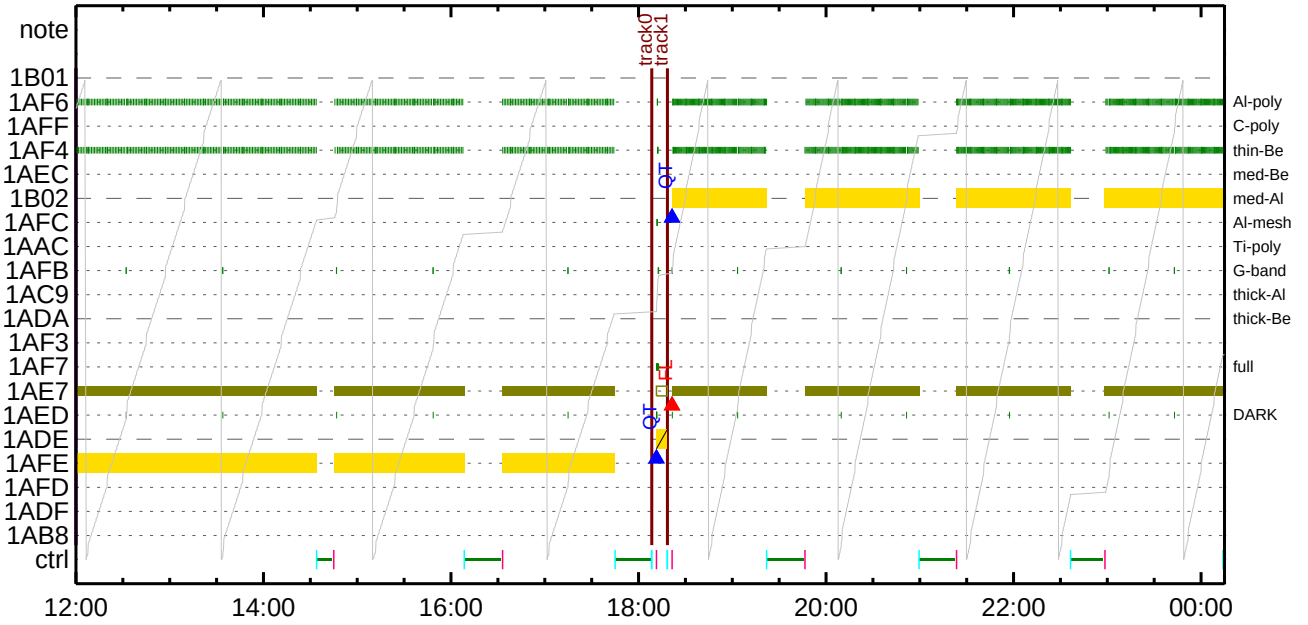
CMDI #0562 2015/12/01



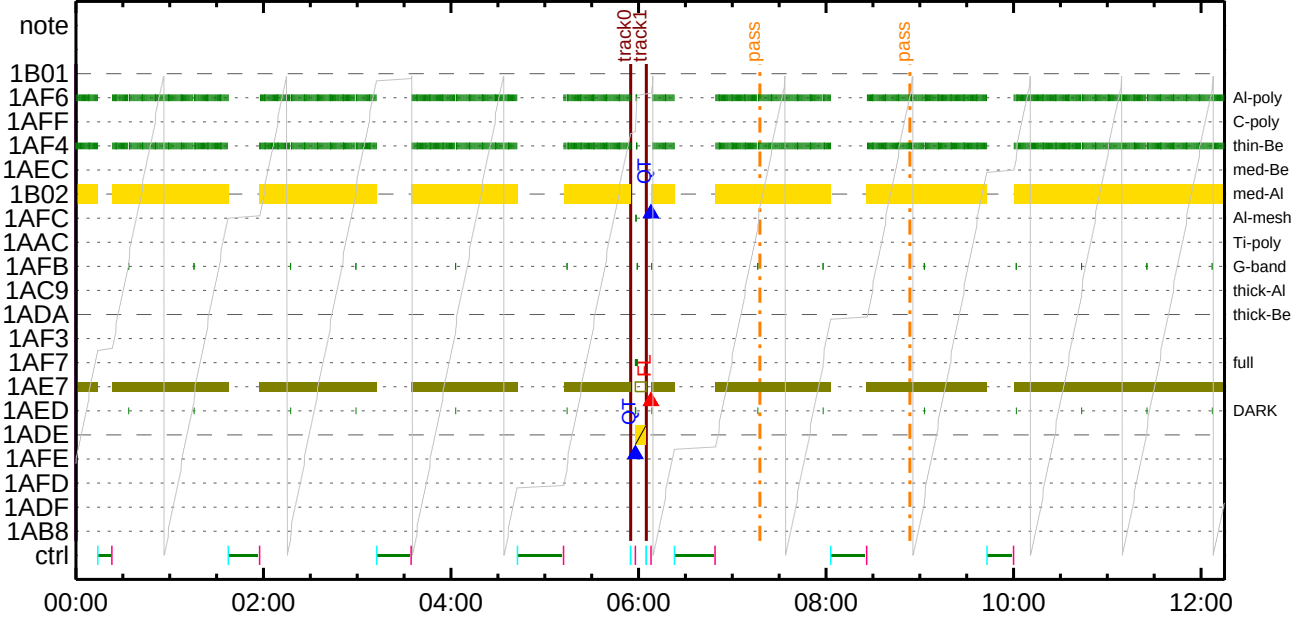
CMDI #0562 2015/12/02



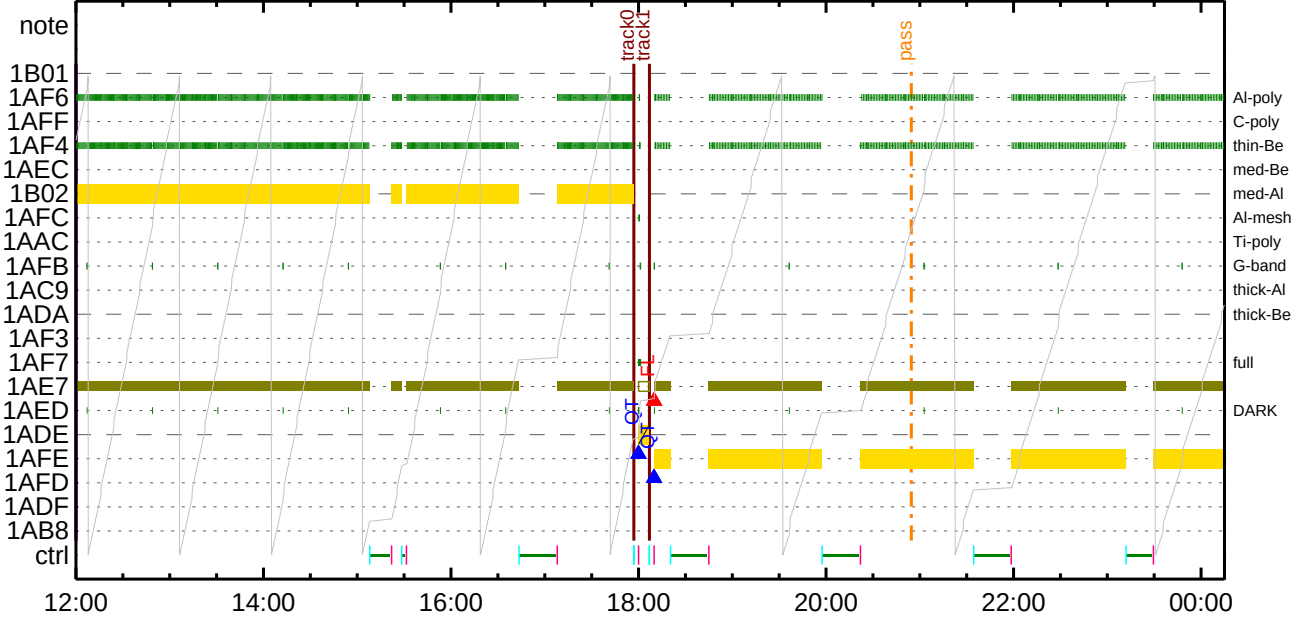
CMDI #0562 2015/12/02



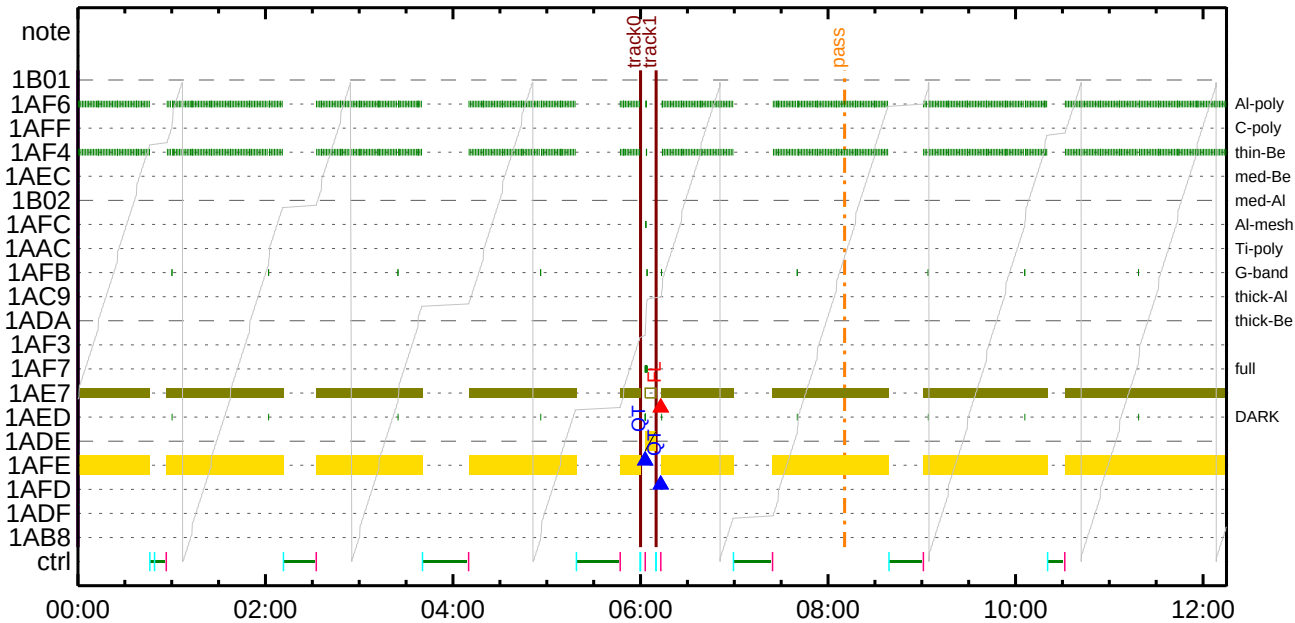
CMDI #0562 2015/12/03



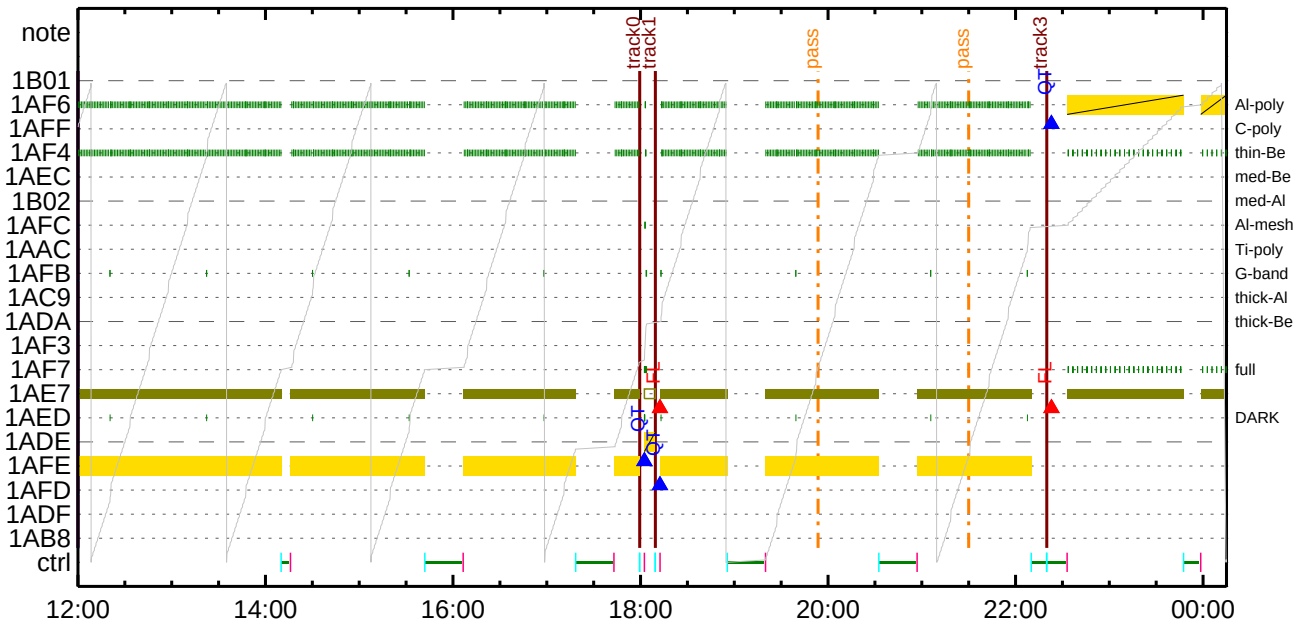
CMDI #0562 2015/12/03



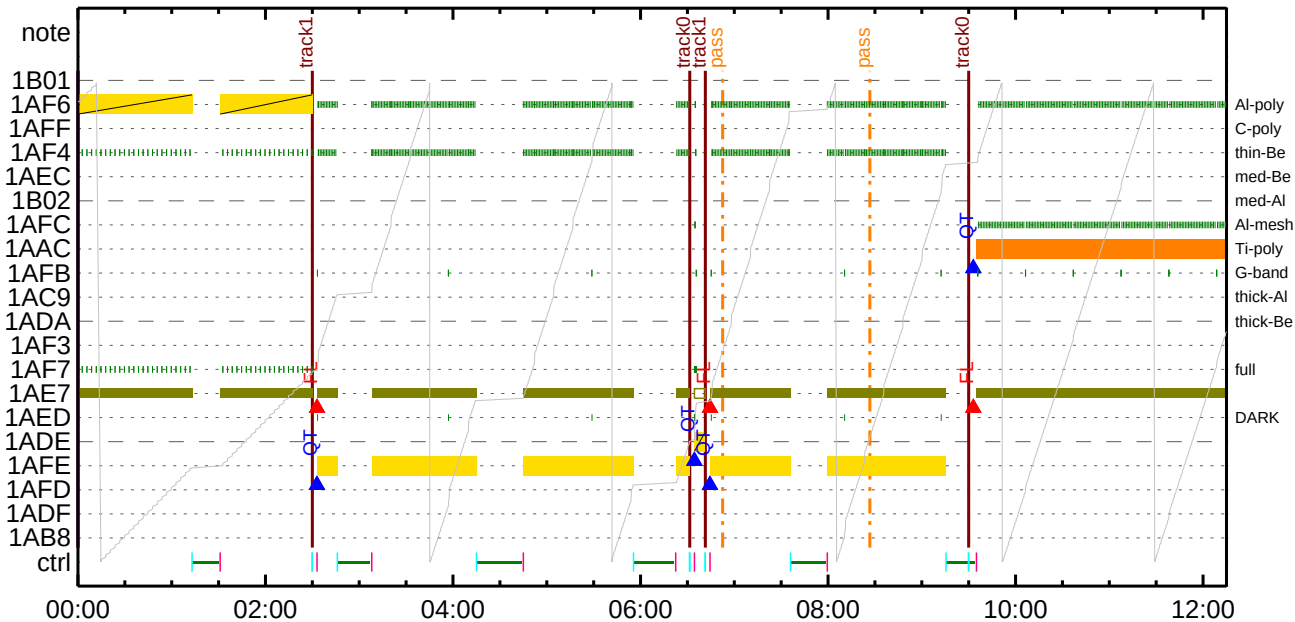
CMDI #0562 2015/12/04



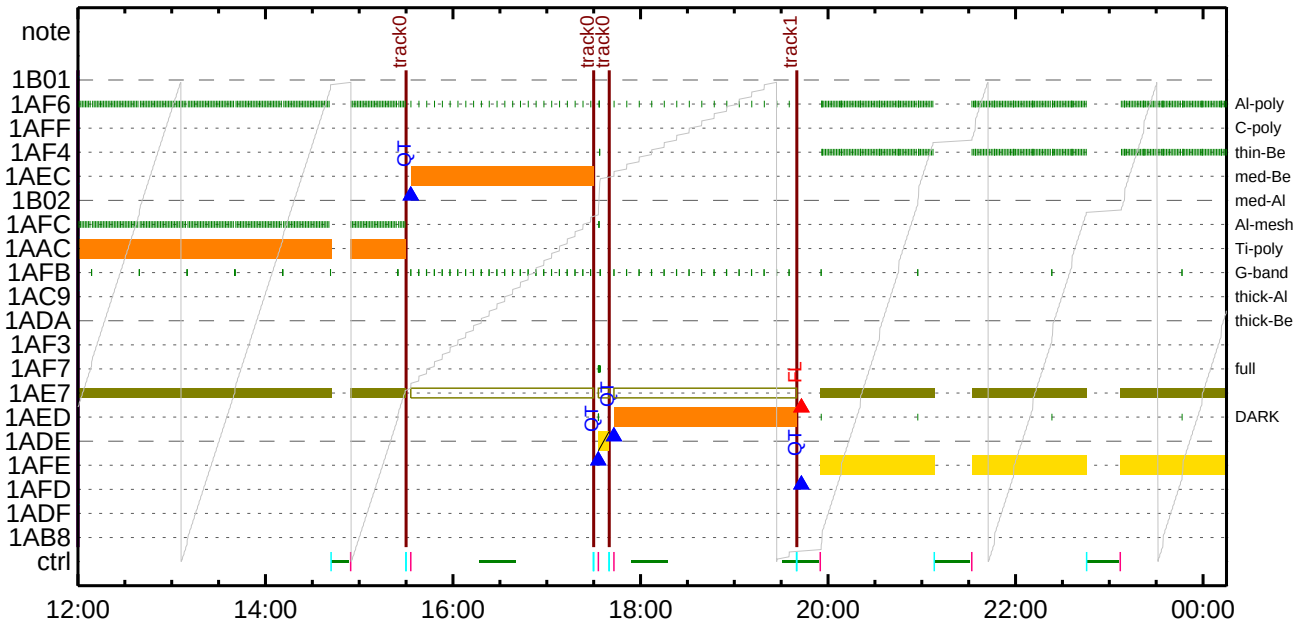
CMDI #0562 2015/12/04



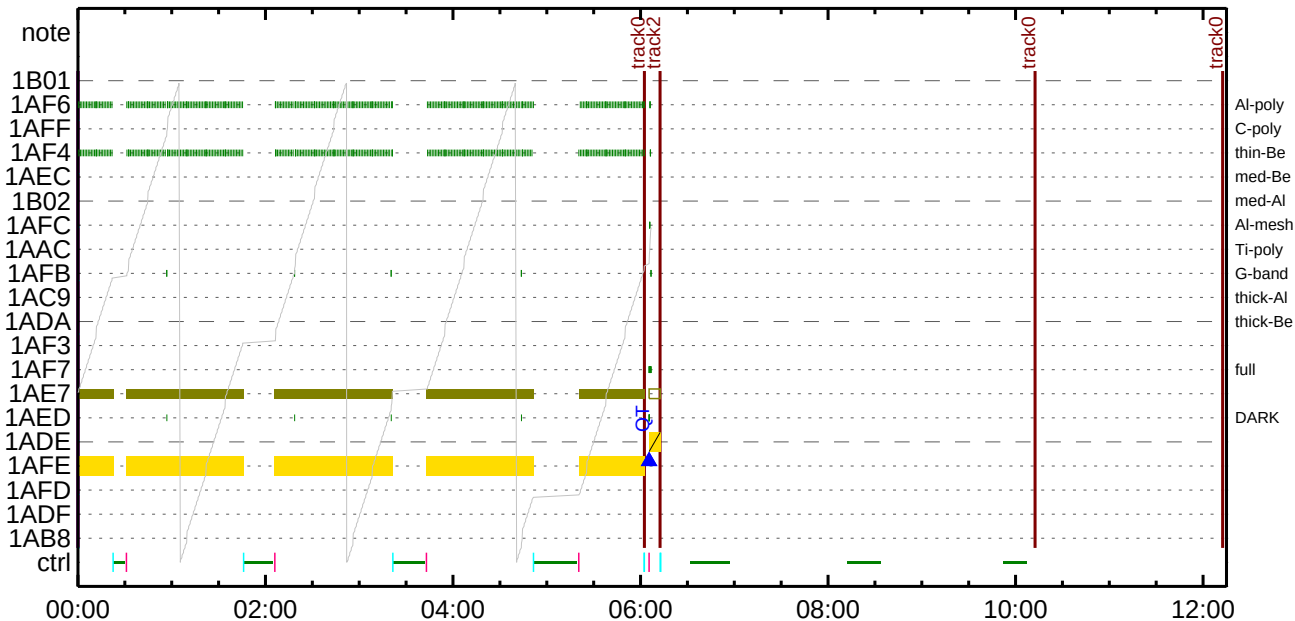
CMDI #0562 2015/12/05



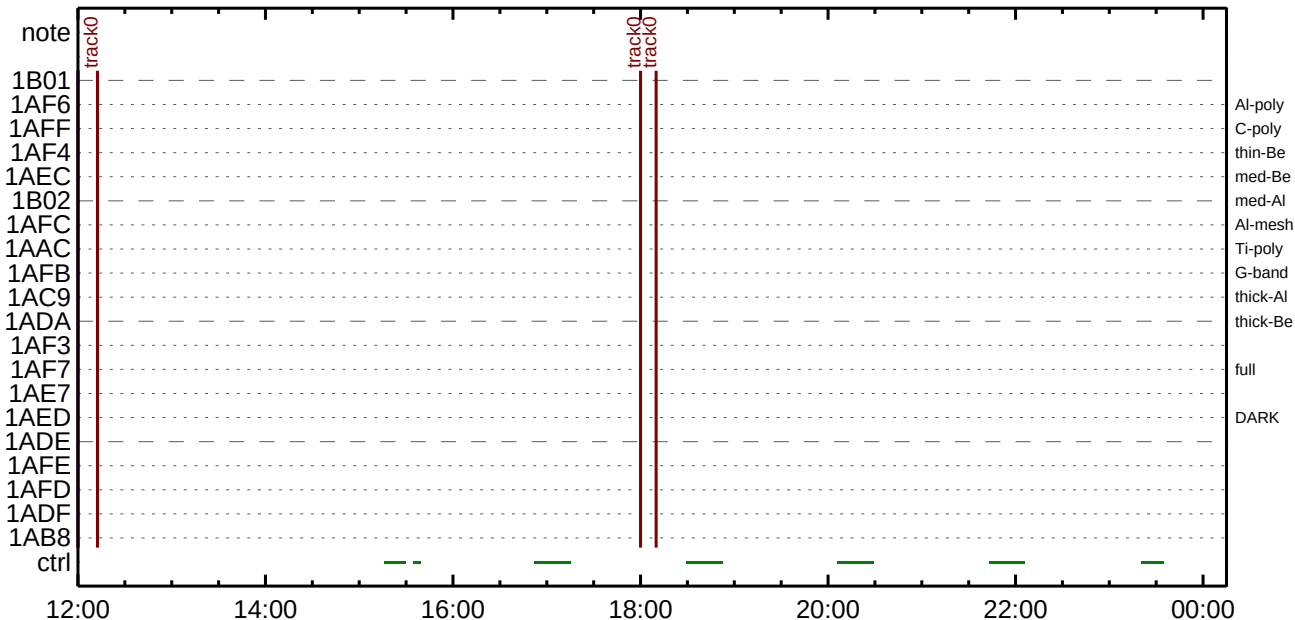
CMDI #0562 2015/12/05



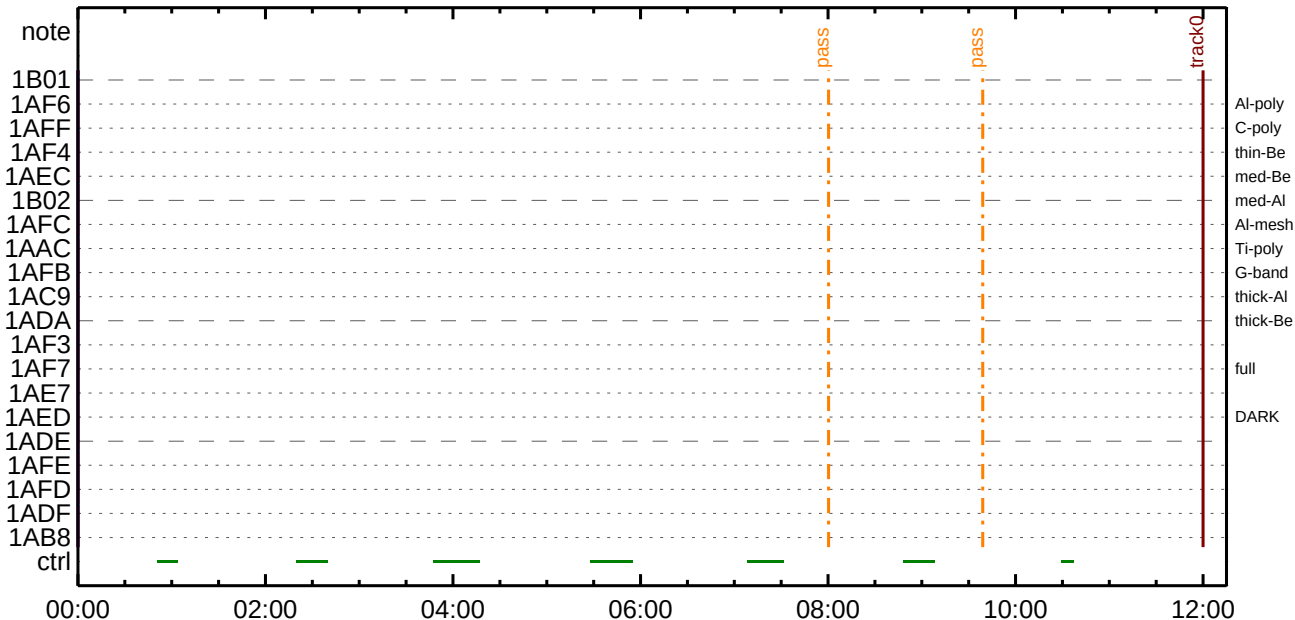
CMDI #0562 2015/12/06



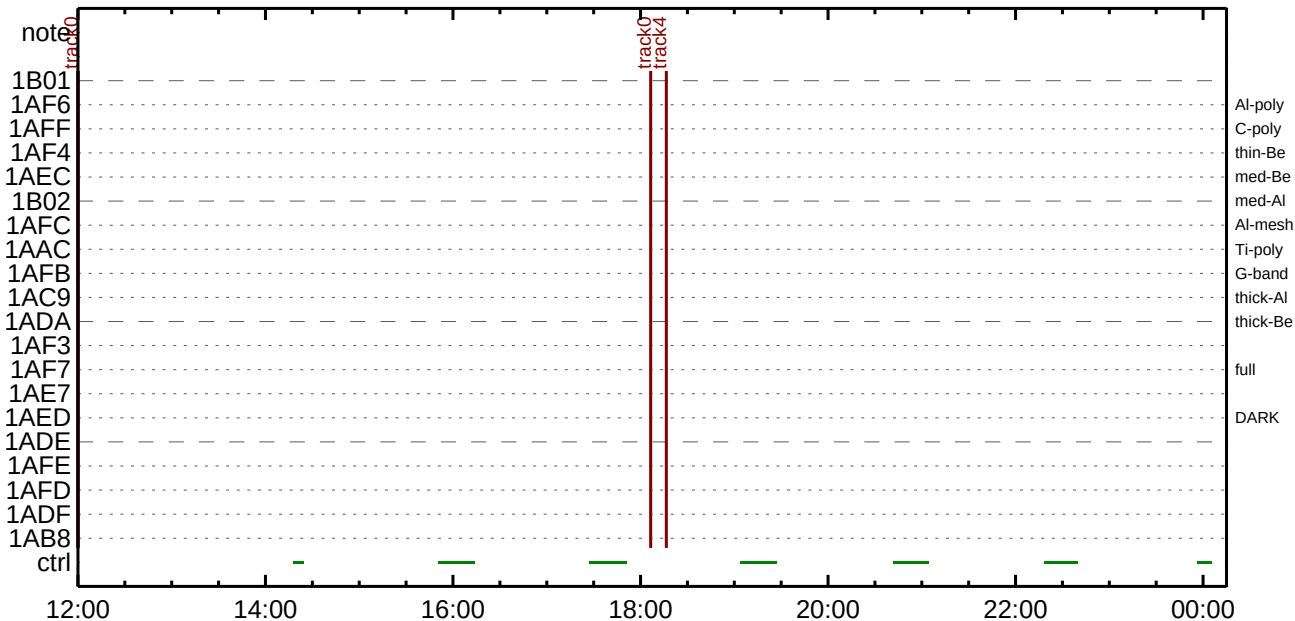
CMDI #0562 2015/12/06



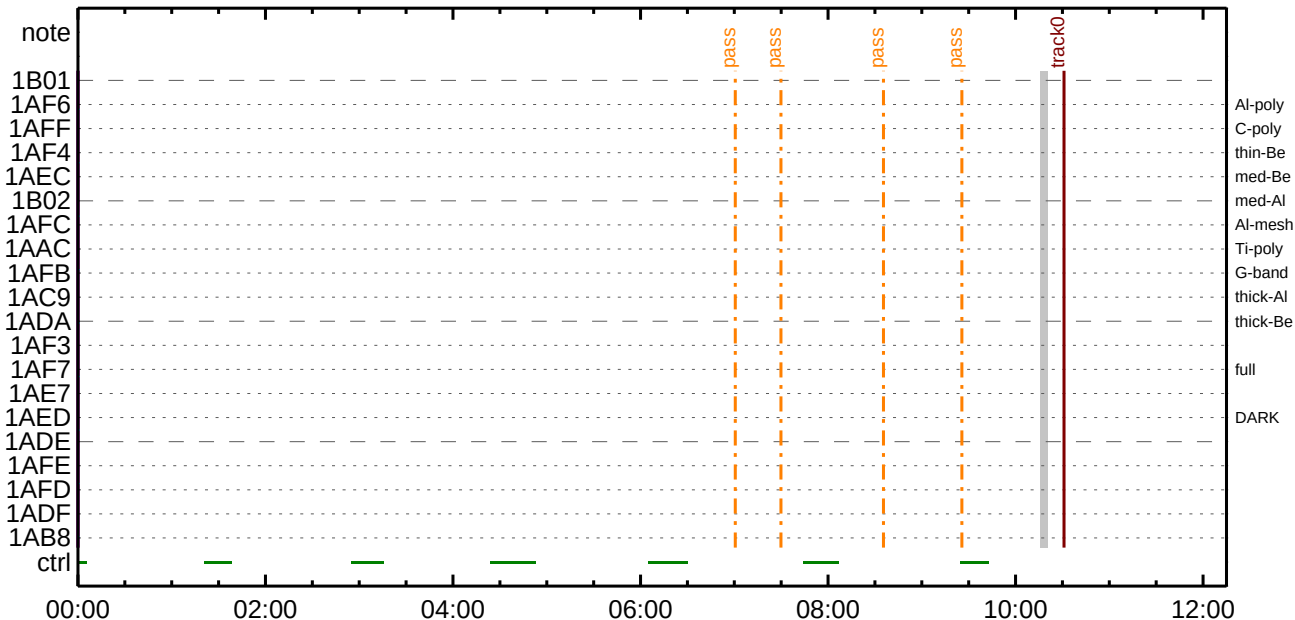
CMDI #0562 2015/12/07



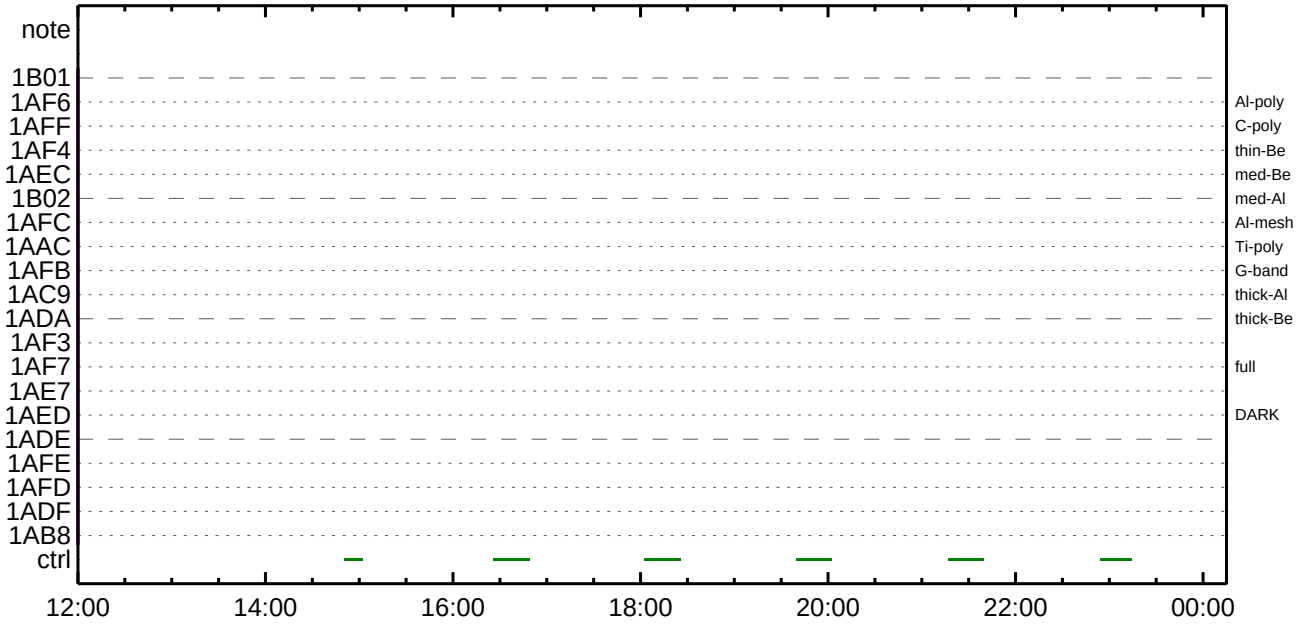
CMDI #0562 2015/12/07



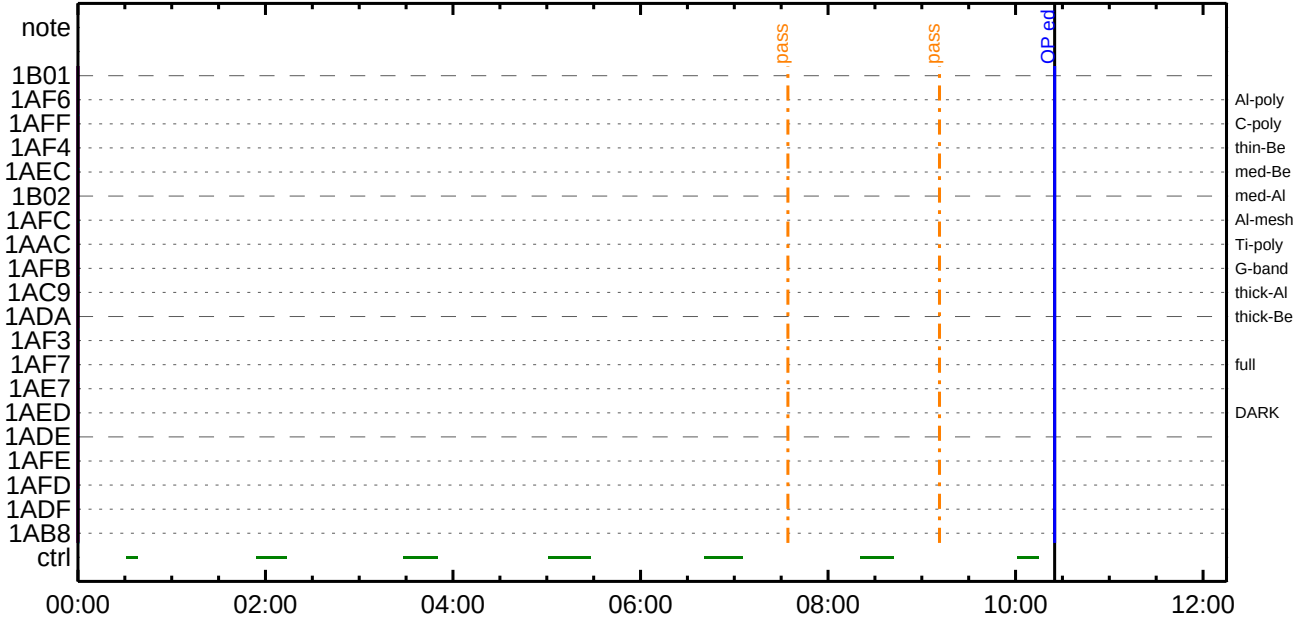
CMDI #0562 2015/12/08



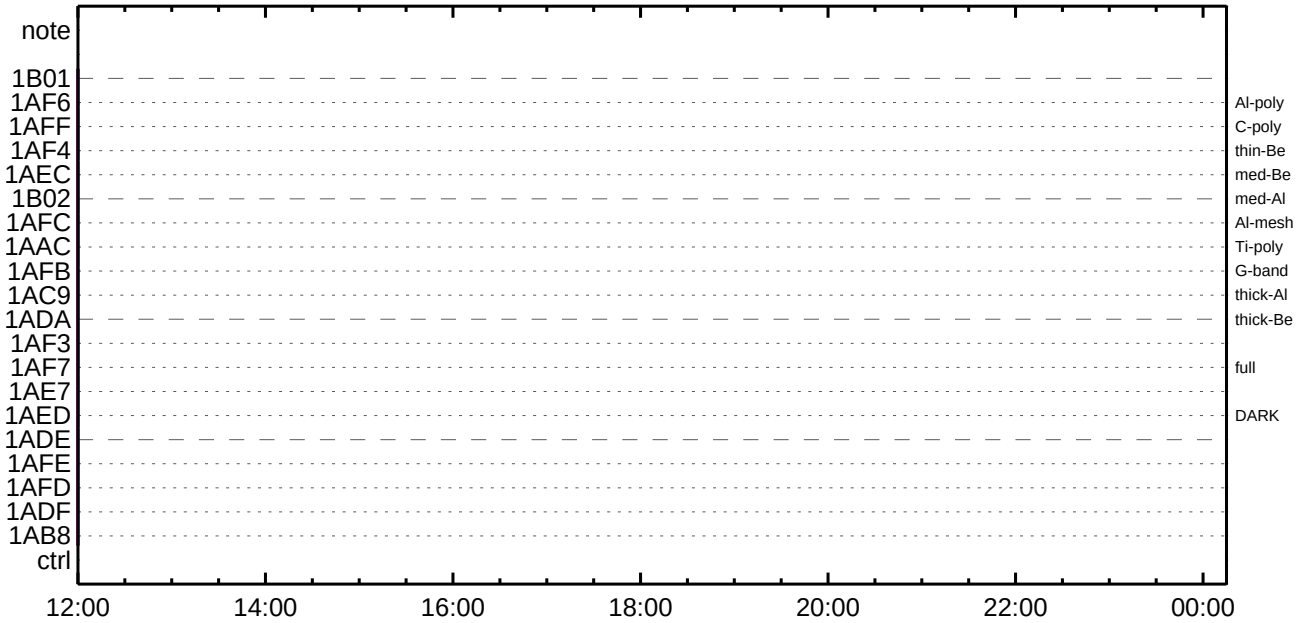
CMDI #0562 2015/12/08



CMDI #0562 2015/12/09



CMDI #0562 2015/12/09



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main-527 2015-12-01 13:21:55 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ¡ãAOS¥Á¥$¥Ã¥¬¼Ä»Ü¡ä
0005 C.
0006 C. ¥Å¥ß¡;¼¥³¥Ð¥Ó¥ÉÅ÷¿®
+ DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí¡Ë¤¿¤À¤•µ°Æ»Ì×AQ¤I¥¢¥Ã¥×¥¡¡;¼¥Ê¡ËË£½µ•ÍÊ¡ËÈ¼°ÇÔ¤•¤¿¾¹¿¤I¡¢À®, ù¤¹¤è¤º¤ÇÄ÷¿®¤•¤Ê¤¤¤³¤Ê¡£
+ DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷¿@µ¡ON
0016 C. *****
0017 C. ¢¨ °ÆÄ, ÍxÊÝ¤àLOS¤º¤Ç¤I»þ´Ö¤ò¹ÍÎ, ¤•¡¢ÊÔÍx¤EXÄÓON¤I¹Ô¤Ê¤I¤Ê¤¤¤³¤Ê¡£
0018 C.
+ DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ¢¢[HK1_XPA_ON/OFF] EQ ON
0025 C. ¢¢[HK1_XPA_PWR_HI/L0] EQ HI
0026 C. ¢¢[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¢¢[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥Ó¥É¥¡¥Ã¥¬¼ÒÄÕ¤¬¼Äê¤•¤¿¤²¡¢¢°Ê²¼¤I°ÆÄ, ¼ê½Ç¤ò¼Ä¹Ô¤¹¤è¡£
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄÍ¾¹°ÆÄ
0033 C. *****
0034 C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤º¤¾¹¹¿¤I¡¢°Ê²¼¤I¾Ä¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035 C.
0036 . C. ¡ãPT1°ÆÄ, ³«»Í¡ä
0037 + DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¡¾Ú)
0043 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¾Ú)
0044 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¾Ú)
0045 C.
0046 . C. ¡ã¥¢¥ó¥Æ¥ÊÄÙÂØ¡ÊÄ•Äº²ÓÊð¡Ë, ä¤I°ÆÄ, °Æ³«¡ä
+ DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¡¾Ú)
0050 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¾Ú)
0051 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¾Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ¤¬¼«Æ°Ää»ß¤•¤¿, ä¡¢°Ê²¼¤ò¼Ä¹Ô¤¹¤è¡£
0055 C. ¥¢¥ó¥Æ¥ÊÄÙÂØ¤âÄ•Äº²ÓÊð¤¬¼á¤¾¹¹¿¤I'°Î»¤¹¤è¤º¤ÇÄÔ¤â¡£
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄÍ¾¹°ÆÄ
0059 C. *****
0060 C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤º¤¾¹¹¿¤I¡¢°Ê²¼¤I¾Ä¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061 C.
0062 . C. ¡ãPT2°ÆÄ, ³«»Í¡ä
0063 + DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¡¾Ú)
0069 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¾Ú)
0070 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¾Ú)
0071 C.
0072 . C. ¡ã¥¢¥ó¥Æ¥ÊÄÙÂØ¡ÊÄ•Äº²ÓÊð¡Ë, ä¤I°ÆÄ, °Æ³«¡ä
+ DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¡¾Ú)
0076 C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¾Ú)
0077 C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¾Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Äã»ß¡¢XÄ÷¿@µ¡OFF
0081 C. *****
0082 C.
0083 . C. ¡ãDR°ÆÄ, Äã»ß¡ä
+ DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¢¢[HK1_REP_STA/STP] EQ STOP
0087 C. ¢¢[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¢¢[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ¡ãXÄ÷¿@µ¡OFF¡ä
+ DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ¢¢[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¢¢[HK1_XPA_ON/OFF] EQ OFF
```

[illegible]

```

0194 C.
0195 +. TI 2015-12-01 10:33:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.                                ☐☐[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °Ê¼☐İÄê¼İİŃ☐İ¥Á¥$¥Ä¥-¹àİÜ
0200 C.                                ☐☐[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.                                ☐☐[HK1_TI_CMD_NUM]                EQ      4
0202 C.                                ☐☐[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.                                ☐☐[HK1_NEXT_EXEC_DC]            EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİİ°è¥Á¥0¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.                                ☐☐[HK1_DMP_TOP_ADRS_1]            EQ      07
0213 C.                                ☐☐[HK1_DMP_TOP_ADRS_0]            EQ      2B
0214 C.                                ☐☐[HK1_DMP_BLOCK_NUM]            EQ      3
0215 C.                                ☐☐[HK1_DMP_REPEAT_NUM]            EQ      0
0216 C.                                ☐☐[HK1_DMA_DMP_PIM]            EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.                                ☐☐[HK1_PKT_FORM_NO]            EQ      7
0220 C.                                ☐☐[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.                                ☐☐[HK1_S_TLM_BIT_RATE]            EQ      32k
0222 C.                                ☐☐[HK1_X_TLM_BIT_RATE]            EQ      4M
0223 C.                                ☐☐[HK1_DMP_CHK_FLG]            EQ      EXEC
0224 C.
0225 C. ¥Á¥0¥×½ªİ»☐ò³İÇ$
0226 C.                                ☐☐[HK1_DMP_CHK_FLG]            EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL☐İ¼Ê¹Ç•è²İOK☐ò³İÇ$
0229 C.
0230 C. DHU¥â¼¥Ê¼Ê¼¥½¥İ¼¥Ê¼İöòİä☐¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.                                ☐☐[HK1_PKT_FORM_NO]            EQ      2
0234 C.                                ☐☐[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.                                ☐☐[HK1_S_TLM_BIT_RATE]            EQ      32K
0236 C.                                ☐☐[HK1_X_TLM_BIT_RATE]            EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2015-12-01 10:33:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2015-12-01 10:33:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2015-12-01 10:33:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2015-12-01 10:33:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ÜÄİİ»ö¼¥☐ÈÄ☐¹☐èDCBC•×²è *****
0276 C. (¼ä°İ¥0¥Á¥È¥p¥È¥Ä¥Ç¥è☐È¼☐☐¼Ä»Ü☐¹☐è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥0¥¹•İ Daily±¼İŃ☐È´0☐¹☐èDCBC•×²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. İäLOS¥Á¥$¥Ä¥-¼Ä»Ü¼ä
0287 C.
0288 C. ***** LOS *****
0289 C.

```

[illegible]

```
main-529 2015-12-01 13:25:55 252 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOS¥Á¥$¥Ã¥¬¼Ä»Ü;ä  
0005 C.  
0006 C. ¥Å¥ß;¼¥³¥Ð¥Ó¥ÉÄ÷¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****  
0010 C. Äí;Ëò¿Àó•µ°Æ»Í×ÁÇòİ¥¢¥Á¥×¥ı;¼¥Ê;ĒĚē½μ•īĒî;ĒōĒ¼°ÇÒó•ñ¿ł¹çòİ; ÇÄ®, ùñ¹ñëõþôÇÄ÷¿®ó•ñĒñññ³ñÈ;£  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 . C. *****  
0015 C. XÄ÷¿@µ;ON  
0016 C. *****  
0017 C. ¢¨ °ÆÄ, İxĔYðàŁOŚþøÇöİ»Þ´Ŏðò¹ÎĪ, ó•;¢ĖŎİ×ñĒXÂÖŐNñİ¹ŎñĒñññ³ñÈ;£  
0018 C.  
0019 +. DC 03-B4 TCIA_XPA_ON/HI  
0020 M. WAIT_SEC 1  
0021 +. DC 03-84 TCIA_XMOD_ON  
0022 M. WAIT_SEC 1  
0023 +. DC 03-95 TCIA_XMOD_QPSK  
0024 C. ¤¤[HK1_XPA_ON/OFF] EQ ON  
0025 C. ¤¤[HK1_XPA_PWR_HI/L0] EQ HI  
0026 C. ¤¤[HK1_XMOD_ON/OFF] EQ ON  
0027 C. ¤¤[HK1_XMOD_QPSK/PM] EQ QPSK  
0028 C.  
0029 C. X¥Ð¥Ó¥É¥İ¥Ã¥¬¾ĐĂŦó¬·ÄĂêó•ñ¿ðé;¢°Ē²¼ñİ°ÆÄ, ¼ē½çòĐ¼Ä¹Ŏñ¹ñè;£  
0030 C.  
0031 . C. *****  
0032 C. DR PT1 Äİ½ı°ÆÄ,  
0033 C. *****  
0034 C. ¢¨ RESTART;ĒPT1;Ēñ•ñ¿ñññ³¹çòİ;¢°Ē²¼ñİ¼Ä¹Ŏñ»ñ°;¢DCBC-150ñØ¿Ēñà;£  
0035 C.  
0036 . C. ;ăPT1°ÆÄ, ³«»İ;ä  
0037 +. DC 01-29 DHU_S/X_VC4_OFF  
0038 +. DC 06-C8 DR_PT1_REP_SEL  
0039 BC (01_00)  
0040 +. DC 06-B3 DR_REP_START  
0041 +. DC 01-32 DHU_X_VC4_ON  
0042 C. ¤¤[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ŏ, i¾Ú)  
0043 C. ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ŏ, i¾Ú)  
0044 C. ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ŏ, i¾Ú)  
0045 C.  
0046 . C. ;ă¥¢¥ó¥Æ¥ĒÄÜÄŦ;ĒÄ•Äº²őĒò;Ē, äñİ°ÆÄ, °Æ³«;ä  
0047 +. DC 06-B3 DR_REP_START  
0048 +. DC 01-32 DHU_X_VC4_ON  
0049 C. ¤¤[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ŏ, i¾Ú)  
0050 C. ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ŏ, i¾Ú)  
0051 C. ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ŏ, i¾Ú)  
0052 C.  
0053 C.  
0054 . C. PT1°ÆÄ, ñ¬¼«Æ°Ää»ßñ•ñ¿, ä;¢°Ē²¼ñò¼Ä¹Ŏñ¹ñè;£  
0055 C. ¥¢¥ó¥Æ¥ĒÄÜÄŦđÄÄ•Äº²őĒòñ¬¼áñññ³¹çòİ´İ»ñ¹ñëõþôÇÄŦñÄ;£  
0056 C.  
0057 . C. *****  
0058 C. DR PT2 Äİ½ı°ÆÄ,  
0059 C. *****  
0060 C. ¢¨ RESTART;ĒPT2;Ēñ•ñ¿ñññ³¹çòİ;¢°Ē²¼ñİ¼Ä¹Ŏñ»ñ°;¢DCBC-151ñØ¿Ēñà;£  
0061 C.  
0062 . C. ;ăPT2°ÆÄ, ³«»İ;ä  
0063 +. DC 01-29 DHU_S/X_VC4_OFF  
0064 +. DC 06-C8 DR_PT2_REP_SEL  
0065 BC (02_00)  
0066 +. DC 06-B3 DR_REP_START  
0067 +. DC 01-32 DHU_X_VC4_ON  
0068 C. ¤¤[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ŏ, i¾Ú)  
0069 C. ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ŏ, i¾Ú)  
0070 C. ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ŏ, i¾Ú)  
0071 C.  
0072 . C. ;ă¥¢¥ó¥Æ¥ĒÄÜÄŦ;ĒÄ•Äº²őĒò;Ē, äñİ°ÆÄ, °Æ³«;ä  
0073 +. DC 06-B3 DR_REP_START  
0074 +. DC 01-32 DHU_X_VC4_ON  
0075 C. ¤¤[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ŏ, i¾Ú)  
0076 C. ¤¤[HK1_REP_STA/STP] EQ START (¼Ä¹Ŏ, i¾Ú)  
0077 C. ¤¤[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ŏ, i¾Ú)  
0078 C.  
0079 . C. *****  
0080 C. DR°ÆÄ, Ää»ß;¢XA÷¿@µ;OFF  
0081 C. *****  
0082 C.  
0083 . C. ;ăDR°ÆÄ, Ää»ß;ä  
0084 +. DC 06-B4 DR_REP_STOP  
0085 +. DC 01-29 DHU_S/X_VC4_OFF  
0086 C. ¤¤[HK1_REP_STA/STP] EQ STOP  
0087 C. ¤¤[HK1_S_VC4_ON/OFF] EQ OFF  
0088 C. ¤¤[HK1_X_VC4_ON/OFF] EQ OFF  
0089 C.  
0090 . C. ;ăXA÷¿@µ;OFF;ä  
0091 +. DC 03-85 TCIA_XMOD_OFF  
0092 M. WAIT_SEC 1  
0093 +. DC 03-B5TCIA_XPA_OFF  
0094 C. ¤¤[HK1_XMOD_ON/OFF] EQ OFF  
0095 C. ¤¤[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload FG Observation Table>
0109 S. RAM ram-264:MDP_OBS_F
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 C. <Upload SP Observation Table>
0127 S. RAM ram-285:MDP_OBS_S
0128 ( )
0129 C.
0130 C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 C. < Upload DPL table >
0138 C.
0139 C. ¥¢¥Ã¥×¥¡¡¥¥É°ÎÁ°¤ËSTS_CHK¤òOFF¤Ë¤¹¤ë
0140 C.
0141 S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHK¤òON¤Ë¤¹¤ë
0152 C.
0153 C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700 [ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2015-12-01 10:33:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** XRT START *****
0174 C.
0175 +. DC 07-F0 MDP_XRT_CTRL_MANU
0176 BC (c1)
0177 +. DC 07-F0 MDP_XRT_MODE_STBY
0178 BC (c3)
0179 C. ----- Success Verify ? OK / NG____
0180 C.
0181 C. XRT Obs. Table Upload
0182 S. RAM ram-291:MDP_OBS_X
0183 ( )
0184 C.
0185 +. DC 07-F0 MDP_DUMP_XRTTBL
0186 BC (84 07 00 00 00 3a d4)
0187 C. ----- Comparison Check ? OK / ERR ____
0188 C.
0189 C.
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 01 b1 b1 04 04)
0192 +. DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 02 b1 b1 08 08)

```



```
0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 03 b1 b1 08 08)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 04 b1 b1 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 06 85 83 06 06)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 07 80 80 20 20)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 08 80 80 20 08)
0204 + DC 07-F0 MDP_XRT_ROI_SET
0205 BC (cd 09 80 80 08 20)
0206 + DC 07-F0 MDP_XRT_ROI_SET
0207 BC (cd 0a 80 60 20 18)
0208 + DC 07-F0 MDP_XRT_ROI_SET
0209 BC (cd 0b a0 80 18 20)
0210 + DC 07-F0 MDP_XRT_ROI_SET
0211 BC (cd 0f 80 80 06 06)
0212 + DC 07-F0 MDP_XRT_ROI_SET
0213 BC (cd 10 80 80 08 08)
0214 + DC 07-F0 MDP_XRT_FLD_ENA
0215 BC (d8)
0216 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0217 BC (c8)
0218 + DC 07-F0 MDP_XRT_ARS_DIS
0219 BC (d5)
0220 + DC 07-F0 MDP_XRT_QT_PROG_SET
0221 BC (c4 0f)
0222 + DC 07-F0 MDP_XRT_FL_PROG_SET
0223 BC (c5 07)
0224 . C. ----- Success Verify ? OK / NG ____
0225 C.
0226 C.
0227 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0228 C.
0229 + DC 07-F0 MDP_XRT_MODE_OBSV
0230 BC (c2)
0231 + TI 2015-12-01 10:33:02.0
0232 DC 07-F0 MDP_XRT_MODE_OBSV
0233 BC (c2)
0234 . C. ----- Success Verify ? OK / NG ____
0235 C.
0236 C. ***** XRT END *****
0237 C.
0238 . C. ***** MDP `ûÃÎ±Î»0¼Ý±ĒÂÐ±¹±ēDCBC•x²è *****
0239 C. (%ã°Î¥0¥Ã¥Ē¥Ð¥Ē¥â¥Ĉ¥ē±Ē¼±¼Ã»Û±¹ē)
0240 . S. DC-BC dcbc-402:DCBC
0241 (MDP_known_event)
0242 C.
0243 C.
0244 . C. ***** ¥Ð¥¹•Ī Daily±¿ÎŃ±Ē´Ø±¹±ēDCBC•x²è *****
0245 . S. DC-BC dcbc-153:DCBC
0246 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0247 C.
0248 C.
0249 . C. ¡ãLOS¥Ã¥$¥Ã¥¬¼Ã»Û¡ä
0250 C.
0251 . C. ***** LOS *****
0252 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-530 2015-12-01 13:21:55 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥P¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¿•μ°£»İ×AÇ¿İ¥C¥A¥×¥i;¼¥ĒjĒĒë½μ•īĒĒjĒĒĒ¼°ÇŒ¿•¿¿¼i¹¿¿İjÇÀ®, ù¿¹¿ë¿¿¿¿¿Á÷¿®¿•¿Ē¿¿¿³¿Ēj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP `ŪĀİ¿İ»Ō¼Ý¿ĒĒ¿¿¹¿ĒDCBC•×²è *****
0016 C. (%ā°İ¥Ō¥Ā¥Ē¥P¥Ē¥ā¥C¥Ēë¿Ē¼¿¿¿Ā»Ū¿¹¿Ē)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** ¥Đ¥¹•İ Daily±¿İŊ¿Ē¿Œ¿¹¿ĒDCBC•×²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. iãLOS¥Á¥S¥Ã¥¼Á»Üjã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

Dec 01, 15 13:22

XRT_OGLIST_0562.chk

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*** OP Sequence for XRT ***

2015/12/01	10:43:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	10:43:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	10:43:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]		
		XRT_FOCUS_RECAL	2	07-F8 78 00
2015/12/01	10:44:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 01 00 00 00 00
2015/12/01	10:47:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/12/01	10:48:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/12/01	10:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/12/01	10:48:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/12/01	10:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/12/01	10:48:26.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	10:50:56.0	XRT_QT_PROG_SET_431_OG [0x1af]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04
2015/12/01	10:50:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/12/01	10:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/01	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	12:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/12/01	13:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 02 00 00 00 00
2015/12/01	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/12/01	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/12/01	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/12/01	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/12/01	13:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	13:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 13
2015/12/01	13:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/12/01	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/01	15:29:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	15:29:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	15:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/12/01	15:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 01 00 00 00 00
2015/12/01	15:30:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/12/01	15:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/12/01	15:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/12/01	15:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/12/01	15:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	15:32:56.0	XRT_QT_PROG_SET_431_OG [0x1af]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04
2015/12/01	15:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/12/01	15:33:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	15:33:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	15:33:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/12/01	15:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/12/01	15:57:00.0	XRT_Custom_430_OG [0x1ae]		
2015/12/01	15:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/01	17:10:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	17:10:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	17:10:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/12/01	17:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9

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2015/12/01	17:33:30.0	XRT_Custom_430_OG [0x1ae]		
2015/12/01	17:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/01	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	18:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/12/01	18:05:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 00 00 00 00 00
2015/12/01	18:05:18.0	XRT_FLD_DIS_406_OG [0x196]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2015/12/01	18:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2015/12/01	18:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/12/01	18:07:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 02
2015/12/01	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/01	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	18:14:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	18:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/12/01	18:15:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 01 00 00 00 00
2015/12/01	18:15:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/12/01	18:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/12/01	18:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/12/01	18:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/12/01	18:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	18:17:56.0	XRT_QT_PROG_SET_431_OG [0x1af]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04
2015/12/01	18:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/12/01	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/01	18:47:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	18:47:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	18:47:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/12/01	18:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/12/01	19:10:30.0	XRT_Custom_430_OG [0x1ae]		
2015/12/01	19:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/01	20:24:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	20:24:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	20:24:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/12/01	20:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/12/01	20:47:30.0	XRT_Custom_430_OG [0x1ae]		
2015/12/01	20:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/01	22:01:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	22:01:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	22:01:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/12/01	22:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/12/01	22:23:30.0	XRT_Custom_430_OG [0x1ae]		
2015/12/01	22:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/01	23:38:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/01	23:38:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/01	23:38:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/12/01	23:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/12/01	23:50:01.0	XRT_Custom_430_OG [0x1ae]		
2015/12/01	23:51:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/02	01:05:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/02	01:05:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da

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2015/12/02	01:05:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	01:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	01:21:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/02	01:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	02:37:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	02:37:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	02:37:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	02:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	02:58:00.5	XRT_Custom_430_OG [0x1ae]			
2015/12/02	02:59:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	04:06:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	04:06:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	04:06:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	04:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	04:35:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/02	04:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	05:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	05:46:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	05:46:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	05:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	06:13:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/02	06:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	06:22:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	06:22:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	06:22:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/02	06:23:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/12/02	06:23:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/02	06:25:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/02	06:25:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/02	06:25:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/12/02	06:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	06:32:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	06:32:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	06:32:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/02	06:33:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2015/12/02	06:33:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/02	06:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/02	06:33:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/02	06:33:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/02	06:33:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	06:35:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/12/02	06:35:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/02	06:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	07:27:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	07:27:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	07:27:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	07:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	07:50:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/02	07:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2015/12/02	09:06:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	09:06:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	09:06:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	09:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	09:25:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/02	09:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	10:49:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	10:49:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	10:49:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	10:51:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/02	10:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	10:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	14:34:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	14:34:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	14:34:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	14:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	14:44:00.5	XRT_Custom_430_OG [0x1ae]			
2015/12/02	14:45:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	16:08:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	16:08:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	16:08:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	16:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	16:32:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/02	16:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	17:45:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	17:45:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	17:45:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	17:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	18:08:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	18:08:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	18:08:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/02	18:08:30.0	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/12/02	18:08:48.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/02	18:11:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/02	18:11:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/02	18:11:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/12/02	18:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	18:18:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	18:18:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	18:18:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/02	18:18:30.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2015/12/02	18:18:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/02	18:18:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/02	18:18:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/02	18:18:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/02	18:18:56.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	18:21:26.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/12/02	18:21:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/02	18:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]			

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2015/12/02	19:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	19:22:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	19:22:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	19:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	19:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	19:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	20:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	20:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	20:59:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	21:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	21:22:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	21:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	22:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/02	22:36:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/02	22:36:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/02	22:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/02	22:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/02	22:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	00:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	00:14:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	00:14:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	00:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	00:22:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	00:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	01:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	01:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	01:37:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	01:37:34.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	01:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	01:56:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	01:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	03:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	03:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	03:12:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	03:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	03:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	03:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	04:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	04:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	04:42:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	04:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	05:11:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	05:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	05:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	05:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	05:54:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	05:55:00.0	AOCs_0Re-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	05:55:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/03	05:57:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	AOCU_NM	5	02-76	00 00 00 00 00
2015/12/03	05:57:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/03	05:57:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/03	05:57:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5

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2015/12/03	05:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/03	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/03	06:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/03	06:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/12/03	06:05:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00 00 00 00
2015/12/03	06:05:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/12/03	06:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/12/03	06:05:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/12/03	06:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/12/03	06:05:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/03	06:07:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2015/12/03	06:07:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/12/03	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/03	06:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/03	06:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/03	06:23:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/03	06:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/03	06:48:00.0	XRT_Custom_430_OG [0x1ae]					
2015/12/03	06:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/03	08:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/03	08:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/03	08:03:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/03	08:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/03	08:25:00.0	XRT_Custom_430_OG [0x1ae]					
2015/12/03	08:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/03	09:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/03	09:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/03	09:43:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/03	09:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/03	09:59:00.0	XRT_Custom_430_OG [0x1ae]					
2015/12/03	10:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/03	15:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/03	15:08:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/03	15:08:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/03	15:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/03	15:21:00.0	XRT_Custom_430_OG [0x1ae]					
2015/12/03	15:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/03	15:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/03	15:28:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/03	15:28:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/03	15:30:30.0	XRT_Custom_430_OG [0x1ae]					
2015/12/03	15:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/03	15:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/03	16:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/03	16:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/03	16:43:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/03	16:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/03	17:07:00.0	XRT_Custom_430_OG [0x1ae]					
2015/12/03	17:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2015/12/03	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	17:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	17:56:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/03	17:57:00.0	AOCS_0Re-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2015/12/03	17:57:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/03	17:59:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/03	17:59:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/03	17:59:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/12/03	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	18:06:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	18:06:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/03	18:07:00.0	AOCS_0Re-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2015/12/03	18:07:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/03	18:07:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/03	18:07:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/03	18:07:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/03	18:07:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	18:09:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/12/03	18:09:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/03	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	18:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	18:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	18:20:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	18:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	18:44:00.0	XRT_Custom_430_OG [0x1ae]				
2015/12/03	18:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	19:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	19:57:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	19:57:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	20:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	20:21:00.0	XRT_Custom_430_OG [0x1ae]				
2015/12/03	20:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	21:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	21:34:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	21:34:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	21:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	21:57:30.0	XRT_Custom_430_OG [0x1ae]				
2015/12/03	21:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/03	23:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/03	23:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/03	23:12:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/03	23:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/03	23:28:30.0	XRT_Custom_430_OG [0x1ae]				
2015/12/03	23:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	00:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	00:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	00:46:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	00:49:00.0	XRT_CTRL_MANU_400_OG [0x190]				

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2015/12/04	00:49:02.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	00:49:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	00:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	00:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	00:55:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	00:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	02:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	02:11:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	02:11:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	02:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	02:31:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	02:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	03:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	03:40:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	03:40:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	03:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	04:09:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	04:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	05:19:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	05:19:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	05:19:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	05:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	05:46:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	05:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/04	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/12/04	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/04	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/04	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/04	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/12/04	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/04	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2015/12/04	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/04	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/04	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/04	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/04	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	06:12:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/12/04	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/04	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	06:59:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	06:59:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	06:59:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2015/12/04	07:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	07:23:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	07:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	08:39:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	08:39:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	08:39:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	08:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	09:00:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	09:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	10:20:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	10:20:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	10:20:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	10:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	10:30:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	10:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	14:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	14:10:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	14:10:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	14:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	14:15:01.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	14:16:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	15:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	15:42:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	15:42:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	15:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	16:05:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	16:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	17:18:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	17:18:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	17:18:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	17:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	17:42:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/04	17:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	17:59:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	17:59:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/04	17:59:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/12/04	17:59:48.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/04	18:02:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/04	18:02:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/04	18:02:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/12/04	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	18:09:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	18:09:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/04	18:09:30.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2015/12/04	18:09:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/04	18:09:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/04	18:09:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/04	18:09:54.0	XRT_ARS_DIS_423_OG [0x1a7]			

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2015/12/04	18:09:56.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/04	18:12:26.0	XRT_QT_PROG_SET_431_0G [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	18:12:28.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/12/04	18:12:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/04	18:55:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	18:55:32.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	18:55:34.0	XRT_PREFLR_STRT_416_0G [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	18:58:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	19:19:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	19:20:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430_0G [0x1ae]	1	07-F0	c0
2015/12/04	20:32:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	20:32:32.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	20:32:34.0	XRT_PREFLR_STRT_416_0G [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	20:35:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	20:56:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	20:57:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430_0G [0x1ae]	1	07-F0	c0
2015/12/04	22:10:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	22:10:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	22:10:04.0	XRT_PREFLR_STRT_416_0G [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	22:13:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	22:19:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	22:19:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	22:19:58.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	22:20:00.0	AOCS_ORe-point_Start_4_0G [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/04	22:20:18.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2015/12/04	22:20:20.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/04	22:20:22.0	XRT_AEC_RESET_448_0G [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/04	22:20:24.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/04	22:20:26.0	XRT_FLD_RESET_433_0G [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/04	22:22:56.0	XRT_QT_PROG_SET_401_0G [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	22:22:58.0	XRT_FL_PROG_SET_436_0G [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2015/12/04	22:32:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/04	22:33:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430_0G [0x1ae]	1	07-F0	c0
2015/12/04	23:47:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/04	23:47:32.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/04	23:47:34.0	XRT_PREFLR_STRT_416_0G [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/04	23:50:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/04	23:57:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/04	23:58:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430_0G [0x1ae]	1	07-F0	c0
2015/12/05	01:13:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/05	01:13:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/05	01:13:04.0	XRT_PREFLR_STRT_416_0G [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/05	01:16:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/05	01:30:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/05	01:31:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CUSTOM_430_0G [0x1ae]	1	07-F0	c0
2015/12/05	02:29:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/05	02:29:56.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/05	02:29:58.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/05	02:30:00.0	AOCS_ORe-point_Start_1_0G [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00

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2015/12/05	02:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	5	02-76 01 00 00 00 00
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/12/05	02:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0 c8
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/12/05	02:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0 d0
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/12/05	02:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/12/05	02:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/05	02:32:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	2	07-F0 c4 04
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04
2015/12/05	02:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	2	07-F0 c5 07
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/12/05	02:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/05	02:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/05	02:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/05	02:46:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/12/05	02:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/12/05	03:07:00.0	XRT_Custom_430_OG [0x1ae]		
2015/12/05	03:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/05	04:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/05	04:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/05	04:15:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/12/05	04:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/12/05	04:44:00.0	XRT_Custom_430_OG [0x1ae]		
2015/12/05	04:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/05	05:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/05	05:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/05	05:55:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2015/12/05	05:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2015/12/05	06:21:30.0	XRT_Custom_430_OG [0x1ae]		
2015/12/05	06:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/05	06:31:24.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/05	06:31:26.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/05	06:31:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	4	07-F8 22 ff aa 00
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2015/12/05	06:31:30.0	AOCS_Ore-point_Start_3_OG [0x099]	5	02-76 00 00 00 00 00
		AOCU_NM	5	02-76 00 00 00 00 00
2015/12/05	06:31:48.0	XRT_FLD_DIS_406_OG [0x196]	1	07-F0 d9
		MDP_XRT_FLD_DIS	1	07-F0 d9
2015/12/05	06:34:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0 c9
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2015/12/05	06:34:26.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/12/05	06:34:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	2	07-F0 c4 05
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2015/12/05	06:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/05	06:41:24.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/05	06:41:26.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2015/12/05	06:41:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	4	07-F8 22 fe 97 00
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2015/12/05	06:41:30.0	AOCS_Ore-point_Start_1_OG [0x097]	5	02-76 01 00 00 00 00
		AOCU_NM	5	02-76 01 00 00 00 00
2015/12/05	06:41:48.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0 d8
		MDP_XRT_FLD_ENA	1	07-F0 d8
2015/12/05	06:41:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0 c8
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2015/12/05	06:41:52.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0 d0
		MDP_XRT_AEC_RESET	1	07-F0 d0
2015/12/05	06:41:54.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2015/12/05	06:41:56.0	XRT_FLD_RESET_433_OG [0x1b1]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2015/12/05	06:44:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	2	07-F0 c4 04
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04
2015/12/05	06:44:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	2	07-F0 c5 07
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2015/12/05	06:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2015/12/05	07:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c0

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2015/12/05	07:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	07:36:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/05	07:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/05	07:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/05	07:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/05	09:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	09:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/05	09:15:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/05	09:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/05	09:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	09:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	09:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/12/05	09:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 b2 4e 01 68	
2015/12/05	09:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/12/05	09:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/12/05	09:30:22.0	XRT_AEC_RESET_438_OG [0x1b6]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/12/05	09:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/12/05	09:30:26.0	XRT_FLD_RESET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/05	09:32:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2015/12/05	09:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/12/05	09:34:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/05	09:35:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	14:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	14:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/05	14:42:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/05	14:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/05	14:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/05	14:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	15:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	15:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	15:29:58.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/12/05	15:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 ad 59 00 00	
2015/12/05	15:30:18.0	XRT_FLD_DIS_443_OG [0x1bb]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/12/05	15:32:54.0	XRT_FLRCTRL_DIS_440_OG [0x1b8]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/12/05	15:32:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/12/05	15:32:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2015/12/05	15:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/05	17:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	17:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	17:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/12/05	17:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/12/05	17:30:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/12/05	17:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/12/05	17:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/12/05	17:32:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2015/12/05	17:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/05	17:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/05	17:39:56.0	XRT_CTRL_MANU_402_OG [0x192]					

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2015/12/05	17:39:58.0	XRT_FOCUS_POSITION_441_OG [0x1b9]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/05	17:40:00.0	AOCs_0Re-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/05	17:40:18.0	XRT_FLD_DIS_443_OG [0x1bb]	AOCU_NM	5	02-76	00 00 00 54 00
2015/12/05	17:42:54.0	XRT_FLRCTRL_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/05	17:42:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/05	17:42:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/05	17:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2015/12/05	19:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/05	19:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/05	19:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/05	19:40:00.0	AOCs_0Re-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/05	19:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2015/12/05	19:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/05	19:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/05	19:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/05	19:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/05	19:42:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/05	19:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/12/05	19:54:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/05	19:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/12/05	21:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/05	21:08:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/05	21:08:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/05	21:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/05	21:31:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/05	21:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/12/05	22:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/05	22:45:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/05	22:45:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/05	22:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/05	23:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/05	23:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/12/06	00:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/06	00:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/06	00:22:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/06	00:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/06	00:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/06	00:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/12/06	01:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/06	01:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/06	01:46:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/06	01:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/06	02:05:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/06	02:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2015/12/06	03:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/06	03:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/06	03:21:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/06	03:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/06	03:42:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2015/12/06	03:43:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/06	04:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/06	04:51:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/06	04:51:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/06	04:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/06	05:19:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/06	05:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/06	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/06	06:02:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/06	06:02:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/06	06:02:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/12/06	06:02:48.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/06	06:05:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/06	06:05:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/06	06:05:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/12/06	06:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/06	06:12:30.0	AOCS_ORe-point_Start_8_OG [0x09e]			
		AOCU_NM	5	02-76	02 f7 1b 2d f3
2015/12/06	06:12:35.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/06	06:12:37.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/06	06:12:39.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/06	06:12:55.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/06	06:13:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_407_OG [0x197]			
		TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2015/12/06	06:15:49.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/06	10:12:30.0	AOCS_ORe-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 c6 36 c6 36
2015/12/06	12:12:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]			
		AOCU_NM	5	02-76	00 f9 59 ac b5
2015/12/06	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/12/06	18:10:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]			
		AOCU_NM	5	02-76	00 f9 59 ac b5
2015/12/07	12:00:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76	00 57 e7 00 00
2015/12/07	18:06:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/12/07	18:16:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]			
		AOCU_NM	5	02-76	04 00 00 00 00
2015/12/08	10:31:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00