

XRT Timeline to be uploaded on 2015/12/22

Period: 2015/12/22 10:15:00 - 2015/12/30 09:48:00

* * * * *

Normal mode

* * * * *

XOB #1AFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
12/22 10:32:00 - 12/22 17:46:54	Track (-952.3, 31.9) @ 12/22 10:25:00	# OP start + 10min AR obs, fixed pointing
12/22 18:00:00 - 12/23 05:59:54	Track (-934.7, 34.3) @ 12/22 17:57:00	# AR obs
12/23 06:13:00 - 12/23 09:59:54	Track (-895.5, 38.2) @ 12/23 06:10:00	# AR obs
12/23 10:03:00 - 12/23 18:03:24	Track (-880.5, 39.4) @ 12/23 10:00:00	# dummy pointing for re-adjust
12/23 18:16:30 - 12/24 06:20:54	Track (-844.4, 42.1) @ 12/23 18:13:30	# AR obs.
12/24 06:34:00 - 12/24 08:59:54	Track (-780.3, 46.0) @ 12/24 06:31:00	# AR obs.
12/25 01:33:00 - 12/25 05:53:24	Track (-708.5, 49.8) @ 12/24 18:17:00	# AR obs.
12/25 06:06:30 - 12/25 09:29:54	Track (-627.3, 53.3) @ 12/25 06:03:30	# AR obs.
12/25 09:33:00 - 12/25 17:55:24	Track (-602.0, 54.3) @ 12/25 09:30:00	dummy pointing for re-adjust
12/25 18:08:30 - 12/26 05:59:54	Track (-535.8, 56.7) @ 12/25 18:05:30	# AR obs.
12/26 06:13:00 - 12/26 09:59:54	Track (-436.6, 59.9) @ 12/26 06:10:00	# AR obs.
12/26 10:03:00 - 12/26 17:57:54	Track (-403.7, 60.8) @ 12/26 10:00:00	dummy pointing for re-adjust
12/26 18:11:00 - 12/27 06:29:54	Track (-332.2, 62.6) @ 12/26 18:08:00	# AR obs.

PROG= 04 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 2	4-time(s)	180.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	55.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	55.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ADF: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Med-Al(40

Term	Pointing (x, y)	Comment
12/22 17:50:00 - 12/22 17:56:54	Fixed (0.0, 0.0)	synoptic, shifted -13.0 min

PROG= 02 1-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	4.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 46	2-time(s)	2.0sec										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 72	2-time(s)	2.0sec										
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 59	2-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + Th

Term	Pointing (x, y)	Comment
12/23 06:03:00 - 12/23 06:09:54	Fixed (0.0, 0.0)	synoptic
12/23 18:06:30 - 12/23 18:13:24	Fixed (0.0, 0.0)	synoptic, shifted 3.5 min
12/24 06:24:00 - 12/24 06:30:54	Fixed (0.0, 0.0)	synoptic, shifted 21.0 min
12/24 18:10:00 - 12/24 18:16:54	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min
12/25 05:56:30 - 12/25 06:03:24	Fixed (0.0, 0.0)	synoptic, shifted -6.5 min
12/25 17:58:30 - 12/25 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
12/26 06:03:00 - 12/26 06:09:54	Fixed (0.0, 0.0)	synoptic
12/26 18:01:00 - 12/26 18:07:54	Fixed (0.0, 0.0)	synoptic, shifted -2.0 min
12/27 06:33:00 - 12/27 06:40:55	Fixed (0.0, 0.0)	synoptic, shifted 30.0 min

PROG= 05 1-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 5 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 33 1-time(s) 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 49 1-time(s) 2.0sec
 - Al-poly/Open Al-poly/Open close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Al-poly/Open Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 39 1-time(s) 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 54 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
 - Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
12/24 09:03:00 - 12/24 17:43:30	Track (-766.0, 46.8) @ 12/24 09:00:00	dummy pointing for re-adjust
12/24 18:20:00 - 12/25 01:29:54	Track (-708.5, 49.8) @ 12/24 18:17:00	# AR obs.

PROG= 18 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 56 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
 - Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
- Subr= 2 5-time(s) 2.0sec
 - Seqn= 75 1-time(s) 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Seqn= 96 4-time(s) 60.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Flare mode

* * * * *

XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
12/22 10:32:00 - 12/22 17:46:54	Track (-952.3, 31.9) @ 12/22 10:25:00	# OP start + 10min AR obs, fixed pointing
12/22 18:00:00 - 12/23 05:59:54	Track (-934.7, 34.3) @ 12/22 17:57:00	# AR obs
12/23 06:13:00 - 12/23 09:59:54	Track (-895.5, 38.2) @ 12/23 06:10:00	# AR obs
12/23 10:03:00 - 12/23 18:03:24	Track (-880.5, 39.4) @ 12/23 10:00:00	# dummy pointing for re-adjust
12/23 18:16:30 - 12/24 06:20:54	Track (-844.4, 42.1) @ 12/23 18:13:30	# AR obs.
12/24 06:34:00 - 12/24 08:59:54	Track (-780.3, 46.0) @ 12/24 06:31:00	# AR obs.
12/24 09:03:00 - 12/24 17:43:30	Track (-766.0, 46.8) @ 12/24 09:00:00	dummy pointing for re-adjust
12/24 18:20:00 - 12/25 01:29:54	Track (-708.5, 49.8) @ 12/24 18:17:00	# AR obs.
12/25 01:33:00 - 12/25 05:53:24	Track (-708.5, 49.8) @ 12/24 18:17:00	# AR obs.
12/25 06:06:30 - 12/25 09:29:54	Track (-627.3, 53.3) @ 12/25 06:03:30	# AR obs.
12/25 09:33:00 - 12/25 17:55:24	Track (-602.0, 54.3) @ 12/25 09:30:00	dummy pointing for re-adjust
12/25 18:08:30 - 12/26 05:59:54	Track (-535.8, 56.7) @ 12/25 18:05:30	# AR obs.

12/26 06:13:00 - 12/26 09:59:54 Track (-436.6, 59.9) @ 12/26 06:10:00 # AR obs.
 12/26 10:03:00 - 12/26 17:57:54 Track (-403.7, 60.8) @ 12/26 10:00:00 dummy pointing for re-adjust
 12/26 18:11:00 - 12/27 06:29:54 Track (-332.2, 62.6) @ 12/26 18:08:00 # AR obs.

PROG= 07 30-time(s)

Subr= 1 20-time(s) 2.0sec														
Seqn= 11 1-time(s) 2.0sec		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec		thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
		med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec														
Seqn= 10 1-time(s) 2.0sec		med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84 1-time(s) 2.0sec		Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

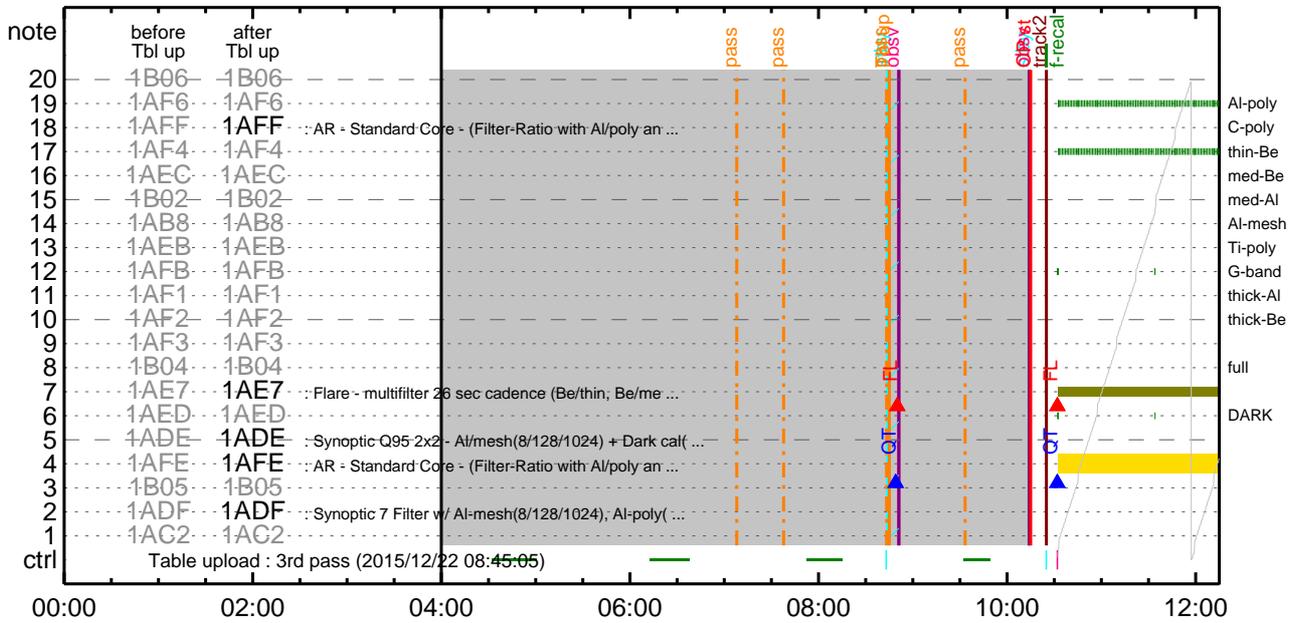
Flare Detection

* * * * *

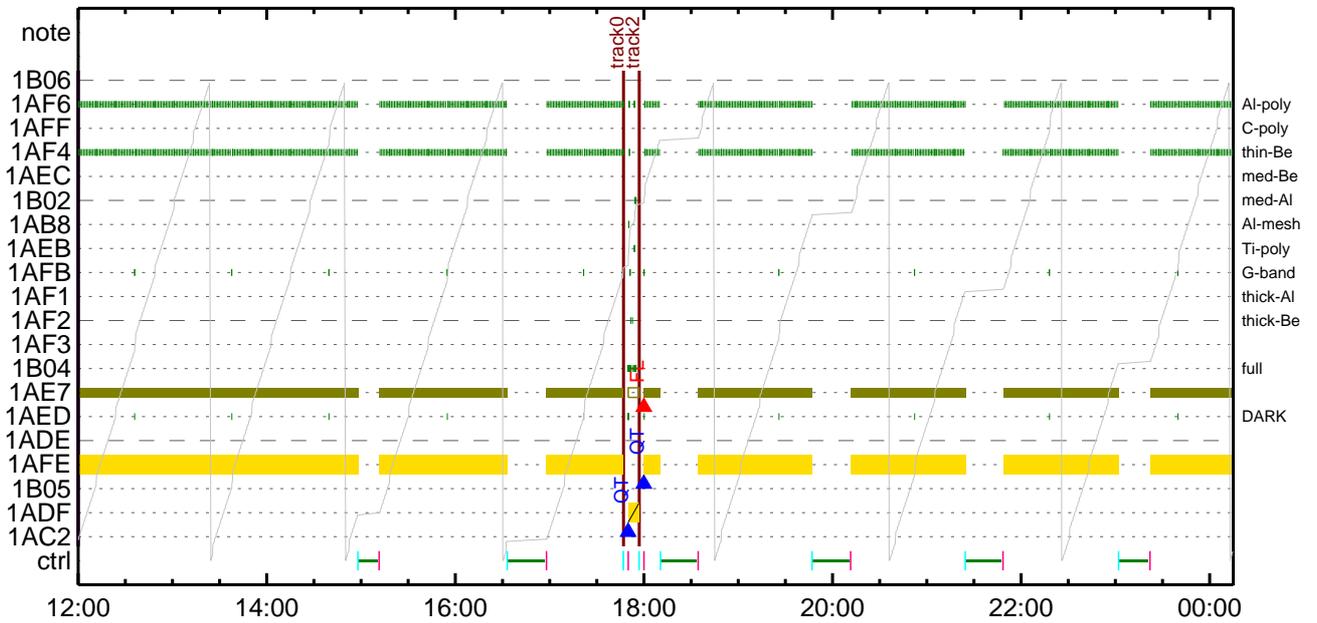
FLD Patrol

Term	Pointing (x, y)		Comment											
12/22 17:57:16 - 12/23 06:00:16	Track (-934.7,	34.3)	@ 12/22 17:57:00	# AR obs										
12/23 06:10:16 - 12/23 18:03:46	Track (-895.5,	38.2)	@ 12/23 06:10:00	# AR obs										
12/23 18:13:46 - 12/24 06:21:16	Track (-844.4,	42.1)	@ 12/23 18:13:30	# AR obs.										
12/24 06:31:16 - 12/24 18:07:16	Track (-780.3,	46.0)	@ 12/24 06:31:00	# AR obs.										
12/24 18:17:16 - 12/25 05:53:46	Track (-708.5,	49.8)	@ 12/24 18:17:00	# AR obs.										
12/25 06:03:46 - 12/25 17:55:46	Track (-627.3,	53.3)	@ 12/25 06:03:30	# AR obs.										
12/25 18:05:46 - 12/26 06:00:16	Track (-535.8,	56.7)	@ 12/25 18:05:30	# AR obs.										
12/26 06:10:16 - 12/26 17:58:16	Track (-436.6,	59.9)	@ 12/26 06:10:00	# AR obs.										
12/26 18:08:16 - 12/27 06:30:16	Track (-332.2,	62.6)	@ 12/26 18:08:00	# AR obs.										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	ROI: size (center)		Q=50	30sec			
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

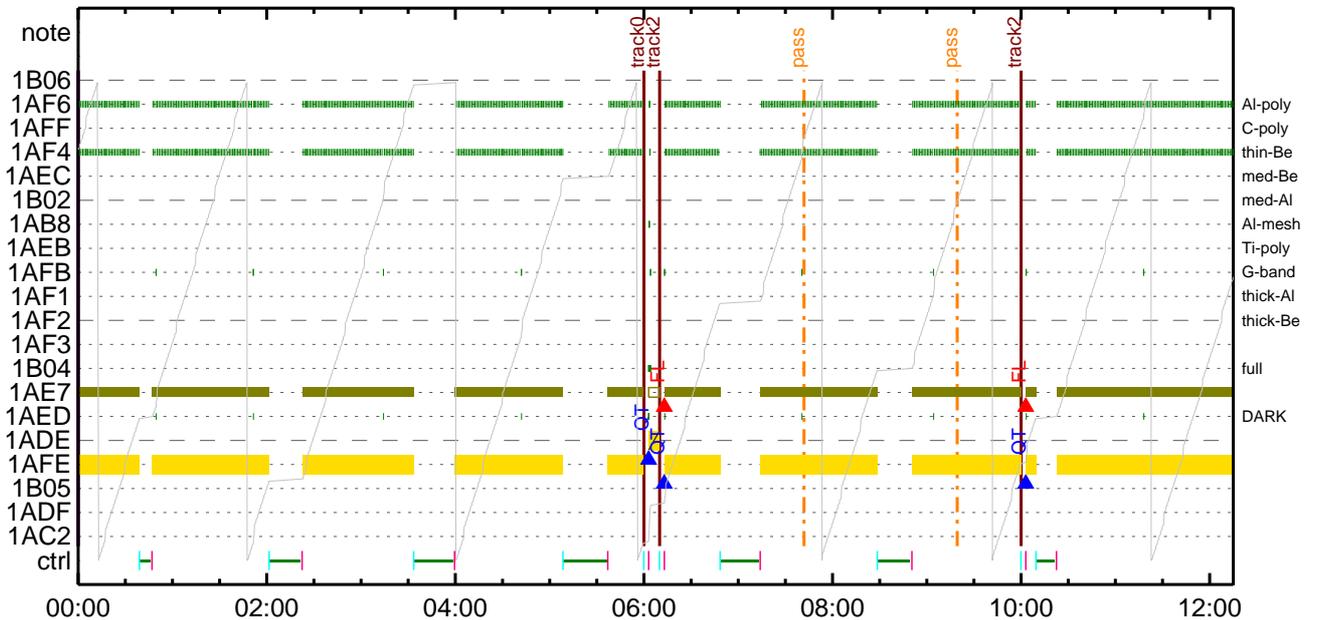
CMDI #0606 2015/12/22



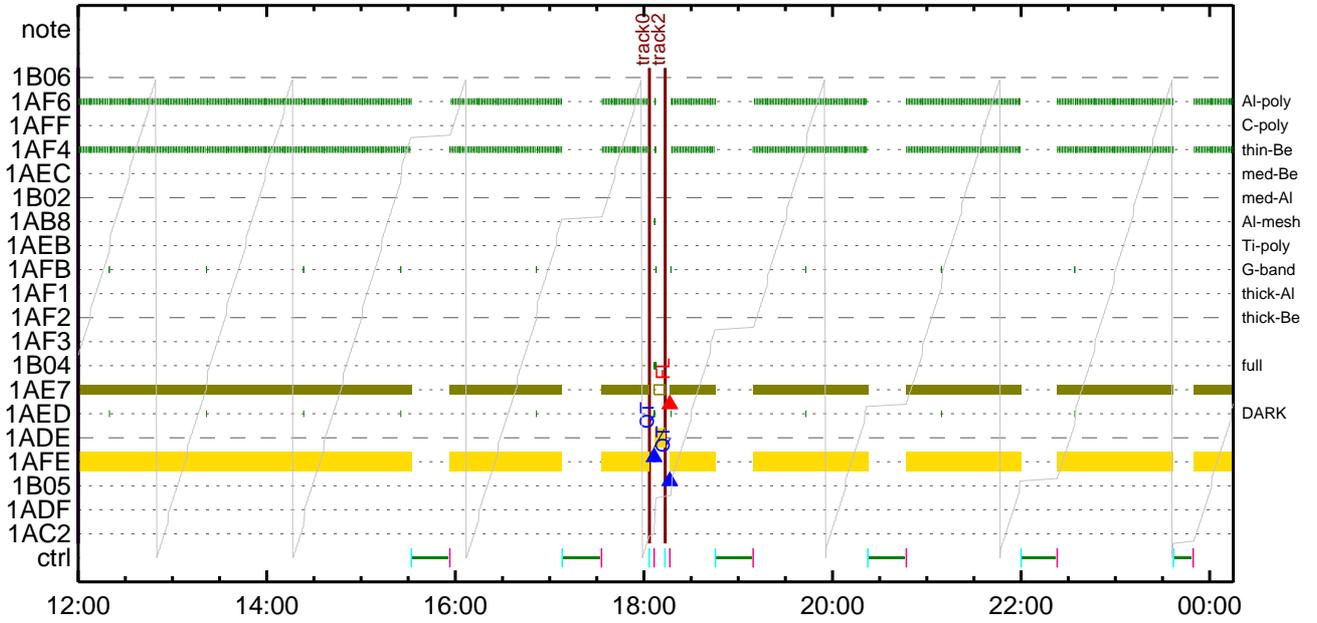
CMDI #0606 2015/12/22



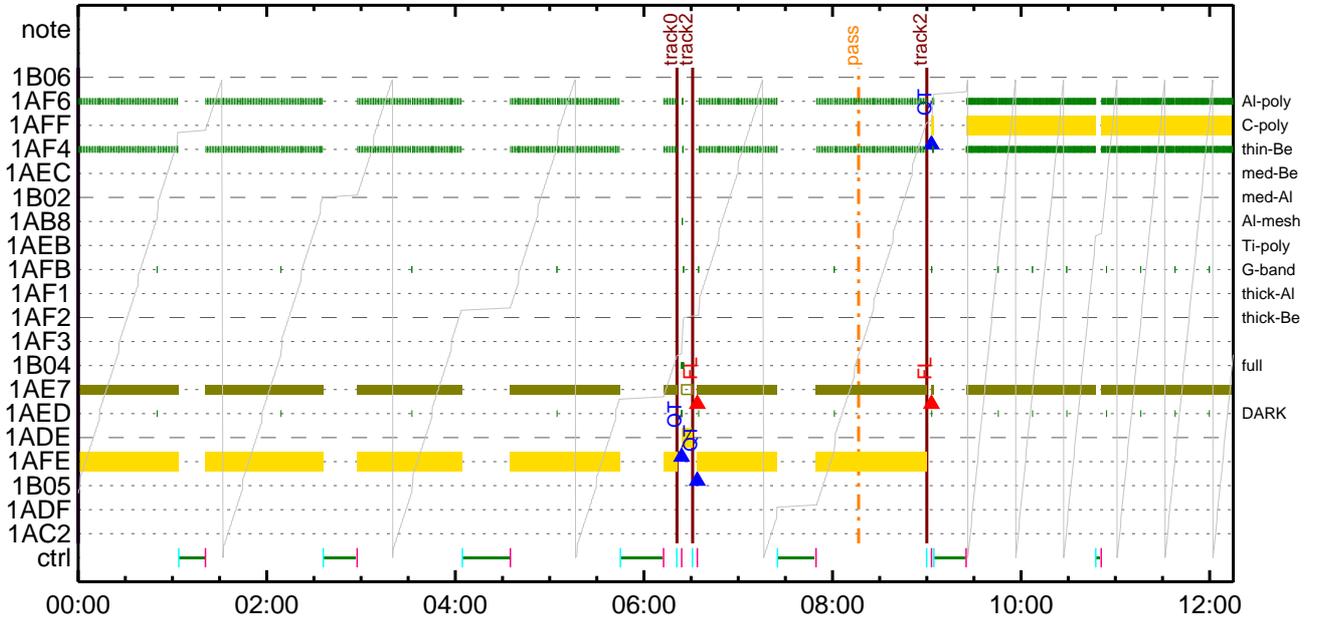
CMDI #0606 2015/12/23



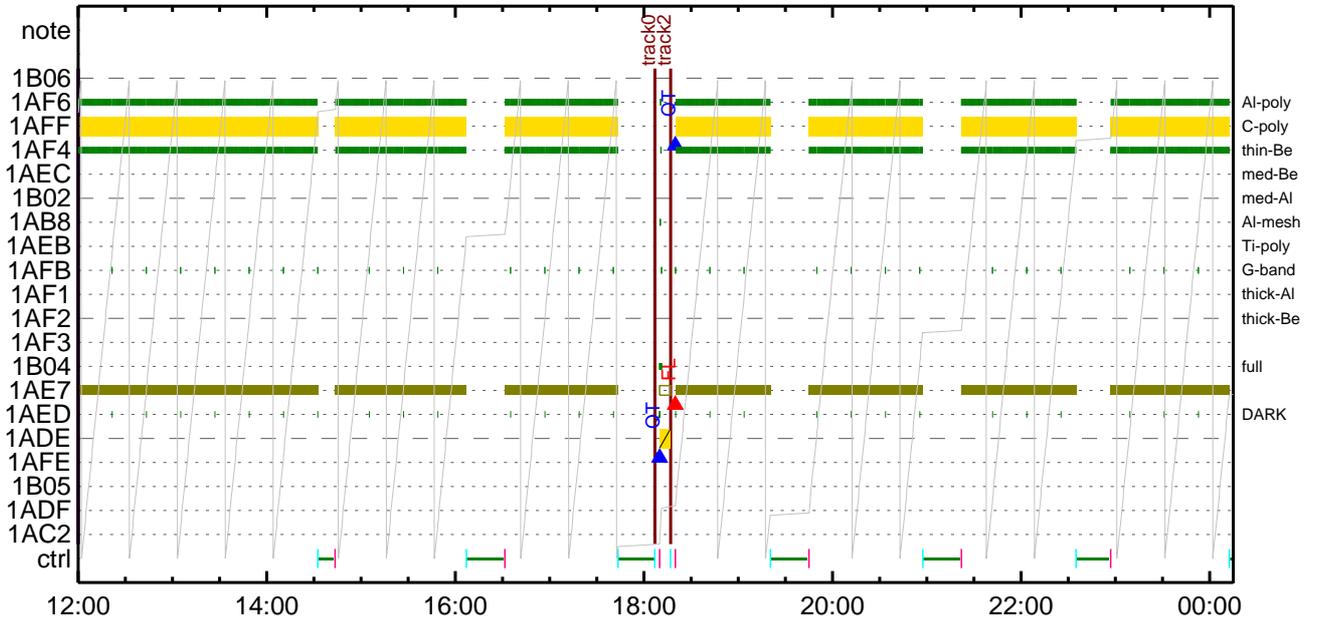
CMDI #0606 2015/12/23



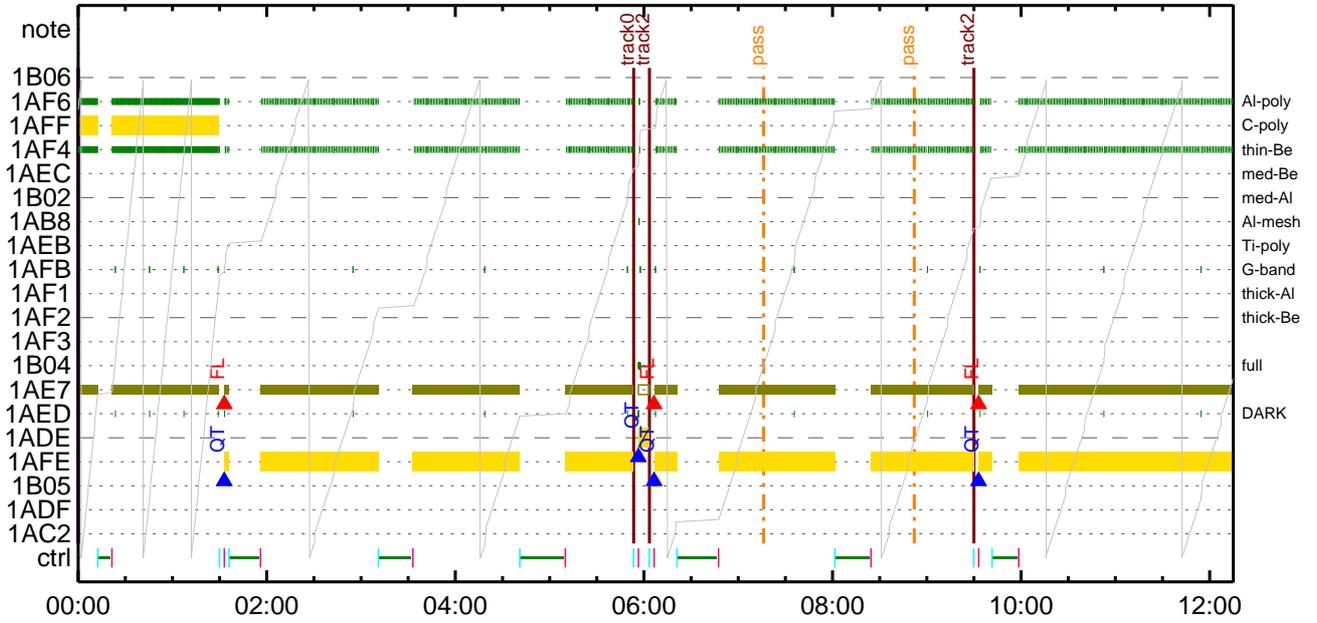
CMDI #0606 2015/12/24



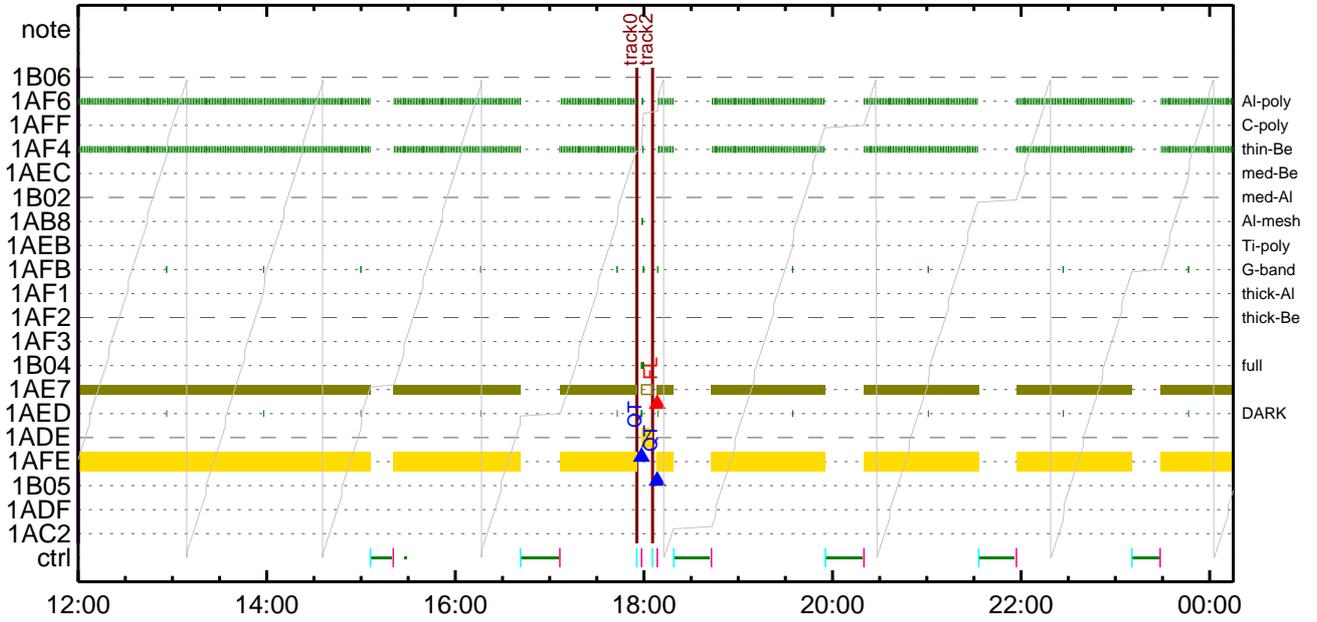
CMDI #0606 2015/12/24



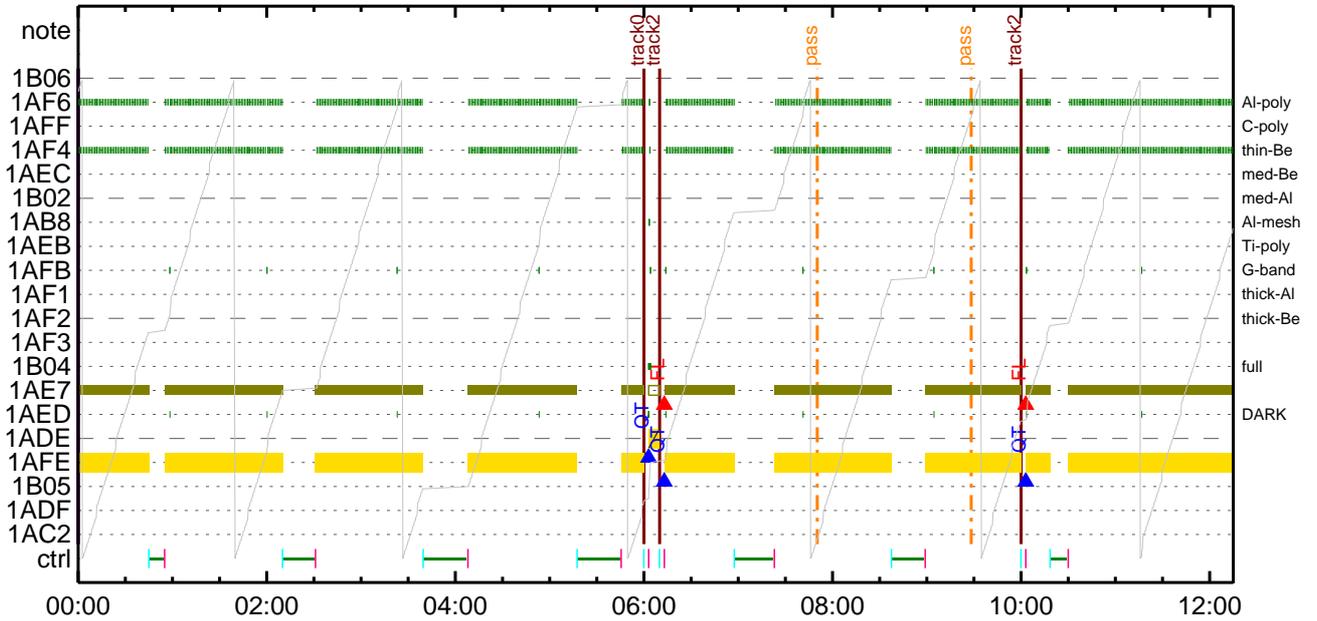
CMDI #0606 2015/12/25



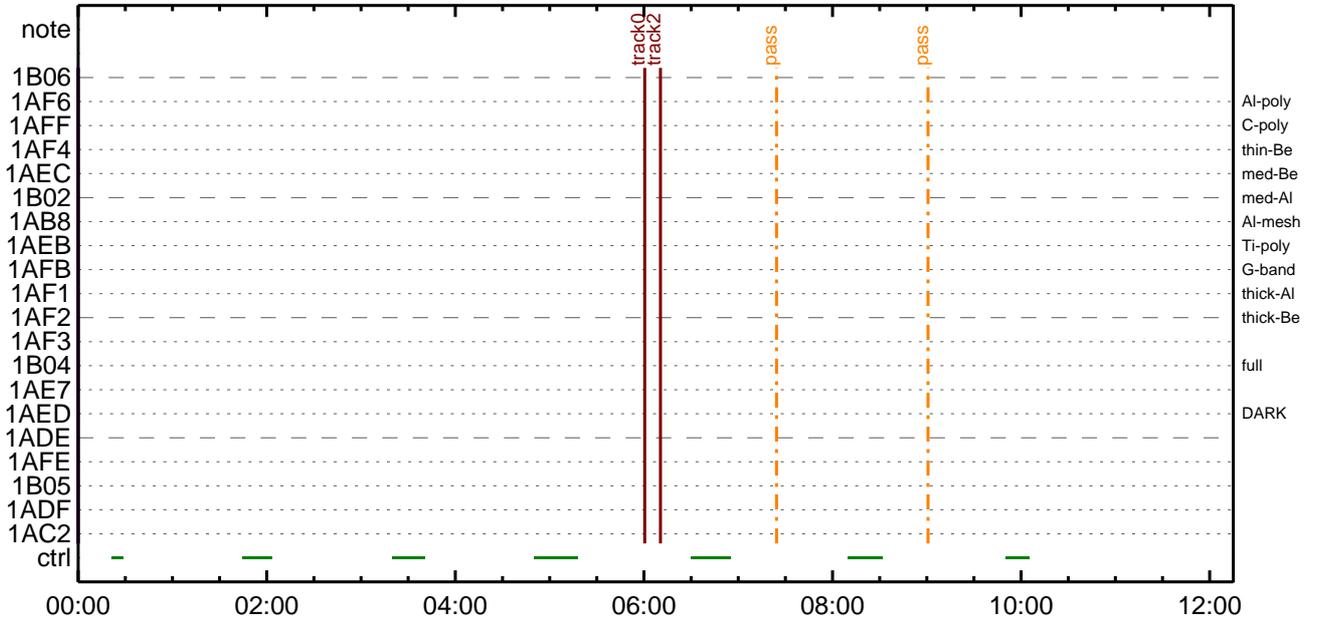
CMDI #0606 2015/12/25



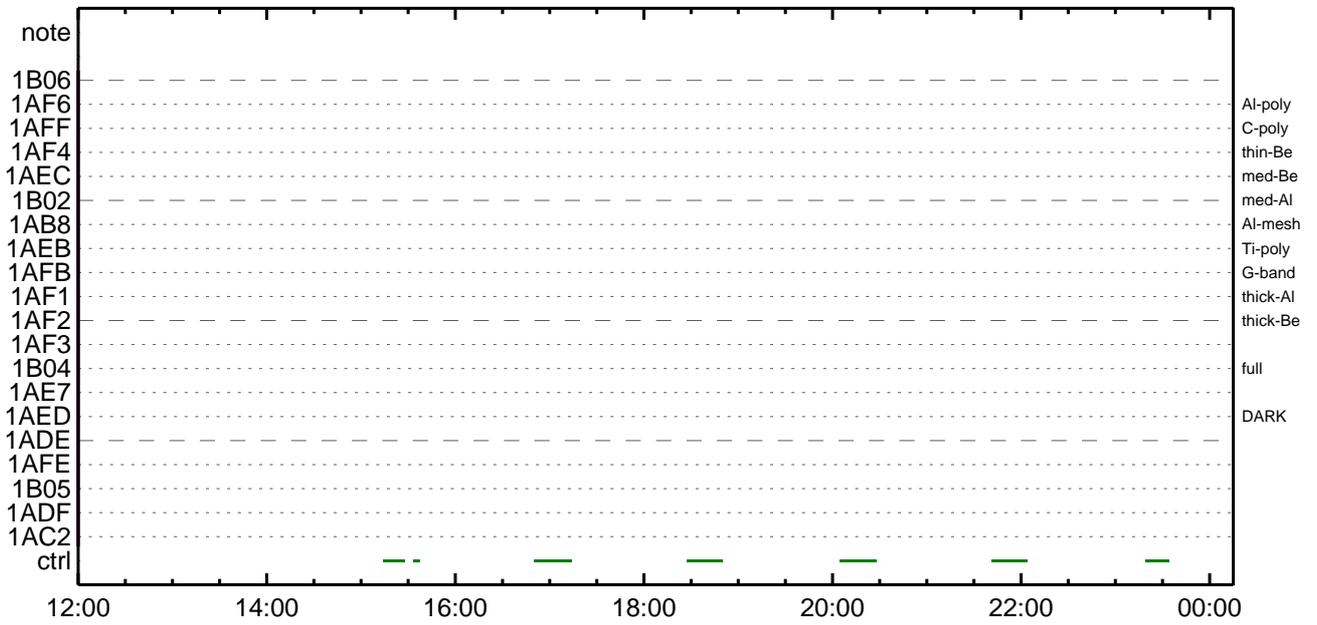
CMDI #0606 2015/12/26



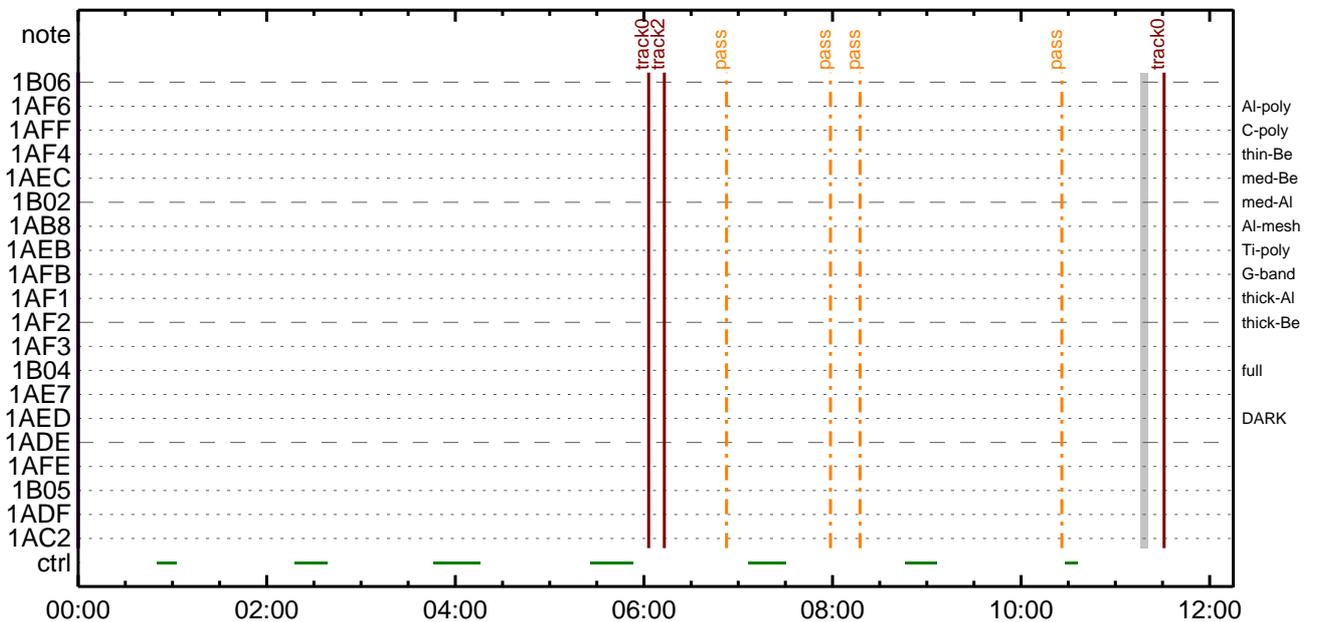
CMDI #0606 2015/12/28



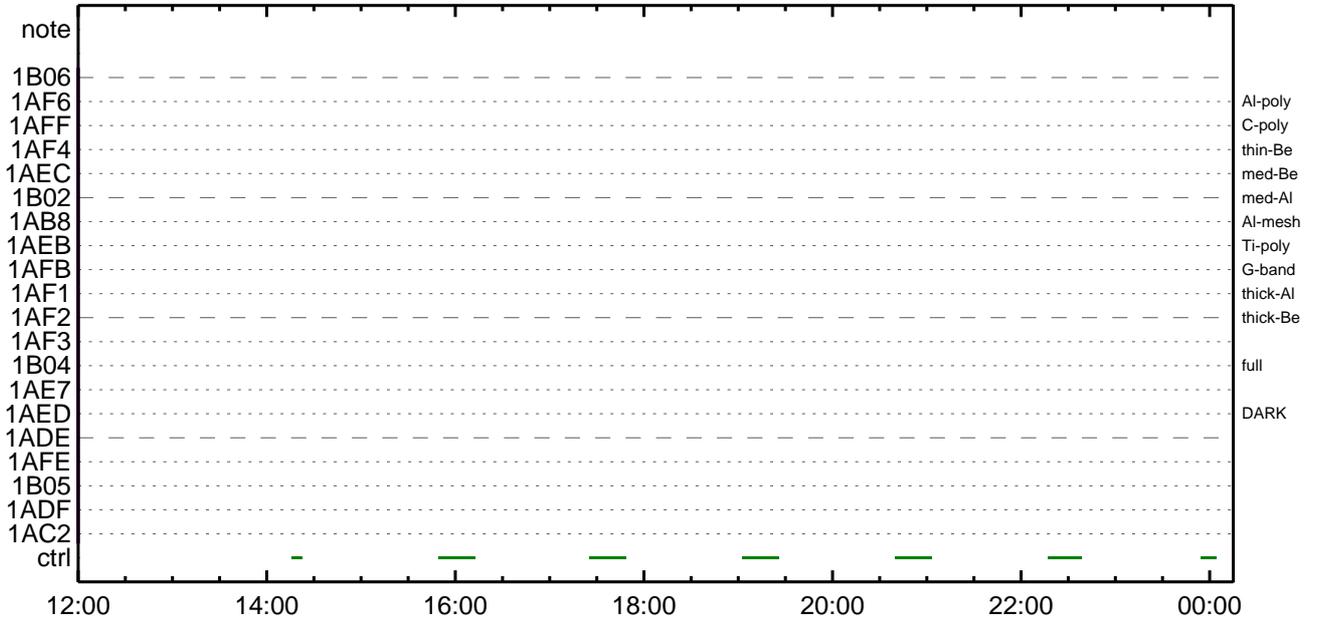
CMDI #0606 2015/12/28



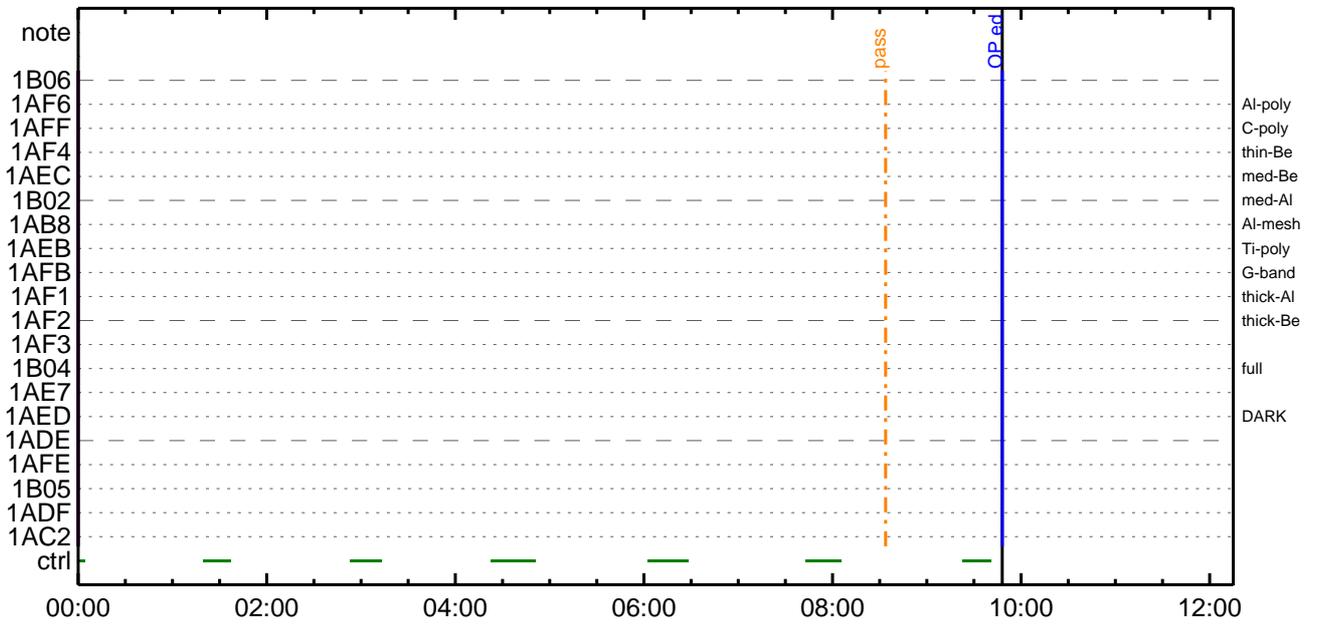
CMDI #0606 2015/12/29



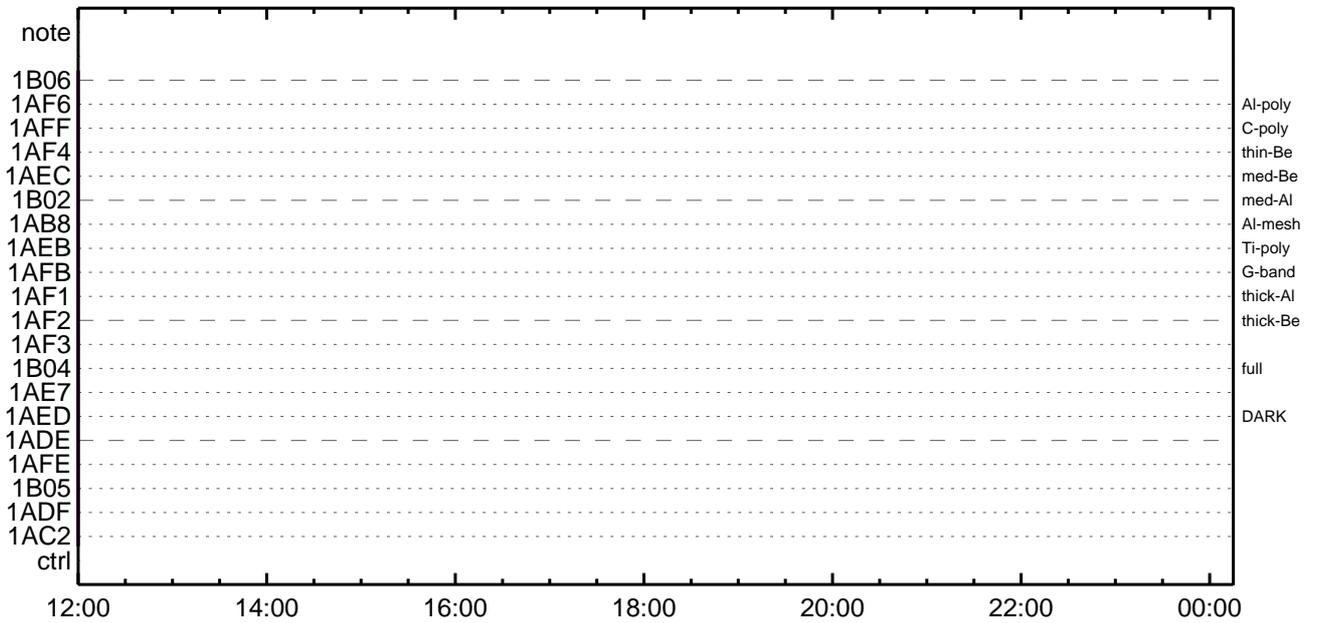
CMDI #0606 2015/12/29



CMDI #0606 2015/12/30



CMDI #0606 2015/12/30




```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Upload DPL table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. ¥ç¥Ã¥×¥í;¼¥É°ÊÁ°¤ESTS_CHK¤ðOFF¤È¤±¤è
0109 C.
0110 . S. RAM ram-271:MDP_DPL
0111 ( )
0112 C.
0113 . C. < Dump RAMID=MDP_DPL >
0114 +. DC 07-F0 MDP_DUMP_FGTBL
0115 BC (82 07 00 38 b8 00 40)
0116 C. -----
0117 C. MDP_DPL verify = OK [ ]
0118 C. -----
0119 C.
0120 C. STS_CHK¤ðON¤È¤±¤è
0121 C.
0122 . C. < Update MDP DSC PAR1 >
0123 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0124 BC (4c)
0125 C. MDP_CMD_CODE = F04C0700[ ]
0126 C. MDP_CMD_CNT (count-up 1) [ ]
0127 C. -----
0128 C.
0129 . C.
0130 . C. < Resume FG table (auto mode) >
0131 +. DC 07-F0 MDP_FG_CTRL_AUTO
0132 BC (50)
0133 . C. -----
0134 C. MDP_FG_CTRL_MODE = AUTO [ ]
0135 C. -----
0136 C.
0137 C.
0138 C. ***** XRT START *****
0139 C.
0140 +. DC 07-F0 MDP_XRT_CTRL_MANU
0141 BC (c1)
0142 + DC 07-F0 MDP_XRT_MODE_STBY
0143 BC (c3)
0144 . C. ----- Success Verify ? OK / NG_____
0145 C.
0146 C. XRT Obs. Table Upload
0147 . S. RAM ram-291:MDP_OBS_X
0148 ( )
0149 C.
0150 +. DC 07-F0 MDP_DUMP_XRTTBL
0151 BC (84 07 00 00 00 3a d4)
0152 . C. ----- Comparison Check ? OK / ERR _____
0153 C.
0154 C.
0155 +. DC 07-F0 MDP_XRT_ROI_SET
0156 BC (cd 01 b1 b1 04 04)
0157 + DC 07-F0 MDP_XRT_ROI_SET
0158 BC (cd 02 b1 b1 08 08)
0159 + DC 07-F0 MDP_XRT_ROI_SET
0160 BC (cd 03 b1 b1 08 08)
0161 + DC 07-F0 MDP_XRT_ROI_SET
0162 BC (cd 04 b1 b1 06 06)
0163 + DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 06 85 83 06 06)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 07 80 80 20 20)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 08 80 80 20 08)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 09 80 80 08 20)
0171 + DC 07-F0 MDP_XRT_ROI_SET
0172 BC (cd 0f 80 80 06 06)
0173 + DC 07-F0 MDP_XRT_ROI_SET
0174 BC (cd 10 80 80 08 08)
0175 + DC 07-F0 MDP_XRT_FLD_ENA
0176 BC (d8)
0177 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0178 BC (c8)
0179 + DC 07-F0 MDP_XRT_AEC_RESET
0180 BC (d0)
0181 + DC 07-F0 MDP_XRT_ARS_DIS
0182 BC (d5)
0183 + DC 07-F0 MDP_XRT_FLD_RESET
0184 BC (da)
0185 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0186 BC (c4 04)
0187 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0188 BC (c5 07)
0189 . C. ----- Success Verify ? OK / NG _____
0190 C.
0191 C.
0192 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0193 C.

```

```
0194 +. DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 +. TI 2015-12-22 10:14:02.0
0197 DC 07-F0 MDP_XRT_MODE_OBSV
0198 BC (c2)
0199 . C. ----- Success Verify ? OK / NG ____
0200 C.
0201 C. ***** XRT END *****
0202 C.
0203 . C. ***** MDP `úÃîñî»ò¼ŷñÊÂñ¹ñèDCBC•x²è *****
0204 C. (¼á°îŷÓŷÃŷÈŷŦŷËŷáŷçŷèñÊ¼ñ¼Ã»Ûñ¹ñè)
0205 . S. DC-BC dcbc-402:DCBC
0206 (MDP_known_event)
0207 C.
0208 C.
0209 . C. ***** ŷDŷ¹•Ï Daily±¿ÍÑñÊ´Øñ¹ñèDCBC•x²è *****
0210 . S. DC-BC dcbc-153:DCBC
0211 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0212 C.
0213 C.
0214 . C. ¡ãLOSŷÁŷSŷÃŷ´¼Ã»Ûñ¹ñè
0215 C.
0216 . C. ***** LOS *****
0217 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-568 2015-12-22 13:34:35 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µ°Æ»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÍÉ;ÈÈÈ¼°ÇÔα•¿¼i¹çαÍ;çÀ®, ùα¹αèαβαÇÁ+¿®α•αÊααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP `úÁÍσÍ»ò¼YαÊÁÐα¹αèDCBC•x²è *****
0016 C. (¼á°íYÓYÁYÈYβYÈYáYçYèαÈ¼αα¼Á»Ûα¹αè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YDY¹•Í Daily+¿ÍÑαÈ´Øα¹αèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

Dec 22, 15 13:34

XRT_OGLIST_0606.chk

Page 1/13

*** OP Sequence for XRT ***

2015/12/22	10:24:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/22	10:24:56.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2015/12/22	10:25:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2015/12/22	10:28:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/22	10:29:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/22	10:29:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/22	10:29:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/22	10:29:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/22	10:29:24.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/22	10:31:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/12/22	10:31:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/22	10:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/22	14:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/22	14:58:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/22	14:58:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/22	15:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/22	15:10:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/22	15:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/22	16:33:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/22	16:33:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/22	16:33:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/22	16:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/22	16:57:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/22	16:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/22	17:46:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/22	17:46:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/22	17:46:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/22	17:47:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/12/22	17:47:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/22	17:49:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/22	17:49:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/22	17:49:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/12/22	17:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/22	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/22	17:56:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/22	17:57:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2015/12/22	17:57:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/22	17:57:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/22	17:57:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/22	17:57:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/22	17:57:24.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/22	17:59:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2015/12/22	17:59:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/22	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/22	18:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/22	18:10:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/22	18:10:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			

2015/12/22	18:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2015/12/22	18:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/22	18:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/22	19:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/22	19:47:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/22	19:47:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/22	19:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/22	20:10:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/22	20:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/22	21:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/22	21:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/22	21:24:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/22	21:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/22	21:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/22	21:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/22	23:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/22	23:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/22	23:02:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/22	23:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/22	23:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/22	23:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/23	00:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/23	00:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/23	00:39:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/23	00:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/23	00:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/23	00:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/23	02:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/23	02:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/23	02:01:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/23	02:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/23	02:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/23	02:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/23	03:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/23	03:33:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/23	03:33:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/23	03:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/23	03:58:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/23	03:59:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/23	05:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/23	05:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/23	05:08:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/23	05:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/23	05:36:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/23	05:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/23	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/23	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/23	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2015/12/23	06:00:16.0	XRT_FLD_DIS_414_OG [0x19e]	AOCU_NM	5	02-76	00 00 00 00 00	
2015/12/23	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	

Dec 22, 15 13:34

XRT_OGLIST_0606.chk

Page 3/13

2015/12/23	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/23	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/12/23	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/23	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/23	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/12/23	06:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/23	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/12/23	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/12/23	06:10:20.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/12/23	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/23	06:10:24.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/23	06:12:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2015/12/23	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/12/23	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/23	06:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/23	06:48:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/23	06:48:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/23	06:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/23	07:13:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/23	07:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/23	08:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/23	08:28:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/23	08:28:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/23	08:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/23	08:49:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/23	08:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/23	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/23	09:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/12/23	10:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/23	10:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/12/23	10:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/12/23	10:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/12/23	10:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/23	10:00:24.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/23	10:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2015/12/23	10:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/12/23	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/23	10:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/23	10:09:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/23	10:09:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/23	10:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/23	10:21:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/23	10:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/23	15:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/23	15:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/23	15:32:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/23	15:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/23	15:55:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/23	15:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

Dec 22, 15 13:34

XRT_OGLIST_0606.chk

Page 4/13

2015/12/23	17:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/12/23	17:08:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/12/23	17:08:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/12/23	17:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/12/23	17:32:00.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2015/12/23	17:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/12/23	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/12/23	18:03:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2015/12/23	18:03:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCS_ORe-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00			
2015/12/23	18:03:46.0	XRT_FLD_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9			
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2015/12/23	18:06:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2015/12/23	18:06:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/12/23	18:06:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2015/12/23	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/12/23	18:13:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/12/23	18:13:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/12/23	18:13:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCS_ORe-point_Start_1_OG [0x097]						
			AOCU_NM	5	02-76	02 00 00 00 00			
2015/12/23	18:13:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/12/23	18:13:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/12/23	18:13:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/12/23	18:13:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/12/23	18:13:54.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/12/23	18:16:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2015/12/23	18:16:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2015/12/23	18:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/12/23	18:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/12/23	18:45:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/12/23	18:45:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/12/23	18:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/12/23	19:08:30.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2015/12/23	19:09:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/12/23	20:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/12/23	20:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/12/23	20:22:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/12/23	20:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/12/23	20:46:00.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2015/12/23	20:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/12/23	22:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/12/23	22:00:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/12/23	22:00:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/12/23	22:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/12/23	22:22:00.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2015/12/23	22:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/12/23	23:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/12/23	23:37:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2015/12/23	23:37:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/12/23	23:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/12/23	23:48:30.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2015/12/23	23:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO_424_OG [0x1a8]						

Dec 22, 15 13:34

XRT_OGLIST_0606.chk

Page 5/13

2015/12/24	01:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	01:04:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	01:04:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	01:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	01:20:00.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	01:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	02:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	02:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	02:36:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	02:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	02:56:30.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	02:57:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	04:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	04:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	04:04:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	04:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	04:34:00.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	04:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	05:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	05:45:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	05:45:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	05:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	06:11:30.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	06:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	06:20:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	06:20:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2015/12/24	06:21:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2015/12/24	06:21:16.0	XRT_FLD_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/12/24	06:23:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/12/24	06:23:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/12/24	06:23:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2015/12/24	06:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	06:30:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	06:30:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2015/12/24	06:31:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00		
2015/12/24	06:31:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/12/24	06:31:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/12/24	06:31:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/12/24	06:31:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/12/24	06:31:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	06:33:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2015/12/24	06:33:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2015/12/24	06:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	07:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	07:25:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	07:25:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	07:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	07:48:30.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	07:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						

Dec 22, 15 13:34

XRT_OGLIST_0606.chk

Page 6/13

2015/12/24	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	08:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2015/12/24	09:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00	00 00 00
2015/12/24	09:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/12/24	09:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/12/24	09:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/12/24	09:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/12/24	09:00:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	09:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2015/12/24	09:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2015/12/24	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	09:04:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	09:04:32.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	09:04:34.5	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	09:07:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	09:24:00.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	09:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	10:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	10:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	10:47:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	10:50:00.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	10:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	10:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	14:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	14:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	14:32:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	14:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	14:42:30.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	14:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	16:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	16:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	16:07:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	16:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	16:30:30.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	16:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	17:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	17:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/12/24	17:43:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/12/24	17:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/12/24	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/12/24	18:06:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2015/12/24	18:07:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00 00 00
2015/12/24	18:07:16.0	XRT_FLD_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2015/12/24	18:09:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2015/12/24	18:09:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/12/24	18:09:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05	
2015/12/24	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/12/24	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

2015/12/24	18:16:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/12/24	18:17:00.0	AOCs_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02	00	00	00
2015/12/24	18:17:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2015/12/24	18:17:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2015/12/24	18:17:20.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0		d0		
2015/12/24	18:17:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2015/12/24	18:17:24.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0		da		
2015/12/24	18:19:56.0	XRT_QT_PROG_SET_447_OG [0x1bf] MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2015/12/24	18:19:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/12/24	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2015/12/24	19:20:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2015/12/24	19:20:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2015/12/24	19:20:34.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2015/12/24	19:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2015/12/24	19:44:00.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	19:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2015/12/24	20:57:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2015/12/24	20:57:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2015/12/24	20:57:34.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2015/12/24	21:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2015/12/24	21:21:00.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	21:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2015/12/24	22:35:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2015/12/24	22:35:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2015/12/24	22:35:04.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2015/12/24	22:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2015/12/24	22:56:00.0	XRT_Custom_430_OG [0x1ae]						
2015/12/24	22:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2015/12/25	00:12:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2015/12/25	00:12:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2015/12/25	00:12:34.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2015/12/25	00:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2015/12/25	00:20:30.0	XRT_Custom_430_OG [0x1ae]						
2015/12/25	00:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2015/12/25	01:29:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2015/12/25	01:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2015/12/25	01:30:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2015/12/25	01:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2015/12/25	01:30:20.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0		d0		
2015/12/25	01:30:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2015/12/25	01:30:24.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0		da		
2015/12/25	01:32:56.0	XRT_QT_PROG_SET_431_OG [0x1af] MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2015/12/25	01:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/12/25	01:33:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2015/12/25	01:36:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2015/12/25	01:36:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2015/12/25	01:36:04.0	XRT_PREFLR_STRT_416_OG [0x1a0] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2015/12/25	01:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2015/12/25	01:55:00.0	XRT_Custom_430_OG [0x1ae]						

2015/12/25	01:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	03:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	03:11:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	03:11:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	03:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	03:32:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/25	03:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	04:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	04:41:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	04:41:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	04:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	05:09:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/25	05:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	05:53:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	05:53:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/12/25	05:53:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/12/25	05:53:46.0	XRT_FLD_DIS_414_OG [0x19e]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/12/25	05:56:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/12/25	05:56:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/25	05:56:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/12/25	05:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	06:03:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/12/25	06:03:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/25	06:03:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/12/25	06:03:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/12/25	06:03:50.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/12/25	06:03:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/25	06:03:54.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	06:06:26.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2015/12/25	06:06:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/12/25	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	06:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	06:21:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	06:21:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	06:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	06:46:30.5	XRT_Custom_430_OG [0x1ae]							
2015/12/25	06:47:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	08:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	08:01:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	08:01:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	08:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	08:23:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/25	08:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	09:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	09:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/12/25	09:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/25	09:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/12/25	09:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							

Dec 22, 15 13:34

XRT_OGLIST_0606.chk

Page 9/13

2015/12/25	09:30:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/12/25	09:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/25	09:30:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	09:32:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2015/12/25	09:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/12/25	09:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	09:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	09:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	09:41:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	09:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	09:57:30.0	XRT_Custom_430_OG [0x1ae]								
2015/12/25	09:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	15:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	15:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	15:06:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	15:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	15:19:30.0	XRT_Custom_430_OG [0x1ae]								
2015/12/25	15:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	16:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	16:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	16:41:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	16:41:34.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	16:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	17:05:30.0	XRT_Custom_430_OG [0x1ae]								
2015/12/25	17:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	17:55:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/12/25	17:55:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2015/12/25	17:55:46.0	XRT_FLD_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/12/25	17:58:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/12/25	17:58:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/25	17:58:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2015/12/25	17:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	18:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	18:05:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/12/25	18:05:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00	00	00	00
2015/12/25	18:05:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/12/25	18:05:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/12/25	18:05:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/12/25	18:05:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/25	18:05:54.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	18:08:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2015/12/25	18:08:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/12/25	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	18:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	18:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	18:19:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	18:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	18:42:00.0	XRT_Custom_430_OG [0x1ae]								

2015/12/25	18:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	19:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	19:55:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	19:55:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	19:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	20:19:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/25	20:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	21:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	21:33:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	21:33:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	21:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	21:56:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/25	21:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/25	23:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/25	23:10:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/25	23:10:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/25	23:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/25	23:27:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/25	23:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	00:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	00:45:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	00:45:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	00:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	00:54:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/26	00:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	02:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	02:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	02:10:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	02:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	02:30:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/26	02:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	03:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	03:39:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	03:39:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	03:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	04:07:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/26	04:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	05:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	05:17:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	05:17:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	05:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	05:44:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/26	05:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/12/26	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/12/26	06:00:16.0	XRT_FLD_DIS_414_OG [0x19e]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/12/26	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/12/26	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/26	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				

Dec 22, 15 13:34

XRT_OGLIST_0606.chk

Page 11/13

2015/12/26	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/12/26	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/26	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/12/26	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/12/26	06:10:20.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/12/26	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/26	06:10:24.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	06:12:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2015/12/26	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/12/26	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	06:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	06:57:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	06:57:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	07:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	07:22:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/26	07:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	08:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	08:37:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	08:37:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	08:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	08:58:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/26	08:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	09:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/12/26	10:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/26	10:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/12/26	10:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/12/26	10:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/12/26	10:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/26	10:00:24.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	10:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2015/12/26	10:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/12/26	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	10:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	10:18:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	10:18:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	10:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	10:29:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/26	10:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	14:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	14:09:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	14:09:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	14:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	14:13:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/26	14:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	15:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	15:40:32.0	XRT_FLD_RESET_415_OG [0x19f]							

Dec 22, 15 13:34

XRT_OGLIST_0606.chk

Page 12/13

2015/12/26	15:40:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	15:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	16:04:00.0	XRT_Custom_430_OG [0x1ae]								
2015/12/26	16:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	17:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	17:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	17:17:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	17:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	17:40:30.0	XRT_Custom_430_OG [0x1ae]								
2015/12/26	17:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	17:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	17:57:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/12/26	17:58:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/12/26	17:58:16.0	XRT_FLD_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/12/26	18:00:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/12/26	18:00:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/26	18:00:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/12/26	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	18:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	18:07:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/12/26	18:08:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/26	18:08:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/12/26	18:08:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/12/26	18:08:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/12/26	18:08:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/26	18:08:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	18:10:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2015/12/26	18:10:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/12/26	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	18:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	18:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	18:54:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	18:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	19:17:00.0	XRT_Custom_430_OG [0x1ae]								
2015/12/26	19:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	20:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	20:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	20:31:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	20:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	20:54:30.0	XRT_Custom_430_OG [0x1ae]								
2015/12/26	20:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	22:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	22:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/26	22:08:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	22:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	22:30:30.0	XRT_Custom_430_OG [0x1ae]								
2015/12/26	22:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/26	23:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/26	23:45:32.0	XRT_FLD_RESET_415_OG [0x19f]								

Dec 22, 15 13:34

XRT_OGLIST_0606.chk

Page 13/13

2015/12/26	23:45:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/26	23:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/26	23:56:00.0	XRT_Custom_430_OG [0x1ae]								
2015/12/26	23:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/27	01:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/27	01:11:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/27	01:11:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/27	01:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/27	01:28:30.0	XRT_Custom_430_OG [0x1ae]								
2015/12/27	01:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/27	02:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/27	02:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/27	02:44:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/27	02:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/27	03:05:00.5	XRT_Custom_430_OG [0x1ae]								
2015/12/27	03:06:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/27	04:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/27	04:13:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/27	04:13:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/27	04:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/27	04:42:30.0	XRT_Custom_430_OG [0x1ae]								
2015/12/27	04:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/27	05:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/27	05:53:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/27	05:53:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/27	05:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/27	06:20:00.0	XRT_Custom_430_OG [0x1ae]								
2015/12/27	06:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/27	06:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/27	06:29:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/12/27	06:30:00.5	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/12/27	06:30:16.0	XRT_FLD_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/12/27	06:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/12/27	06:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/27	06:32:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/12/27	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/27	06:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/27	06:40:55.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/27	06:41:00.5	XRT_TCIB_XRT_S_HTR_A_ENA_432_OG [0x1b0]	TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2015/12/27	18:15:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/12/27	18:25:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/28	06:00:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/12/28	06:10:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/29	06:03:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2015/12/29	06:13:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00				
2015/12/29	11:31:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				