

XRT Timeline to be uploaded on 2015/12/29

Period: 2015/12/29 11:21:00 - 2016/01/06 10:40:00

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Normal mode

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XOB #1AF1: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Al/Poly(1443ms) - w leak image-3ms

Term	Pointing (x, y)	Comment
12/30 12:03:00 - 12/30 12:09:54	Fixed (-528.4, -528.4)	XRT quadrant 1/4
PROG= 11 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 86 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Al-poly/Open	med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AF2: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
12/30 12:13:00 - 12/30 12:19:54	Fixed (528.4, -528.4)	XRT quadrant 2/4
PROG= 10 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 15 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Al-poly/Open	med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AF3: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
12/30 12:23:00 - 12/30 12:29:54	Fixed (528.4, 528.4)	XRT quadrant 3/4
PROG= 09 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 35 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Al-poly/Open	med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AF4: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
12/30 12:33:05 - 12/30 12:39:54	Fixed (-528.4, 528.4)	XRT quadrant 4/4
PROG= 17 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 3 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
Open/thick-Be	Open/thick-Be close Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
Subr= 2 1-time(s) 2.0sec		

Seqn= 55	2-time(s)	2.0sec																		
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Subr= 3	2-time(s)	2.0sec																		
Seqn= 54	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec								
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

XOB #1B02: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
12/30 12:43:00 - 12/30 17:36:54	Track (565.2, -310.9) @ 12/30 12:40:00	AR12473 tracking
12/30 17:50:00 - 12/31 05:54:54	Track (599.4, -311.9) @ 12/30 17:47:00	AR12473 tracking
12/31 06:08:00 - 12/31 09:29:54	Track (675.6, -314.8) @ 12/31 06:05:00	AR12473 tracking

PROG= 15 Inf-time(s)

Subr= 1	1-time(s)	2.0sec																		
Seqn= 56	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec								
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec								
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec								
Subr= 2	5-time(s)	2.0sec																		
Seqn= 75	1-time(s)	2.0sec																		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec								
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
Seqn= 79	4-time(s)	120.0sec																		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	34.0sec								
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	34.0sec								
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + Th

Term	Pointing (x, y)	Comment
12/30 17:44:00 - 12/30 17:46:54	Fixed (0.0, 0.0)	synoptic, shifted -23.0 min
12/31 18:03:00 - 12/31 18:09:54	Fixed (0.0, 0.0)	synoptic
01/01 06:14:30 - 01/01 06:21:24	Fixed (0.0, 0.0)	synoptic, shifted 11.5 min
01/01 18:02:30 - 01/01 18:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min
01/02 05:46:30 - 01/02 05:53:24	Fixed (0.0, 0.0)	synoptic, shifted -16.5 min
01/02 17:49:00 - 01/02 17:55:54	Fixed (0.0, 0.0)	synoptic, shifted -14.0 min
01/03 06:03:00 - 01/03 06:09:54	Fixed (0.0, 0.0)	synoptic
01/03 18:05:00 - 01/03 18:11:54	Fixed (0.0, 0.0)	synoptic, shifted 2.0 min
01/04 06:23:00 - 01/04 06:29:54	Fixed (0.0, 0.0)	synoptic, shifted 20.0 min
01/04 18:09:00 - 01/04 18:15:54	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min
01/05 05:55:30 - 01/05 09:40:00	Fixed (0.0, 0.0)	synoptic, shifted -7.5 min

PROG= 05 1-time(s)

Subr= 1	1-time(s)	2.0sec																		
Seqn= 5	1-time(s)	2.0sec																		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec								
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec								
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec								
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec								
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec								
Seqn= 33	1-time(s)	2.0sec																		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Seqn= 49	1-time(s)	2.0sec																		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Seqn= 39	1-time(s)	2.0sec																		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Seqn= 54	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec								
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

XOB #1ADF: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Med-Al(40

Term	Pointing (x, y)	Comment
12/31 05:58:00 - 12/31 06:04:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min

PROG= 02 1-time(s)

Subr= 1												
1-time(s) 2.0sec												
Seqn= 5												
1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 33												
1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49												
1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77												
1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54												
1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2												
1-time(s) 2.0sec												
Seqn= 46												
2-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 72												
2-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 59												
2-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B07: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
12/31 09:33:00 - 12/31 17:59:54	Track (695.2, -315.7) @ 12/31 09:30:00	dummy pointing
12/31 18:13:00 - 01/01 06:11:24	Track (741.5, -318.1) @ 12/31 18:10:00	AR12473 tracking
01/01 06:24:30 - 01/01 09:29:54	Track (798.0, -322.1) @ 01/01 06:21:30	AR12473 tracking
01/01 09:33:00 - 01/01 17:34:00	Track (810.8, -323.1) @ 01/01 09:30:00	dummy pointing
01/01 18:12:30 - 01/02 05:43:24	Track (842.5, -326.3) @ 01/01 18:09:30	AR12473 tracking
01/02 05:56:30 - 01/02 09:14:54	Track (876.2, -330.9) @ 01/02 05:53:30	AR12473 tracking
01/02 09:18:00 - 01/02 17:45:54	Fixed (883.9, -332.3)	Fixed Pointing
01/02 17:59:00 - 01/03 05:59:54	Fixed (883.9, -332.3)	AR12473 tracking
01/03 06:13:00 - 01/03 18:01:54	Fixed (883.9, -332.3)	AR12473 tracking
01/03 18:15:00 - 01/04 06:19:54	Fixed (883.9, -332.3)	AR12473 tracking
01/04 06:33:00 - 01/04 17:42:30	Fixed (883.9, -332.3)	AR12473 tracking
01/04 18:19:00 - 01/05 01:35:00	Fixed (883.9, -332.3)	AR12473 tracking

PROG= 19 Inf.-time(s)

Subr= 1												
1-time(s) 2.0sec												
Seqn= 56												
1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2												
1-time(s) 2.0sec												
Seqn= 75												
5-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96												
8-time(s) 360.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
01/05 01:55:30 - 01/05 03:52:24	Fixed (0.0, 930.0)	N-limb Coalignment

PROG= 16

Subr= 1												
24-time(s) 300.0sec												
Seqn= 98												
1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Seqn= 63												
1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Seqn= 45												
1-time(s) 2.0sec												

Al-poly/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x1536 (1024, 768)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
01/05 03:55:30 - 01/05 05:52:24	Fixed (-945.0, 0.0)	E-limb Coalignment
PROG= 06 1-time(s)		
└─ Subr= 1 15-time(s) 480.0sec		
└─ Seqn= 19 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Seqn= 43 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Seqn= 70 1-time(s) 2.0sec		
Al-poly/Open	med-Be/Open	close Safe Norm 1.41s Obs 2x2 1024x1024 (1536, 1536) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Flare mode

* * * * *

XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
12/30 12:43:00 - 12/30 17:36:54	Track (565.2, -310.9) @ 12/30 12:40:00	AR12473 tracking
12/30 17:50:00 - 12/31 05:54:54	Track (599.4, -311.9) @ 12/30 17:47:00	AR12473 tracking
12/31 06:08:00 - 12/31 09:29:54	Track (675.6, -314.8) @ 12/31 06:05:00	AR12473 tracking
12/31 09:33:00 - 12/31 17:59:54	Track (695.2, -315.7) @ 12/31 09:30:00	dummy pointing
12/31 18:13:00 - 01/01 06:11:24	Track (741.5, -318.1) @ 12/31 18:10:00	AR12473 tracking
01/01 06:24:30 - 01/01 09:29:54	Track (798.0, -322.1) @ 01/01 06:21:30	AR12473 tracking
01/01 09:33:00 - 01/01 17:34:00	Track (810.8, -323.1) @ 01/01 09:30:00	dummy pointing
01/01 18:12:30 - 01/02 05:43:24	Track (842.5, -326.3) @ 01/01 18:09:30	AR12473 tracking
01/02 05:56:30 - 01/02 09:14:54	Track (876.2, -330.9) @ 01/02 05:53:30	AR12473 tracking
01/02 09:18:00 - 01/02 17:45:54	Fixed (883.9, -332.3)	Fixed Pointing
01/02 17:59:00 - 01/03 05:59:54	Fixed (883.9, -332.3)	AR12473 tracking
01/03 06:13:00 - 01/03 18:01:54	Fixed (883.9, -332.3)	AR12473 tracking
01/03 18:15:00 - 01/04 06:19:54	Fixed (883.9, -332.3)	AR12473 tracking
01/04 06:33:00 - 01/04 17:42:30	Fixed (883.9, -332.3)	AR12473 tracking
01/04 18:19:00 - 01/05 01:35:00	Fixed (883.9, -332.3)	AR12473 tracking

PROG= 07 30-time(s)												
└─ Subr= 1 20-time(s) 2.0sec												
└─ Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 84 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

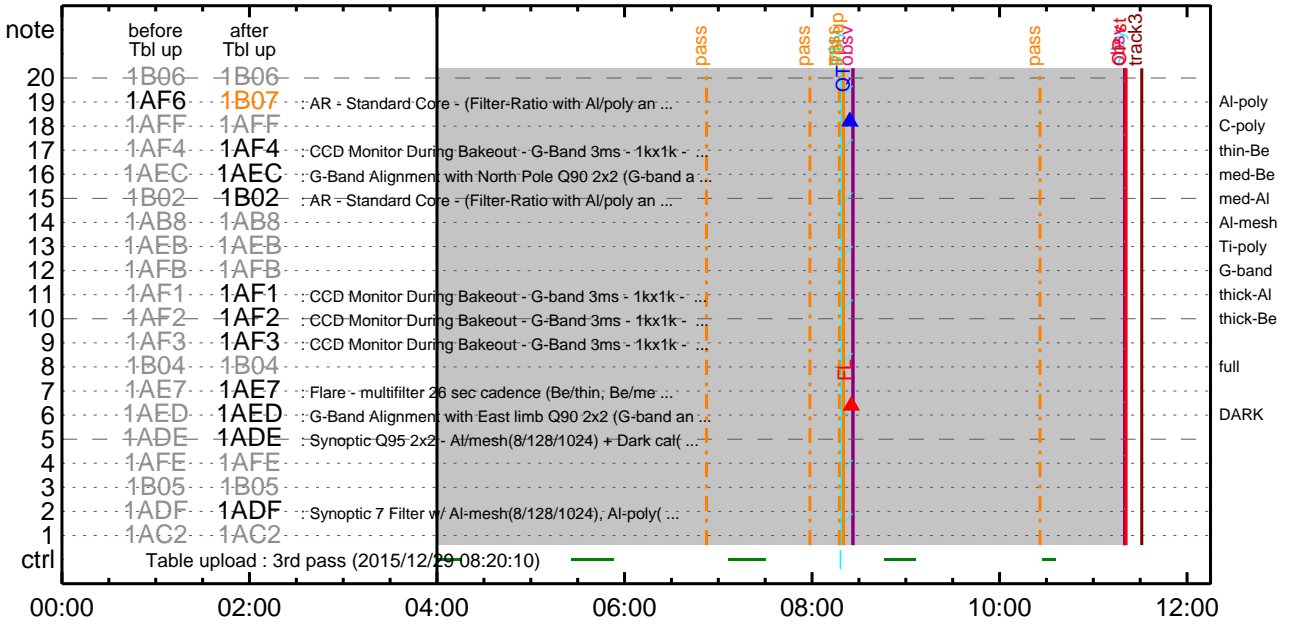
Flare Detection

* * * * *

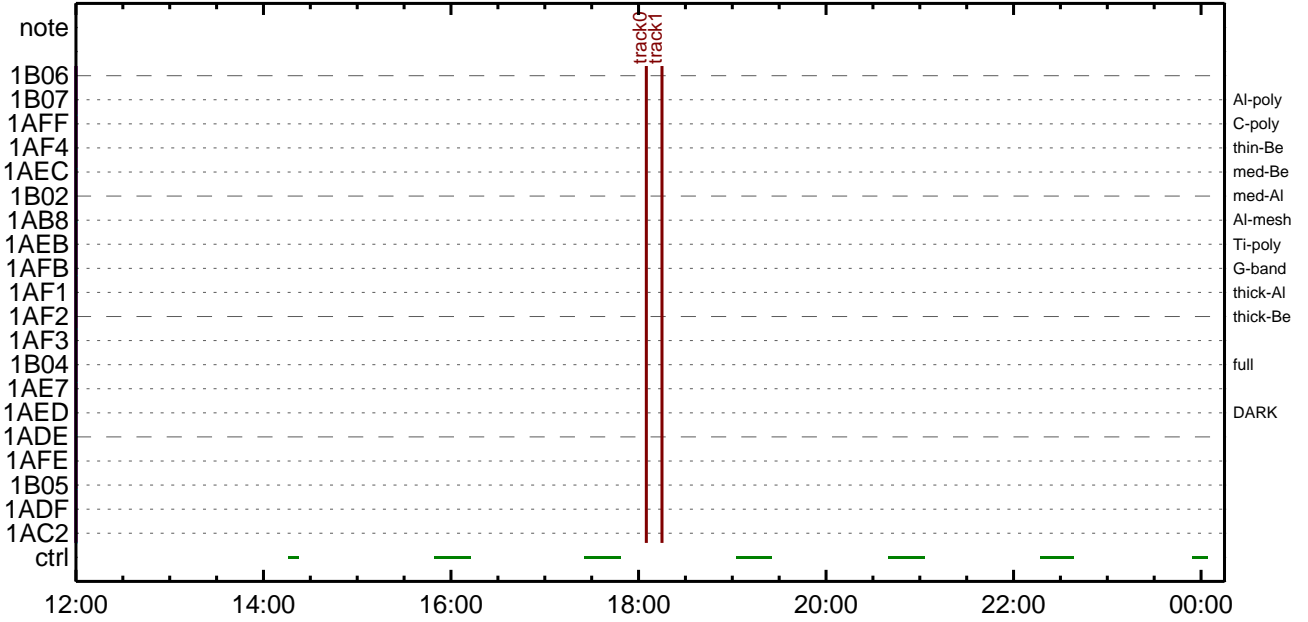
FLD Patrol

Term	Pointing (x, y)	Comment
12/30 12:40:18 - 12/30 17:41:18	Track (565.2, -310.9) @ 12/30 12:40:00	AR12473 tracking
12/30 17:47:18 - 12/31 05:55:18	Track (599.4, -311.9) @ 12/30 17:47:00	AR12473 tracking
12/31 06:05:18 - 12/31 18:00:18	Track (675.6, -314.8) @ 12/31 06:05:00	AR12473 tracking
12/31 18:10:18 - 01/01 06:11:48	Track (741.5, -318.1) @ 12/31 18:10:00	AR12473 tracking
01/01 06:21:48 - 01/01 17:59:48	Track (798.0, -322.1) @ 01/01 06:21:30	AR12473 tracking
01/01 18:09:48 - 01/02 05:43:48	Track (842.5, -326.3) @ 01/01 18:09:30	AR12473 tracking
01/02 05:53:48 - 01/02 17:46:18	Track (876.2, -330.9) @ 01/02 05:53:30	AR12473 tracking
01/02 17:56:18 - 01/03 06:00:18	Fixed (883.9, -332.3)	AR12473 tracking
01/03 06:10:18 - 01/03 18:02:18	Fixed (883.9, -332.3)	AR12473 tracking
01/03 18:12:18 - 01/04 06:20:18	Fixed (883.9, -332.3)	AR12473 tracking
01/04 06:30:18 - 01/04 18:06:18	Fixed (883.9, -332.3)	AR12473 tracking
01/04 18:16:18 - 01/05 01:52:48	Fixed (883.9, -332.3)	AR12473 tracking
Open/TI-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec

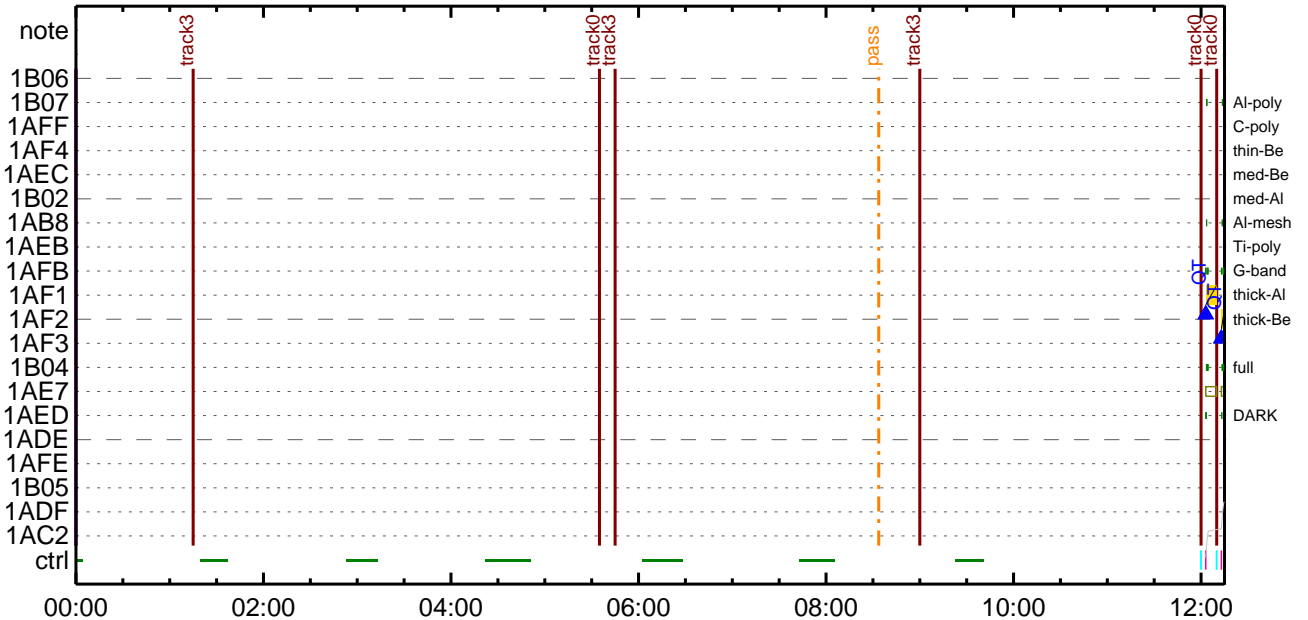
CMDI #0619 2015/12/29



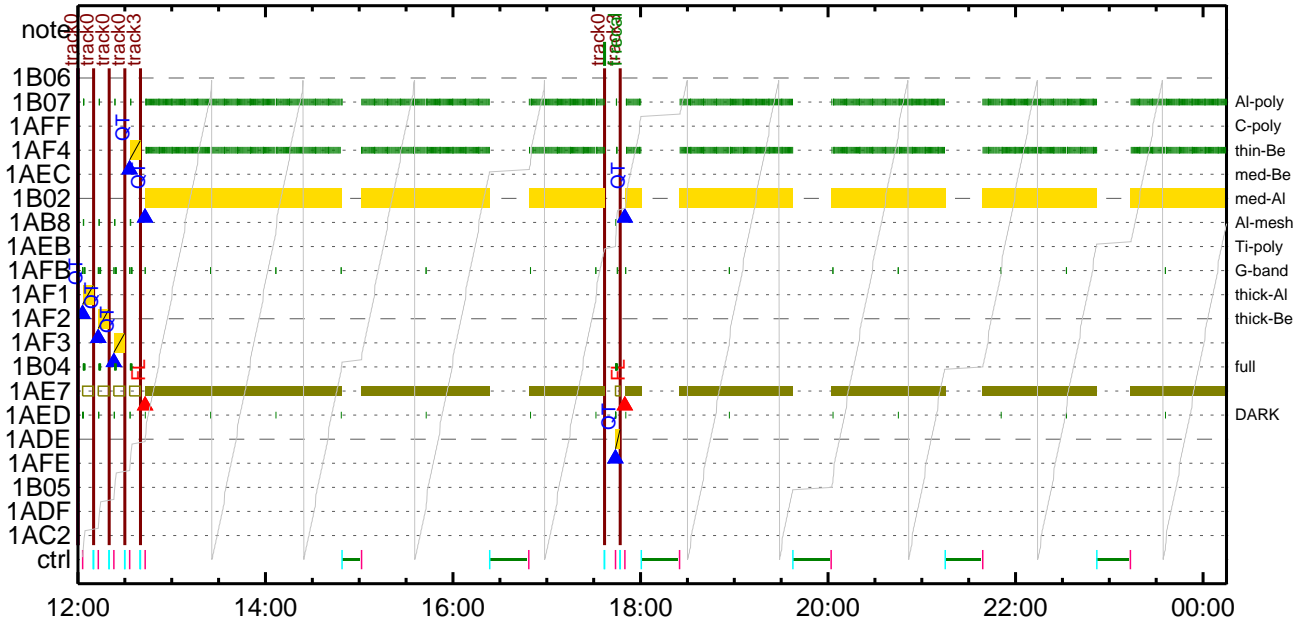
CMDI #0619 2015/12/29



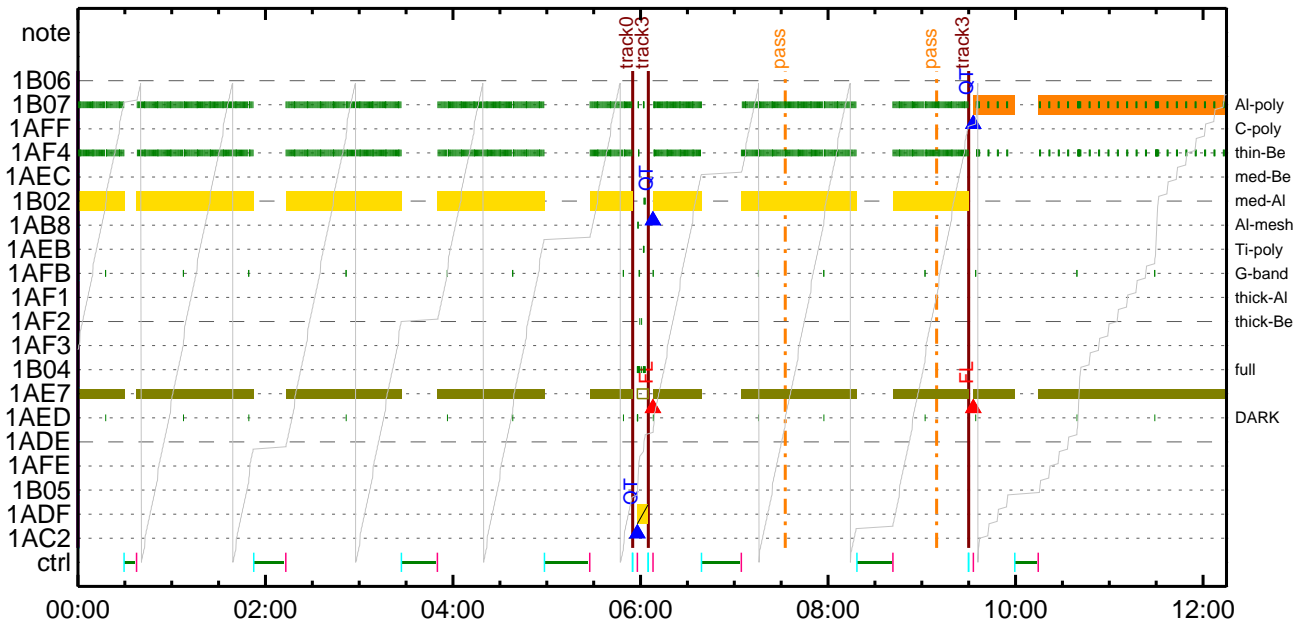
CMDI #0619 2015/12/30



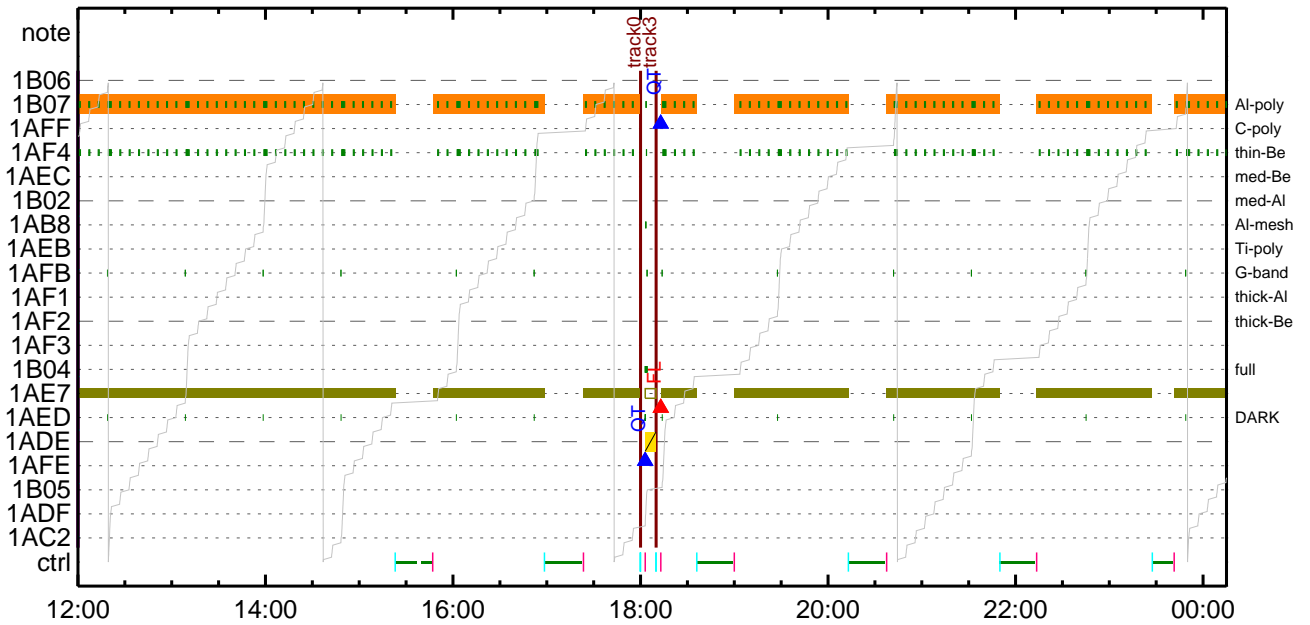
CMDI #0619 2015/12/30



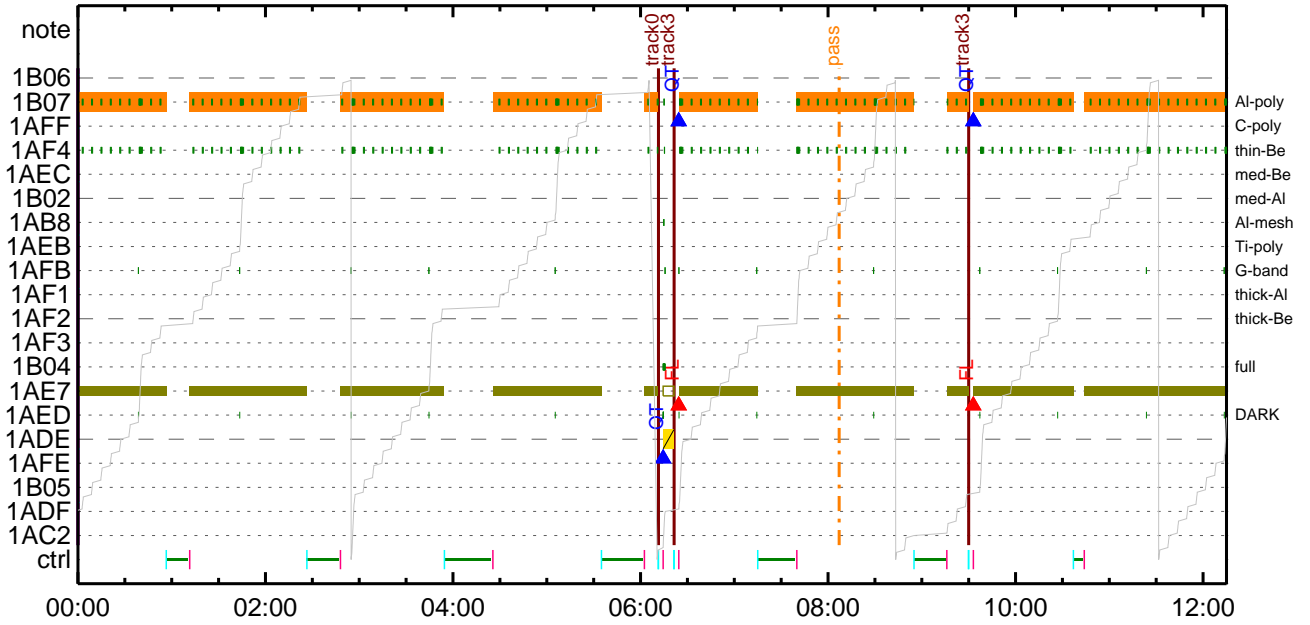
CMDI #0619 2015/12/31



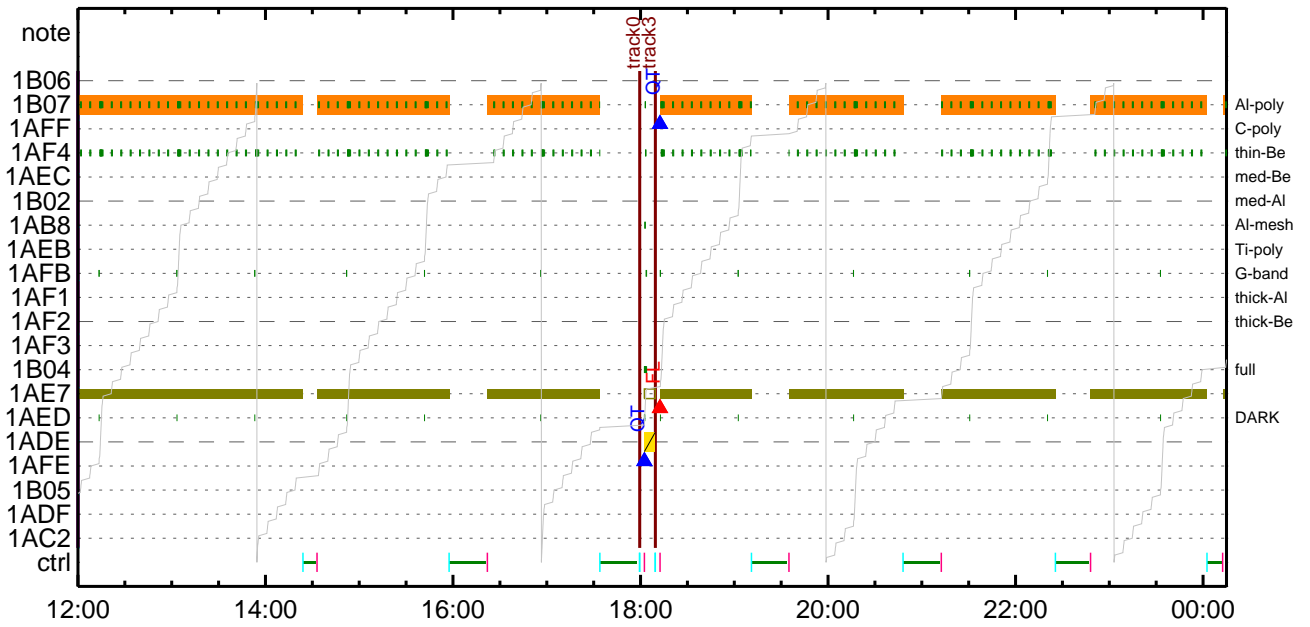
CMDI #0619 2015/12/31



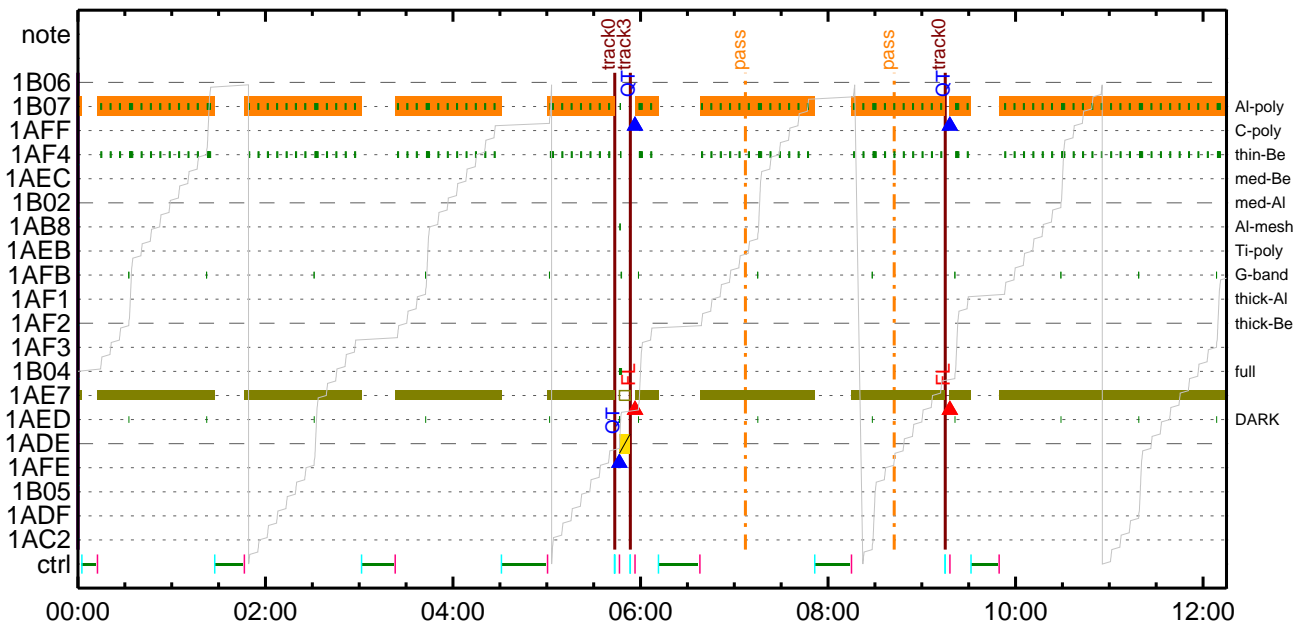
CMDI #0619 2016/01/01



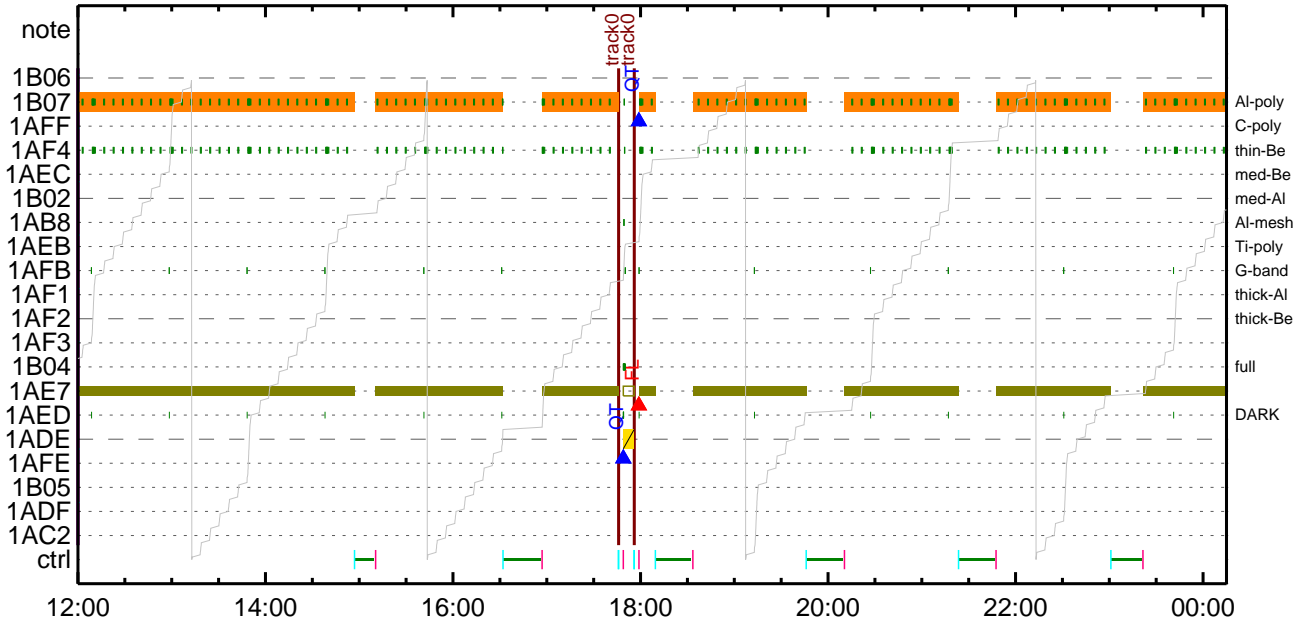
CMDI #0619 2016/01/01



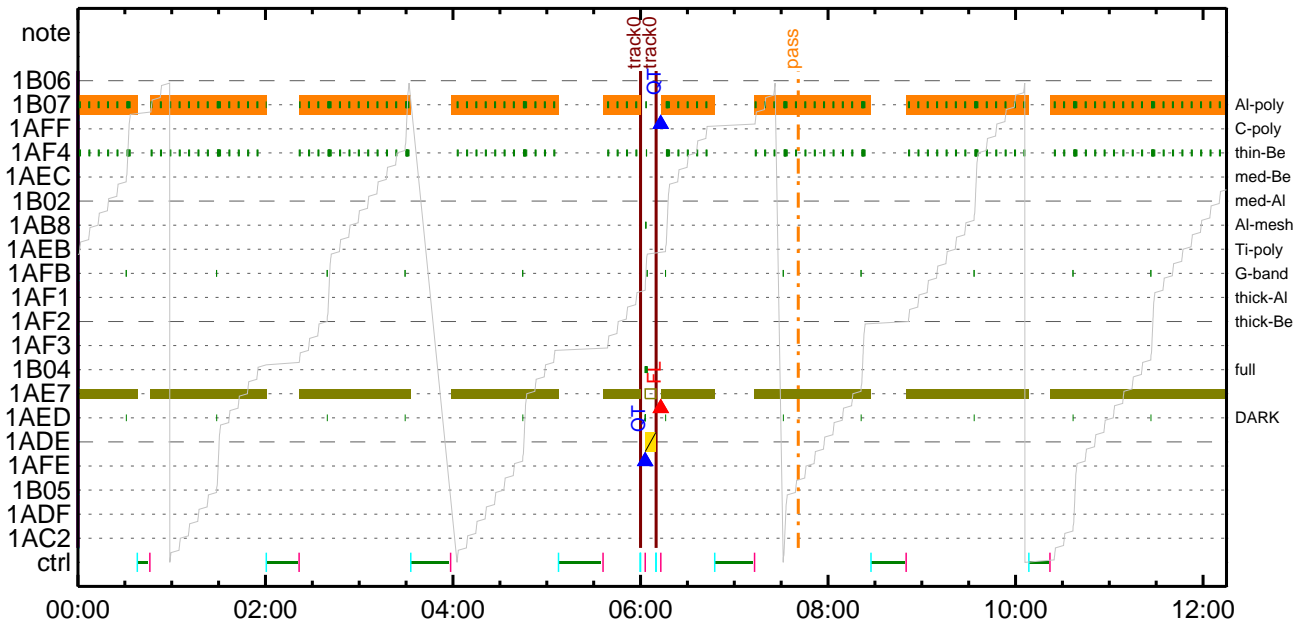
CMDI #0619 2016/01/02



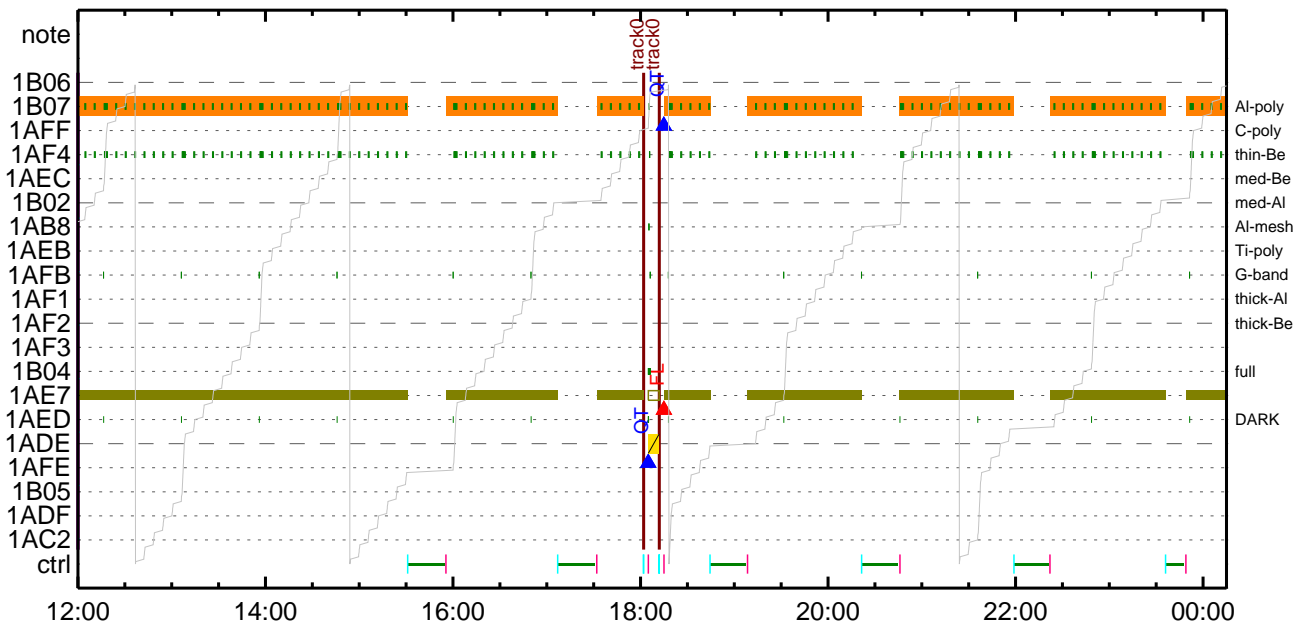
CMDI #0619 2016/01/02



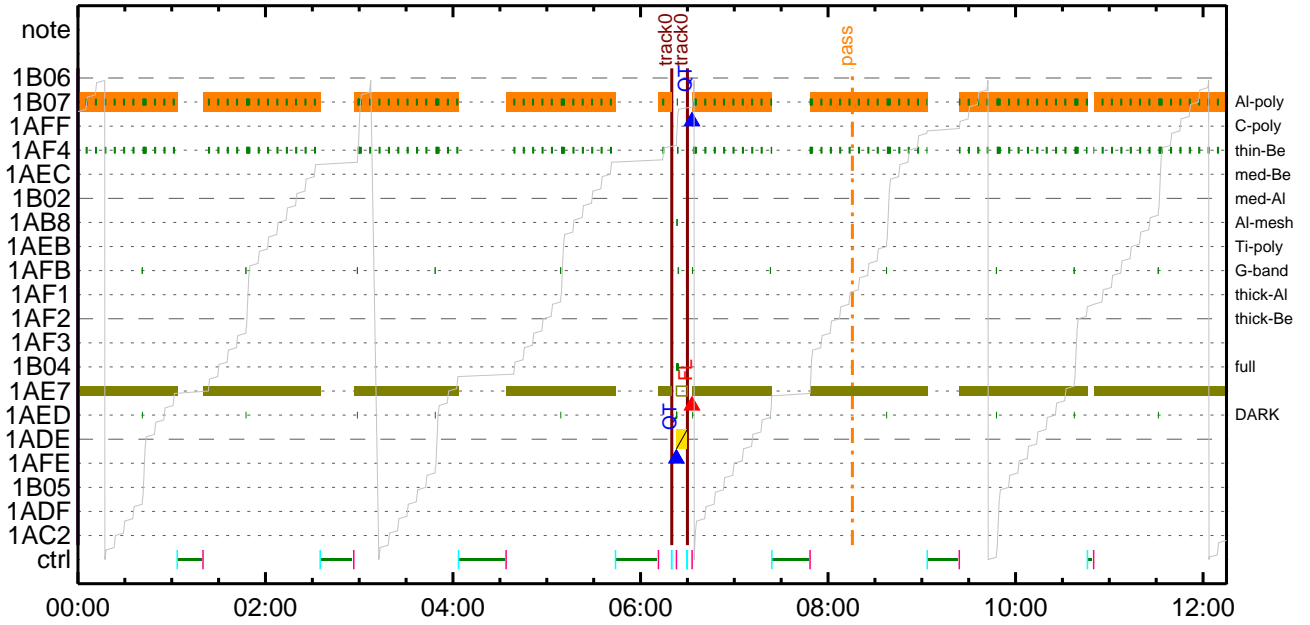
CMDI #0619 2016/01/03



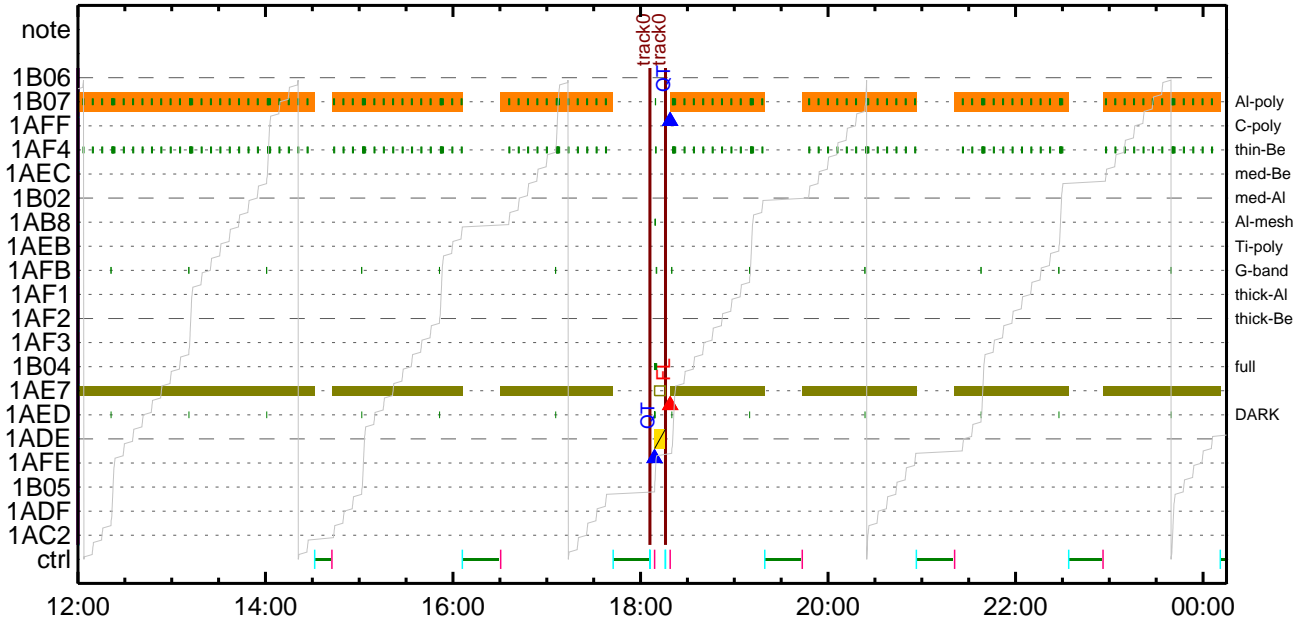
CMDI #0619 2016/01/03



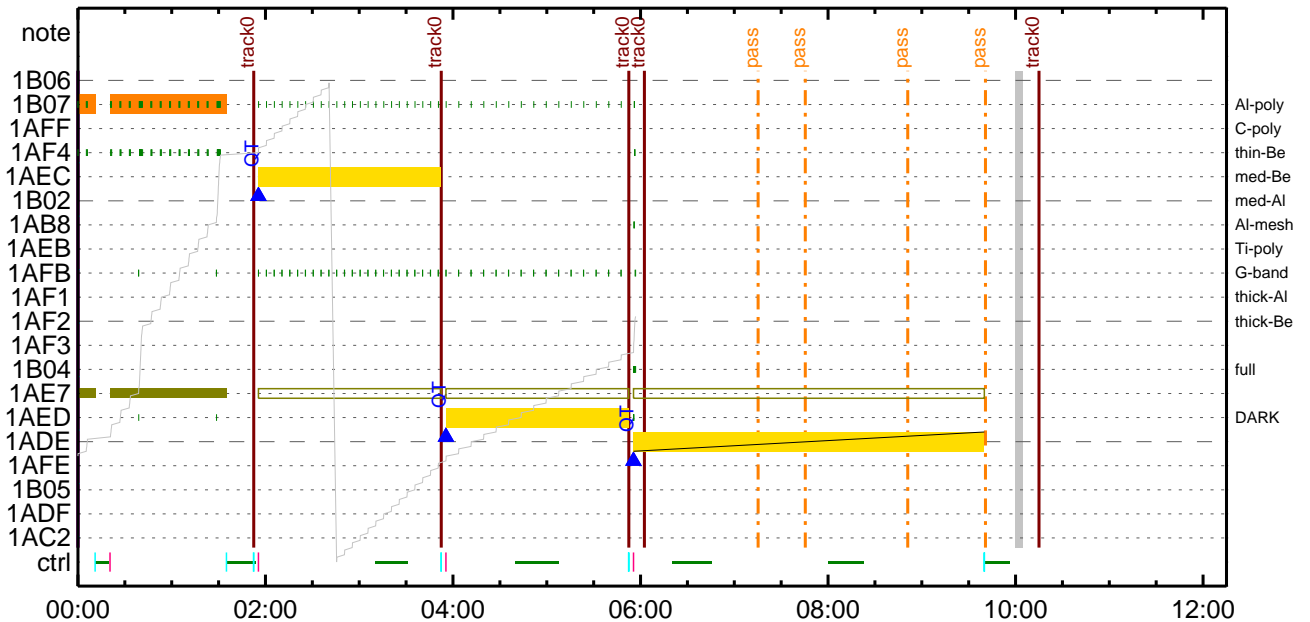
CMDI #0619 2016/01/04



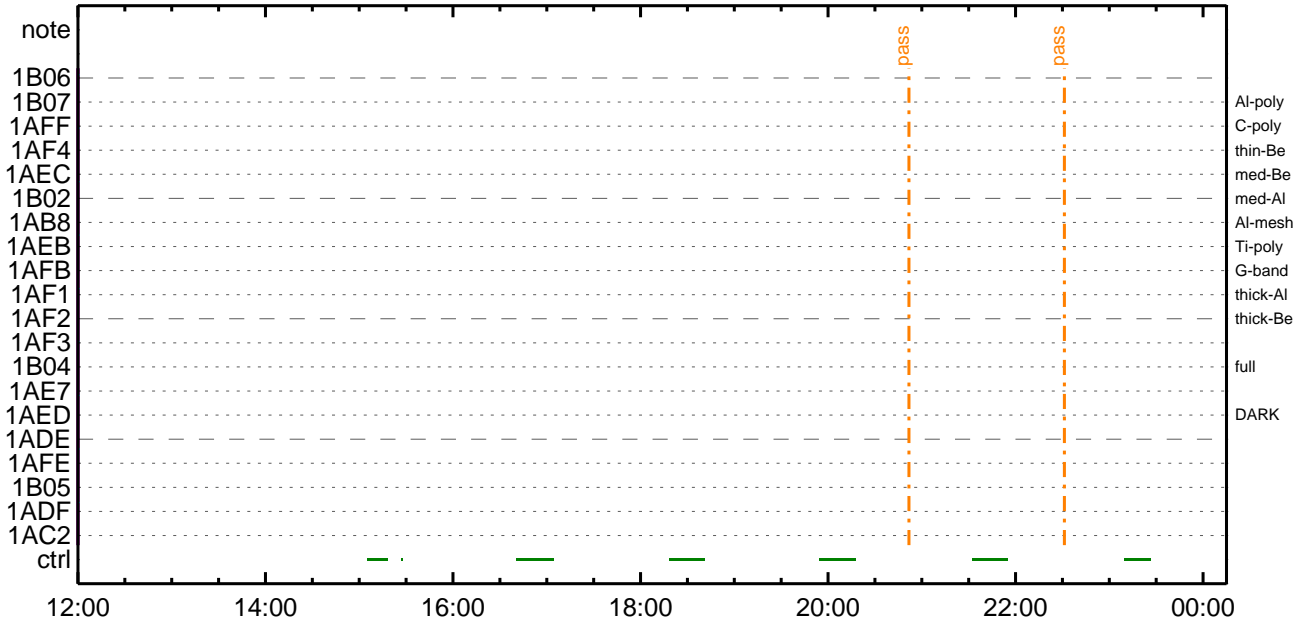
CMDI #0619 2016/01/04



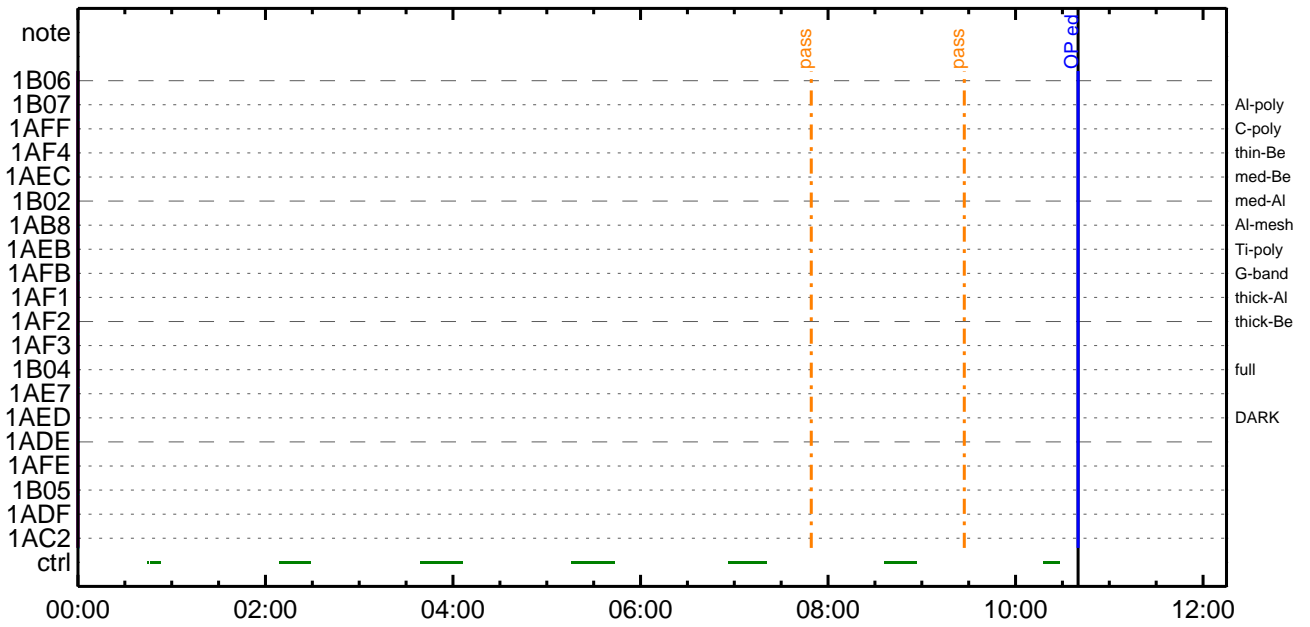
CMDI #0619 2016/01/05



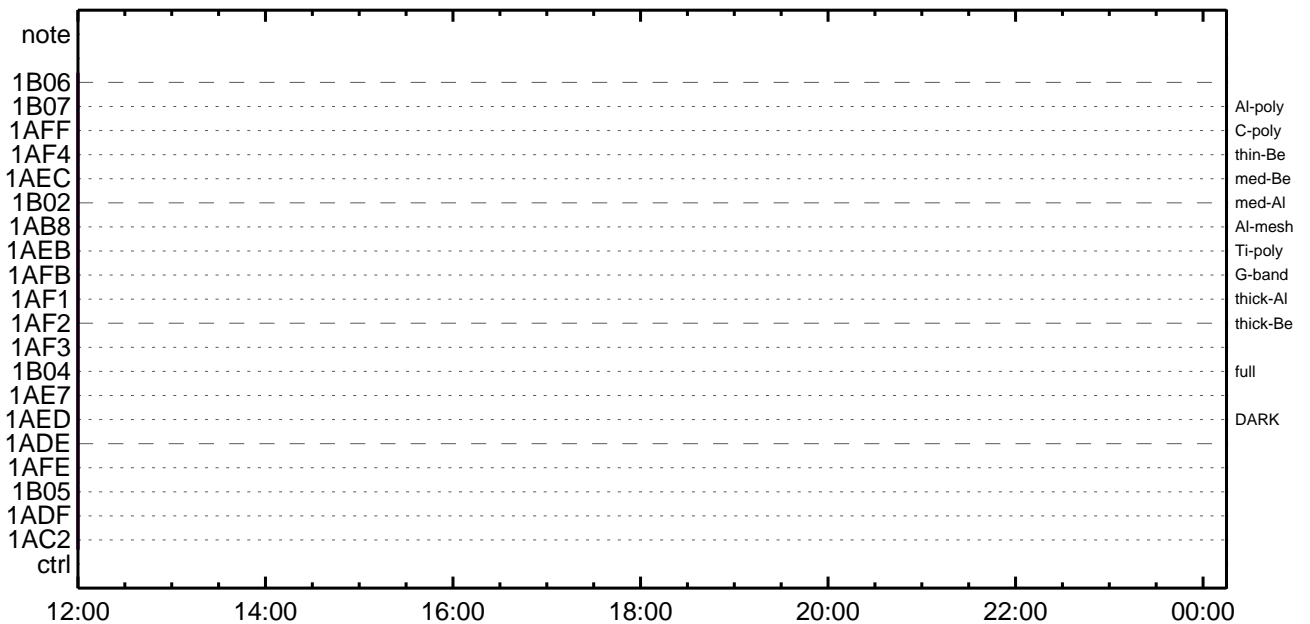
CMDI #0619 2016/01/05



CMDI #0619 2016/01/06



CMDI #0619 2016/01/06




```

0096 C.                0303;çSET0EDUMP0IÆ±°iYÑY¹aÇ¹Ôa|a³aE;f
0097 C.
0098 . C. TIY³¥P¥Ö¥E0dADİđ(UT)
0099 +. TI 2015-12-29 11:16:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]            EQ      1COUNTUP
0102 C.
0103 +. TI 2015-12-29 11:16:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]            EQ      1COUNTUP
0106 C.
0107 +. TI 2015-12-29 11:16:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]            EQ      1COUNTUP
0110 C.
0111 +. TI 2015-12-29 11:20:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]            EQ      1COUNTUP
0114 C.
0115 C.                0Ê²¼0IÄè%îÍÑ0I¥Á¥§¥Á¥-¹àìÛ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]        EQ      ENA
0117 C.                çç[HK1_TI_CMD_NUM]          EQ      4
0118 C.                çç[HK1_NEXT_EXEC_PIM]        EQ      DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]         EQ      0xB3
0120 C.
0121 . C. *****
0122 C. TIÎî°è¥À¥Ö¥×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]        EQ      07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]        EQ      2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]         EQ      3
0131 C.                çç[HK1_DMP_REPEAT_NUM]        EQ      0
0132 C.                çç[HK1_DMA_DMP_PIM]          EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]          EQ      7
0136 C.                çç[HK1_PKT_GEN_TIME]          EQ      0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]        EQ      32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]        EQ      4M
0139 C.                çç[HK1_DMP_CHK_FLG]          EQ      EXEC
0140 C.
0141 . C. ¥À¥Ö¥×¼ªª î»0d³îÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]          EQ      NON
0143 C.
0144 . C. RAM ID=TI_TBL0IÊ¹ç•è²ÏOK0d³îÇ§
0145 C.
0146 . C. DHU¥â;¼¥É;Ê¼¥¼,¥ì;¼¥È;Ë0dîá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]          EQ      2
0150 C.                çç[HK1_PKT_GEN_TIME]          EQ      0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]        EQ      32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]        EQ      4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2015-12-29 11:20:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2015-12-29 11:20:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2015-12-29 11:20:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 . C.                [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2015-12-29 11:20:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ´ûÃî0î»ö¼¥0ËÄ0d¹0èDCBC•×²è *****
0192 C. (%â°î¥Ö¥À¥Ë¥P¥Ë¥¥¥¥ç¥è0E¼¼0¼¼A»Û0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 6793268.2 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 1m29s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2015-12-29 11:20:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÂÎÎ»ö¼ÝðËÄð¹¹ëDCBC•x²è *****
0165 C. (%á°îÿÓÿÄÿËÿPÿËÿÄÿ¼ÿËÿèèè¼ð¼Ä»Û¹èè)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ½D½¹•Ï Daily±¿ÏÑèÉ'Ø¹¹ëDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSŸÁÿSŸÄÿ¹¼Ä»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-580 2015-12-29 13:49:38 174 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿αÁσ•μ°E»Í×ÁÇσÍYçYÁY×Yí;¼YÉ;ÈÈ%μ•íÉ;ÈσE¼°ÇÔσ•α¿¼í¹çαÍ;çÀ®, ùα¹αÈσBαÇÁ+¿®σ•σÉσσσ³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-269:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-288:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 . C. < Upload DPL table >
0054 C.
0055 C. YçYÁY×Yí;¼YÉσÍÁ°σÉSTS_CHKσδOFFσÈσ¹σÈ
0056 C.
0057 . S. RAM ram-271:MDP_DPL
0058 ( )
0059 C.
0060 . C. < Dump RAMID=MDP_DPL >
0061 +. DC 07-F0 MDP_DUMP_FGTBL
0062 BC (82 07 00 38 b8 00 40)
0063 C. -----
0064 C. MDP_DPL verify = OK [ ]
0065 C. -----
0066 C.
0067 C. STS_CHKσδONσÈσ¹σÈ
0068 C.
0069 . C. < Update MDP DSC PAR1 >
0070 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0071 BC (4c)
0072 C. MDP_CMD_CODE = F04C0700 [ ]
0073 C. MDP_CMD_CNT (count-up 1) [ ]
0074 C. -----
0075 C.
0076 . C.
0077 C. *****
0078 C. SOT TI command set
0079 C. *****
0080 C. Execute, after the success of TBL upload.
0081 +. TI 2015-12-29 11:20:18.0
0082 DC 07-F0 MDP_SOT_MODE_OBSV
0083 BC (40)
0084 C. -----
0085 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0086 C. -----
0087 C.
0088 C.
0089 C. ***** XRT START *****
0090 C.
0091 +. DC 07-F0 MDP_XRT_CTRL_MANU
0092 BC (c1)
0093 +. DC 07-F0 MDP_XRT_MODE_STBY
0094 BC (c3)
0095 . C. ----- Success Verify ? OK / NG_____
```

```

0096 C.
0097 C. XRT Obs. Table Upload
0098 . S. RAM ram-291:MDP_OBS_X
0099 ( )
0100 C.
0101 +. DC 07-F0 MDP_DUMP_XRTTBL
0102 BC (84 07 00 00 00 3a d4)
0103 . C. ----- Comparison Check ? OK / ERR ____
0104 C.
0105 C.
0106 +. DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 01 b1 b1 04 04)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 02 b1 b1 08 08)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 03 b1 b1 08 08)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 04 b1 b1 06 06)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 06 85 83 06 06)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 07 c0 c0 10 10)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 08 80 80 20 20)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 09 40 c0 10 10)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 0a 40 40 10 10)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 0b c0 40 10 10)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 0c 80 80 20 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 0d 80 80 08 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 0e 80 60 20 18)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0f 80 80 06 06)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 10 80 80 08 08)
0136 + DC 07-F0 MDP_XRT_FLD_ENA
0137 BC (d8)
0138 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0139 BC (c8)
0140 + DC 07-F0 MDP_XRT_ARS_DIS
0141 BC (d5)
0142 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0143 BC (c4 13)
0144 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0145 BC (c5 07)
0146 . C. ----- Success Verify ? OK / NG ____
0147 C.
0148 C.
0149 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0150 C.
0151 +. DC 07-F0 MDP_XRT_MODE_OBSV
0152 BC (c2)
0153 +. TI 2015-12-29 11:20:02.0
0154 DC 07-F0 MDP_XRT_MODE_OBSV
0155 BC (c2)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C. ***** XRT END *****
0159 C.
0160 . C. ***** MDP `uAiuI»o%YœEÂDø¹œèDCBC•x²è *****
0161 C. (%â°iYÓYÄYÈYËYÏYáYçYëœE%¼œ¼Ä»Ûœ¹œè)
0162 . S. DC-BC dcbc-402:DCBC
0163 (MDP_known_event)
0164 C.
0165 C.
0166 . C. ***** YDY¹•Ï Daily±;jÎÑœE´Øœ¹œèDCBC•x²è *****
0167 . S. DC-BC dcbc-153:DCBC
0168 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0169 C.
0170 C.
0171 . C. ;ãLOSŸÁYŠYÄY¬¼Ä»Û;ã
0172 C.
0173 . C. ***** LOS *****
0174 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-581 2015-12-29 13:49:38 29 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOSYÁY$YÁY^-¼Á»Û;ã  
0005 C.  
0006 C. YÀYB;¼Y³YF¥óYÉÁ+¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****  
0010 C. Áí;È¿áÄ±µ°Æ»Í×ÁÇúÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈÈÈ¼°ÇÔ±±¿¼i¹ç±Í;çÀ®, ù±±è±±çÁ+¿®±±È±±±±±È;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 C.  
0015 . C. ***** MDP `úÁí±í»ò¼Y±ÈÄ±±±±±DCBC•x²è *****  
0016 C. (¼á°íYóYÁYÈY±YÈYáYçYè±È¼±±¼Á»Û±±è)  
0017 . S. DC-BC dcbc-402:DCBC  
0018 (MDP_known_event)  
0019 C.  
0020 C.  
0021 . C. ***** YD¥¹•İ Daily±¿íÑ±È´Ø±±±±±±DCBC•x²è *****  
0022 . S. DC-BC dcbc-153:DCBC  
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0024 C.  
0025 C.  
0026 . C. ;ãLOSÁY$YÁY^-¼Á»Û;ã  
0027 C.  
0028 . C. ***** LOS *****  
0029 C.
```

*** OP Sequence for XRT ***

```

2015/12/29 11:31:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 03 00 00 00 00
2015/12/29 18:05:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2015/12/29 18:15:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 01 00 00 00 00
2015/12/30 01:15:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 03 00 00 00 00
2015/12/30 05:35:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2015/12/30 05:45:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 03 00 00 00 00
2015/12/30 05:52:45.5 XRT_TCIB_XRT_S_HTR_A_DIS_417_OG [0x1a1]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2015/12/30 09:00:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 03 00 00 00 00
2015/12/30 11:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/12/30 11:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/12/30 11:59:58.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/12/30 12:00:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2015/12/30 12:00:18.0 XRT_FLD_DIS_422_OG [0x1a6]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2015/12/30 12:00:20.0 XRT_FLRCTRL_DIS_428_OG [0x1ac]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2015/12/30 12:02:56.0 XRT_ARS_DIS_435_OG [0x1b3]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2015/12/30 12:02:58.0 XRT_QT_PROG_SET_404_OG [0x194]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 0b
2015/12/30 12:03:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2015/12/30 12:09:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/12/30 12:09:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/12/30 12:09:58.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/12/30 12:10:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2015/12/30 12:10:18.0 XRT_FLD_DIS_422_OG [0x1a6]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2015/12/30 12:10:20.0 XRT_FLRCTRL_DIS_428_OG [0x1ac]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2015/12/30 12:12:56.0 XRT_ARS_DIS_435_OG [0x1b3]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2015/12/30 12:12:58.0 XRT_QT_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 0a
2015/12/30 12:13:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2015/12/30 12:19:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/12/30 12:19:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/12/30 12:19:58.0 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/12/30 12:20:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2015/12/30 12:20:18.0 XRT_FLD_DIS_422_OG [0x1a6]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2015/12/30 12:20:20.0 XRT_FLRCTRL_DIS_428_OG [0x1ac]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2015/12/30 12:22:56.0 XRT_ARS_DIS_435_OG [0x1b3]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2015/12/30 12:22:58.0 XRT_QT_PROG_SET_429_OG [0x1ad]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 09
2015/12/30 12:23:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2015/12/30 12:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/12/30 12:29:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2015/12/30 12:29:58.0 XRT_ROI_A_440_OG [0x1b8]
                        MDP_XRT_ROI_SET          6 07-F0 cd 06 85 83 06 06
                        MDP_XRT_ROI_SET          6 07-F0 cd 07 a0 80 18 20
                        MDP_XRT_ROI_SET          6 07-F0 cd 08 80 80 20 20
                        MDP_XRT_ROI_SET          6 07-F0 cd 0b c0 40 10 10
                        MDP_XRT_ROI_SET          6 07-F0 cd 0c 80 80 20 08
                        MDP_XRT_ROI_SET          6 07-F0 cd 0d 80 80 08 20
                        MDP_XRT_ROI_SET          6 07-F0 cd 0e 80 60 20 18
                        MDP_XRT_ROI_SET          6 07-F0 cd 0f 80 80 06 06
2015/12/30 12:29:58.5 XRT_ROI_B_441_OG [0x1b9]
                        MDP_XRT_ROI_SET          6 07-F0 cd 0f 80 80 06 06
                        MDP_XRT_ROI_SET          6 07-F0 cd 10 80 80 08 08
2015/12/30 12:30:00.0 AOCs_OrE-point_Start_7_OG [0x09d]
                        AOCU_NM                    5 02-76 00 d1 07 2e f9
2015/12/30 12:30:03.5 XRT_FOCUS_POSITION_420_OG [0x1a4]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2015/12/30 12:30:23.5 XRT_FLD_DIS_422_OG [0x1a6]

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2015/12/30	12:30:25.5	XRT_FLRCTRL_DIS_428_OG [0x1ac]	MDP_XRT_FLD_DIS	1	07-F0	d9	
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/12/30	12:33:01.5	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/12/30	12:33:03.5	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2015/12/30	12:33:05.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/30	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/30	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/30	12:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/12/30	12:40:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00 00 00 00
2015/12/30	12:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/12/30	12:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/12/30	12:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/12/30	12:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/12/30	12:40:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/30	12:42:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2015/12/30	12:42:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/12/30	12:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/30	14:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/30	14:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/30	14:49:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/30	14:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/30	15:00:30.0	XRT_Custom_430_OG [0x1ae]					
2015/12/30	15:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/30	16:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/30	16:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/12/30	16:23:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/12/30	16:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/12/30	16:47:30.0	XRT_Custom_430_OG [0x1ae]					
2015/12/30	16:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/30	17:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/30	17:36:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/30	17:36:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78	00
2015/12/30	17:37:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2015/12/30	17:40:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2015/12/30	17:41:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/12/30	17:43:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/12/30	17:43:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/12/30	17:43:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2015/12/30	17:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/12/30	17:46:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/30	17:46:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/12/30	17:46:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/12/30	17:47:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00 00 00 00
2015/12/30	17:47:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/12/30	17:47:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/12/30	17:47:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/12/30	17:47:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/12/30	17:47:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2015/12/30	17:49:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/12/30	17:49:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/30	17:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/30	18:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/30	18:00:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/30	18:00:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/30	18:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/30	18:24:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/30	18:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/30	19:37:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/30	19:37:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/30	19:37:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/30	19:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/30	20:01:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/30	20:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/30	21:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/30	21:15:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/30	21:15:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/30	21:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/30	21:38:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/30	21:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/30	22:52:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/30	22:52:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/30	22:52:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/30	22:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/30	23:12:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/30	23:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/31	00:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	00:29:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/31	00:29:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/31	00:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/31	00:36:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/31	00:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/31	01:52:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	01:52:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/31	01:52:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/31	01:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/31	02:12:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/31	02:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/31	03:27:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	03:27:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/31	03:27:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/31	03:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/31	03:49:00.0	XRT_Custom_430_OG [0x1ae]			
2015/12/31	03:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/31	04:58:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	04:58:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/31	04:58:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/31	05:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/31	05:26:30.0	XRT_Custom_430_OG [0x1ae]			
2015/12/31	05:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			

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2015/12/31	05:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	05:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	05:54:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/12/31	05:55:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2015/12/31	05:55:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2015/12/31	05:57:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/12/31	05:57:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/31	05:57:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2015/12/31	05:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/31	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	06:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	06:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/31	06:05:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2015/12/31	06:05:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/31	06:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/31	06:05:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/31	06:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/31	06:05:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/31	06:07:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2015/12/31	06:07:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/31	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/31	06:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	06:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/31	06:39:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/31	06:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/31	07:03:30.0	XRT_Custom_430_OG [0x1ae]				
2015/12/31	07:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/31	08:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	08:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/31	08:18:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/12/31	08:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/12/31	08:40:30.0	XRT_Custom_430_OG [0x1ae]				
2015/12/31	08:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/31	09:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	09:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	09:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/12/31	09:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00
2015/12/31	09:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/12/31	09:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/12/31	09:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/12/31	09:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/12/31	09:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/12/31	09:32:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2015/12/31	09:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2015/12/31	09:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/12/31	09:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/12/31	09:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da

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2015/12/31	09:59:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/31	10:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/31	10:13:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/31	10:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/31	15:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	15:23:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/31	15:23:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/31	15:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/31	15:46:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/31	15:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/31	16:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	16:58:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/31	16:58:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/31	17:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/31	17:22:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/31	17:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/31	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/12/31	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/12/31	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/12/31	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/12/31	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/31	18:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/12/31	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/31	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/12/31	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2015/12/31	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/12/31	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/12/31	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/12/31	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/12/31	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/31	18:12:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2015/12/31	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/12/31	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/31	18:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	18:36:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/31	18:36:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/31	18:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/31	18:59:00.0	XRT_Custom_430_OG [0x1ae]							
2015/12/31	19:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/31	20:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	20:13:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/31	20:13:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/31	20:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/31	20:36:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/31	20:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2015/12/31	21:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	21:50:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/31	21:50:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/31	21:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/31	22:12:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/31	22:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/12/31	23:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/12/31	23:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/12/31	23:27:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/12/31	23:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/12/31	23:40:30.0	XRT_Custom_430_OG [0x1ae]							
2015/12/31	23:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/01	00:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/01	00:56:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/01	00:56:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/01	00:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/01	01:10:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/01	01:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/01	02:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/01	02:26:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/01	02:26:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/01	02:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/01	02:47:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/01	02:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/01	03:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/01	03:54:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/01	03:54:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/01	03:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/01	04:24:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/01	04:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/01	05:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/01	05:35:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/01	05:35:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/01	05:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/01	06:01:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/01	06:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/01	06:11:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/01	06:11:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/01	06:11:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/01	06:11:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/01	06:11:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/01	06:14:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/01	06:14:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/01	06:14:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/01	06:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/01	06:21:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/01	06:21:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/01	06:21:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/01	06:21:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/01/01	06:21:48.0	XRT_FLD_ENA_411_OG [0x19b]							

2016/01/01	06:21:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/01	06:21:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/01	06:21:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/01	06:21:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/01	06:24:26.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
2016/01/01	06:24:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/01/01	06:24:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/01	07:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/01	07:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/01	07:15:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/01	07:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/01	07:39:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/01	07:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/01	08:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/01	08:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/01	08:55:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/01	08:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/01	09:15:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/01	09:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/01	09:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/01	09:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/01	09:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/01/01	09:30:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	00 00 00 00
2016/01/01	09:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/01	09:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/01	09:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/01	09:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/01	09:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/01	09:32:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
2016/01/01	09:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/01/01	09:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/01	10:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/01	10:37:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/01	10:37:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/01	10:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/01	10:43:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/01	10:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/01	14:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/01	14:24:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/01	14:24:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/01	14:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/01	14:32:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/01	14:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/01	15:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/01	15:57:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/01	15:57:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/01	16:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/01	16:21:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/01	16:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					

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2016/01/01	17:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/01	17:34:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/01	17:34:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/01	17:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/01	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/01	17:59:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/01	17:59:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/01/01	17:59:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2016/01/01	17:59:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/01/01	18:02:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/01/01	18:02:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/01	18:02:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2016/01/01	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/01	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/01	18:09:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/01	18:09:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/01/01	18:09:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00		
2016/01/01	18:09:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/01/01	18:09:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/01/01	18:09:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/01/01	18:09:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/01	18:09:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/01	18:12:26.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2016/01/01	18:12:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/01/01	18:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/01	19:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/01	19:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/01	19:11:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/01	19:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/01	19:34:00.0	XRT_Custom_430_OG [0x1ae]						
2016/01/01	19:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/01	20:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/01	20:48:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/01	20:48:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/01	20:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/01	21:11:30.0	XRT_Custom_430_OG [0x1ae]						
2016/01/01	21:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/01	22:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/01	22:25:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/01	22:25:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/01	22:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/01	22:47:00.0	XRT_Custom_430_OG [0x1ae]						
2016/01/01	22:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/02	00:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/02	00:02:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/02	00:02:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/02	00:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/02	00:11:30.0	XRT_Custom_430_OG [0x1ae]						

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2016/01/02	00:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/02	01:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/02	01:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/02	01:27:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/02	01:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/02	01:45:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/02	01:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/02	03:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/02	03:01:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/02	03:01:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/02	03:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/02	03:22:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/02	03:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/02	04:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/02	04:31:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/02	04:31:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/02	04:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/02	04:59:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/02	05:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/02	05:43:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/02	05:43:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/02	05:43:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/02	05:43:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/02	05:43:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/02	05:46:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/02	05:46:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/02	05:46:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/02	05:46:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/02	05:53:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/02	05:53:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/02	05:53:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/02	05:53:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/01/02	05:53:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/02	05:53:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/02	05:53:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/02	05:53:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/02	05:53:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/02	05:56:26.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2016/01/02	05:56:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/01/02	05:56:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/02	06:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/02	06:11:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/02	06:11:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/02	06:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/02	06:37:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/02	06:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/02	07:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/02	07:51:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/02	07:51:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							

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2016/01/02	07:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/02	08:14:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/02	08:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/02	09:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/02	09:14:56.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/02	09:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/01/02	09:15:00.0	AOCS_OrE-point_Start_8_OG [0x09e]							
			AOCU_NM	5	02-76	00 1d 89 b1 71			
2016/01/02	09:15:18.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/01/02	09:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/01/02	09:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/01/02	09:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/01/02	09:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/02	09:17:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2016/01/02	09:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/01/02	09:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/02	09:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/02	09:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/02	09:31:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/02	09:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/02	09:48:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/02	09:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/02	14:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/02	14:57:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/02	14:57:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/02	15:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/02	15:09:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/02	15:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/02	16:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/02	16:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/02	16:32:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/02	16:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/02	16:56:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/02	16:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/02	17:45:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/02	17:45:56.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/02	17:45:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/01/02	17:46:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
			AOCU_NM	5	02-76	00 00 00 00 00			
2016/01/02	17:46:18.0	XRT_FLD_DIS_406_OG [0x196]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/01/02	17:48:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/01/02	17:48:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/01/02	17:48:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2016/01/02	17:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/02	17:55:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/02	17:55:56.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/02	17:55:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/01/02	17:56:00.0	AOCS_OrE-point_Start_8_OG [0x09e]							
			AOCU_NM	5	02-76	00 1d 89 b1 71			
2016/01/02	17:56:18.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/01/02	17:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			

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2016/01/02	17:56:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2016/01/02	17:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2016/01/02	17:56:26.0	XRT_FLD_RESET_433_OG [0x1b1]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/01/02	17:58:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 13
2016/01/02	17:58:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 07
2016/01/02	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/01/02	18:09:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/01/02	18:09:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/01/02	18:09:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/01/02	18:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/01/02	18:32:30.0	XRT_Custom_430_OG [0x1ae]		
2016/01/02	18:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/01/02	19:46:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/01/02	19:46:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/01/02	19:46:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/01/02	19:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/01/02	20:09:30.0	XRT_Custom_430_OG [0x1ae]		
2016/01/02	20:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/01/02	21:23:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/01/02	21:23:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/01/02	21:23:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/01/02	21:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/01/02	21:46:30.0	XRT_Custom_430_OG [0x1ae]		
2016/01/02	21:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/01/02	23:01:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/01/02	23:01:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/01/02	23:01:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/01/02	23:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/01/02	23:20:30.0	XRT_Custom_430_OG [0x1ae]		
2016/01/02	23:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/01/03	00:38:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/01/03	00:38:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/01/03	00:38:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/01/03	00:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/01/03	00:45:00.0	XRT_Custom_430_OG [0x1ae]		
2016/01/03	00:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/01/03	02:00:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/01/03	02:00:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/01/03	02:00:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/01/03	02:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/01/03	02:20:30.0	XRT_Custom_430_OG [0x1ae]		
2016/01/03	02:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/01/03	03:33:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/01/03	03:33:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/01/03	03:33:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/01/03	03:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/01/03	03:57:30.0	XRT_Custom_430_OG [0x1ae]		
2016/01/03	03:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2016/01/03	05:07:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/01/03	05:07:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da

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2016/01/03	05:07:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/03	05:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/03	05:35:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/03	05:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/03	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/03	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/03	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/03	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/03	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/03	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/03	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/03	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/03	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/03	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/03	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/03	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/03	06:10:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 1d 89 b1 71				
2016/01/03	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/03	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/03	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/03	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/03	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/03	06:12:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2016/01/03	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/01/03	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/03	06:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/03	06:47:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/03	06:47:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/03	06:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/03	07:12:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/03	07:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/03	08:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/03	08:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/03	08:27:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/03	08:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/03	08:49:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/03	08:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/03	10:08:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/03	10:08:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/03	10:08:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/03	10:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/03	10:21:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/03	10:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/03	15:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/03	15:31:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/03	15:31:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/03	15:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/03	15:54:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/03	15:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2016/01/03	17:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/03	17:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/03	17:07:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/03	17:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/03	17:31:00.0	XRT_Custom_430_OG [0x1ae]						
2016/01/03	17:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/03	18:01:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/03	18:01:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/03	18:01:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/01/03	18:02:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2016/01/03	18:02:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/01/03	18:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/01/03	18:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/03	18:04:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2016/01/03	18:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/03	18:11:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/03	18:11:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/03	18:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/01/03	18:12:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 1d 89 b1 71		
2016/01/03	18:12:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/01/03	18:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/01/03	18:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/01/03	18:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/03	18:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/03	18:14:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2016/01/03	18:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/01/03	18:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/03	18:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/03	18:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/03	18:44:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/03	18:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/03	19:07:30.0	XRT_Custom_430_OG [0x1ae]						
2016/01/03	19:08:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/03	20:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/03	20:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/03	20:21:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/03	20:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/03	20:45:00.0	XRT_Custom_430_OG [0x1ae]						
2016/01/03	20:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/03	21:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/03	21:59:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/03	21:59:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/03	22:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/03	22:21:00.0	XRT_Custom_430_OG [0x1ae]						
2016/01/03	22:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/03	23:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/03	23:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/03	23:36:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/03	23:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						

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2016/01/03	23:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/03	23:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/04	01:03:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/04	01:03:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/04	01:03:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/04	01:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/04	01:19:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/04	01:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/04	02:35:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/04	02:35:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/04	02:35:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/04	02:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/04	02:55:30.0	XRT_Custom_430_OG [0x1ae]					
2016/01/04	02:56:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/04	04:03:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/04	04:03:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/04	04:03:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/04	04:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/04	04:33:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/04	04:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/04	05:44:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/04	05:44:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/04	05:44:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/04	05:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/04	06:10:30.0	XRT_Custom_430_OG [0x1ae]					
2016/01/04	06:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/04	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/04	06:19:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/04	06:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/01/04	06:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2016/01/04	06:20:18.0	XRT_FLD_DIS_406_OG [0x196]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/01/04	06:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/01/04	06:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/04	06:22:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2016/01/04	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/04	06:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/04	06:29:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/04	06:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/01/04	06:30:00.0	AOCS_Ore-point_Start_8_OG [0x09e]					
			AOCU_NM	5	02-76	00 1d 89 b1 71	
2016/01/04	06:30:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/04	06:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/04	06:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/04	06:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/04	06:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/04	06:32:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2016/01/04	06:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/01/04	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/04	07:24:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/04	07:24:02.0	XRT_FLD_RESET_415_OG [0x19f]					

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2016/01/04	07:24:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/04	07:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/04	07:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/04	07:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/04	09:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	09:03:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/04	09:03:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/04	09:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/04	09:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/04	09:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	10:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/04	10:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/04	10:46:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/04	10:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/04	10:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	10:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/04	14:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/04	14:31:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/04	14:31:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/04	14:31:34.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	14:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/04	14:41:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/04	14:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/04	16:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/04	16:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	16:06:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/04	16:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/04	16:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/04	16:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/04	17:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	17:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/04	17:42:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/04	17:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/04	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	18:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	18:05:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/01/04	18:06:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2016/01/04	18:06:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/01/04	18:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/01/04	18:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/04	18:08:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/01/04	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/04	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	18:15:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/04	18:15:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/01/04	18:16:00.0	AOCS_Orе-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 1d 89 b1 71
2016/01/04	18:16:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/01/04	18:16:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/01/04	18:16:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/01/04	18:16:24.0	XRT_ARS_DIS_423_OG [0x1a7]				

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2016/01/04	18:16:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/04	18:18:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13	
2016/01/04	18:18:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/01/04	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/04	19:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/04	19:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/04	19:19:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/04	19:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/04	19:42:30.0	XRT_Custom_430_OG [0x1ae]						
2016/01/04	19:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/04	20:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/04	20:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/04	20:56:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/04	20:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/04	21:20:00.0	XRT_Custom_430_OG [0x1ae]						
2016/01/04	21:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/04	22:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/04	22:34:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/04	22:34:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/04	22:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/04	22:55:00.0	XRT_Custom_430_OG [0x1ae]						
2016/01/04	22:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/05	00:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/05	00:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/05	00:11:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/05	00:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/05	00:19:30.0	XRT_Custom_430_OG [0x1ae]						
2016/01/05	00:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/05	01:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/05	01:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/05	01:35:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/05	01:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/05	01:52:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/05	01:52:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/05	01:52:28.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2016/01/05	01:52:30.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	ad 59 00 00	
2016/01/05	01:52:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/01/05	01:55:24.0	XRT_FLRCTRL_DIS_427_OG [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/01/05	01:55:26.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/05	01:55:28.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10	
2016/01/05	01:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/05	03:52:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/05	03:52:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/05	03:52:28.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2016/01/05	03:52:30.0	AOCS_OrE-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	00 00 54 00	
2016/01/05	03:52:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/01/05	03:55:24.0	XRT_FLRCTRL_DIS_427_OG [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/01/05	03:55:26.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		

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2016/01/05	03:55:28.0	XRT_QT_PROG_SET_413_OG [0x19d]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2016/01/05	03:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/05	05:52:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	05:52:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	05:52:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2016/01/05	05:52:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2016/01/05	05:52:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/05	05:55:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/05	05:55:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/05	05:55:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2016/01/05	05:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/05	06:02:30.0	AOCS_OrE-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	1d	89	b1	71
2016/01/05	09:40:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	09:40:00.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	09:40:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/05	09:40:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/05	09:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/05	10:15:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00