

XRT Timeline to be uploaded on 2016/01/05

Period: 2016/01/05 10:05:00 - 2016/01/14 10:31:00

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Normal mode

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XOB #1AEB: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 240s cad													
Term	Pointing (x, y)	Comment											
01/05 10:18:00 - 01/05 17:50:24	Track (331.7, -185.2) ^{Ⓜ 01/05 10:15:00}	# OP start + 10min, CH observations for EIS.											
01/05 18:07:30 - 01/05 23:59:54	Fixed (940.0, -220.0)	# Filament Observations											
01/06 06:13:00 - 01/06 10:17:30	Fixed (940.0, -200.0)	# Filament observations											
01/06 10:33:00 - 01/06 17:56:24	Track (527.9, -189.6) ^{Ⓜ 01/06 10:30:00}	# CH observations for EIS											
01/06 18:09:30 - 01/06 23:59:54	Fixed (940.0, -200.0)	# Filament observations											
01/07 06:42:00 - 01/07 10:59:54	Fixed (940.0, -200.0)	# Filament observations											
01/07 11:03:00 - 01/07 17:51:00	Track (696.9, -197.2) ^{Ⓜ 01/07 11:00:00}	# CH observations for EIS											
PROG= 13 Inf.-time(s)													
└─ Subr= 1 1-time(s) 240.0sec													
└─ Seqn= 29 1-time(s) 4.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 26 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + Th													
Term	Pointing (x, y)	Comment											
01/05 17:57:30 - 01/05 18:04:24	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min											
01/06 06:03:00 - 01/06 06:09:54	Fixed (0.0, 0.0)	synoptic											
01/06 17:59:30 - 01/06 18:06:24	Fixed (0.0, 0.0)	synoptic, shifted -3.5 min											
01/07 06:32:00 - 01/07 06:38:54	Fixed (0.0, 0.0)	synoptic, shifted 29.0 min											
01/08 06:03:00 - 01/08 06:09:54	Fixed (0.0, 0.0)	synoptic											
01/08 18:03:00 - 01/08 18:09:54	Fixed (0.0, 0.0)	synoptic											
01/09 06:05:00 - 01/09 06:11:54	Fixed (0.0, 0.0)	synoptic, shifted 2.0 min											
01/09 18:07:00 - 01/09 18:13:54	Fixed (0.0, 0.0)	synoptic, shifted 4.0 min											
01/10 05:37:00 - 01/10 05:43:54	Fixed (0.0, 0.0)	synoptic, shifted -26.0 min											
01/10 18:26:00 - 01/10 18:32:54	Fixed (0.0, 0.0)	synoptic, shifted 23.0 min											
01/11 05:57:00 - 01/11 06:03:54	Fixed (0.0, 0.0)	synoptic, shifted -6.0 min											
01/11 18:03:00 - 01/11 18:09:54	Fixed (0.0, 0.0)	synoptic											
01/12 06:13:30 - 01/12 06:20:24	Fixed (0.0, 0.0)	synoptic, shifted 10.5 min											
PROG= 05 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 33 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 49 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 39 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 54 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms													
Term	Pointing (x, y)	Comment											
01/06 00:03:00 - 01/06 05:59:54	Fixed (-16.0, 874.0)	# HOP 81 (North Pole)											
01/07 00:03:00 - 01/07 06:28:54	Fixed (-16.0, -946.0)	# HOP 81 (South Pole)											
PROG= 01 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 9 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 7 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 30-time(s) 2.0sec													
└─ Seqn= 57 1-time(s) 60.0sec													

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ADF: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(16/362/1443), Thin-Be(88/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(256/2048), Med-Al(40

Term	Pointing (x, y)	Comment
01/07 18:17:30 - 01/07 18:24:24	Fixed (0.0, 0.0)	synoptic, shifted 14.5 min

PROG= 02 1-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 46 2-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 72 2-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 59 2-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AC9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
01/07 18:27:30 - 01/08 05:59:54	Track (202.8, -131.7) ^{Ⓜ 01/07 18:24:30}	# AR 12476
01/08 06:13:00 - 01/08 17:59:54	Track (307.7, -132.7) ^{Ⓜ 01/08 06:10:00}	# AR cont.
01/08 18:13:00 - 01/09 06:01:54	Track (410.5, -134.6) ^{Ⓜ 01/08 18:10:00}	# AR cont.
01/09 06:15:00 - 01/09 18:03:54	Track (508.1, -137.3) ^{Ⓜ 01/09 06:12:00}	# AR cont.
01/09 18:17:00 - 01/10 05:33:54	Track (598.7, -140.9) ^{Ⓜ 01/09 18:14:00}	# AR cont.
01/10 05:47:00 - 01/10 17:59:30	Track (677.8, -145.1) ^{Ⓜ 01/10 05:44:00}	# AR cont.
01/10 18:36:00 - 01/11 05:53:54	Track (755.9, -150.6) ^{Ⓜ 01/10 18:33:00}	# AR cont.
01/11 06:07:00 - 01/11 17:59:54	Track (816.3, -156.2) ^{Ⓜ 01/11 06:04:00}	# AR cont.

PROG= 14 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 56 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec												
Seqn= 75 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 23 4-time(s) 180.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1ACA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
01/11 18:13:00 - 01/12 06:10:24	Track (868.7, -162.8) ^{Ⓜ 01/11 18:10:00}	# AR cont.
01/12 06:23:30 - 01/12 10:02:30	Track (909.5, -169.9) ^{Ⓜ 01/12 06:20:30}	# AR cont.

PROG= 20 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
└ Seqn= 56	1-time(s)	2.0sec										
└└ Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└└ Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└└ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	4-time(s)	2.0sec										
└ Seqn= 75	1-time(s)	2.0sec										
└└ Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
└└ Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└└ thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
└└ thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└ Seqn= 23	4-time(s)	240.0sec										
└└ Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
└└ thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
└└ Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
└└ thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
└└ Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
└└ thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1B09: Flare - CME watch -Be-thin - 4x4 - AEC 2 - 20s cadence slot16												
Term	Pointing (x, y)							Comment				
01/05 10:18:00 - 01/05 17:50:24	Track (331.7, -185.2) @ 01/05 10:15:00							# OP start + 10min, CH observations for EIS.				
01/05 18:07:30 - 01/05 23:59:54	Fixed (940.0, -220.0)							# Filament Observations				
01/06 00:03:00 - 01/06 05:59:54	Fixed (-16.0, 874.0)							# HOP 81 (North Pole)				
01/06 06:13:00 - 01/06 10:17:30	Fixed (940.0, -200.0)							# Filament observations				
01/06 10:33:00 - 01/06 17:56:24	Track (527.9, -189.6) @ 01/06 10:30:00							# CH observations for EIS				
01/06 18:09:30 - 01/06 23:59:54	Fixed (940.0, -200.0)							# Filament observations				
01/07 00:03:00 - 01/07 06:28:54	Fixed (-16.0, -946.0)							# HOP 81 (South Pole)				
01/07 06:42:00 - 01/07 10:59:54	Fixed (940.0, -200.0)							# Filament observations				
01/07 11:03:00 - 01/07 17:51:00	Track (696.9, -197.2) @ 01/07 11:00:00							# CH observations for EIS				
PROG= 08 Inf.-time(s)												
└ Subr= 1	90-time(s)	20.0sec										
└└ Seqn= 40	1-time(s)	2.0sec										
└└└ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B08: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384) + Gband (3m												
Term	Pointing (x, y)							Comment				
01/07 18:27:30 - 01/08 05:59:54	Track (202.8, -131.7) @ 01/07 18:24:30							# AR 12476				
01/08 06:13:00 - 01/08 17:59:54	Track (307.7, -132.7) @ 01/08 06:10:00							# AR cont.				
01/08 18:13:00 - 01/09 06:01:54	Track (410.5, -134.6) @ 01/08 18:10:00							# AR cont.				
01/09 06:15:00 - 01/09 18:03:54	Track (508.1, -137.3) @ 01/09 06:12:00							# AR cont.				
01/09 18:17:00 - 01/10 05:33:54	Track (598.7, -140.9) @ 01/09 18:14:00							# AR cont.				
01/10 05:47:00 - 01/10 17:59:30	Track (677.8, -145.1) @ 01/10 05:44:00							# AR cont.				
01/10 18:36:00 - 01/11 05:53:54	Track (755.9, -150.6) @ 01/10 18:33:00							# AR cont.				
01/11 06:07:00 - 01/11 17:59:54	Track (816.3, -156.2) @ 01/11 06:04:00							# AR cont.				
01/11 18:13:00 - 01/12 06:10:24	Track (868.7, -162.8) @ 01/11 18:10:00							# AR cont.				
01/12 06:23:30 - 01/12 10:02:30	Track (909.5, -169.9) @ 01/12 06:20:30							# AR cont.				
PROG= 12 Inf.-time(s)												
└ Subr= 1	20-time(s)	2.0sec										
└└ Seqn=100	1-time(s)	25.0sec										
└└└ thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
└└└ med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└└└ Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└ Subr= 2	1-time(s)	2.0sec										
└└ Seqn= 52	1-time(s)	2.0sec										
└└└ Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└└└ Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└└└ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

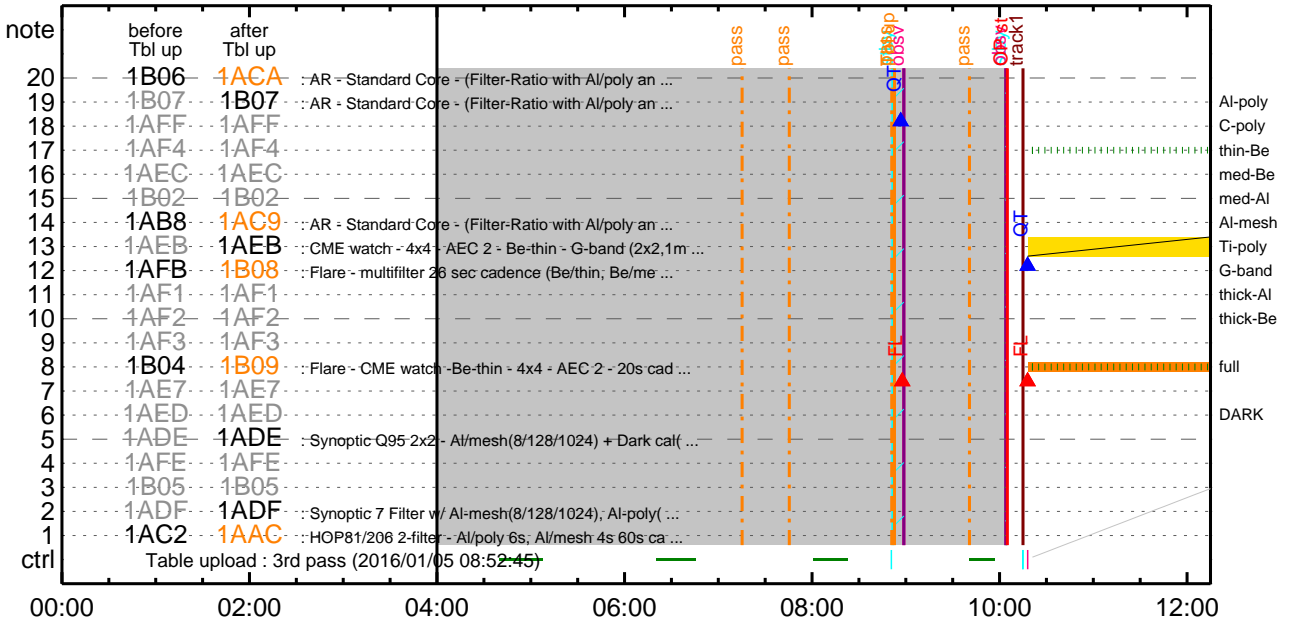
* * * * *

Flare Detection

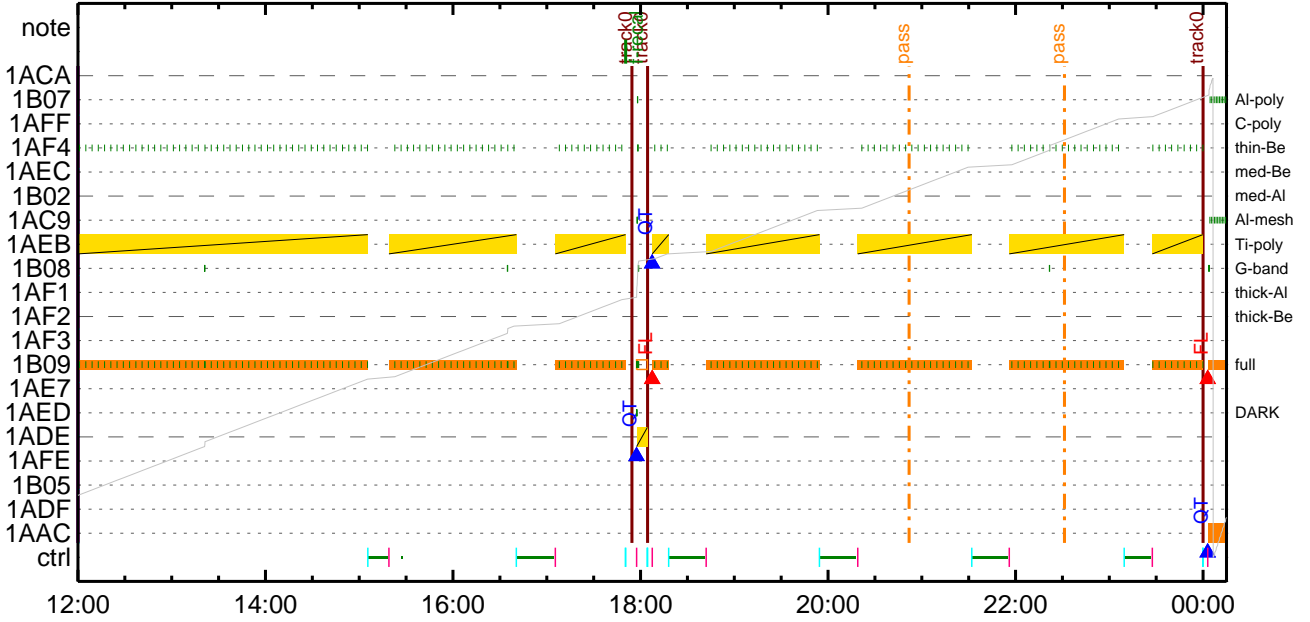
* * * * *

FLD Patrol												
Term	Pointing (x, y)							Comment				
01/05 18:04:48 - 01/06 06:00:18	Fixed (940.0, -220.0)							# Filament Observations				
01/06 06:10:18 - 01/06 17:56:48	Fixed (940.0, -200.0)							# Filament observations				
01/06 18:06:48 - 01/07 06:29:18	Fixed (940.0, -200.0)							# Filament observations				
01/07 06:39:18 - 01/07 18:14:48	Fixed (940.0, -200.0)							# Filament observations				
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	

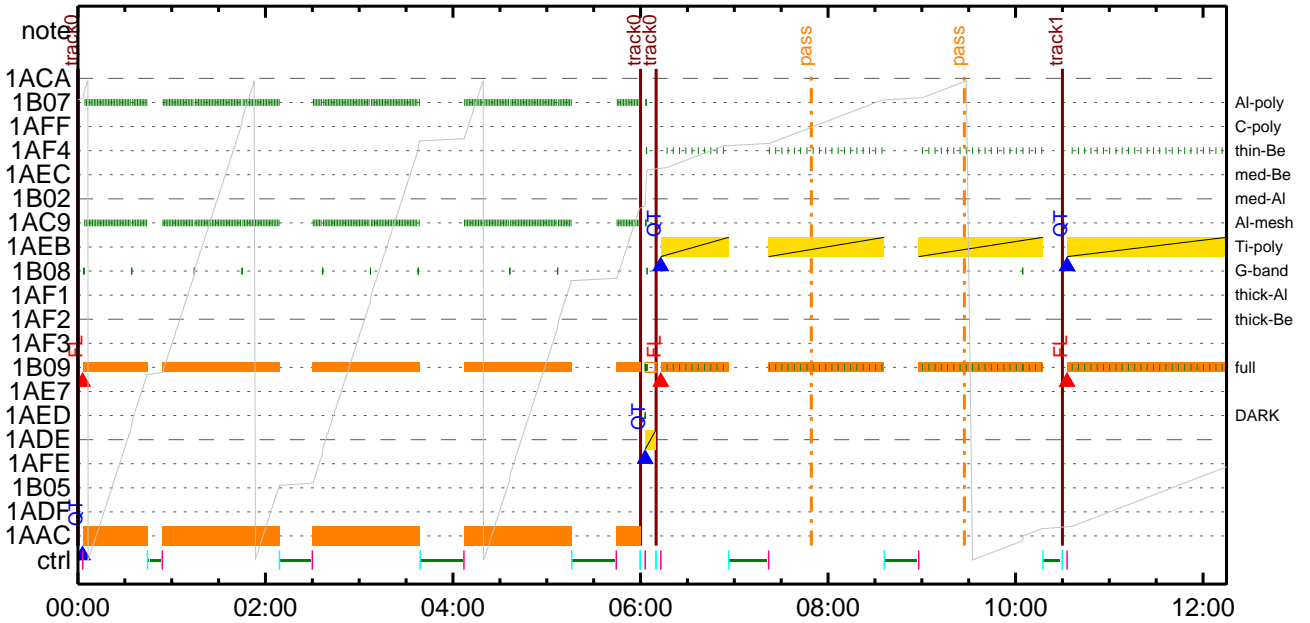
CMDI #0626 2016/01/05



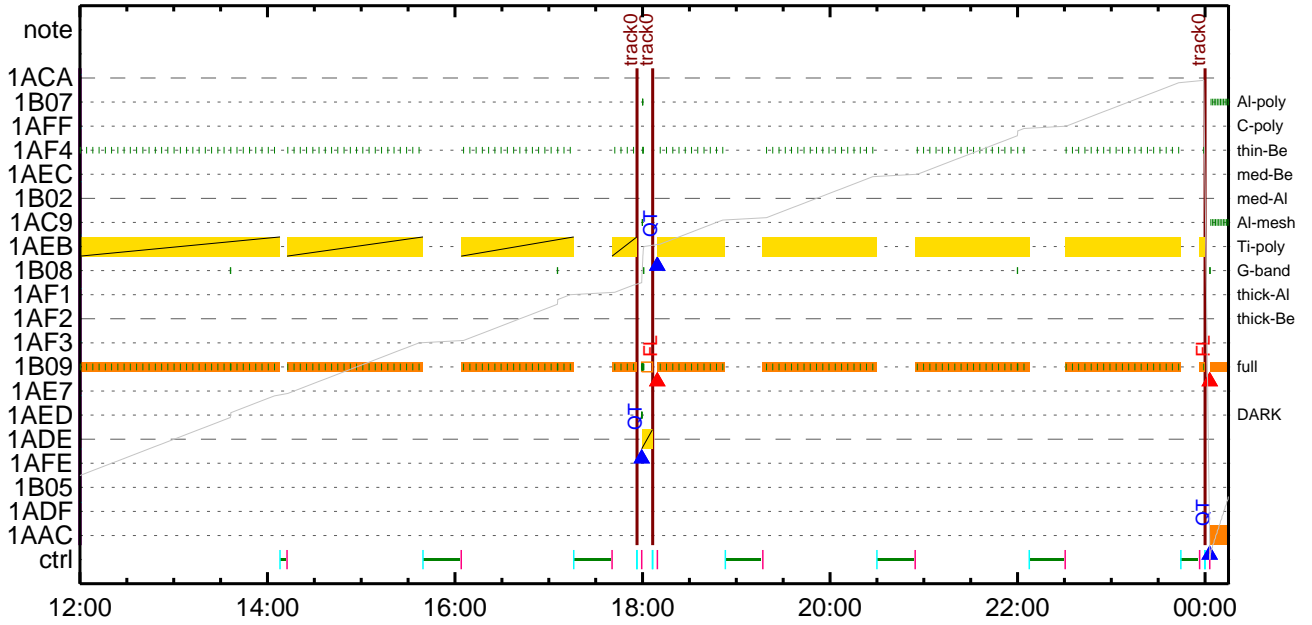
CMDI #0626 2016/01/05



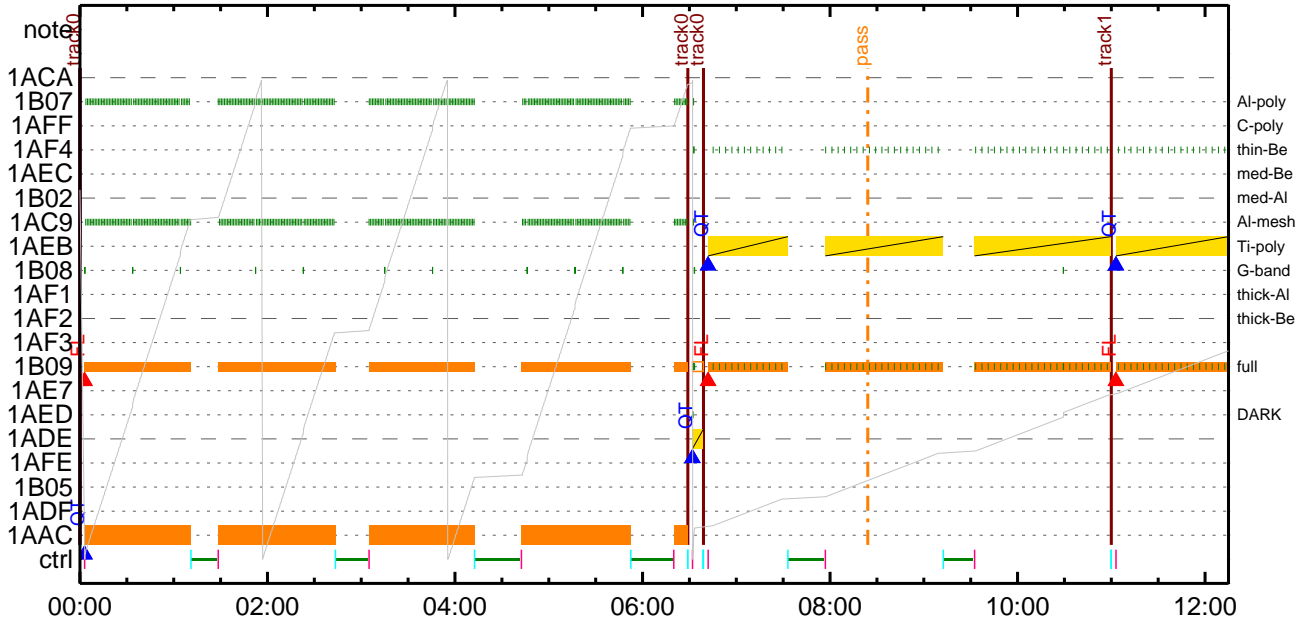
CMDI #0626 2016/01/06



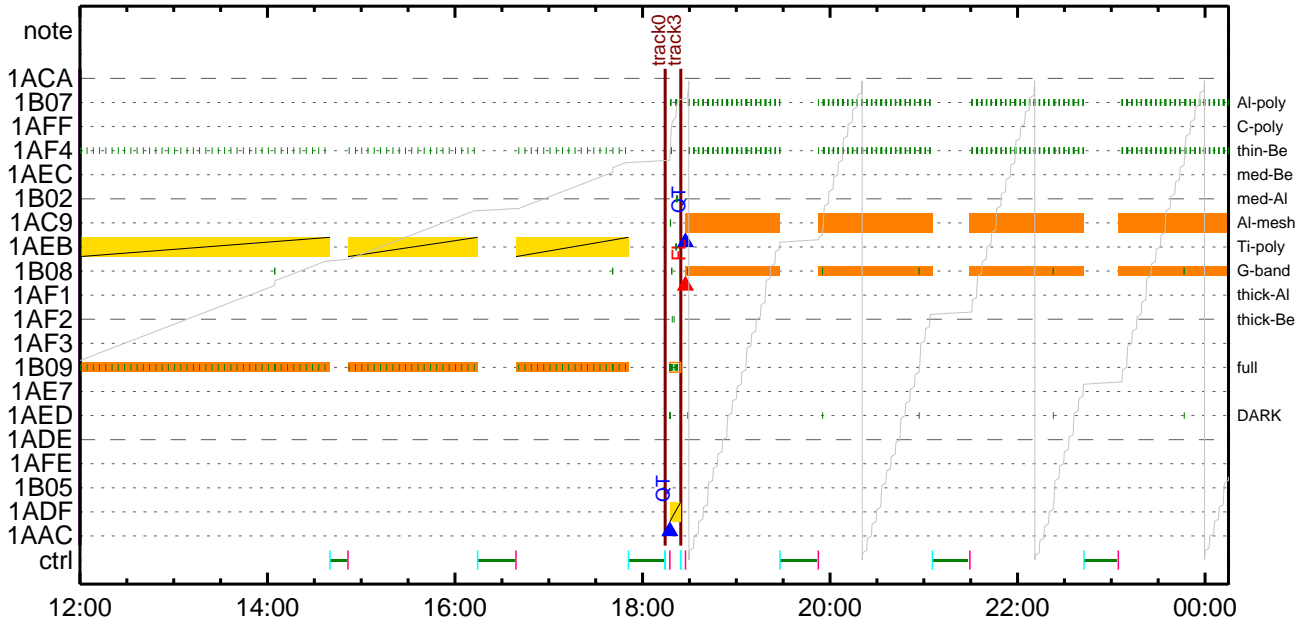
CMDI #0626 2016/01/06



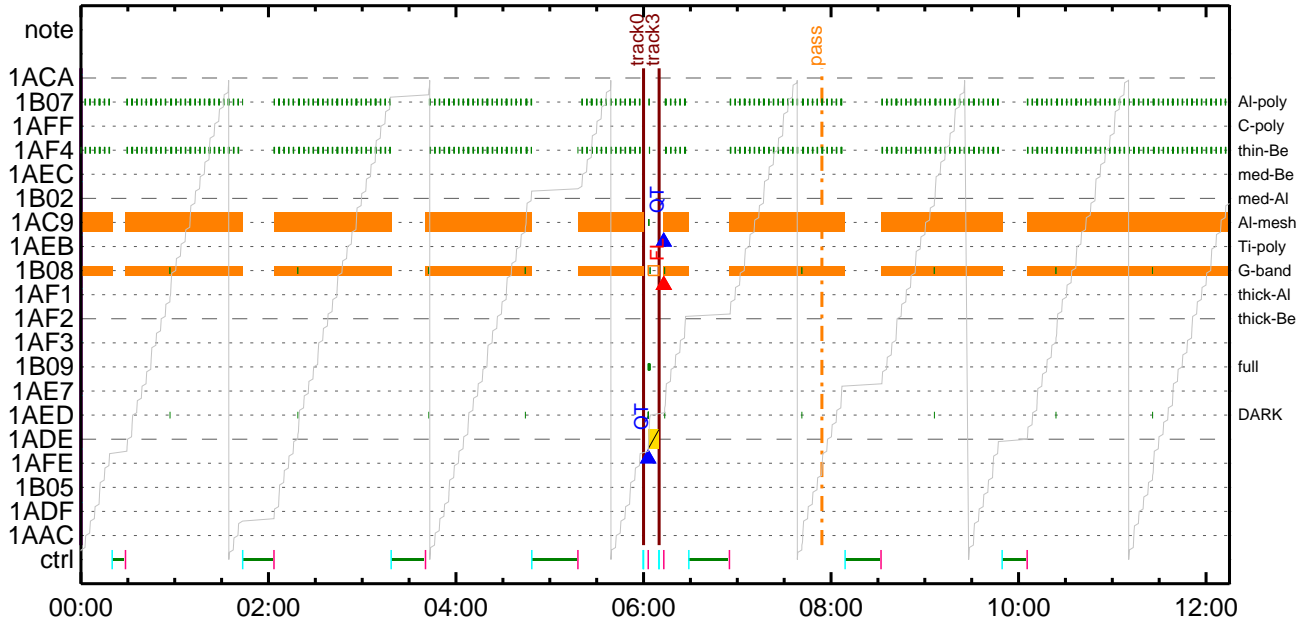
CMDI #0626 2016/01/07



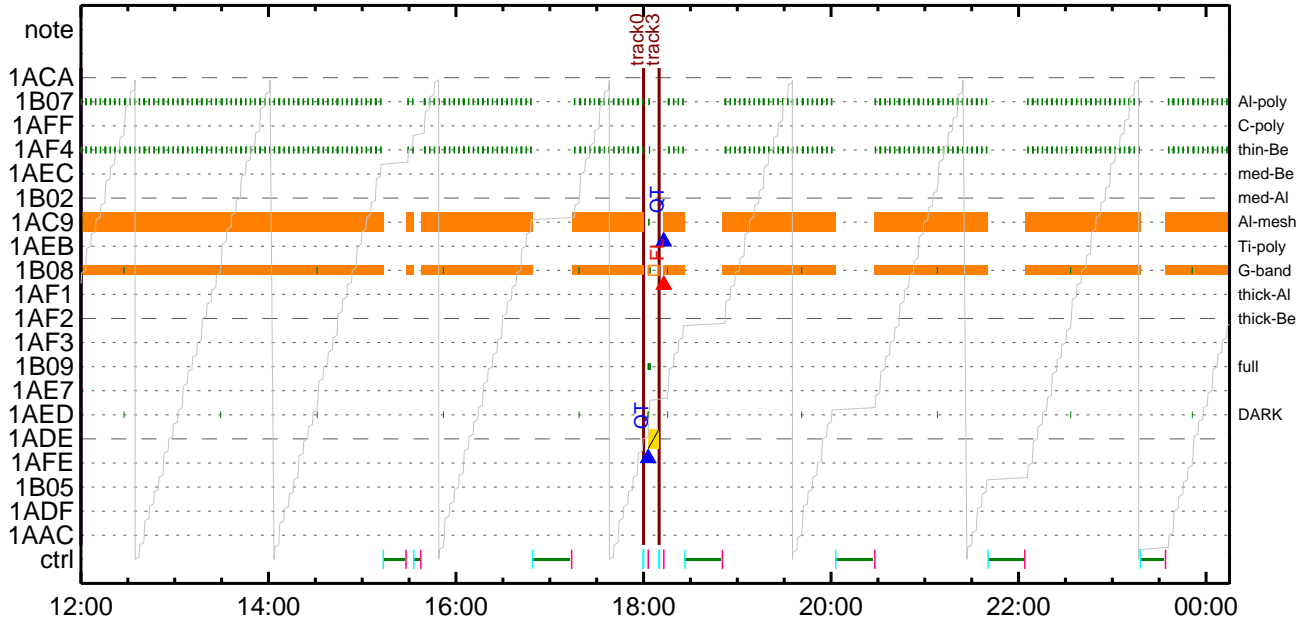
CMDI #0626 2016/01/07



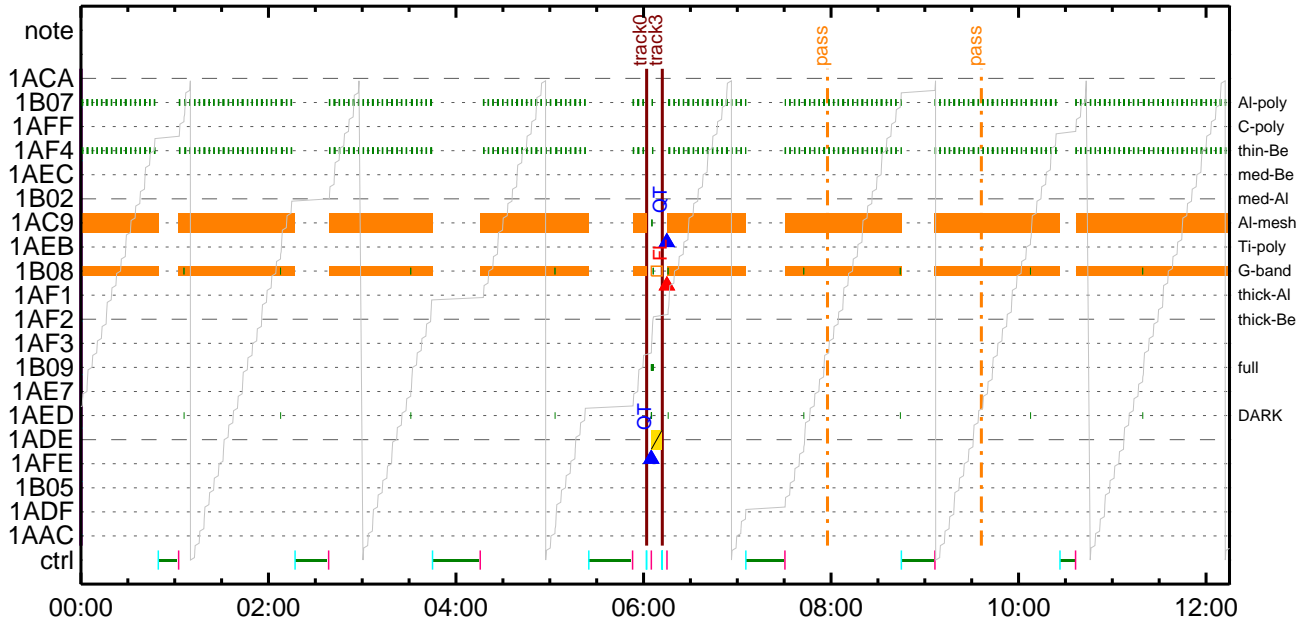
CMDI #0626 2016/01/08



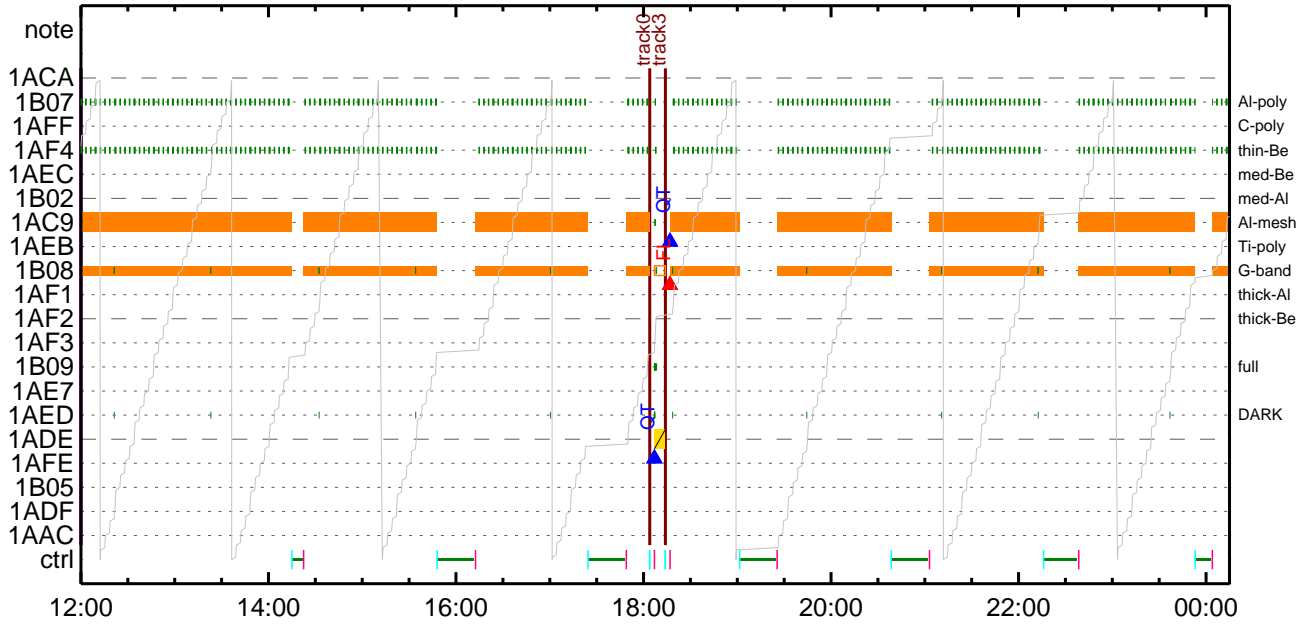
CMDI #0626 2016/01/08



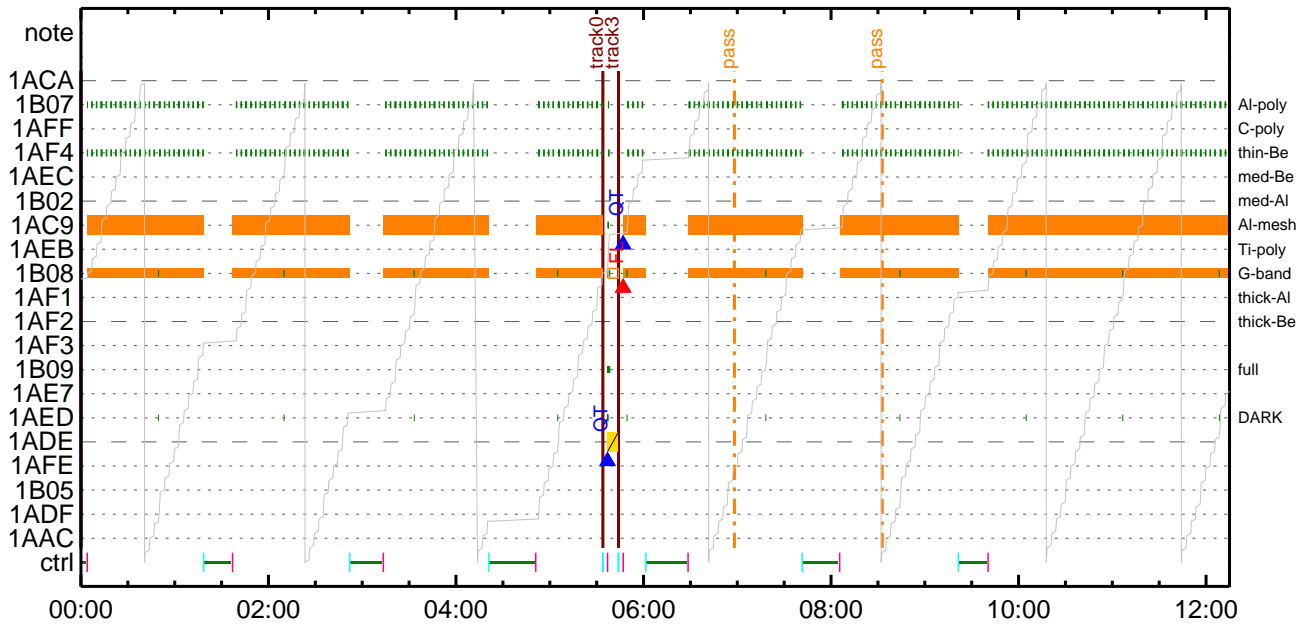
CMDI #0626 2016/01/09



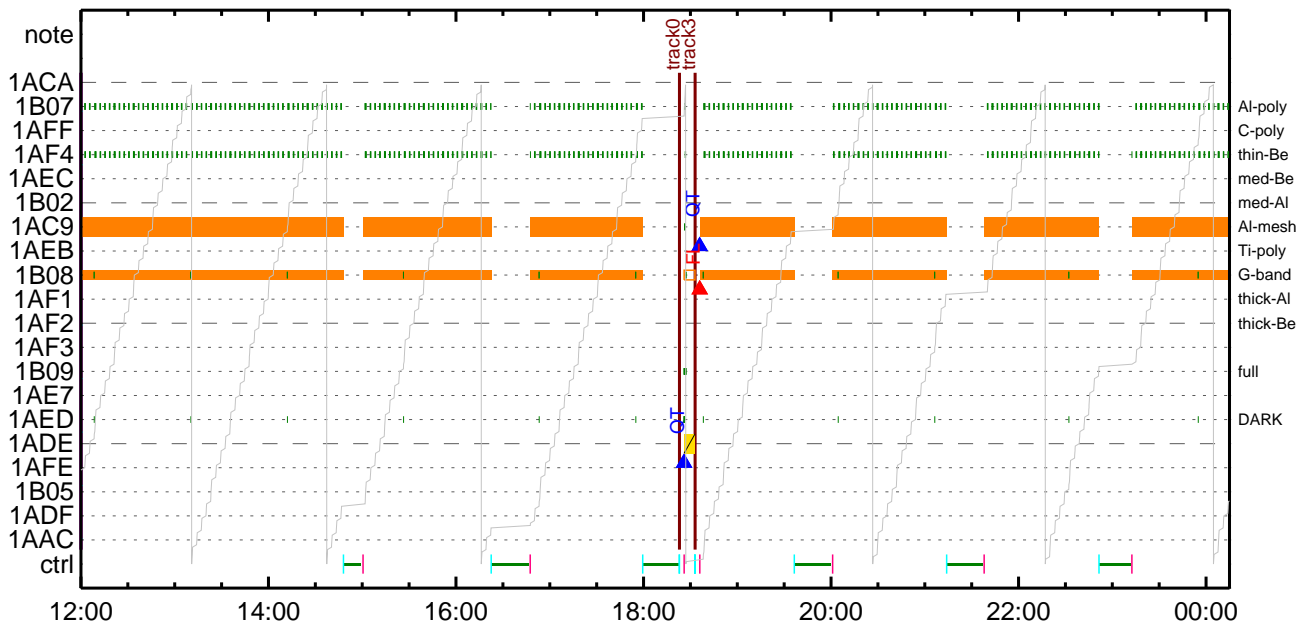
CMDI #0626 2016/01/09



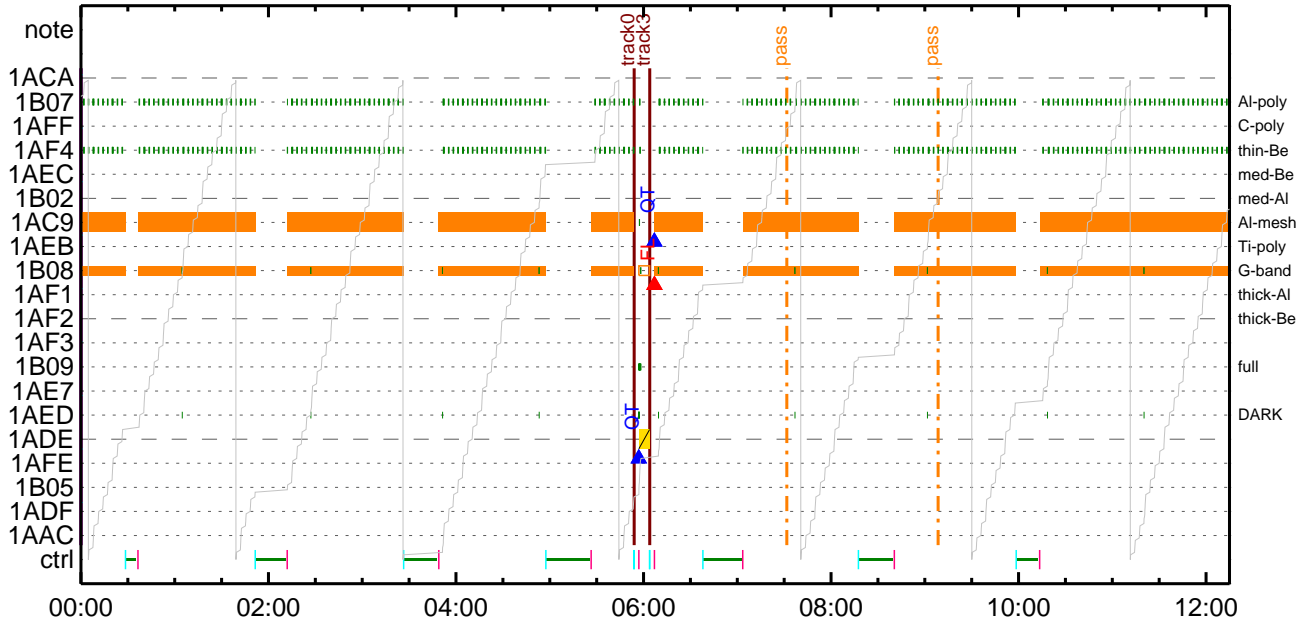
CMDI #0626 2016/01/10



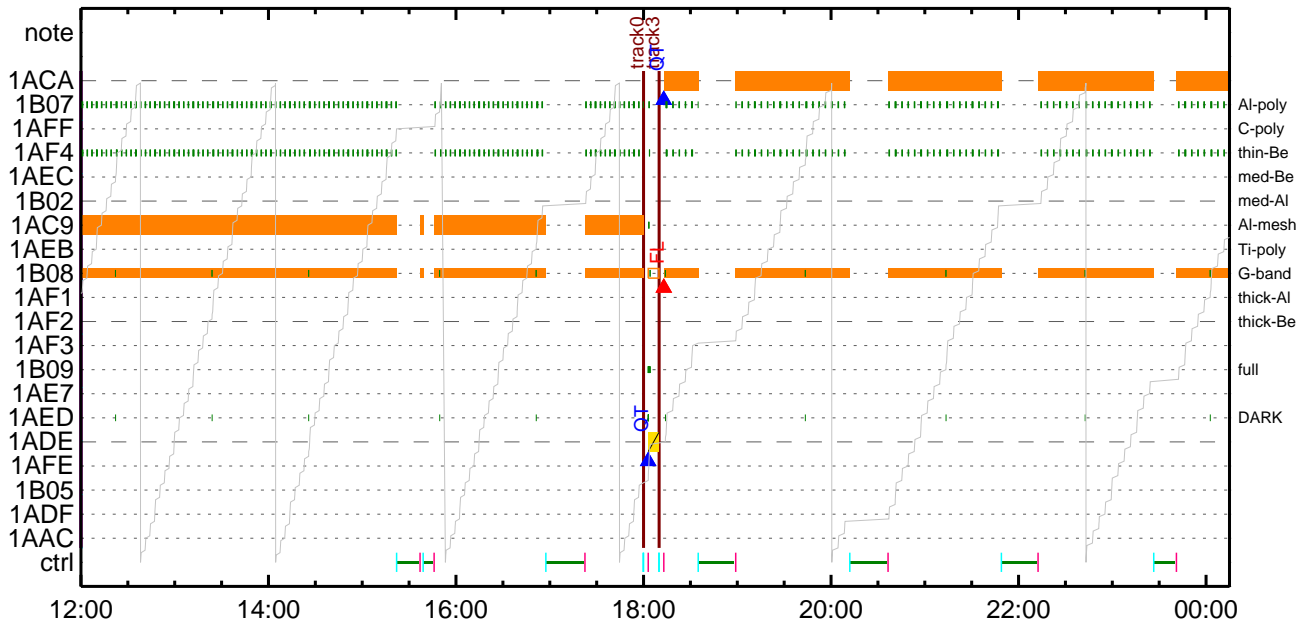
CMDI #0626 2016/01/10



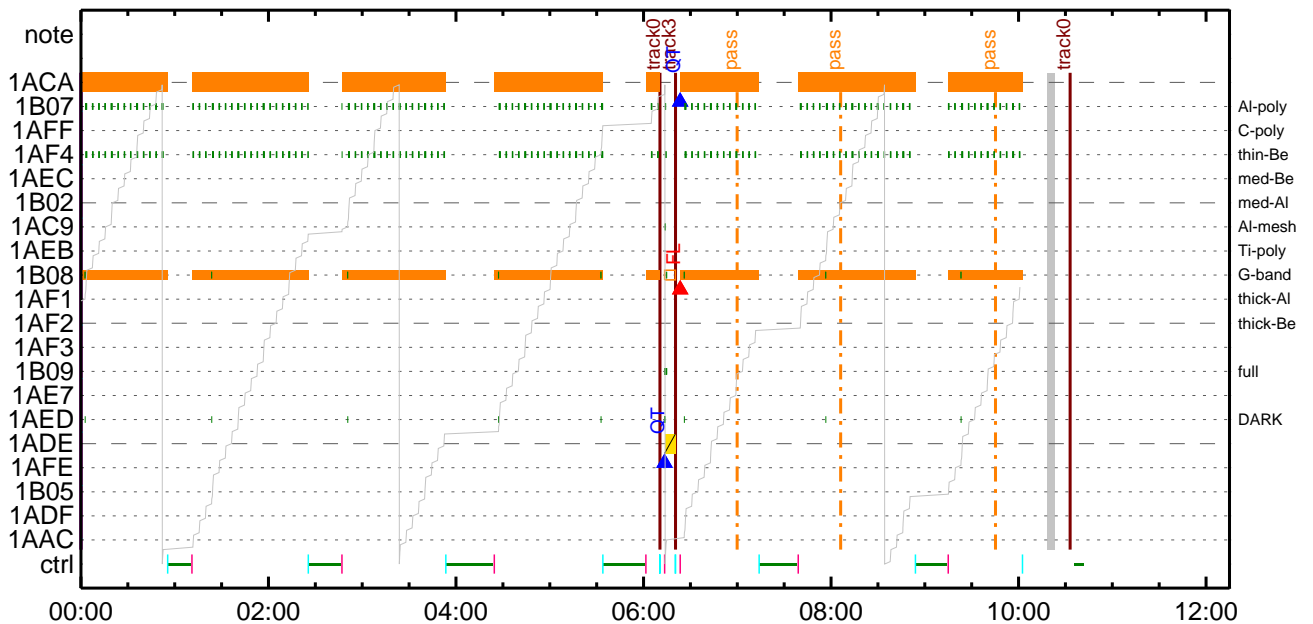
CMDI #0626 2016/01/11



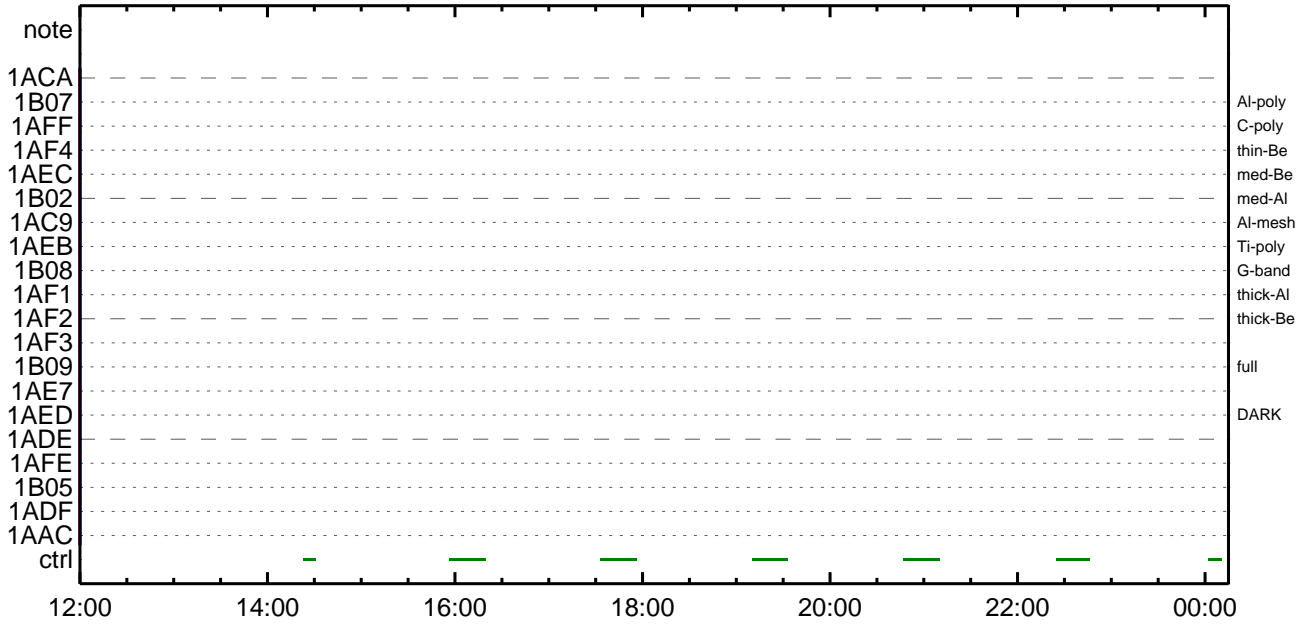
CMDI #0626 2016/01/11



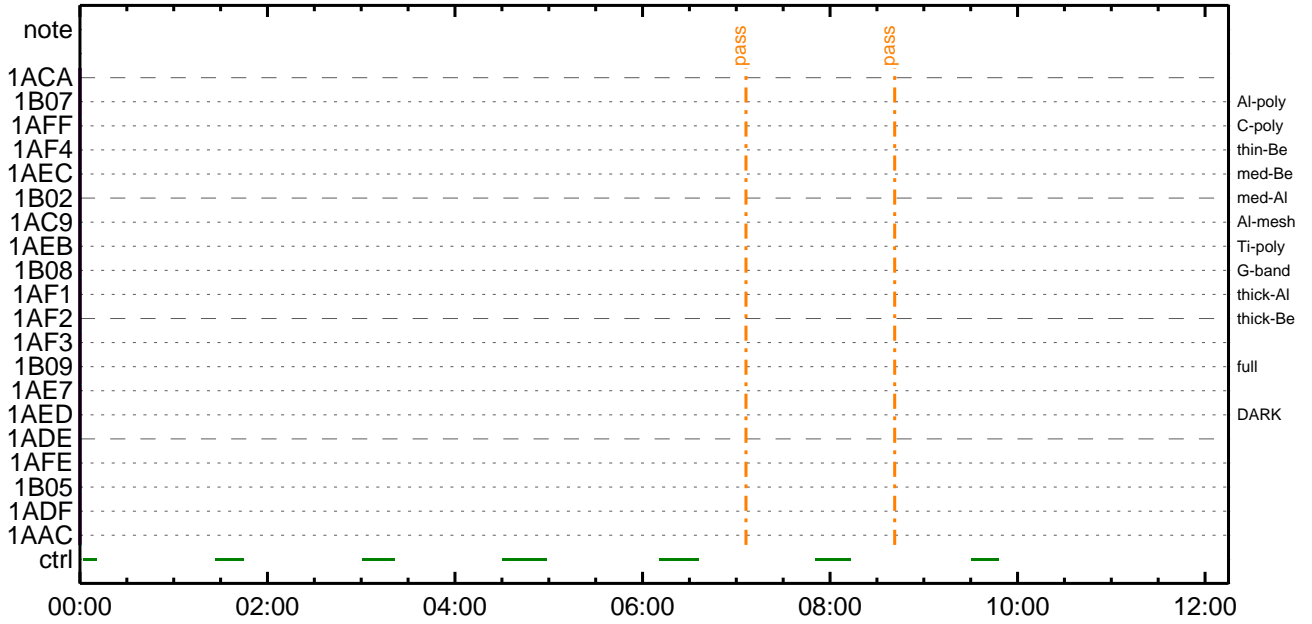
CMDI #0626 2016/01/12



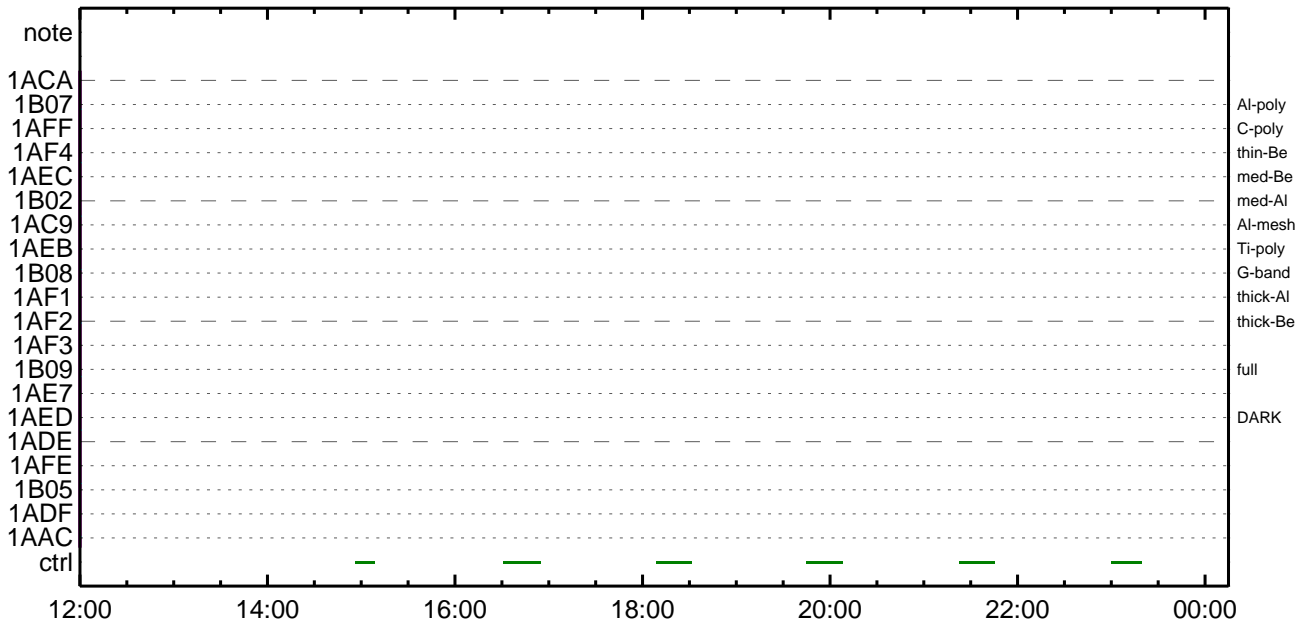
CMDI #0626 2016/01/12



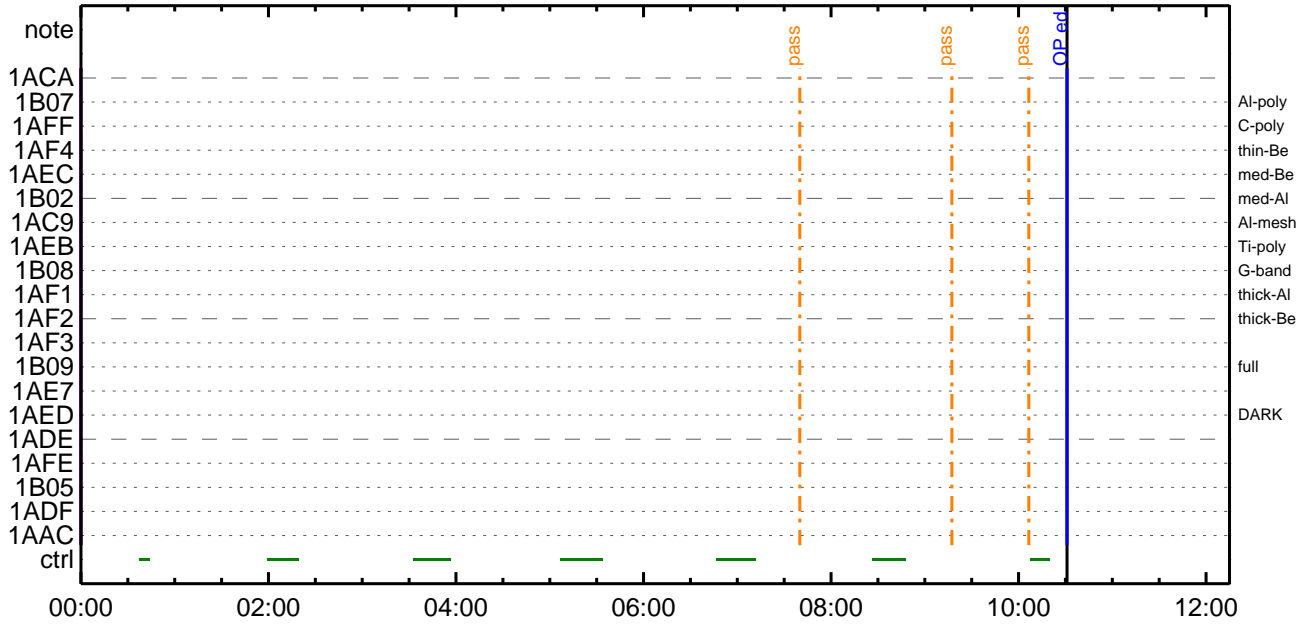
CMDI #0626 2016/01/13



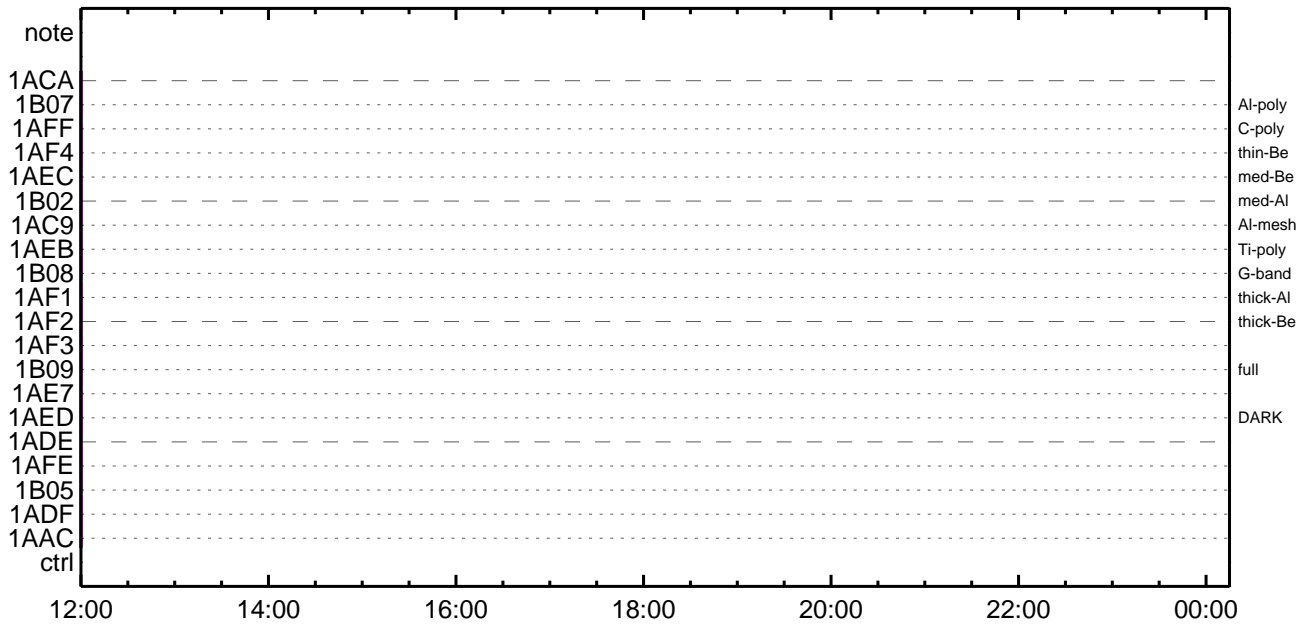
CMDI #0626 2016/01/13



CMDI #0626 2016/01/14



CMDI #0626 2016/01/14



0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-584:OP
0104 (
0105 S. OG og-584:OG
0106 (
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ;c[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ;c[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ;c[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ;c[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ;c[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ;c[HK1_PKT_FORM_NO] EQ 7
0120 C. ;c[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ;c[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx1/2^i»oð³îçš
0125 C. ;c[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGqî%ê¹ç•ë²îOKoð³îçš
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ;c[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ;c[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ;c[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ;c[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ;c[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ;c[HK1_PKT_FORM_NO] EQ 7
0139 C. ;c[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ;c[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx1/2^i»oð³îçš
0144 C. ;c[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGqî%ê¹ç•ë²îOKoð³îçš
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ;c[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ;c[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ;c[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ;c[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ;c[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ;c[HK1_PKT_FORM_NO] EQ 7
0158 C. ;c[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ;c[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx1/2^i»oð³îçš
0163 C. ;c[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPqî%ê¹ç•ë²îOKoð³îçš
0165 C.
0166 C. ***** oê²¼oî%Ã´¶Á°oêê¬o°Á÷¿@ (¼âµ-YAYOXx1/2^e½çoðÁÔæoç¼¹a°oê%î¹çoçoâ) *****
0167 C. DHUYâ;4YE;ê½½, Yî;4YE;êoðîá¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ;c[HK1_PKT_FORM_NO] EQ 2
0171 C. ;c[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;š OPOG UPLOADo-Á÷¿@NGuî%î¹ç;ç°ê²¼oîTl-CMDÁ÷¿@oî%Á¹Ôo•oêoð³oê;f
0180 C. oðo¿;çSEToêDUMPoîÆ±°iYNY¹oç¹Ôo|o³oê;f
0181 C.
0182 C. TIY³YpYóYêoðoÁDî¿(UT)
0183 +. TI 2016-01-05 10:00:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ;c[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-01-05 10:00:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ;c[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-01-05 10:00:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ;c[HK1_TI_CMD_NUM] EQ 1COUNTUP

0194 C.
0195 +. TI 2016-01-05 10:04:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñαîí¥Á¥§¥Á¥~¹ãîü
0200 C. ¢¢[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. ¢¢[HK1_TI_CMD_NUM] EQ 4
0202 C. ¢¢[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. ¢¢[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TÍíî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0220 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ª î»αδ³îÇ§
0226 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¼Ê¹Ç•ë²îOKαδ³îÇ§
0229 C.
0230 C. DHU¥ªî¼¥Ê;Ê¼¥½. ¥î;¼¥Ê;Êαδîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0234 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2016-01-05 10:04:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2016-01-05 10:04:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2016-01-05 10:04:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2016-01-05 10:04:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `úÄîαî»ö¼ÝαÊÄα¹αèDCBC•x²è *****
0276 C. (%á°îí¥Ö¥Á¥Ê¥Ï¥¼¥Ç¥èαÊ¼αα¼Ä»Ûα¹αè)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥~¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-585 2016-01-05 13:21:23 94 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY~¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YFÏYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿òÄó•µ°Æ»Í×ÁÇóÍYçYÄY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈóÈ¼°ÇÏó•ò¿¼í¹çóí;çÄ®, ùò¹òèòòòóÇÄ+¿®ó•òÈóóó³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 7394472.3 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_CHG_ENA
0056 BC (20)
0057 . C. Verify EIS_MODE_CHG_FLG is ENA
0058 +. DC 07-FC EIS_MODE_MANU
0059 BC (21 02)
0060 . C. Verify EIS in MANUAL mode
0061 . C. Estimated OBSTBL upload time is 35s
0062 C. *****
0063 C. EIS START OBSTBL LOAD
0064 C. *****
0065 . S. RAM ram-820:EIS_OBSTBL
0066 ( )
0067 +. DC 07-FC EIS_DUMP_OBSTBL
0068 BC (07 07 07 00 00 70 00)
0069 C.
0070 C. Execute, after the success of OBSTBL upload.
0071 C. Set EIS TI-commands
0072 +. TI 2016-01-05 10:04:50.0
0073 DC 07-FC EIS_MODE_CHG_ENA
0074 BC (20)
0075 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0076 C. *****
0077 C. EIS END OBSTBL LOAD
0078 C. *****
0079 C.
0080 . C. ***** MDP `ûÄÏòÏ»ò¼YòÈÄò¹òÈDCBC•×²è *****
0081 C. (¼á°íYÓYÄYÈYÏYÉYáYçYèòÈ%¼ò¼Ä»Û¹òè)
0082 . S. DC-BC dcbc-402:DCBC
0083 (MDP_known_event)
0084 C.
0085 C.
0086 . C. ***** YDÿ!•Ï Daily±¿ÍÑòÈ´Øò¹òÈDCBC•×²è *****
0087 . S. DC-BC dcbc-153:DCBC
0088 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 C.
0090 C.
0091 C. ;ãLOSÁYŞYÄY~¼Ä»Û;ä
0092 C.
0093 . C. ***** LOS *****
0094 C.
```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 20 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 80 80 08 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0f 80 80 06 06)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 10 80 80 20 20)
0136 + DC 07-F0 MDP_XRT_FLD_ENA
0137 BC (d8)
0138 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0139 BC (c8)
0140 + DC 07-F0 MDP_XRT_ARS_DIS
0141 BC (d5)
0142 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0143 BC (c4 13)
0144 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0145 BC (c5 08)
0146 . C. ----- Success Verify ? OK / NG ____
0147 C.
0148 C.
0149 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0150 C.
0151 +. DC 07-F0 MDP_XRT_MODE_OBSV
0152 BC (c2)
0153 +. TI 2016-01-05 10:04:02.0
0154 DC 07-F0 MDP_XRT_MODE_OBSV
0155 BC (c2)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C. ***** XRT END *****
0159 C. *****
0160 C. SOT table upload
0161 C. *****
0162 . C. < Stop FG table >
0163 +. DC 07-F0 MDP_FG_CTRL_MANU
0164 BC (51)
0165 . C. -----
0166 C. MDP_FG_CTRL_MODE = MANU [ ]
0167 C. -----
0168 C.
0169 . C. <Upload FG Observation Table>
0170 . S. RAM ram-261:MDP_OBS_F
0171 ( )
0172 C.
0173 . C. < Dump RAMID=MDP_OBS_F >
0174 +. DC 07-F0 MDP_DUMP_FGTBL
0175 BC (82 07 00 00 00 38 b8)
0176 C. -----
0177 C. MDP_OBS_F verify = OK/NG [ ]
0178 C. -----
0179 C.
0180 . C. < Stop SP table >
0181 +. DC 07-F0 MDP_SP_CTRL_MANU
0182 BC (61)
0183 C. -----
0184 C. MDP_SP_CTRL_MODE = MANU [ ]
0185 C. -----
0186 C.
0187 . C. <Upload SP Observation Table>
0188 . S. RAM ram-289:MDP_OBS_S
0189 ( )
0190 C.
0191 . C. < Dump RAMID=MDP_OBS_S >
0192 +. DC 07-F0 MDP_DUMP_SPTBL
0193 BC (83 07 00 00 00 38 b8)

```

```

0194 C. -----
0195 C. MDP_OBS_S verify = OK/NG [ ]
0196 C. -----
0197 C.
0198 . C. < Upload DPL table >
0199 C.
0200 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°¤ESTS_CHK¤ðOFF¤È¤¹¤è
0201 C.
0202 . S. RAM ram-271:MDP_DPL
0203 ( )
0204 C.
0205 . C. < Dump RAMID=MDP_DPL >
0206 +. DC 07-F0 MDP_DUMP_FGTBL
0207 BC (82 07 00 38 b8 00 40)
0208 C. -----
0209 C. MDP_DPL verify = OK [ ]
0210 C. -----
0211 C.
0212 C. STS_CHK¤ðON¤È¤¹¤è
0213 C.
0214 . C. < Update MDP DSC PAR1 >
0215 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0216 BC (4c)
0217 C. MDP_CMD_CODE = F04C0700[ ]
0218 C. MDP_CMD_CNT (count-up 1) [ ]
0219 C. -----
0220 C.
0221 . C.
0222 C. *****
0223 C. SOT TI command set
0224 C. *****
0225 C. Execute, after the success of TBL upload.
0226 +. TI 2016-01-05 10:04:18.0
0227 DC 07-F0 MDP_SOT_MODE_OBSV
0228 BC (40)
0229 C. -----
0230 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0231 C. -----
0232 C.
0233 C.
0234 . C. ***** MDP `ûÃîî»ò¼¥¤È¤¹¤èDCBC•x²è *****
0235 C. (¼á°î¥Ö¥Ã¥È¥Þ¥È¥á¥ç¥è¤È¼¤¤¼¤¤»Û¤¹¤è)
0236 . S. DC-BC dcbc-402:DCBC
0237 (MDP_known_event)
0238 C.
0239 C.
0240 . C. ***** ¥Ð¥¹•İ Daily±;îÑ¤È`Ø¤¹¤èDCBC•x²è *****
0241 . S. DC-BC dcbc-153:DCBC
0242 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0243 C.
0244 C.
0245 . C. ;ãLOS¥Á¥S¥Ã¥`¼¤¤»Û;ã
0246 C.
0247 . C. ***** LOS *****
0248 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-587 2016-01-05 13:21:23 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µ°Æ»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÍÉ;ÈÈÈ¼°ÇÔα•α¿¼i¹çαÍ;çÀ®, ùα¹αèαβαÇÁ+¿®α•αÊααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP `úÁÍσÍ»ò¼YαÈÁÐα¹αèDCBC•x²è *****
0016 C. (¼á°íYÓYÁYÈYβYÈYáYçYèαÈ¼αα¼Á»Ûα¹αè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YDÝ¹•İ Daily+¿ÍÑαÈ´Øα¹αèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

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*** OP Sequence for XRT ***

2016/01/05	10:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	10:14:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	10:14:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/05	10:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2016/01/05	10:15:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/05	10:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/05	10:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/05	10:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/05	10:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/05	10:17:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/01/05	10:17:58.0	XRT_FL_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2016/01/05	10:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/05	15:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	15:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/05	15:05:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/05	15:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/05	15:18:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/05	15:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/05	16:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	16:40:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/05	16:40:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/05	16:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/05	17:04:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/05	17:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/05	17:50:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	17:50:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	17:50:28.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2016/01/05	17:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/05	17:54:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/05	17:54:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/05	17:57:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/05	17:57:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/05	17:57:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/05	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/05	18:04:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	18:04:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/05	18:04:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/05	18:04:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 13 8d ac 73				
2016/01/05	18:04:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/05	18:04:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/05	18:04:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/05	18:04:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/05	18:04:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/05	18:07:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/01/05	18:07:28.0	XRT_FL_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2016/01/05	18:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/05	18:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							

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2016/01/05	18:18:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/05	18:18:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/05	18:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/05	18:41:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/05	18:42:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/05	19:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/05	19:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/05	19:54:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/05	19:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/05	20:18:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/05	20:19:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/05	21:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/05	21:32:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/05	21:32:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/05	21:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/05	21:55:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/05	21:56:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/05	23:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/05	23:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/05	23:09:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/05	23:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/05	23:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/05	23:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/05	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/05	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/05	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/01/06	00:00:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 b2 4e 01 68
2016/01/06	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/01/06	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/01/06	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/01/06	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/06	00:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/06	00:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/01/06	00:02:58.0	XRT_FL_PROG_SET_425_OG [0x1a9]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2016/01/06	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/06	00:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	00:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/06	00:44:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/06	00:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/06	00:53:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/06	00:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	02:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	02:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/06	02:09:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/06	02:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/06	02:29:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/06	02:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	03:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	03:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da

2016/01/06	03:39:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/06	03:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/06	04:06:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/06	04:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/06	05:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/06	05:16:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/06	05:16:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/06	05:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/06	05:43:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/06	05:44:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/06	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/06	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/06	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/06	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/06	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/06	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/06	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/06	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/06	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/06	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/06	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/06	06:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/06	06:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 11 ca ac 73				
2016/01/06	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/06	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/06	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/06	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/06	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/06	06:12:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/01/06	06:12:58.0	XRT_FL_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2016/01/06	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/06	06:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/06	06:56:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/06	06:56:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/06	06:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/06	07:21:00.5	XRT_Custom_430_OG [0x1ae]							
2016/01/06	07:22:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/06	08:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/06	08:36:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/06	08:36:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/06	08:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/06	08:57:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/06	08:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/06	10:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/06	10:17:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/06	10:17:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/06	10:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/06	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/06	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							

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2016/01/06	10:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	10:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/01/06	10:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2016/01/06	10:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/01/06	10:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/01/06	10:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/01/06	10:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/06	10:32:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/06	10:32:58.0	XRT_FL_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2016/01/06	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2016/01/06	14:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/06	14:08:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	14:08:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/06	14:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/06	14:11:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/06	14:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/01/06	15:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/06	15:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	15:39:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/06	15:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/06	16:03:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/06	16:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/01/06	17:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/06	17:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	17:16:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/06	17:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/06	17:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/06	17:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/01/06	17:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/06	17:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	17:56:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	17:56:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/01/06	17:56:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/01/06	17:59:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/01/06	17:59:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/01/06	17:59:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/06	17:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/01/06	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/06	18:06:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	18:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/06	18:06:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/01/06	18:06:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 11 ca ac 73
2016/01/06	18:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/01/06	18:06:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/01/06	18:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/01/06	18:06:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/06	18:09:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/06	18:09:28.0	XRT_FL_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d

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2016/01/06	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/06	18:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/06	18:53:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/06	18:53:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/06	18:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/06	19:16:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/06	19:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/06	20:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/06	20:30:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/06	20:30:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/06	20:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/06	20:53:30.0	XRT_Custom_430_OG [0x1ae]					
2016/01/06	20:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/06	22:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/06	22:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/06	22:07:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/06	22:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/06	22:29:30.0	XRT_Custom_430_OG [0x1ae]					
2016/01/06	22:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/06	23:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/06	23:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/06	23:44:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/06	23:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/06	23:55:30.0	XRT_Custom_430_OG [0x1ae]					
2016/01/06	23:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/06	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/06	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/06	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/01/07	00:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCS_Ore-point_Start_6_OG [0x09c]	5	02-76	00 54 18 01 68	
2016/01/07	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/07	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/07	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/07	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/07	00:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/07	00:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2016/01/07	00:02:58.0	XRT_FL_PROG_SET_425_OG [0x1a9]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2016/01/07	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/07	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/07	01:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/07	01:11:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/07	01:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/07	01:27:30.0	XRT_Custom_430_OG [0x1ae]					
2016/01/07	01:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/07	02:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/07	02:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/07	02:43:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/07	02:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/07	03:04:00.5	XRT_Custom_430_OG [0x1ae]					
2016/01/07	03:05:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2016/01/07	04:12:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	04:12:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/07	04:12:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/07	04:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/07	04:41:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/07	04:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/07	05:52:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	05:52:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/07	05:52:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/07	05:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/07	06:19:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/07	06:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/07	06:28:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	06:28:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	06:28:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/07	06:29:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/07	06:29:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/07	06:31:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/07	06:31:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/07	06:31:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/07	06:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/07	06:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	06:38:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	06:38:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/07	06:39:00.0	AOCS_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 11 ca ac 73				
2016/01/07	06:39:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/07	06:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/07	06:39:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/07	06:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/07	06:39:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/07	06:41:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/01/07	06:41:58.0	XRT_FL_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2016/01/07	06:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/07	07:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	07:33:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/07	07:33:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/07	07:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/07	07:56:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/07	07:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/07	09:12:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	09:12:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/07	09:12:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/07	09:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/07	09:31:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/07	09:32:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/07	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/07	10:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/07	11:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]							

2016/01/07	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01	00	00	00	00
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/01/07	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2016/01/07	11:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2016/01/07	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/01/07	11:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/01/07	11:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0d			
2016/01/07	11:02:58.0	XRT_FL_PROG_SET_425_OG [0x1a9]								
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	08			
2016/01/07	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/01/07	14:40:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/01/07	14:40:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/01/07	14:40:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/01/07	14:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/01/07	14:50:30.0	XRT_Custom_430_OG [0x1ae]								
2016/01/07	14:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/01/07	16:14:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/01/07	16:14:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/01/07	16:14:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/01/07	16:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/01/07	16:38:00.0	XRT_Custom_430_OG [0x1ae]								
2016/01/07	16:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/01/07	17:51:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/01/07	17:51:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/01/07	17:51:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/01/07	17:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/01/07	18:14:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/01/07	18:14:26.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/01/07	18:14:28.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2016/01/07	18:14:30.0	AOCS_Ore-point_Start_2_OG [0x098]								
		AOCU_NM		5	02-76	00	00	00	00	00
2016/01/07	18:14:48.0	XRT_FLD_DIS_406_OG [0x196]								
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2016/01/07	18:17:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2016/01/07	18:17:26.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/01/07	18:17:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	02			
2016/01/07	18:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/01/07	18:24:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/01/07	18:24:26.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/01/07	18:24:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2016/01/07	18:24:30.0	AOCS_Ore-point_Start_7_OG [0x09d]								
		AOCU_NM		5	02-76	03	00	00	00	00
2016/01/07	18:24:48.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/01/07	18:24:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2016/01/07	18:24:52.0	XRT_AEC_RESET_448_OG [0x1c0]								
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2016/01/07	18:24:54.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/01/07	18:24:56.0	XRT_FLD_RESET_433_OG [0x1b1]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/01/07	18:27:26.0	XRT_QT_PROG_SET_438_OG [0x1b6]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0e			
2016/01/07	18:27:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]								
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0c			
2016/01/07	18:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/01/07	19:28:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/01/07	19:28:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				

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2016/01/07	19:28:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/07	19:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/07	19:51:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/07	19:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/07	21:05:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/07	21:05:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/07	21:05:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/07	21:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/07	21:28:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/07	21:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/07	22:42:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/07	22:42:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/07	22:42:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/07	22:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/07	23:03:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/07	23:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	00:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	00:20:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	00:20:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	00:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	00:27:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/08	00:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	01:43:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	01:43:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	01:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	02:02:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/08	02:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	03:18:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	03:18:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	03:18:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	03:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	03:39:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/08	03:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	04:48:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	04:48:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	04:48:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	04:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	05:17:00.0	XRT_Custom_430_OG [0x1ae]			
2016/01/08	05:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/01/08	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2016/01/08	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/01/08	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/01/08	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/08	06:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/01/08	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2016/01/08	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/01/08	06:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	03 00 00 00 00
2016/01/08	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/01/08	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/01/08	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/01/08	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/08	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	06:12:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2016/01/08	06:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2016/01/08	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	06:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	06:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	06:29:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	06:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	06:54:00.0	XRT_Custom_430_OG [0x1ae]				
2016/01/08	06:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	08:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	08:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	08:09:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	08:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	08:31:00.0	XRT_Custom_430_OG [0x1ae]				
2016/01/08	08:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	09:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	09:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	09:49:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	09:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	10:04:30.0	XRT_Custom_430_OG [0x1ae]				
2016/01/08	10:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	15:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	15:13:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	15:13:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	15:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	15:27:00.0	XRT_Custom_430_OG [0x1ae]				
2016/01/08	15:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	15:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	15:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	15:33:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	15:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	15:36:30.0	XRT_Custom_430_OG [0x1ae]				
2016/01/08	15:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	16:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	16:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/08	16:49:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/08	16:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/08	17:13:00.0	XRT_Custom_430_OG [0x1ae]				
2016/01/08	17:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/08	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/08	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]				

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2016/01/08	18:00:00.0	AOCS_Or-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2016/01/08	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/01/08	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/01/08	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/01/08	18:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2016/01/08	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/08	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/08	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/08	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/01/08	18:10:00.0	AOCS_Or-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	03	00	00	00
2016/01/08	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/01/08	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/01/08	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/01/08	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/01/08	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/08	18:12:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2016/01/08	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0c		
2016/01/08	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/08	18:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/08	18:26:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/08	18:26:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/08	18:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/08	18:49:30.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2016/01/08	18:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/08	20:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/08	20:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/08	20:03:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/08	20:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/08	20:27:00.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2016/01/08	20:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/08	21:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/08	21:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/08	21:40:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/08	21:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/08	22:03:00.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2016/01/08	22:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/08	23:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/08	23:18:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/08	23:18:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/08	23:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/08	23:33:00.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2016/01/08	23:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/09	00:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/09	00:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/09	00:49:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/09	00:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/09	01:01:30.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2016/01/09	01:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/09	02:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_MANU_400_OG [0x190]						

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2016/01/09	02:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	02:17:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	02:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/09	02:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/09	02:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	03:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	03:45:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	03:45:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/09	03:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/09	04:14:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	04:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	05:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	05:25:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	05:25:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/09	05:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/09	05:52:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	05:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	06:01:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	06:01:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	06:01:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/09	06:02:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/09	06:02:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/09	06:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/09	06:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/09	06:04:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/09	06:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	06:11:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	06:11:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	06:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/09	06:12:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	03 00 00 00 00				
2016/01/09	06:12:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/09	06:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/09	06:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/09	06:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/09	06:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	06:14:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2016/01/09	06:14:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2016/01/09	06:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	07:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	07:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	07:05:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/09	07:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/09	07:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	07:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	08:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	08:45:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	08:45:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/09	08:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2016/01/09	09:05:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/09	09:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	10:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	10:26:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	10:26:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/09	10:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/09	10:35:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/09	10:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	14:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	14:15:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	14:15:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/09	14:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/09	14:21:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/09	14:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	15:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	15:48:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	15:48:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/09	15:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/09	16:11:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/09	16:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	17:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	17:24:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	17:24:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/09	17:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/09	17:48:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/09	17:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	18:03:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	18:03:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/09	18:04:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/09	18:04:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/09	18:06:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/09	18:06:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/09	18:06:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/09	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	18:13:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	18:13:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	18:13:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/09	18:14:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/01/09	18:14:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/09	18:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/09	18:14:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/09	18:14:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/09	18:14:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/09	18:16:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2016/01/09	18:16:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2016/01/09	18:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/09	19:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/09	19:01:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2016/01/09	19:01:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/09	19:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/09	19:24:30.5	XRT_Custom_430_OG [0x1ae]			
2016/01/09	19:25:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/09	20:38:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/09	20:38:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/09	20:38:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/09	20:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/09	21:02:00.0	XRT_Custom_430_OG [0x1ae]			
2016/01/09	21:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/09	22:16:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/09	22:16:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/09	22:16:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/09	22:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/09	22:37:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/09	22:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/09	23:53:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/09	23:53:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/09	23:53:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/09	23:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/10	00:03:00.0	XRT_Custom_430_OG [0x1ae]			
2016/01/10	00:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/10	01:18:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	01:18:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/10	01:18:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/10	01:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/10	01:36:00.0	XRT_Custom_430_OG [0x1ae]			
2016/01/10	01:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/10	02:52:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	02:52:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/10	02:52:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/10	02:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/10	03:12:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/10	03:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/10	04:21:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	04:21:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/10	04:21:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/10	04:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/10	04:50:00.0	XRT_Custom_430_OG [0x1ae]			
2016/01/10	04:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/10	05:33:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	05:33:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	05:33:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/01/10	05:34:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2016/01/10	05:34:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/01/10	05:36:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/01/10	05:36:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/10	05:36:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/01/10	05:37:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/10	05:43:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2016/01/10	05:43:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/10	05:43:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/10	05:44:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/01/10	05:44:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/10	05:44:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/10	05:44:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/10	05:44:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/10	05:44:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/10	05:46:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2016/01/10	05:46:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2016/01/10	05:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/10	06:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/10	06:01:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/10	06:01:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/10	06:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/10	06:27:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/10	06:28:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/10	07:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/10	07:41:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/10	07:41:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/10	07:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/10	08:04:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/10	08:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/10	09:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/10	09:21:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/10	09:21:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/10	09:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/10	09:39:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/10	09:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/10	14:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/10	14:48:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/10	14:48:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/10	14:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/10	14:59:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/10	15:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/10	16:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/10	16:22:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/10	16:22:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/10	16:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/10	16:46:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/10	16:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/10	17:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/10	17:59:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/10	17:59:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/10	18:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/10	18:22:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/10	18:22:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/10	18:22:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/10	18:23:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				

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2016/01/10	18:23:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/01/10	18:25:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/01/10	18:25:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/10	18:25:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/01/10	18:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/10	18:32:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	18:32:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	18:32:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/01/10	18:33:00.0	AOCS_ORe-point_Start_7_OG [0x09d]			
		AOCU_NM	5	02-76	03 00 00 00 00
2016/01/10	18:33:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/01/10	18:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/01/10	18:33:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/01/10	18:33:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/10	18:33:26.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/10	18:35:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2016/01/10	18:35:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2016/01/10	18:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/10	19:36:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	19:36:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/10	19:36:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/10	19:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/10	20:00:00.0	XRT_Custom_430_OG [0x1ae]			
2016/01/10	20:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/10	21:14:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	21:14:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/10	21:14:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/10	21:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/10	21:37:00.0	XRT_Custom_430_OG [0x1ae]			
2016/01/10	21:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/10	22:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/10	22:51:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/10	22:51:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/10	22:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/10	23:11:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/10	23:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/11	00:28:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/11	00:28:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/11	00:28:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/11	00:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/11	00:35:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/11	00:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/11	01:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/11	01:51:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/11	01:51:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/11	01:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/11	02:11:00.0	XRT_Custom_430_OG [0x1ae]			
2016/01/11	02:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/11	03:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/11	03:26:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da

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2016/01/11	03:26:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	03:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/11	03:48:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/11	03:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	04:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	04:57:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	04:57:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	05:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/11	05:25:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/11	05:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	05:53:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	05:53:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	05:53:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/11	05:54:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/11	05:54:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/11	05:56:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/11	05:56:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/11	05:56:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/11	05:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	06:03:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	06:03:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	06:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/11	06:04:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/01/11	06:04:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/11	06:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/11	06:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/11	06:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/11	06:04:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	06:06:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2016/01/11	06:06:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2016/01/11	06:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	06:38:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	06:38:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	06:38:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	06:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/11	07:02:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/11	07:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	08:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	08:17:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	08:17:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	08:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/11	08:39:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/11	08:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	09:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	09:58:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	09:58:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	10:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/11	10:12:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/11	10:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2016/01/11	15:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	15:22:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	15:22:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	15:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/11	15:36:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/11	15:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	15:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	15:39:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	15:39:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	15:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/11	15:45:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/11	15:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	16:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	16:57:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	16:57:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	17:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/11	17:21:30.0	XRT_Custom_430_OG [0x1ae]							
2016/01/11	17:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/11	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/11	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/11	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/11	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/11	18:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/01/11	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/11	18:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/01/11	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/11	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/11	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/11	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/11	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	18:12:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2016/01/11	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2016/01/11	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	18:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	18:35:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	18:35:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	18:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/11	18:58:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/11	18:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/11	20:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/11	20:12:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/11	20:12:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/11	20:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2016/01/11	20:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/11	20:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/01/11	21:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/11	21:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/11	21:49:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/11	21:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/11	22:11:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/11	22:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/01/11	23:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/11	23:26:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/11	23:26:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/11	23:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/11	23:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/11	23:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/01/12	00:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/12	00:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/12	00:55:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/12	00:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/12	01:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/12	01:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/01/12	02:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/12	02:25:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/12	02:25:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/12	02:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/12	02:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/12	02:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/01/12	03:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/12	03:53:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/12	03:53:34.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/12	03:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/12	04:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/12	04:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/01/12	05:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/12	05:34:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/12	05:34:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/12	05:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/12	06:00:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/12	06:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/01/12	06:10:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/12	06:10:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/12	06:10:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/12	06:10:30.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/01/12	06:10:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/01/12	06:13:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/01/12	06:13:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/01/12	06:13:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/01/12	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/01/12	06:20:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/12	06:20:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/12	06:20:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2016/01/12	06:20:30.0	AOCS_ORe-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			AOCU_NM	5	02-76	03	00	00	00
2016/01/12	06:20:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/01/12	06:20:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/01/12	06:20:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/01/12	06:20:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/01/12	06:20:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/12	06:23:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2016/01/12	06:23:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0c		
2016/01/12	06:23:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/12	07:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/12	07:14:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/12	07:14:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/12	07:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/12	07:38:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/12	07:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/12	08:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/12	08:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/01/12	08:54:04.0	XRT_PREFLR_STRT_416_OG [0x1a0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/01/12	08:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/01/12	09:14:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/12	09:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/01/12	10:02:30.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/01/12	10:33:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00