

XRT Timeline to be uploaded on 2016/01/12

Period: 2016/01/12 10:23:00 - 2016/01/16 10:04:00

* * * * *

Normal mode

* * * * *

XOB #1AFD: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (3ms/3ms)

Term	Pointing (x, y)	Comment
01/12 10:43:00 - 01/12 10:59:54	Track (-5.3, 343.5) @ 01/12 10:33:00	# OP start + 10min/AR12483
01/12 15:13:00 - 01/12 17:33:00	Track (36.2, 343.7) @ 01/12 15:10:00	track AR12483
01/12 18:11:30 - 01/13 05:42:24	Track (63.0, 343.8) @ 01/12 18:08:30	tracking AR12483
01/13 09:03:00 - 01/13 17:44:24	Track (195.2, 343.4) @ 01/13 09:00:00	tracking AR12483
01/13 17:57:30 - 01/14 05:59:54	Track (272.8, 342.5) @ 01/13 17:54:30	track AR12483
01/14 06:13:00 - 01/14 10:02:00	Track (376.1, 340.3) @ 01/14 06:10:00	track AR12483

PROG= 18 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band open	Safe Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
Open/G-band	Open/G-band close	Safe Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
Seqn= 42	3-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open close	Safe Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Al close	Safe Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
Seqn= 32	30-time(s)	120.0sec										
thin-Be/Open	med-Be/Open close	Safe Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	34.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	34.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1AC2: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 120s cad

Term	Pointing (x, y)	Comment
01/12 11:03:00 - 01/12 11:15:54	Fixed (936.0, 0.0)	HOP130/01
01/12 11:19:00 - 01/12 11:31:54	Fixed (468.0, 0.0)	HOP130/02
01/12 11:51:00 - 01/12 12:03:54	Fixed (-468.0, 0.0)	HOP130/04
01/12 12:07:00 - 01/12 12:19:54	Fixed (-936.0, 0.0)	HOP130/05
01/12 12:23:00 - 01/12 12:29:54	Fixed (815.0, 470.0)	HOP130/06
01/12 12:33:00 - 01/12 12:45:54	Fixed (411.0, 847.0)	HOP130/07
01/12 12:49:00 - 01/12 13:01:54	Fixed (0.0, 941.0)	HOP130/08
01/12 13:05:00 - 01/12 13:17:54	Fixed (-411.0, 847.0)	HOP130/09
01/12 13:21:00 - 01/12 13:27:54	Fixed (-815.0, 470.0)	HOP130/10
01/12 13:31:00 - 01/12 13:37:54	Fixed (815.0, -470.0)	HOP130/11
01/12 13:41:00 - 01/12 13:53:54	Fixed (411.0, -847.0)	HOP130/12
01/12 13:57:00 - 01/12 14:23:00	Fixed (0.0, -941.0)	HOP130/13
01/12 14:40:00 - 01/12 14:52:54	Fixed (-411.0, -847.0)	HOP130/14
01/12 14:56:00 - 01/12 15:09:54	Fixed (-815.0, -471.0)	HOP130/15
01/13 05:55:30 - 01/13 08:59:54	Fixed (655.0, 655.0)	Spectral atlas of off-limb by EIS

PROG= 03 Inf.-time(s)

Subr= 1	45-time(s)	120.0sec									
Seqn= 29	1-time(s)	4.0sec									
thin-Be/Open	med-Be/Open close	Safe Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec	
Subr= 2	1-time(s)	2.0sec									
Seqn= 26	1-time(s)	2.0sec									
Open/G-band	Open/G-band open	Safe Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band close	Safe Norm	1ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B0B: HOP130-Synoptic 3 Filter- 2x2 Q95 (Al/mesh 12/128/723ms; Thin-Be 88/1024/5795ms; Al/thick 65536ms) with G-band (3ms)

Term	Pointing (x, y)	Comment
01/12 11:35:00 - 01/12 11:47:54	Fixed (0.0, 0.0)	HOP13/03

PROG= 16 2-time(s)

Subr= 1	1-time(s)	180.0sec									
Seqn= 68	1-time(s)	25.0sec									
Open/Al-mesh	Open/Al-mesh close	Safe Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh close	Safe Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh close	Safe Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 77	1-time(s)	25.0sec									
thin-Be/Open	thin-Be/Open close	Safe Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open close	Safe Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open close	Safe Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 58	1-time(s)	2.0sec									
Open/thick-Al	Open/thick-Al close	Safe Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 54	1-time(s)	2.0sec									
Open/G-band	Open/G-band open	Safe Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band close	Safe Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	

XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T

Term	Pointing (x, y)	Comment
01/12 18:01:30 - 01/12 18:08:24	Fixed (0.0, 0.0)	synoptic, shifted -1.5 min
01/13 05:45:30 - 01/13 05:52:24	Fixed (0.0, 0.0)	synoptic, shifted -17.5 min
01/13 17:47:30 - 01/13 17:54:24	Fixed (0.0, 0.0)	synoptic, shifted -15.5 min
01/14 06:03:00 - 01/14 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 04 1-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 91	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 93	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 77	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

* * * * *

Flare mode

* * * * *

XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
01/12 10:43:00 - 01/12 10:59:54	Track (-5.3, 343.5) @ 01/12 10:33:00	# OP start + 10min/AR12483
01/12 11:03:00 - 01/12 11:15:54	Fixed (936.0, 0.0)	HOP130/01
01/12 11:19:00 - 01/12 11:31:54	Fixed (468.0, 0.0)	HOP130/02
01/12 11:51:00 - 01/12 12:03:54	Fixed (-468.0, 0.0)	HOP130/04
01/12 12:07:00 - 01/12 12:19:54	Fixed (-936.0, 0.0)	HOP130/05
01/12 12:23:00 - 01/12 12:29:54	Fixed (815.0, 470.0)	HOP130/06
01/12 12:33:00 - 01/12 12:45:54	Fixed (411.0, 847.0)	HOP130/07
01/12 12:49:00 - 01/12 13:01:54	Fixed (0.0, 941.0)	HOP130/08
01/12 13:05:00 - 01/12 13:17:54	Fixed (-411.0, 847.0)	HOP130/09
01/12 13:21:00 - 01/12 13:27:54	Fixed (-815.0, 470.0)	HOP130/10
01/12 13:31:00 - 01/12 13:37:54	Fixed (815.0, -470.0)	HOP130/11
01/12 13:41:00 - 01/12 13:53:54	Fixed (411.0, -847.0)	HOP130/12
01/12 13:57:00 - 01/12 14:23:00	Fixed (0.0, -941.0)	HOP130/13
01/12 14:40:00 - 01/12 14:52:54	Fixed (-411.0, -847.0)	HOP130/14
01/12 14:56:00 - 01/12 15:09:54	Fixed (-815.0, -471.0)	HOP130/15
01/12 15:13:00 - 01/12 17:33:00	Track (36.2, 343.7) @ 01/12 15:10:00	track AR12483
01/12 18:11:30 - 01/13 05:42:24	Track (63.0, 343.8) @ 01/12 18:08:30	tracking AR12483
01/13 05:55:30 - 01/13 08:59:54	Fixed (655.0, 655.0)	Spectral atlas of off-limb by EIS
01/13 09:03:00 - 01/13 17:44:24	Track (195.2, 343.4) @ 01/13 09:00:00	tracking AR12483
01/13 17:57:30 - 01/14 05:59:54	Track (272.8, 342.5) @ 01/13 17:54:30	track AR12483
01/14 06:13:00 - 01/14 10:02:00	Track (376.1, 340.3) @ 01/14 06:10:00	track AR12483

PROG= 07 30-time(s)

Subr= 1	20-time(s)	2.0sec										
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100	1-time(s)	10.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

* * * * *

Active Region Search

* * * * *

NOT USED

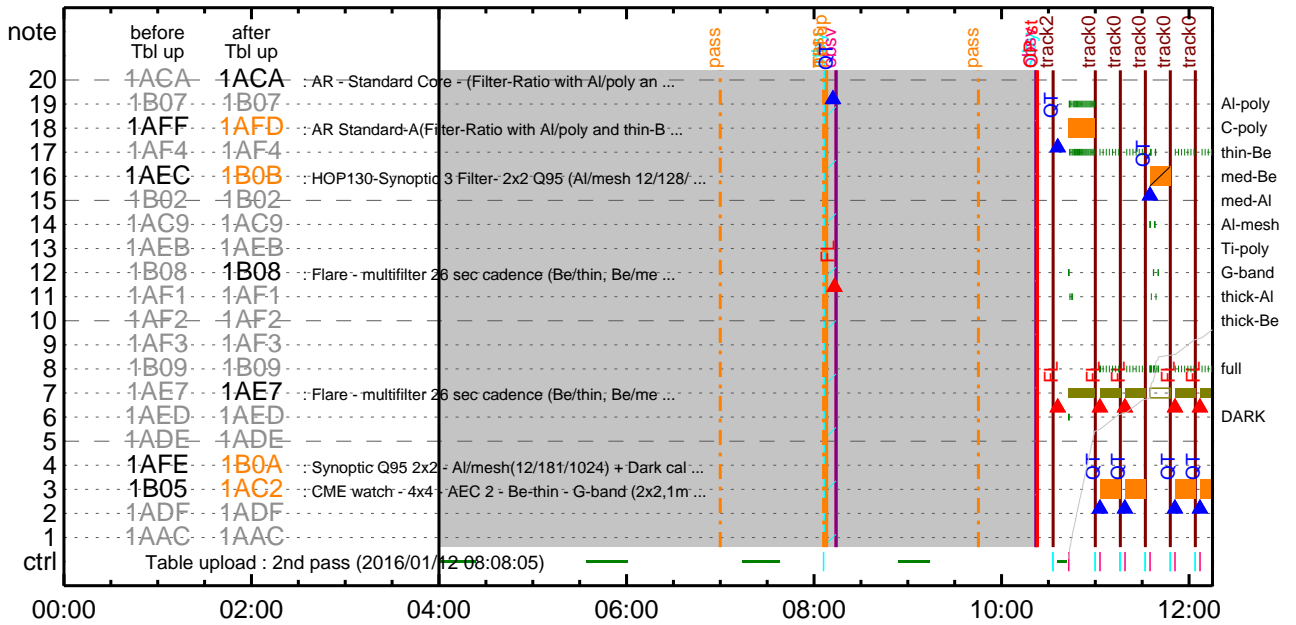
* * * * *

Flare Detection

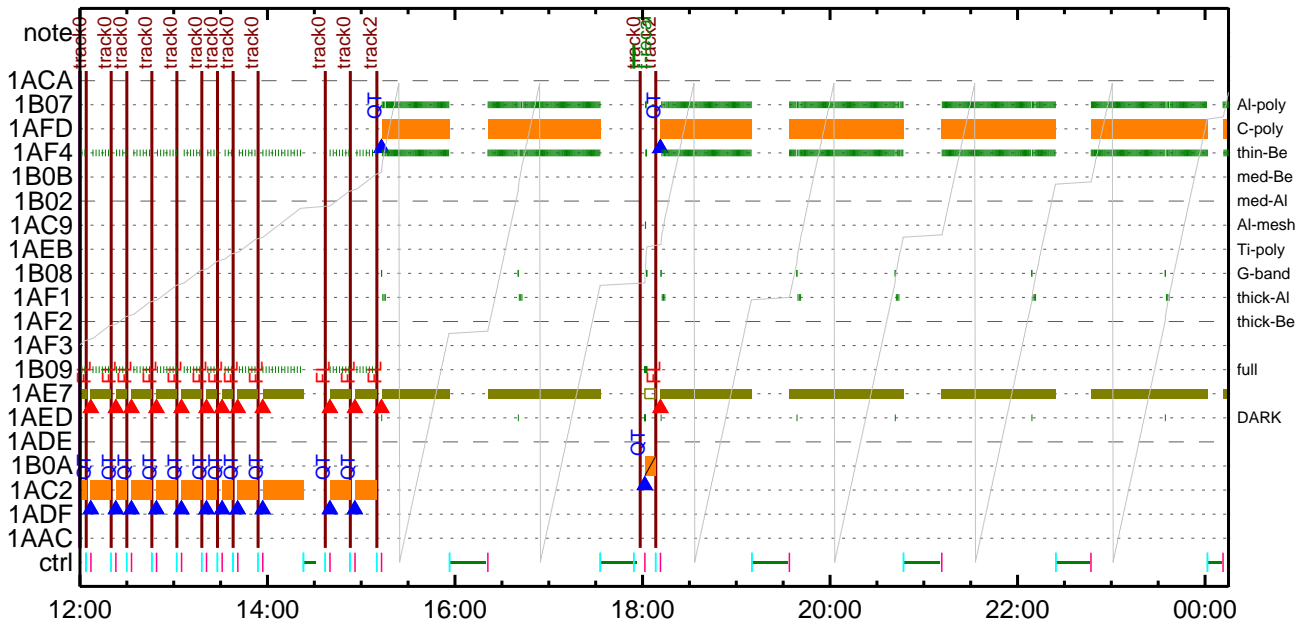
* * * * *

FLD Patrol										
Term		Pointing (x, y)					Comment			
01/12 11:48:18 - 01/12 17:58:48		Fixed (-468.0, 0.0)					HOP130/04			
01/12 18:08:48 - 01/13 05:42:48		Track (63.0, 343.8) @ 01/12 18:08:30					tracking AR12483			
01/13 05:52:48 - 01/13 17:44:48		Fixed (655.0, 655.0)					Spectral atlas of off-limb by EIS			
01/13 17:54:48 - 01/14 06:00:18		Track (272.8, 342.5) @ 01/13 17:54:30					track AR12483			
01/14 06:10:18 - 01/16 10:04:00		Track (376.1, 340.3) @ 01/14 06:10:00					track AR12483			
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

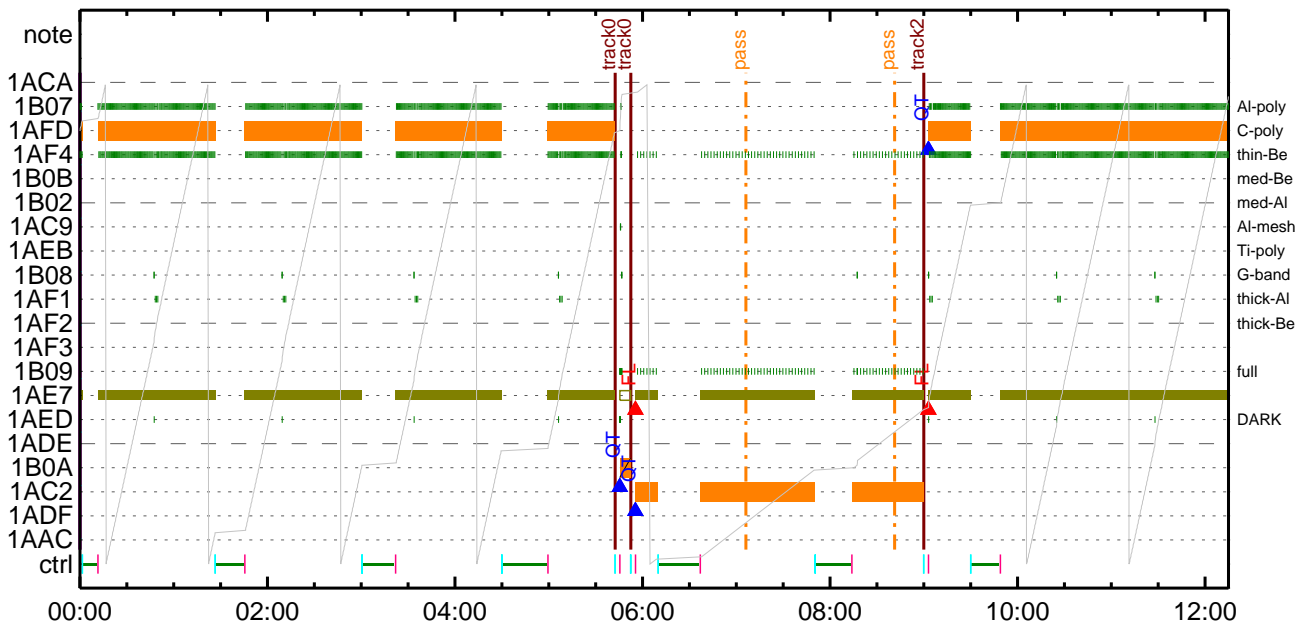
CMDI #0640 2016/01/12



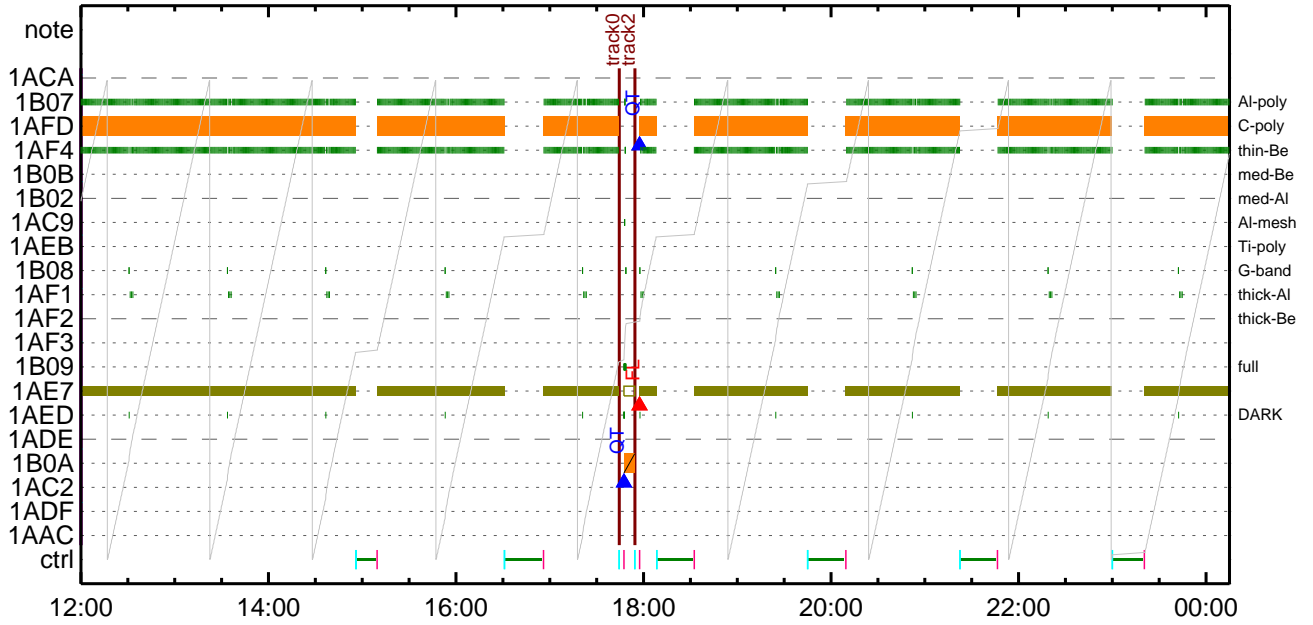
CMDI #0640 2016/01/12



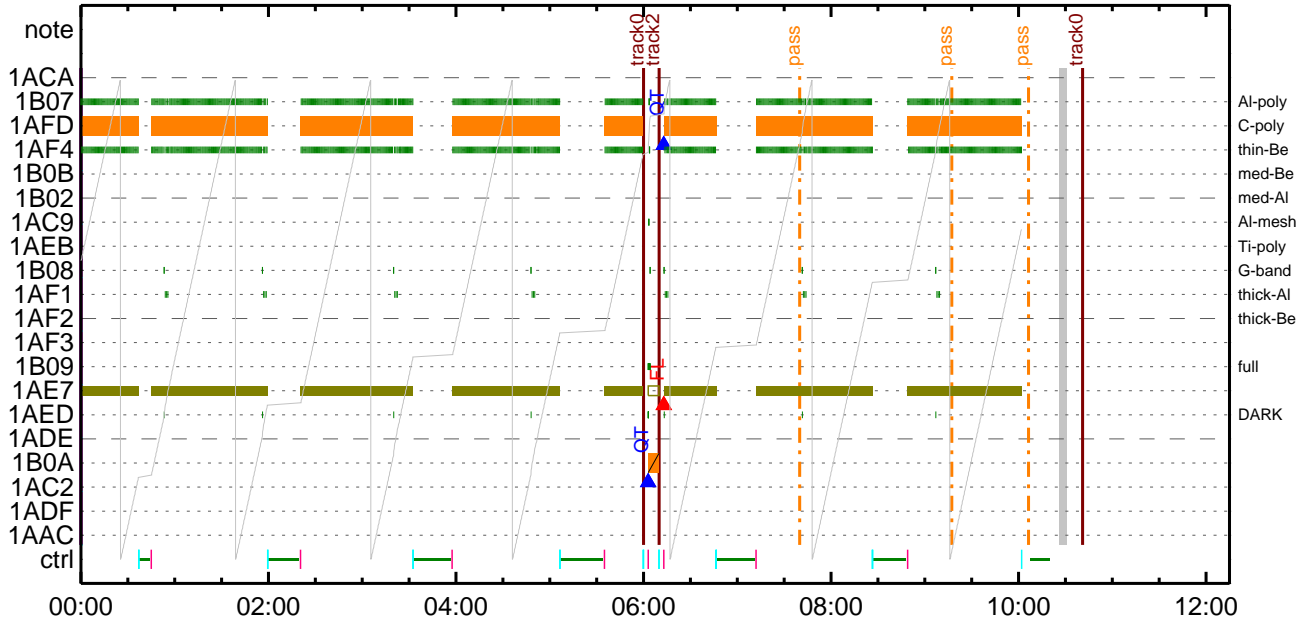
CMDI #0640 2016/01/13



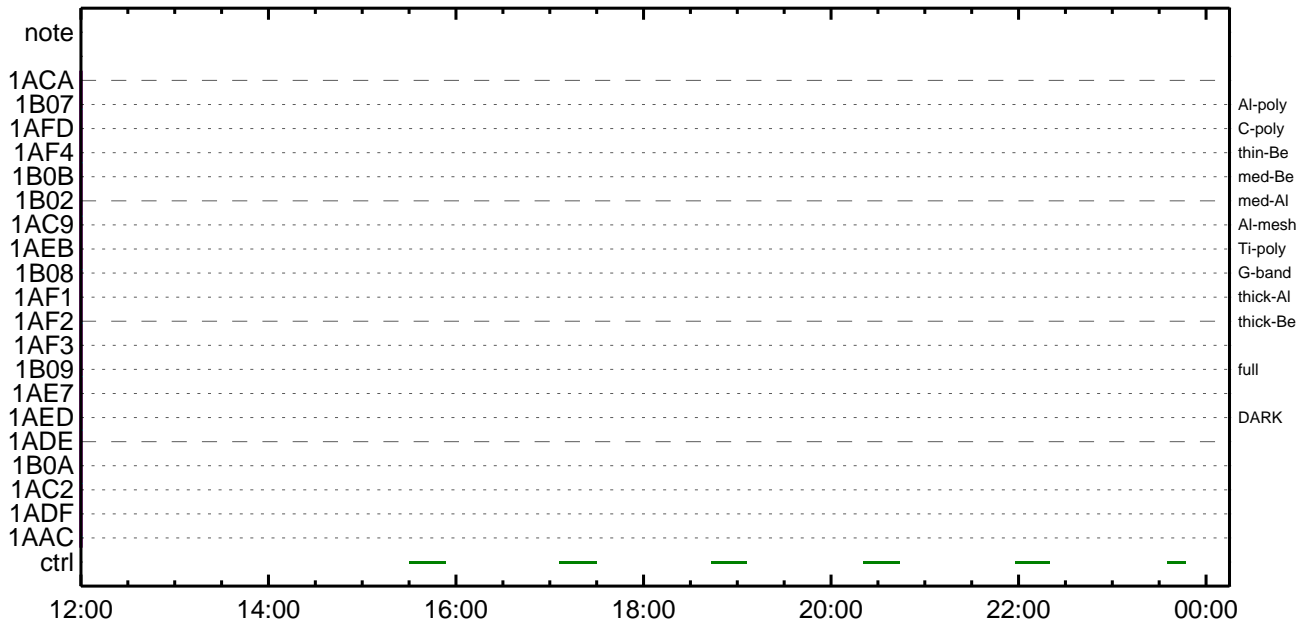
CMDI #0640 2016/01/13



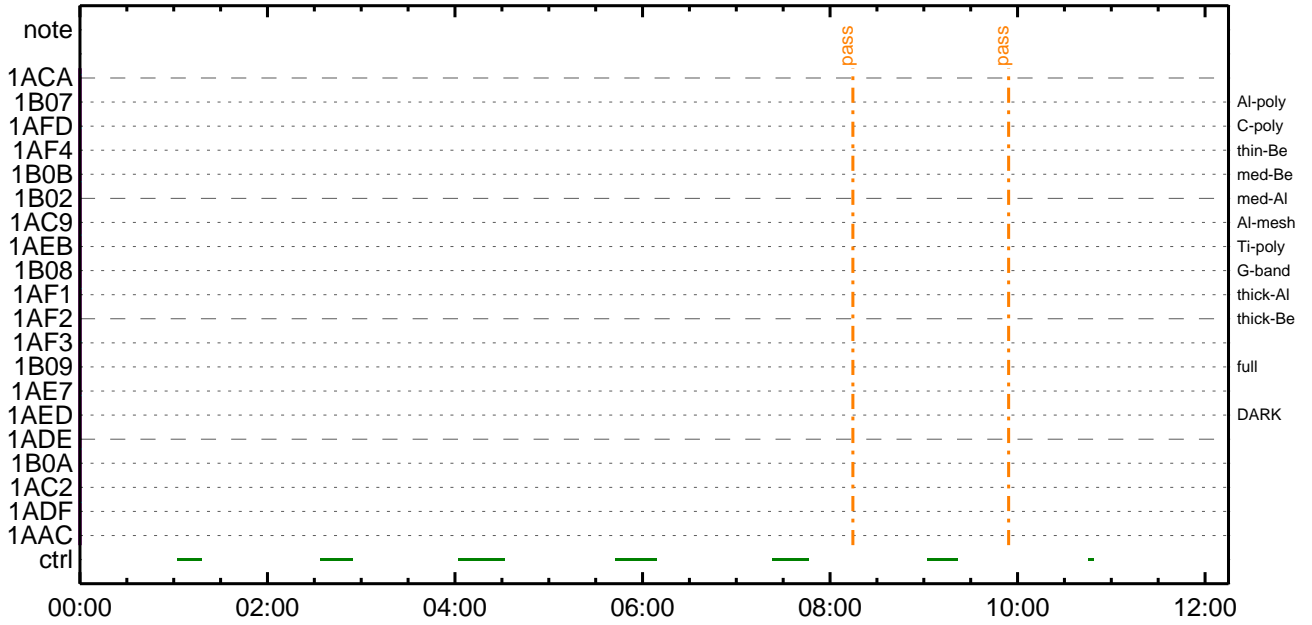
CMDI #0640 2016/01/14



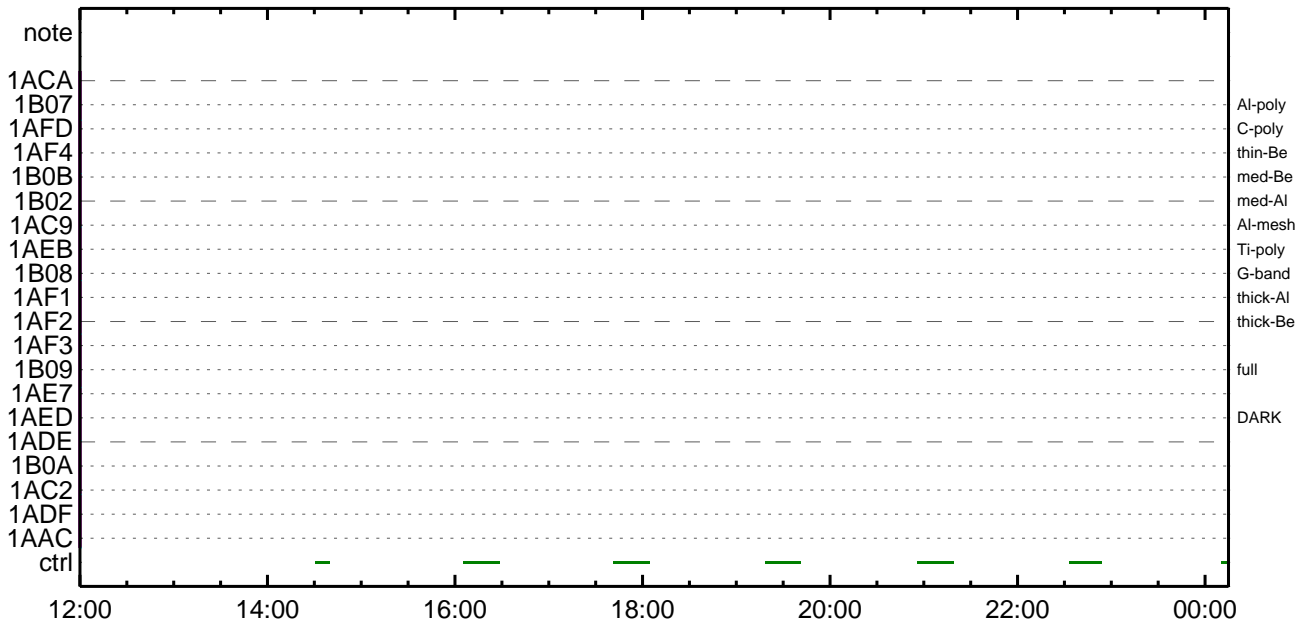
CMDI #0640 2016/01/14



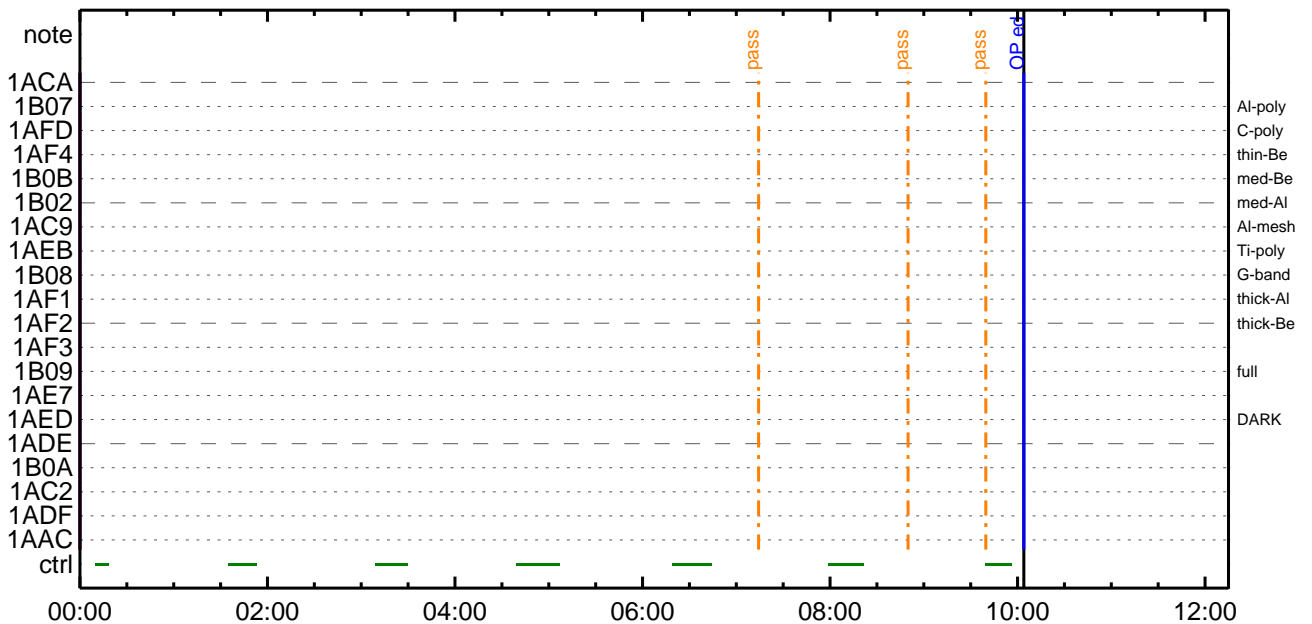
CMDI #0640 2016/01/15



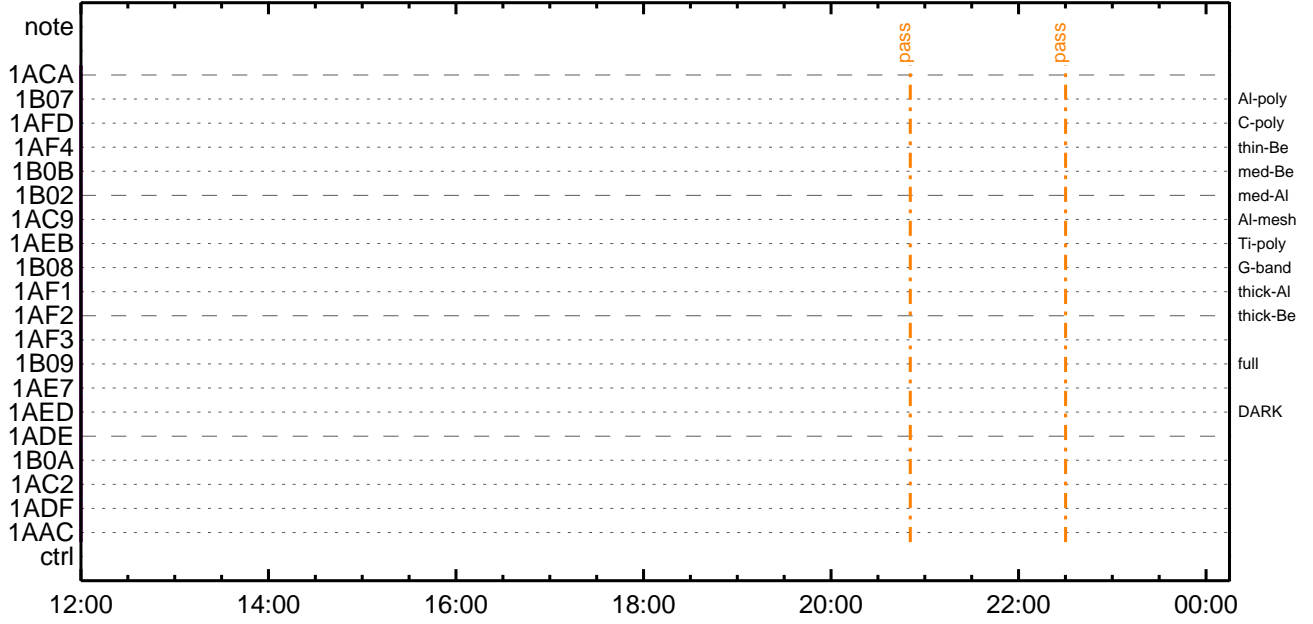
CMDI #0640 2016/01/15



CMDI #0640 2016/01/16



CMDI #0640 2016/01/16




```

0096 C.          0303; SET 03EDUMP 03I 0303 0303 0303 0303 0303 0303
0097 C.
0098 C. TI 0303 0303 0303 0303 0303 (UT)
0099 +. TI 2016-01-12 10:18:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          03[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2016-01-12 10:18:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          03[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2016-01-12 10:18:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          03[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2016-01-12 10:22:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          03[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C.          0303 0303 0303 0303 0303 0303 0303 0303
0116 C.          03[HK1_TI_CMD_ENA/DIS]          EQ          ENA
0117 C.          03[HK1_TI_CMD_NUM]          EQ          4
0118 C.          03[HK1_NEXT_EXEC_PIM]          EQ          DHU
0119 C.          03[HK1_NEXT_EXEC_DC]          EQ          0xB3
0120 C.
0121 C. *****
0122 C. TI 0303 0303 0303
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; $ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          03[HK1_DMP_TOP_ADRS_1]          EQ          07
0129 C.          03[HK1_DMP_TOP_ADRS_0]          EQ          2B
0130 C.          03[HK1_DMP_BLOCK_NUM]          EQ          3
0131 C.          03[HK1_DMP_REPEAT_NUM]          EQ          0
0132 C.          03[HK1_DMA_DMP_PIM]          EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          03[HK1_PKT_FORM_NO]          EQ          7
0136 C.          03[HK1_PKT_GEN_TIME]          EQ          0.25 s
0137 C.          03[HK1_S_TLM_BIT_RATE]          EQ          32k
0138 C.          03[HK1_X_TLM_BIT_RATE]          EQ          4M
0139 C.          03[HK1_DMP_CHK_FLG]          EQ          EXEC
0140 C.
0141 C.          0303 0303 0303 0303 0303
0142 C.          03[HK1_DMP_CHK_FLG]          EQ          NON
0143 C.
0144 C. RAM ID=TI_TBL 0303 0303 0303 0303 0303
0145 C.
0146 C. DHU 0303 0303 0303 0303 0303 0303 0303 0303
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          03[HK1_PKT_FORM_NO]          EQ          2
0150 C.          03[HK1_PKT_GEN_TIME]          EQ          0.5S
0151 C.          03[HK1_S_TLM_BIT_RATE]          EQ          32K
0152 C.          03[HK1_X_TLM_BIT_RATE]          EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2016-01-12 10:22:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 C. -----
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2016-01-12 10:22:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2016-01-12 10:22:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2016-01-12 10:22:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 0303 0303 0303 0303 0303 *****
0192 C. (0303 0303 0303 0303 0303 0303 0303 0303)
0193 C. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 8002876.6 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 C. ***** XRT START *****
0140 C.
0141 +. DC 07-F0 MDP_XRT_CTRL_MANU
0142 BC (c1)
0143 + DC 07-F0 MDP_XRT_MODE_STBY
0144 BC (c3)
0145 . C. ----- Success Verify ? OK / NG_____
0146 C.
0147 C. XRT Obs. Table Upload
0148 . S. RAM ram-291:MDP_OBS_X
0149 ( )
0150 C.
0151 +. DC 07-F0 MDP_DUMP_XRTTBL
0152 BC (84 07 00 00 00 3a d4)
0153 . C. ----- Comparison Check ? OK / ERR _____
0154 C.
0155 C.
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 01 b1 b1 04 04)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 02 b1 b1 08 08)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 03 b1 b1 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 04 b1 b1 06 06)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 05 85 83 06 06)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 06 85 83 06 06)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 07 85 83 08 08)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 08 80 80 20 20)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 09 80 80 20 08)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 0a 80 80 08 20)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 0f 80 80 06 06)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 10 80 80 08 08)
0180 + DC 07-F0 MDP_XRT_FLD_ENA
0181 BC (d8)
0182 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0183 BC (c8)
0184 + DC 07-F0 MDP_XRT_AEC_RESET
0185 BC (d0)
0186 + DC 07-F0 MDP_XRT_ARS_DIS
0187 BC (d5)
0188 + DC 07-F0 MDP_XRT_FLD_RESET
0189 BC (da)
0190 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0191 BC (c4 14)
0192 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0193 BC (c5 0c)

```

```
0194 . C. ----- Success Verify ?           OK / NG ____
0195 . C.
0196 . C.
0197 . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0198 . C.
0199 +. DC 07-F0 MDP_XRT_MODE_OBSV
0200 . BC      (c2)
0201 +. TI 2016-01-12 10:22:02.0
0202 . DC 07-F0 MDP_XRT_MODE_OBSV
0203 . BC      (c2)
0204 . C. ----- Success Verify ?           OK / NG ____
0205 . C.
0206 . C. ***** XRT END *****
0207 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0208 +. DC 07-FC EIS_MODE_CHG_ENA
0209 . BC      (20)
0210 . C. Verify EIS_MODE_CHG_FLG is ENA
0211 +. DC 07-FC EIS_MODE_MANU
0212 . BC      (21 02)
0213 . C. Verify EIS in MANUAL mode
0214 . C. Estimated OBSTBL upload time is 40s
0215 . C. *****
0216 . C. EIS START OBSTBL LOAD
0217 . C. *****
0218 . S. RAM   ram-820:EIS_OBSTBL
0219 . ( )
0220 +. DC 07-FC EIS_DUMP_OBSTBL
0221 . BC      (07 07 07 00 00 70 00)
0222 . C.
0223 . C. Execute, after the success of OBSTBL upload.
0224 . C. Set EIS TI-commands
0225 +. TI 2016-01-12 10:22:50.0
0226 . DC 07-FC EIS_MODE_CHG_ENA
0227 . BC      (20)
0228 . C.                               [      ] [HK1_TI_CMD_NUM]   EQ   1 COUNTUP
0229 . C. *****
0230 . C. EIS END OBSTBL LOAD
0231 . C. *****
0232 . C.
0233 . C. ***** MDP `úÃîï»ö¼ÝðĒÂð¹ñēDCBC•x²è *****
0234 . C. (¼á°îÿÓŸÃŸĒŸPŸĒŸáŸçŸēñĒ¼ñ¼Ā»Ūñ¹ē)
0235 . S. DC-BC dcbc-402:DCBC
0236 . (MDP_known_event)
0237 . C.
0238 . C.
0239 . C. ***** ŸDŸ¹•Ī Daily±¿ĪññĒ`Øñ¹ñēDCBC•x²è *****
0240 . S. DC-BC dcbc-153:DCBC
0241 . (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0242 . C.
0243 . C.
0244 . C. ;ãLOSŸÁŸŸŸĀŸ~¼Ā»Ū;ã
0245 . C.
0246 . C. ***** LOS *****
0247 . C.
```



```

0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload FG Observation Table>
0109 . S. RAM ram-262:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 C. <Upload SP Observation Table>
0127 . S. RAM ram-281:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¥çYÃ¥×¥í;¥ÉñÊ°ñÊSTS_CHKñðOFFñÊñ¹ñë
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHKñðONñÊñ¹ñë
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700[ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2016-01-12 10:22:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** MDP `úÃîñÊ»ö¼YñÊñ¹ñëDCBC•x²è *****
0174 C. (¼ã°îYóYÃYÉY¥YÉYá¥çYÉñÊñ¼ññ¼Ã»Üñ¹ñë)
0175 . S. DC-BC dcbc-402:DCBC
0176 (MDP_known_event)
0177 C.
0178 C.
0179 . C. ***** YĐY¹•ï Daily±¼îñÊ'Øñ¹ñëDCBC•x²è *****
0180 . S. DC-BC dcbc-153:DCBC
0181 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0182 C.
0183 C.
0184 . C. ;ãLOS¥Á¥S¥Ã¥-¼Ã»Ü;ã
0185 C.
0186 . C. ***** LOS *****
0187 C.

```

*** OP Sequence for XRT ***

2016/01/12	10:32:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/12	10:32:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/12	10:32:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/01/12	10:33:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2016/01/12	10:33:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/01/12	10:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/01/12	10:33:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/01/12	10:33:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/12	10:33:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/12	10:35:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2016/01/12	10:35:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/01/12	10:42:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/12	10:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/12	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/12	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/12	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/01/12	11:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 ac cd		
2016/01/12	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/01/12	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/01/12	11:00:22.0	XRT_AEC_RESET_407_OG [0x197]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/01/12	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/12	11:00:26.0	XRT_FLD_RESET_414_OG [0x19e]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/12	11:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2016/01/12	11:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/01/12	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/12	11:15:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/12	11:15:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/12	11:15:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/01/12	11:16:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 d6 67		
2016/01/12	11:16:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/01/12	11:16:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/01/12	11:16:22.0	XRT_AEC_RESET_407_OG [0x197]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/01/12	11:16:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/12	11:16:26.0	XRT_FLD_RESET_414_OG [0x19e]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/12	11:18:56.0	XRT_QT_PROG_SET_431_OG [0x1af]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2016/01/12	11:18:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/01/12	11:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/12	11:31:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/12	11:31:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/12	11:31:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/01/12	11:32:00.0	AOCS_ORe-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2016/01/12	11:32:18.0	XRT_FLD_DIS_406_OG [0x196]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/01/12	11:34:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/01/12	11:34:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/12	11:34:58.0	XRT_QT_PROG_SET_401_OG [0x191]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2016/01/12	11:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		

Jan 12, 16 11:51

XRT_OGLIST_0640.chk

Page 2/10

2016/01/12	11:47:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	11:47:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	11:47:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/12	11:48:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 29 99				
2016/01/12	11:48:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/12	11:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/12	11:48:22.0	XRT_AEC_RESET_407_OG [0x197]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/12	11:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/12	11:48:26.0	XRT_FLD_RESET_414_OG [0x19e]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/12	11:50:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2016/01/12	11:50:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/01/12	11:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/12	12:03:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	12:03:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	12:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/12	12:04:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 53 33				
2016/01/12	12:04:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/12	12:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/12	12:04:22.0	XRT_AEC_RESET_407_OG [0x197]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/12	12:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/12	12:04:26.0	XRT_FLD_RESET_414_OG [0x19e]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/12	12:06:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2016/01/12	12:06:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/01/12	12:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/12	12:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	12:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	12:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/12	12:20:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 d6 36 b7 8e				
2016/01/12	12:20:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/12	12:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/12	12:20:22.0	XRT_AEC_RESET_407_OG [0x197]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/12	12:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/12	12:20:26.0	XRT_FLD_RESET_414_OG [0x19e]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/12	12:22:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2016/01/12	12:22:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/01/12	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/12	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	12:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/12	12:30:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 b4 b5 db 75				
2016/01/12	12:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/12	12:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/12	12:30:22.0	XRT_AEC_RESET_407_OG [0x197]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/12	12:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/12	12:30:26.0	XRT_FLD_RESET_414_OG [0x19e]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/12	12:32:56.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2016/01/12	12:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							

Jan 12, 16 11:51

XRT_OGLIST_0640.chk

Page 3/10

2016/01/12	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/12	12:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	12:45:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	12:45:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2016/01/12	12:46:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 ac	5b 00 00
2016/01/12	12:46:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/12	12:46:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/12	12:46:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/12	12:46:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/12	12:46:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/12	12:48:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2016/01/12	12:48:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/01/12	12:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/12	13:01:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:01:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:01:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2016/01/12	13:02:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 b4	b5 24 8b
2016/01/12	13:02:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/12	13:02:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/12	13:02:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/12	13:02:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/12	13:02:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/12	13:04:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2016/01/12	13:04:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/01/12	13:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/12	13:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:17:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2016/01/12	13:18:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 d6	36 48 72
2016/01/12	13:18:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/12	13:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/12	13:18:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/12	13:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/12	13:18:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/12	13:20:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2016/01/12	13:20:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/01/12	13:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/12	13:27:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:27:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:27:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2016/01/12	13:28:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 29	ca b7 8e
2016/01/12	13:28:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/12	13:28:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/12	13:28:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/12	13:28:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/12	13:28:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	

2016/01/12	13:30:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2016/01/12	13:30:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/01/12	13:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/12	13:37:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:37:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:37:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2016/01/12	13:38:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 4b 4b	db 75
2016/01/12	13:38:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/12	13:38:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/12	13:38:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/12	13:38:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/12	13:38:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/12	13:40:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2016/01/12	13:40:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/01/12	13:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/12	13:53:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:53:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	13:53:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2016/01/12	13:54:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 53 a5	00 00
2016/01/12	13:54:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/12	13:54:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/12	13:54:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/12	13:54:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/12	13:54:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/12	13:56:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2016/01/12	13:56:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/01/12	13:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/12	14:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	14:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	14:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/12	14:23:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/12	14:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/12	14:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	14:36:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	14:36:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2016/01/12	14:37:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 4b 4b	24 8b
2016/01/12	14:37:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/12	14:37:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/12	14:37:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/12	14:37:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/12	14:37:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/12	14:39:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2016/01/12	14:39:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/01/12	14:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/12	14:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	14:52:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/12	14:52:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					

Jan 12, 16 11:51

XRT_OGLIST_0640.chk

Page 5/10

2016/01/12	14:53:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	00	29	db	48 72
2016/01/12	14:53:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0			d8	
2016/01/12	14:53:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0			c8	
2016/01/12	14:53:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0			d0	
2016/01/12	14:53:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0			d5	
2016/01/12	14:53:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0			da	
2016/01/12	14:55:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0			c4	03
2016/01/12	14:55:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0			c5	07
2016/01/12	14:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0			c0	
2016/01/12	15:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	15:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	15:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/01/12	15:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00	00	00 00
2016/01/12	15:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0			d8	
2016/01/12	15:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0			c8	
2016/01/12	15:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0			d0	
2016/01/12	15:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0			d5	
2016/01/12	15:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0			da	
2016/01/12	15:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0			c4	12
2016/01/12	15:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0			c5	07
2016/01/12	15:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0			c0	
2016/01/12	15:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	15:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	15:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0			da	
2016/01/12	15:56:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0			e8	
2016/01/12	15:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0			e9	
2016/01/12	16:20:00.5	XRT_Custom_430_OG [0x1ae]							
2016/01/12	16:21:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0			c0	
2016/01/12	17:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	17:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	17:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0			da	
2016/01/12	17:33:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0			e8	
2016/01/12	17:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0			e9	
2016/01/12	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	17:54:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	17:54:28.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78			00
2016/01/12	17:58:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/01/12	17:58:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00	00	00 00
2016/01/12	17:58:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0			d9	
2016/01/12	18:01:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0			c9	
2016/01/12	18:01:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0			d5	
2016/01/12	18:01:28.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0			c4	04
2016/01/12	18:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0			c0	
2016/01/12	18:08:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	18:08:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0			c1	
2016/01/12	18:08:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/01/12	18:08:30.0	AOCS_ORe-point_Start_1_OG [0x097]							

2016/01/12	18:08:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02	00	00	00	00
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/12	18:08:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/12	18:08:52.0	XRT_AEC_RESET_448_OG [0x1c0]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/12	18:08:54.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/12	18:08:56.0	XRT_FLD_RESET_433_OG [0x1b1]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/12	18:11:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2016/01/12	18:11:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/01/12	18:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/12	19:10:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	19:10:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	19:10:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/12	19:10:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/12	19:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/12	19:33:00.0	XRT_Custom_430_OG [0x1ae]								
2016/01/12	19:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/12	20:47:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	20:47:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	20:47:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/12	20:47:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/12	20:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/12	21:10:30.0	XRT_Custom_430_OG [0x1ae]								
2016/01/12	21:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/12	22:24:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	22:24:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/12	22:24:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/12	22:24:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/12	22:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/12	22:46:00.0	XRT_Custom_430_OG [0x1ae]								
2016/01/12	22:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/13	00:01:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/13	00:01:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/13	00:01:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/13	00:01:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/13	00:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/13	00:10:30.0	XRT_Custom_430_OG [0x1ae]								
2016/01/13	00:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/13	01:26:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/13	01:26:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/13	01:26:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/13	01:26:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/13	01:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/13	01:44:30.0	XRT_Custom_430_OG [0x1ae]								
2016/01/13	01:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/13	03:00:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/13	03:00:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/13	03:00:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/13	03:00:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/13	03:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/13	03:21:00.0	XRT_Custom_430_OG [0x1ae]								

2016/01/13	03:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/13	04:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	04:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	04:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/13	04:30:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/13	04:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/13	04:58:30.0	XRT_Custom_430_OG [0x1ae]					
2016/01/13	04:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/13	05:42:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	05:42:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	05:42:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/01/13	05:42:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00	
2016/01/13	05:42:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/01/13	05:45:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/01/13	05:45:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/13	05:45:28.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2016/01/13	05:45:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/13	05:52:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	05:52:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	05:52:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/01/13	05:52:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 c5 cb c5 cb	
2016/01/13	05:52:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/01/13	05:52:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/01/13	05:52:52.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/01/13	05:52:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/01/13	05:52:56.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/13	05:55:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2016/01/13	05:55:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/01/13	05:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/13	06:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	06:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	06:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/13	06:10:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/13	06:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/13	06:36:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/13	06:37:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/13	07:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	07:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	07:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/01/13	07:50:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/01/13	07:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/01/13	08:13:00.0	XRT_Custom_430_OG [0x1ae]					
2016/01/13	08:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/01/13	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	08:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/01/13	08:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/01/13	09:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00	
2016/01/13	09:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	

2016/01/13	09:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/01/13	09:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/01/13	09:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/13	09:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/13	09:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2016/01/13	09:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/01/13	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/13	09:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	09:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	09:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/13	09:30:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/13	09:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/13	09:48:00.0	XRT_Custom_430_OG [0x1ae]						
2016/01/13	09:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/13	14:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	14:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	14:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/13	14:56:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/13	14:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/13	15:08:30.0	XRT_Custom_430_OG [0x1ae]						
2016/01/13	15:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/13	16:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	16:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	16:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/13	16:31:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/01/13	16:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/01/13	16:55:00.0	XRT_Custom_430_OG [0x1ae]						
2016/01/13	16:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/13	17:44:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	17:44:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	17:44:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2016/01/13	17:44:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00 00 00 00	
2016/01/13	17:44:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/01/13	17:47:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/01/13	17:47:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/13	17:47:28.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2016/01/13	17:47:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/01/13	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	17:54:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/01/13	17:54:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2016/01/13	17:54:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00 00 00 00	
2016/01/13	17:54:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/01/13	17:54:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/01/13	17:54:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/01/13	17:54:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/01/13	17:54:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/01/13	17:57:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2016/01/13	17:57:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	

2016/01/13	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/13	18:08:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/13	18:08:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/13	18:08:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/13	18:08:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/13	18:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/13	18:31:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/13	18:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/13	19:45:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/13	19:45:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/13	19:45:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/13	19:45:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/13	19:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/13	20:08:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/13	20:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/13	21:22:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/13	21:22:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/13	21:22:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/13	21:22:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/13	21:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/13	21:45:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/13	21:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/13	23:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/13	23:00:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/13	23:00:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/13	23:00:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/13	23:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/13	23:19:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/13	23:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/14	00:37:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/14	00:37:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/14	00:37:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/14	00:37:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/14	00:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/14	00:44:00.0	XRT_Custom_430_OG [0x1ae]			
2016/01/14	00:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/14	01:59:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/14	01:59:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/14	01:59:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/14	01:59:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/14	02:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/14	02:19:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/14	02:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/01/14	03:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/14	03:32:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/01/14	03:32:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/01/14	03:32:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/01/14	03:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/01/14	03:56:30.0	XRT_Custom_430_OG [0x1ae]			
2016/01/14	03:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

Jan 12, 16 11:51

XRT_OGLIST_0640.chk

Page 10/10

2016/01/14	05:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	05:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	05:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/14	05:06:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/14	05:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/14	05:34:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/14	05:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/14	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/01/14	06:00:00.0	AOCS_Or-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/01/14	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/01/14	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/01/14	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/14	06:02:58.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2016/01/14	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/14	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/01/14	06:10:00.0	AOCS_Or-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2016/01/14	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/01/14	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/01/14	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/01/14	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/01/14	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/14	06:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2016/01/14	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/01/14	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/14	06:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	06:46:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	06:46:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/14	06:46:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/14	06:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/14	07:11:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/14	07:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/14	08:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	08:26:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	08:26:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/01/14	08:26:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/01/14	08:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/01/14	08:48:00.0	XRT_Custom_430_OG [0x1ae]							
2016/01/14	08:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/01/14	10:02:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/01/14	10:41:00.0	AOCS_Or-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				