

XRT Timeline to be uploaded on 2016/02/13

Period: 2016/02/13 09:57:00 - 2016/02/18 10:32:00

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Normal mode

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XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
02/13 10:10:00 - 02/13 17:59:54	Track (397.2, 292.3) ^{Ⓜ 02/13 10:07:00}	# OP start + 10min, AR 12497 observations.
PROG= 10 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Subr= 2 5-time(s) 2.0sec		
└─ Seqn= 75 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Seqn= 96 4-time(s) 60.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 15.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 15.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1B0D: Synoptic Exposure Test Q95 2x2 - Al/mesh(24/181/512/1024/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) +

Term	Pointing (x, y)	Comment
02/13 18:03:00 - 02/13 18:09:54	Fixed (0.0, 0.0) synoptic	
PROG= 04 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024)	DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024)	DPCM 0 0 2.0sec
└─ Seqn= 69 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 94 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 80 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 54 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AEB: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 240s cad

Term	Pointing (x, y)	Comment
02/13 18:13:00 - 02/14 00:09:54	Track (-614.3, 223.6) ^{Ⓜ 02/13 18:10:00}	# NE limb AR 12499 for EIS.
02/14 06:20:00 - 02/14 08:50:00	Fixed (-957.8, 159.3)	# EIS spectral atlas at East limb.
02/14 18:07:30 - 02/14 23:58:00	Track (-427.6, 238.6) ^{Ⓜ 02/14 18:04:30}	# NE limb AR 12499 for EIS.
02/15 17:54:00 - 02/15 23:50:54	Track (-219.1, 248.1) ^{Ⓜ 02/15 17:51:00}	# NE limb AR 12499 for EIS.
PROG= 05 Inf.-time(s)		
└─ Subr= 1 45-time(s) 240.0sec		
└─ Seqn= 29 1-time(s) 4.0sec		
thin-Be/Open	med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024)	Q=98 2 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 26 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
02/14 00:13:00 - 02/14 06:06:54	Track (510.9, 285.4) ^{Ⓢ 02/14 00:10:00}	# Back to AR 12497.
02/15 00:08:30 - 02/15 05:38:54	Track (681.3, 269.4) ^{Ⓢ 02/15 00:04:30}	# AR 12497.
02/15 05:52:00 - 02/15 17:40:54	Track (717.1, 264.9) ^{Ⓢ 02/15 05:49:00}	# AR 12497.
02/16 06:12:00 - 02/16 09:50:00	Track (843.1, 243.1) ^{Ⓢ 02/16 06:09:00}	# AR 12497.

PROG= 13 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 2	4-time(s)	180.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	55.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	55.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B0C: Synoptic Q95 2x2 - Al/mesh(24/256/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/1443) + T

Term	Pointing (x, y)	Comment
02/14 06:10:00 - 02/14 06:16:54	Fixed (0.0, 0.0)	synoptic, shifted 7.0 min
02/14 17:57:30 - 02/14 18:04:24	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min
02/15 05:42:00 - 02/15 05:48:54	Fixed (0.0, 0.0)	synoptic, shifted -21.0 min
02/15 17:45:26 - 02/15 17:50:54	Fixed (0.0, 0.0)	synoptic, shifted -19.0 min
02/16 06:02:00 - 02/16 06:08:54	Fixed (0.0, 0.0)	synoptic, shifted -1.0 min

PROG= 11 1-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 65	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 94	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AFB: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
02/14 09:11:30 - 02/14 17:29:30	Track (577.5, 280.1) ^{Ⓢ 02/14 09:00:00}	# AR 12497.
02/15 23:54:00 - 02/16 05:58:54	Track (814.7, 249.1) ^{Ⓢ 02/15 23:51:00}	# AR 12497.

PROG= 08 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	4-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 24	4-time(s)	240.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	75.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	75.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
02/13 10:10:00 - 02/13 17:59:54	Track (397.2, 292.3) ^{Ⓜ 02/13 10:07:00}	# OP start + 10min, AR 12497 observations.
02/13 18:13:00 - 02/14 00:09:54	Track (-614.3, 223.6) ^{Ⓜ 02/13 18:10:00}	# NE limb AR 12499 for EIS.
02/14 00:13:00 - 02/14 06:06:54	Track (510.9, 285.4) ^{Ⓜ 02/14 00:10:00}	# Back to AR 12497.
02/14 06:20:00 - 02/14 08:50:00	Fixed (-957.8, 159.3)	# EIS spectral atlas at East limb.
02/14 09:11:30 - 02/14 17:29:30	Track (577.5, 280.1) ^{Ⓜ 02/14 09:00:00}	# AR 12497.
02/14 18:07:30 - 02/14 23:58:00	Track (-427.6, 238.6) ^{Ⓜ 02/14 18:04:30}	# NE limb AR 12499 for EIS.
02/15 00:08:30 - 02/15 05:38:54	Track (681.3, 269.4) ^{Ⓜ 02/15 00:04:30}	# AR 12497.
02/15 05:52:00 - 02/15 17:40:54	Track (717.1, 264.9) ^{Ⓜ 02/15 05:49:00}	# AR 12497.
02/15 17:54:00 - 02/15 23:50:54	Track (-219.1, 248.1) ^{Ⓜ 02/15 17:51:00}	# NE limb AR 12499 for EIS.
02/15 23:54:00 - 02/16 05:58:54	Track (814.7, 249.1) ^{Ⓜ 02/15 23:51:00}	# AR 12497.
02/16 06:12:00 - 02/16 09:50:00	Track (843.1, 243.1) ^{Ⓜ 02/16 06:09:00}	# AR 12497.

PROG= 07 30-time(s)

Subr= 1	20-time(s)	2.0sec										
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100	1-time(s)	10.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

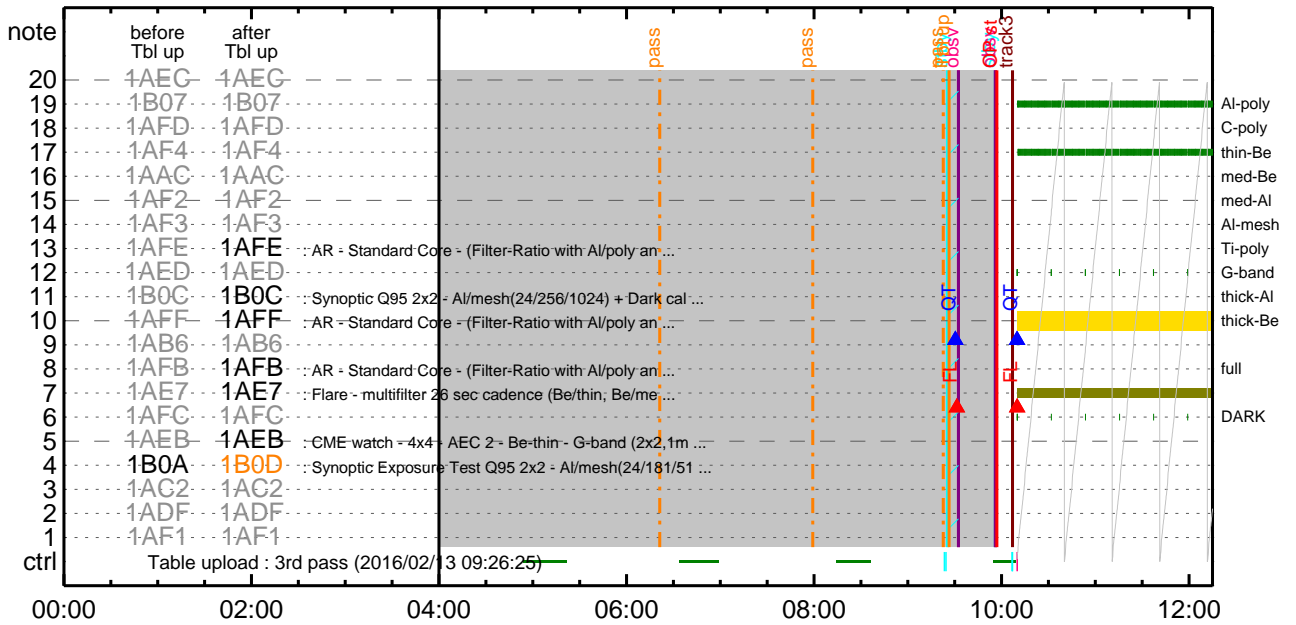
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Flare Detection

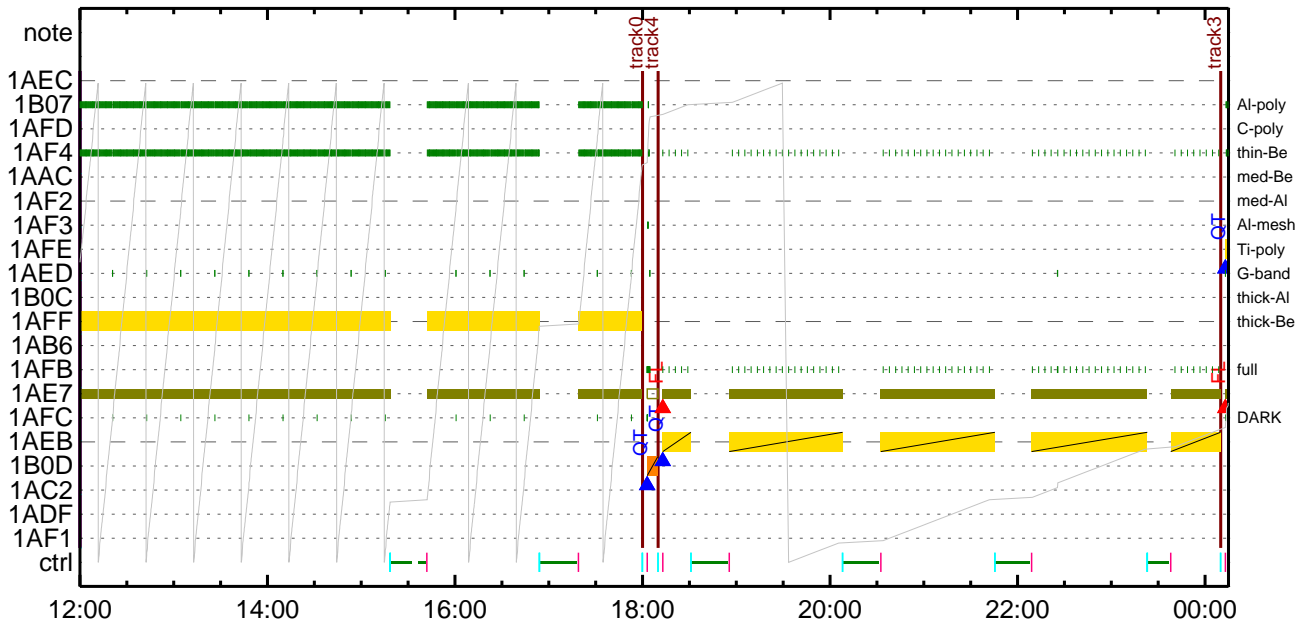
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FLD Patrol												
Term	Pointing (x, y)	Comment										
02/13 18:10:18 - 02/14 06:07:18	Track (-614.3, 223.6) ^{Ⓜ 02/13 18:10:00}	# NE limb AR 12499 for EIS.										
02/14 06:17:18 - 02/14 17:54:48	Fixed (-957.8, 159.3)	# EIS spectral atlas at East limb.										
02/14 18:04:48 - 02/15 05:39:18	Track (-427.6, 238.6) ^{Ⓜ 02/14 18:04:30}	# NE limb AR 12499 for EIS.										
02/15 05:49:18 - 02/15 17:45:18	Track (717.1, 264.9) ^{Ⓜ 02/15 05:49:00}	# AR 12497.										
02/15 17:51:18 - 02/16 05:59:18	Track (-219.1, 248.1) ^{Ⓜ 02/15 17:51:00}	# NE limb AR 12499 for EIS.										
02/16 06:09:18 - 02/18 10:32:00	Track (843.1, 243.1) ^{Ⓜ 02/16 06:09:00}	# AR 12497.										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

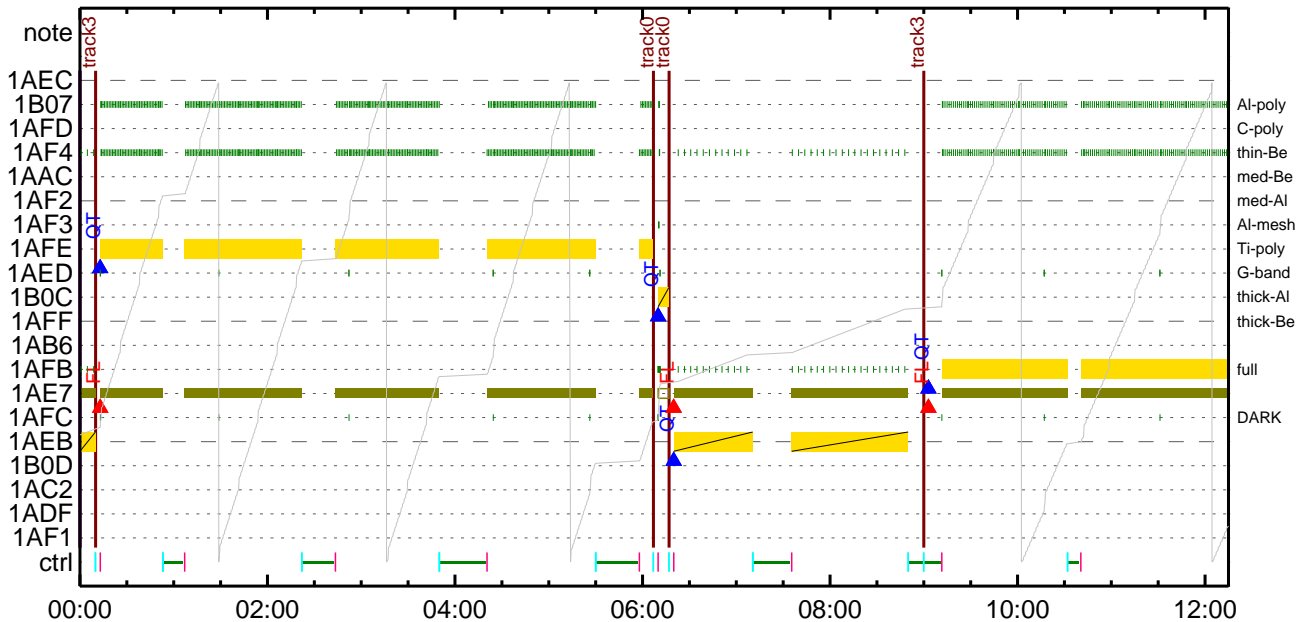
CMDI #0699 2016/02/13



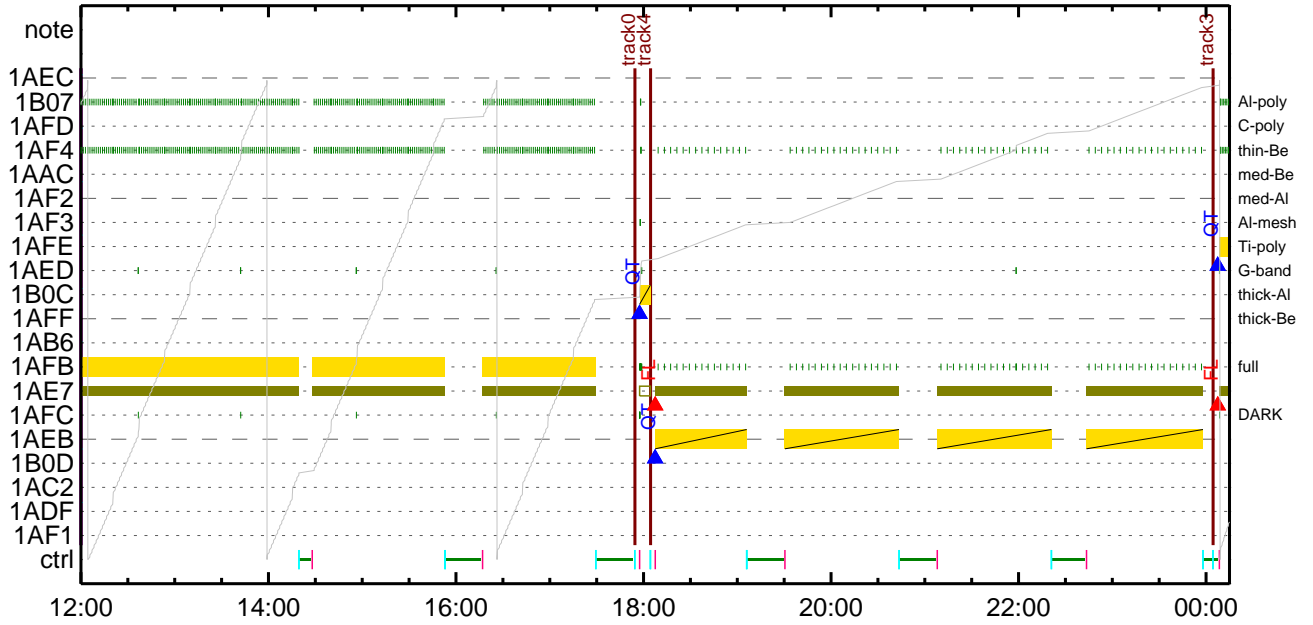
CMDI #0699 2016/02/13



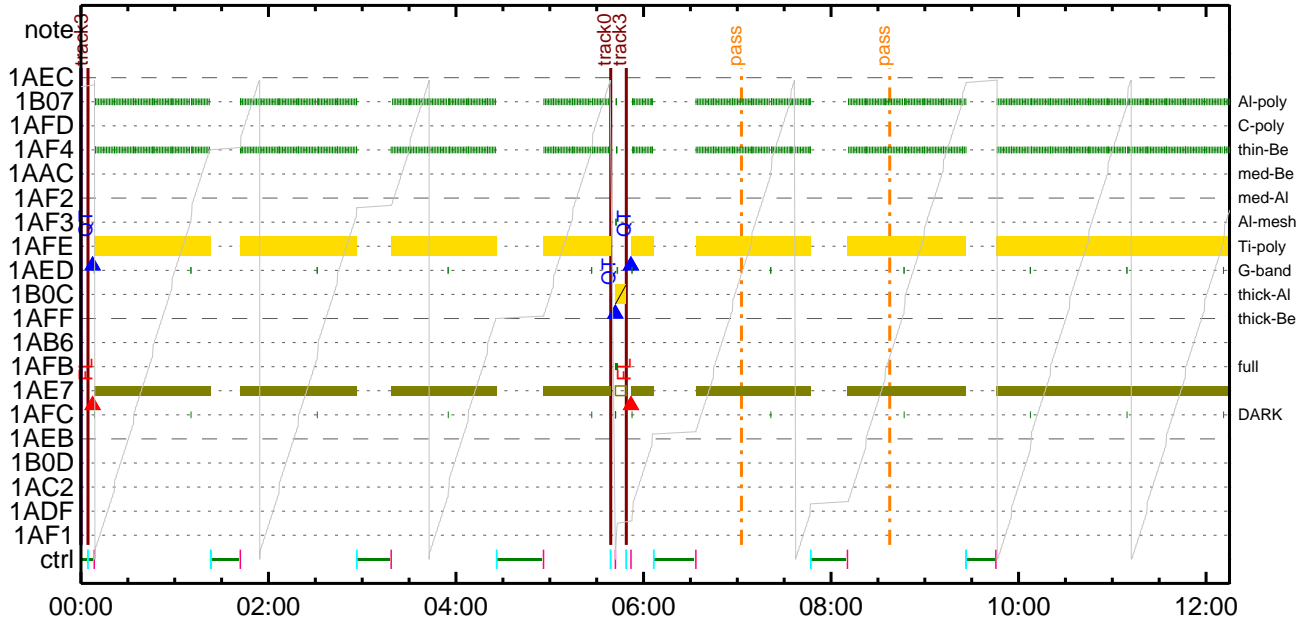
CMDI #0699 2016/02/14



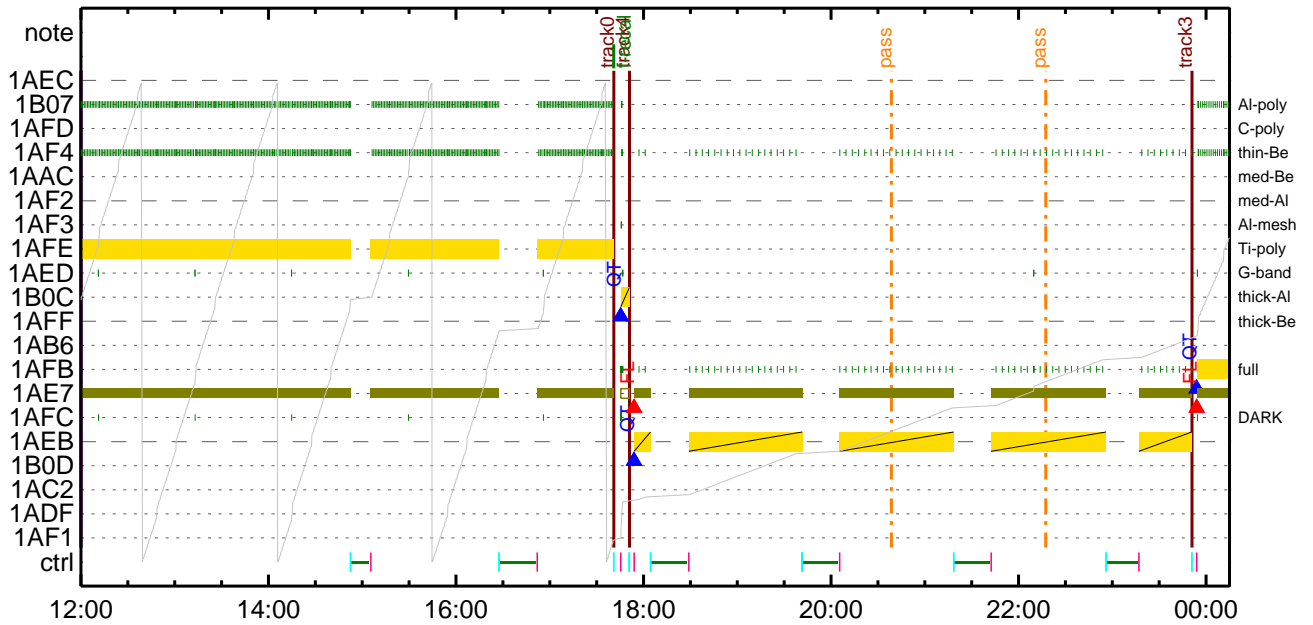
CMDI #0699 2016/02/14



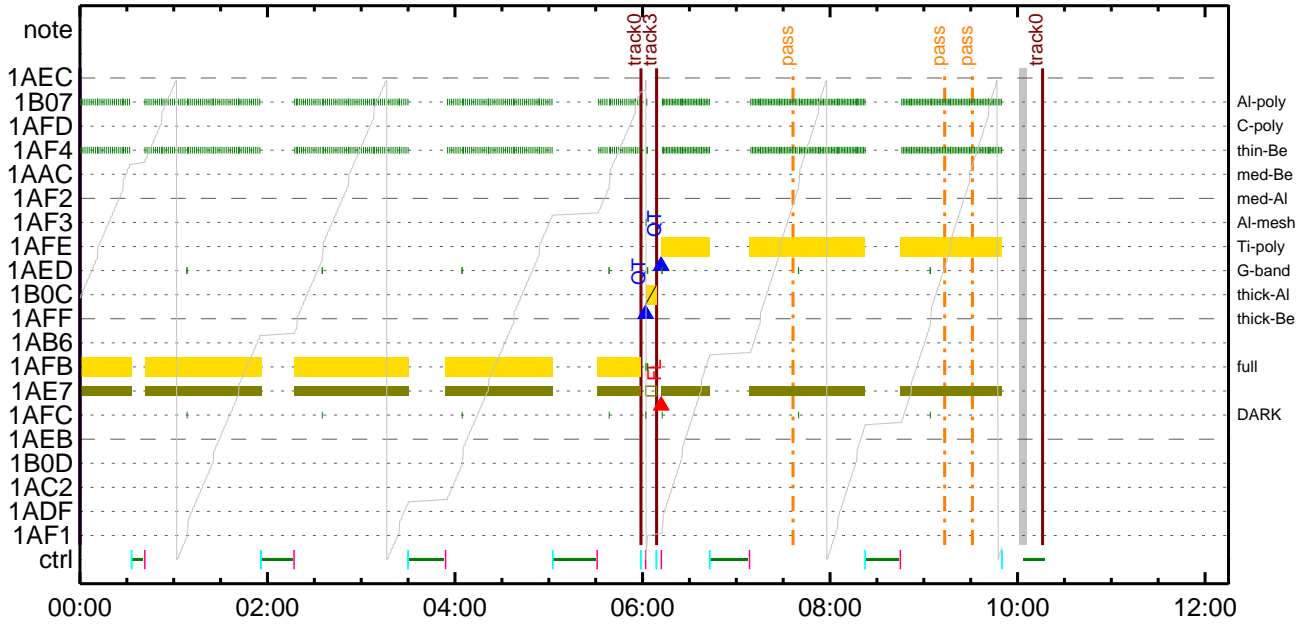
CMDI #0699 2016/02/15



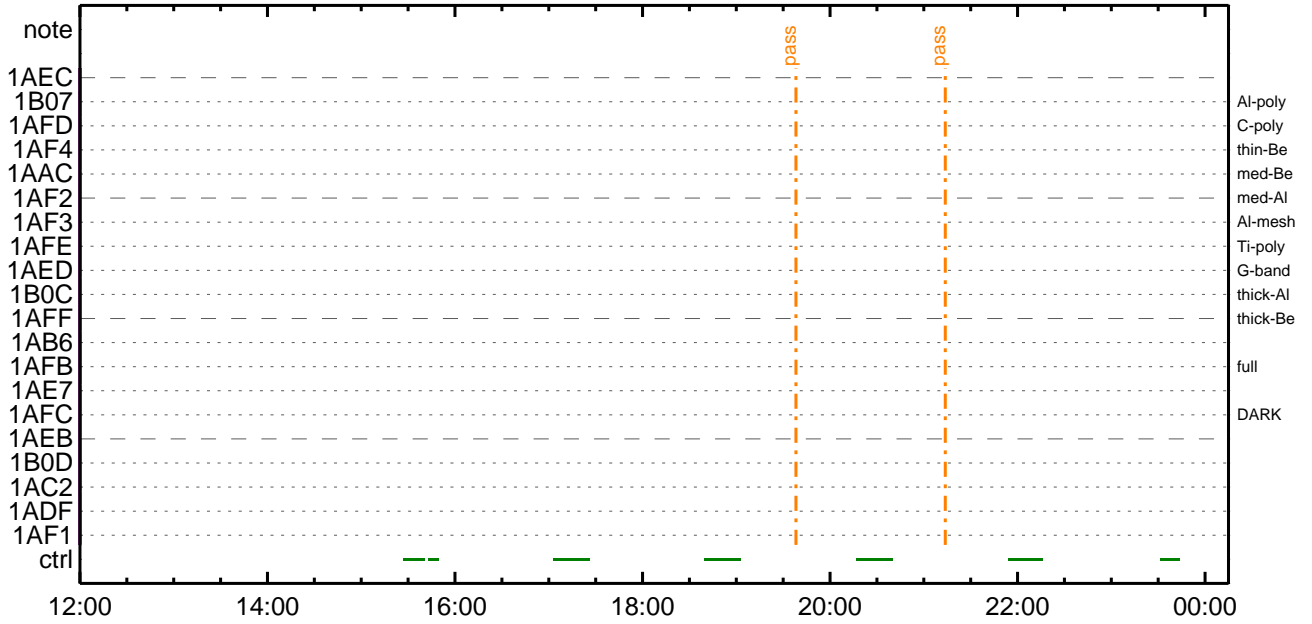
CMDI #0699 2016/02/15



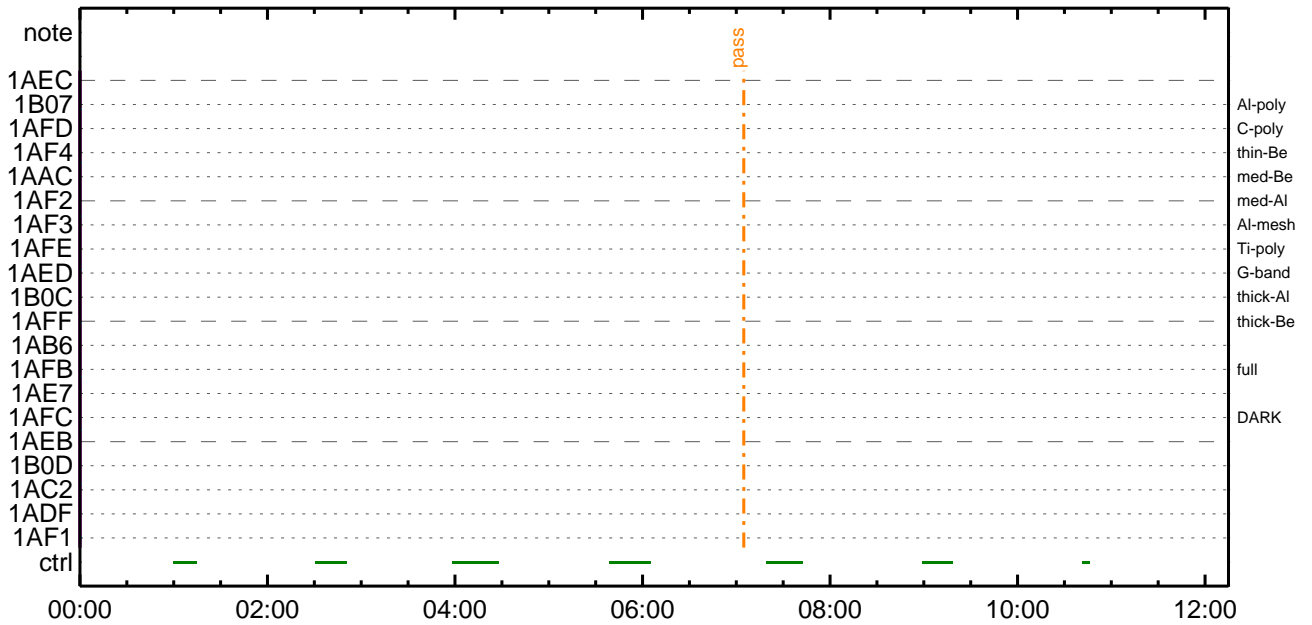
CMDI #0699 2016/02/16



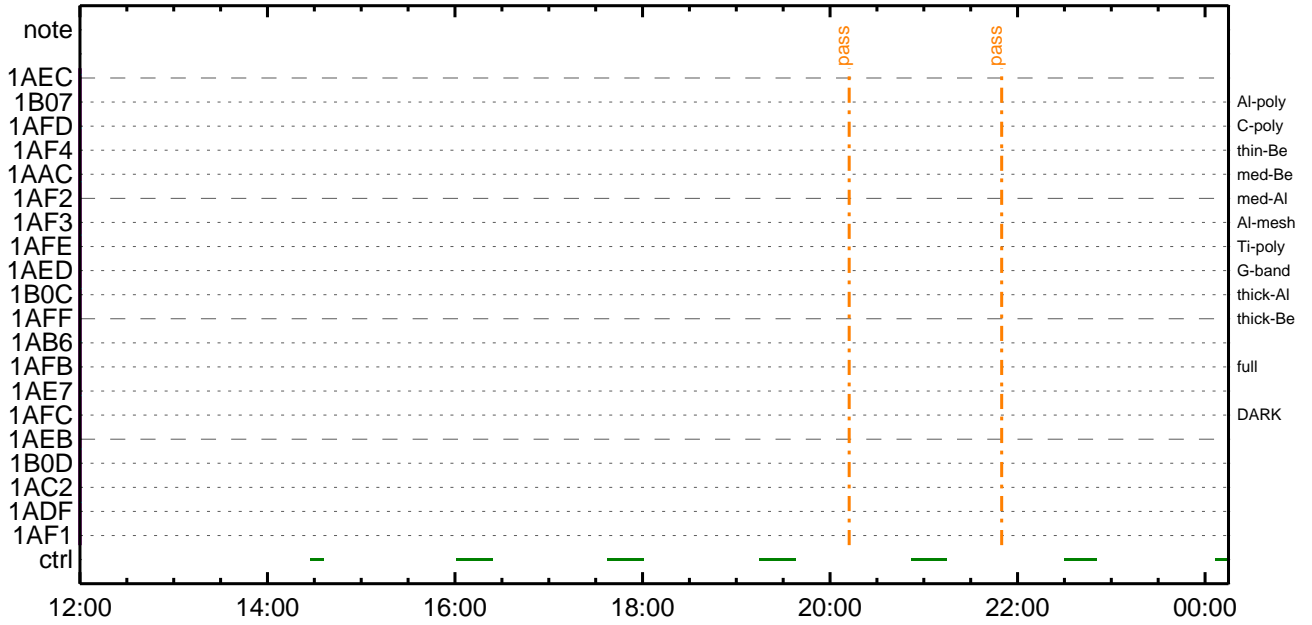
CMDI #0699 2016/02/16



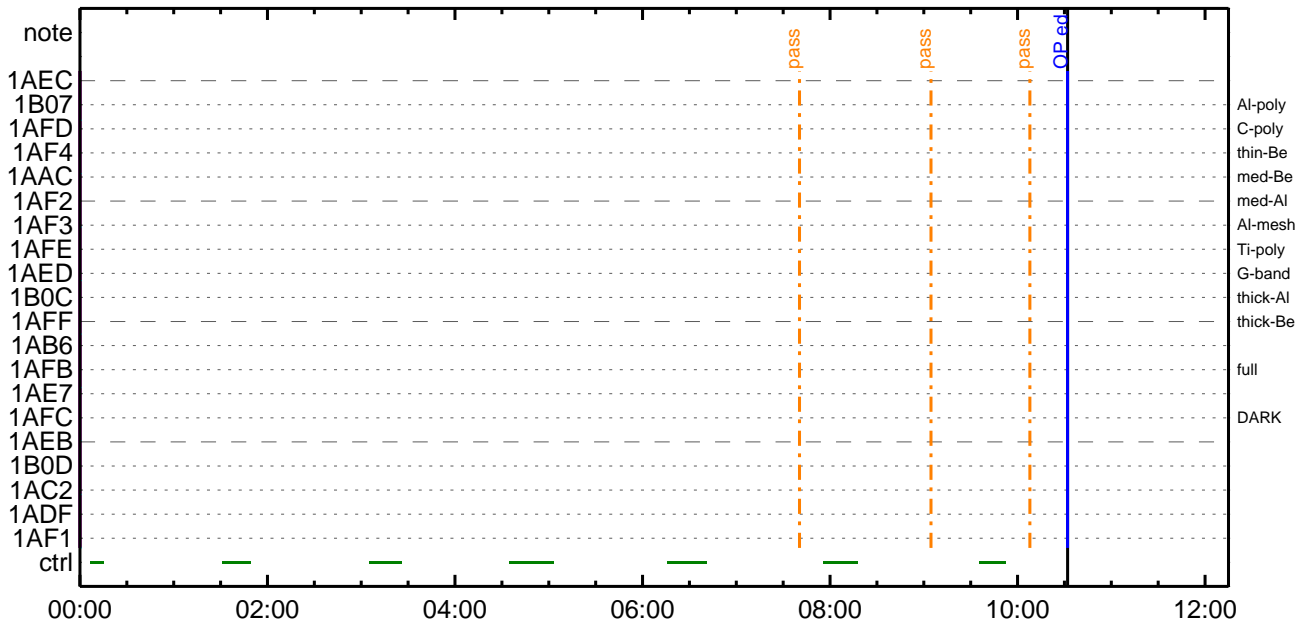
CMDI #0699 2016/02/17



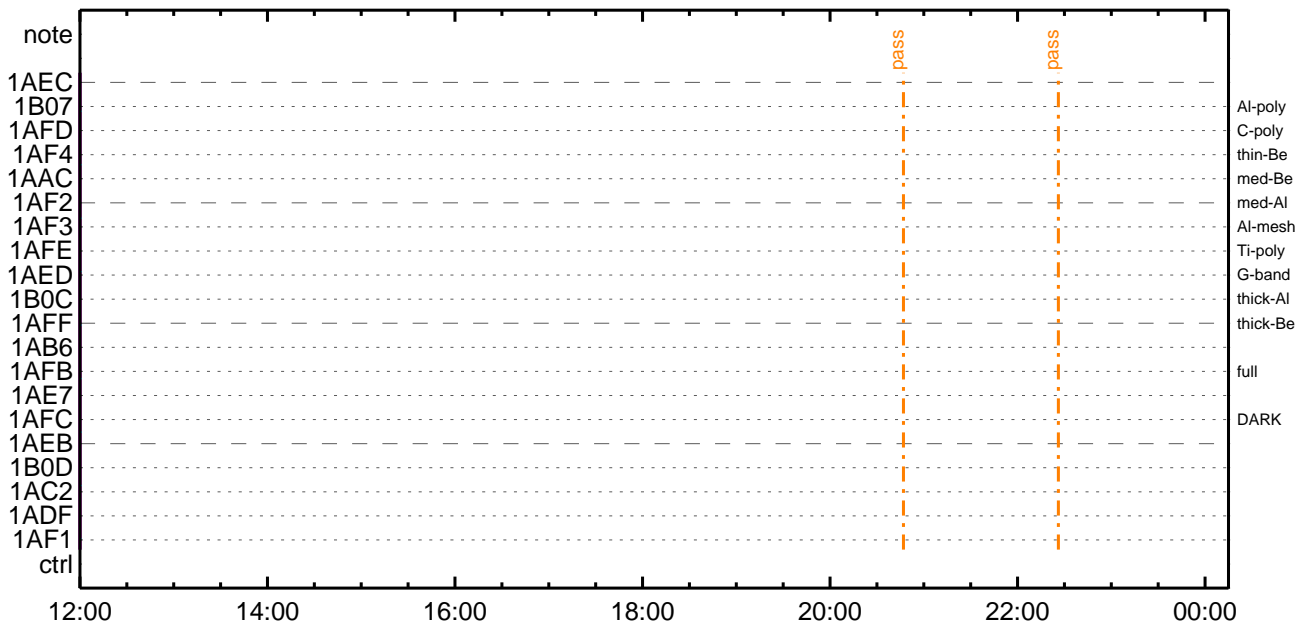
CMDI #0699 2016/02/17



CMDI #0699 2016/02/18



CMDI #0699 2016/02/18



(a) Spacecraft Operation Procedure (real-commands)

```
main-659 2016-02-13 11:44:57 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YBYÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èçð¿ðÁð•μ°È»Í×ÁÇçðíYçYÁY×Yí;¼YÉ;ÈÈè¿μ•ííÉ;ÈðÈ¼°ÇÓð••¿¼í¹ççí;çÁ®, ùñ¹ðèððçÁ÷¿®ð•ðÈððð³ðÈ;é
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-659:OP
0020 ()
0021 . S. OG og-659:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPíî°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYóY×½ªî»ðð³ÍÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðî¿È¹ç•è²ÍOKðð³ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYóY×½ªî»ðð³ÍÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðî¿È¹ç•è²ÍOKðð³ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYóY×½ªî»ðð³ÍÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPðî¿È¹ç•è²ÍOKðð³ÍÇ§
0081 C.
0082 . C. ***** øÈ²¼ðí¼Ä´¶Á°ðÈÈ-ð°Á÷¿® (¼áμ-YÁYóY×½¿¼çððÁÓÃæç¾ª°ñ·ð¿¼í¹çççðá) *****
0083 C. DHUYâ;¼YÉ;È¼Y¼, Yí;¼YÉ;ÈððÍáð¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADð-Á÷¿®NGðî¿È¹ç;ç°È²¼ðí¼TI-CMDÁ÷¿®ðî¿È¹Ôð•ðÈððð³ðÈ;é
```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 08)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 08 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0f 80 80 06 06)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 10 80 80 08 08)
0104 + DC 07-F0 MDP_XRT_FLD_ENA
0105 BC (d8)
0106 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0107 BC (c8)
0108 + DC 07-F0 MDP_XRT_ARS_DIS
0109 BC (d5)
0110 + DC 07-F0 MDP_XRT_AEC_RESET
0111 BC (d0)
0112 + DC 07-F0 MDP_XRT_FLD_RESET
0113 BC (da)
0114 + DC 07-F0 MDP_XRT_QT_PROG_SET
0115 BC (c4 0a)
0116 + DC 07-F0 MDP_XRT_FL_PROG_SET
0117 BC (c5 07)
0118 . C. ----- Success Verify ? OK / NG ____
0119 C.
0120 C.
0121 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0122 C.
0123 + DC 07-F0 MDP_XRT_MODE_OBSV
0124 BC (c2)
0125 + TI 2016-02-13 09:56:02.0
0126 DC 07-F0 MDP_XRT_MODE_OBSV
0127 BC (c2)
0128 . C. ----- Success Verify ? OK / NG ____
0129 C.
0130 C. ***** XRT END *****
0131 C.
0132 . C. ***** MDP `uãîí»ö%ýðëâð¹ðëDCBC•x²è *****
0133 C. (%á°îÿóÿãÿëÿþÿÿÿáÿçÿëðë%¼ð¼â»Û¹ðë)
0134 . S. DC-BC dcbc-402:DCBC
0135 (MDP_known_event)
0136 C.
0137 C.
0138 . C. ***** ÿDÿ¹•ï Daily±¿îñðë´ø¹ðëDCBC•x²è *****
0139 . S. DC-BC dcbc-153:DCBC
0140 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0141 C.
0142 C.
0143 . C. ;ãLOSÿÁÿ$ÿÃÿ~¼â»Û;ä
0144 C.
0145 . C. ***** LOS *****
0146 C.
```

*** OP Sequence for XRT ***

2016/02/13	10:06:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	10:06:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	10:06:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/02/13	10:07:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2016/02/13	10:07:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/02/13	10:07:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/02/13	10:07:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/02/13	10:07:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/02/13	10:07:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/02/13	10:09:00.5	XRT_Custom_430_OG [0x1ae]						
2016/02/13	10:09:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2016/02/13	10:09:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/02/13	10:10:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/02/13	15:18:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	15:18:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	15:18:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/02/13	15:18:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/02/13	15:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/02/13	15:41:00.0	XRT_Custom_430_OG [0x1ae]						
2016/02/13	15:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/02/13	16:54:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	16:54:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	16:54:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/02/13	16:54:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/02/13	16:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/02/13	17:18:00.0	XRT_Custom_430_OG [0x1ae]						
2016/02/13	17:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/02/13	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/02/13	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2016/02/13	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/02/13	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/02/13	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/02/13	18:02:58.0	XRT_QT_PROG_SET_413_OG [0x19d]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2016/02/13	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/02/13	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/02/13	18:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/02/13	18:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	04 00 00 00 00			
2016/02/13	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/02/13	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/02/13	18:10:22.0	XRT_AEC_RESET_407_OG [0x197]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/02/13	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/02/13	18:10:26.0	XRT_FLD_RESET_414_OG [0x19e]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/02/13	18:12:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2016/02/13	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			

2016/02/13	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/13	18:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/13	18:31:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/13	18:31:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/13	18:31:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/13	18:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/13	18:54:30.0	XRT_Custom_430_OG [0x1ae]							
2016/02/13	18:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/13	20:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/13	20:08:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/13	20:08:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/13	20:08:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/13	20:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/13	20:31:30.0	XRT_Custom_430_OG [0x1ae]							
2016/02/13	20:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/13	21:45:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/13	21:45:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/13	21:45:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/13	21:45:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/13	21:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/13	22:08:00.0	XRT_Custom_430_OG [0x1ae]							
2016/02/13	22:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/13	23:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/13	23:23:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/13	23:23:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/13	23:23:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/13	23:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/13	23:37:00.0	XRT_Custom_430_OG [0x1ae]							
2016/02/13	23:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/14	00:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	00:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	00:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/02/14	00:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/02/14	00:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/02/14	00:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/02/14	00:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/02/14	00:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/02/14	00:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/14	00:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/02/14	00:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/02/14	00:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/14	00:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	00:53:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	00:53:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/14	00:53:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/14	00:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/14	01:06:00.0	XRT_Custom_430_OG [0x1ae]							
2016/02/14	01:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/14	02:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2016/02/14	02:22:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	02:22:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/14	02:22:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/14	02:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/14	02:42:30.0	XRT_Custom_430_OG [0x1ae]			
2016/02/14	02:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/14	03:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	03:50:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	03:50:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/14	03:50:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/14	03:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/14	04:19:30.0	XRT_Custom_430_OG [0x1ae]			
2016/02/14	04:20:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/14	05:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	05:30:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	05:30:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/14	05:30:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/14	05:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/14	05:57:00.5	XRT_Custom_430_OG [0x1ae]			
2016/02/14	05:58:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/14	06:06:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	06:06:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	06:06:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/02/14	06:07:00.0	AOCS_Orе-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2016/02/14	06:07:18.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/02/14	06:09:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/02/14	06:09:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/02/14	06:09:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/02/14	06:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/14	06:16:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	06:16:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	06:16:58.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/02/14	06:17:00.0	AOCS_Orе-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 f1 d3 55 26
2016/02/14	06:17:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/02/14	06:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/02/14	06:17:22.0	XRT_AEC_RESET_407_OG [0x197]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/02/14	06:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/02/14	06:17:26.0	XRT_FLD_RESET_414_OG [0x19e]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/14	06:19:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2016/02/14	06:19:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/02/14	06:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/14	07:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	07:10:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/14	07:10:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/14	07:10:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/14	07:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/14	07:34:30.0	XRT_Custom_430_OG [0x1ae]			
2016/02/14	07:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/14	08:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			

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2016/02/14	08:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	08:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	08:50:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/02/14	08:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/02/14	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/02/14	08:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	08:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	09:00:00.5	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/02/14	09:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00		
2016/02/14	09:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/02/14	09:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/02/14	09:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/02/14	09:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/02/14	09:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/02/14	09:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2016/02/14	09:10:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/02/14	09:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0		
2016/02/14	10:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/02/14	10:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	10:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	10:32:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/02/14	10:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/02/14	10:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/02/14	10:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0		
2016/02/14	14:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/02/14	14:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	14:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	14:19:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/02/14	14:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/02/14	14:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/02/14	14:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0		
2016/02/14	15:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/02/14	15:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	15:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	15:53:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/02/14	15:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/02/14	16:16:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/02/14	16:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0		
2016/02/14	17:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/02/14	17:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	17:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	17:29:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/02/14	17:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/02/14	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/02/14	17:54:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	17:54:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/02/14	17:54:30.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/02/14	17:54:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00		
			MDP_XRT_FLD_DIS	1	07-F0	d9		

2016/02/14	17:57:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/02/14	17:57:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/02/14	17:57:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2016/02/14	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/14	18:04:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	18:04:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	18:04:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/02/14	18:04:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2016/02/14	18:04:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/02/14	18:04:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/02/14	18:04:52.0	XRT_AEC_RESET_407_OG [0x197]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/02/14	18:04:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/02/14	18:04:56.0	XRT_FLD_RESET_414_OG [0x19e]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/14	18:07:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2016/02/14	18:07:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/02/14	18:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/14	19:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	19:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	19:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/14	19:06:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/14	19:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/14	19:29:30.0	XRT_Custom_430_OG [0x1ae]							
2016/02/14	19:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/14	20:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	20:43:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	20:43:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/14	20:43:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/14	20:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/14	21:07:00.0	XRT_Custom_430_OG [0x1ae]							
2016/02/14	21:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/14	22:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	22:21:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	22:21:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/14	22:21:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/14	22:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/14	22:42:30.5	XRT_Custom_430_OG [0x1ae]							
2016/02/14	22:43:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/14	23:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	23:58:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/14	23:58:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/14	23:58:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/15	00:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/15	00:04:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	00:04:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	00:04:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/02/15	00:04:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/02/15	00:04:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/02/15	00:04:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				

2016/02/15	00:04:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/02/15	00:04:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/02/15	00:04:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/15	00:07:26.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/02/15	00:07:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/02/15	00:07:30.5	XRT_Custom_430_OG [0x1ae]							
2016/02/15	00:08:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/15	01:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	01:23:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	01:23:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/15	01:23:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/15	01:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/15	01:41:00.0	XRT_Custom_430_OG [0x1ae]							
2016/02/15	01:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/15	02:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	02:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	02:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/15	02:56:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/15	02:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/15	03:17:30.0	XRT_Custom_430_OG [0x1ae]							
2016/02/15	03:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/15	04:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	04:26:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	04:26:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/15	04:26:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/15	04:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/15	04:55:00.0	XRT_Custom_430_OG [0x1ae]							
2016/02/15	04:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/15	05:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	05:38:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	05:38:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/02/15	05:39:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/02/15	05:39:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/02/15	05:41:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/02/15	05:41:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/02/15	05:41:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2016/02/15	05:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/15	05:48:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	05:48:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/15	05:48:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/02/15	05:49:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/02/15	05:49:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/02/15	05:49:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/02/15	05:49:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/02/15	05:49:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/02/15	05:49:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/15	05:51:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/02/15	05:51:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/02/15	05:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

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2016/02/15	06:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	06:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	06:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/15	06:06:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/15	06:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/15	06:32:30.0	XRT_Custom_430_OG [0x1ae]				
2016/02/15	06:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/15	07:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	07:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	07:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/15	07:47:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/15	07:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/15	08:09:30.0	XRT_Custom_430_OG [0x1ae]				
2016/02/15	08:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/15	09:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	09:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	09:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/15	09:26:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/15	09:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/15	09:44:30.0	XRT_Custom_430_OG [0x1ae]				
2016/02/15	09:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/15	14:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	14:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	14:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/15	14:52:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/15	14:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/15	15:04:30.0	XRT_Custom_430_OG [0x1ae]				
2016/02/15	15:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/15	16:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	16:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	16:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/15	16:27:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/15	16:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/15	16:51:00.0	XRT_Custom_430_OG [0x1ae]				
2016/02/15	16:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/15	17:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	17:40:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	17:40:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78 00
2016/02/15	17:41:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2016/02/15	17:44:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/02/15	17:45:18.0	XRT_FLD_DIS_443_OG [0x1bb]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/02/15	17:45:20.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/02/15	17:45:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/02/15	17:45:24.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/02/15	17:45:26.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/15	17:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	17:50:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/15	17:50:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/02/15	17:51:00.0	AOCS_Ore-point_Start_3_OG [0x099]				

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2016/02/15	17:51:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04	00	00	00	00
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/02/15	17:51:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	d0				
2016/02/15	17:51:22.0	XRT_AEC_RESET_407_OG [0x197]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d5				
2016/02/15	17:51:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	da				
2016/02/15	17:51:26.0	XRT_FLD_RESET_414_OG [0x19e]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/02/15	17:53:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]		2	07-F0	c4	05			
		MDP_XRT_QT_PROG_SET		2	07-F0	c5	07			
2016/02/15	17:53:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c0				
2016/02/15	17:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2016/02/15	18:04:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/02/15	18:04:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	da				
2016/02/15	18:04:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	e8				
2016/02/15	18:04:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e9				
2016/02/15	18:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	c0				
2016/02/15	18:28:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c1				
2016/02/15	18:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c1				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2016/02/15	19:41:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/02/15	19:41:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	da				
2016/02/15	19:41:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	e8				
2016/02/15	19:41:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e9				
2016/02/15	19:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	c0				
2016/02/15	20:04:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c1				
2016/02/15	20:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c1				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2016/02/15	21:18:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/02/15	21:18:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	da				
2016/02/15	21:18:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	e8				
2016/02/15	21:18:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e9				
2016/02/15	21:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	c0				
2016/02/15	21:41:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c1				
2016/02/15	21:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c1				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2016/02/15	22:56:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/02/15	22:56:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	da				
2016/02/15	22:56:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	e8				
2016/02/15	22:56:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e9				
2016/02/15	22:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	c0				
2016/02/15	23:16:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c1				
2016/02/15	23:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c1				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2016/02/15	23:50:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/02/15	23:50:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/02/15	23:50:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2016/02/15	23:51:00.0	AOCs_Or-e-point_Start_1_OG [0x097]		5	02-76	03	00	00	00	00
		AOCU_NM		5	02-76	03	00	00	00	00
2016/02/15	23:51:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	c8				
2016/02/15	23:51:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	d0				
2016/02/15	23:51:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d5				
2016/02/15	23:51:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	da				
2016/02/15	23:51:26.0	XRT_FLD_RESET_433_OG [0x1b1]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/02/15	23:53:56.0	XRT_QT_PROG_SET_431_OG [0x1af]		2	07-F0	c4	08			
		MDP_XRT_QT_PROG_SET		2	07-F0	c5	07			
2016/02/15	23:53:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c0				
2016/02/15	23:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				

2016/02/16	00:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	00:33:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	00:33:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/16	00:33:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/16	00:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/16	00:40:30.0	XRT_Custom_430_OG [0x1ae]							
2016/02/16	00:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/16	01:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	01:56:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	01:56:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/16	01:56:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/16	01:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/16	02:16:00.0	XRT_Custom_430_OG [0x1ae]							
2016/02/16	02:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/16	03:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	03:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	03:30:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/16	03:30:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/16	03:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/16	03:53:00.0	XRT_Custom_430_OG [0x1ae]							
2016/02/16	03:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/16	05:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	05:02:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	05:02:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/16	05:02:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/02/16	05:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/02/16	05:30:00.0	XRT_Custom_430_OG [0x1ae]							
2016/02/16	05:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/16	05:58:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	05:58:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	05:58:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/02/16	05:59:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/02/16	05:59:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/02/16	06:01:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/02/16	06:01:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/02/16	06:01:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2016/02/16	06:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/02/16	06:08:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	06:08:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/02/16	06:08:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/02/16	06:09:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/02/16	06:09:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/02/16	06:09:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/02/16	06:09:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/02/16	06:09:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/02/16	06:09:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/02/16	06:11:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2016/02/16	06:11:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/02/16	06:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

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2016/02/16	06:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/16	06:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/16	06:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/16	06:43:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/16	06:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/16	07:07:30.0	XRT_Custom_430_OG [0x1ae]				
2016/02/16	07:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/16	08:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/16	08:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/16	08:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/02/16	08:22:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/02/16	08:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/02/16	08:44:00.0	XRT_Custom_430_OG [0x1ae]				
2016/02/16	08:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/02/16	09:50:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/16	09:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/02/16	10:16:00.0	AOCS_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00