

XRT Timeline to be uploaded on 2016/03/05

Period: 2016/03/05 10:12:00 - 2016/03/10 10:27:00

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Normal mode

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XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms												
Term	Pointing (x, y)	Comment										
03/05 10:25:00 - 03/05 16:16:30	Fixed (-16.0, -946.0)	# OP start + 10min/ HOP206										
03/06 10:07:30 - 03/06 17:59:54	Fixed (-16.0, -966.0)	HOP81 S-Pole										
PROG= 16 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 9 2-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0 0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 7 1-time(s) 30.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0	2.0sec
└─ Subr= 3 30-time(s) 2.0sec												
└─ Seqn= 57 1-time(s) 60.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1AFC: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)												
Term	Pointing (x, y)	Comment										
03/05 16:41:00 - 03/05 17:53:00	Track (-425.0, 167.5) ^{Ⓜ 03/05 16:22:00}	AR12510 tracking										
03/05 18:28:30 - 03/06 06:01:24	Track (-407.4, 168.6) ^{Ⓜ 03/05 18:26:30}	AR12510 tracking										
03/06 06:14:30 - 03/06 09:51:30	Track (-304.8, 173.7) ^{Ⓜ 03/06 06:11:30}	AR12510 tracking										
03/06 18:13:00 - 03/07 06:03:54	Track (-196.2, 177.4) ^{Ⓜ 03/06 18:10:00}	AR12510 tracking										
03/07 06:21:00 - 03/07 18:05:54	Track (-84.1, 179.4) ^{Ⓜ 03/07 06:14:00}	AR12510 tracking										
03/07 18:19:00 - 03/08 01:20:30	Track (28.8, 179.8) ^{Ⓜ 03/07 18:16:00}	AR12510 tracking										
03/08 05:49:00 - 03/08 09:23:30	Track (136.4, 178.5) ^{Ⓜ 03/08 05:46:00}	AR12510 tracking										
PROG= 03 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 56 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0 0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0 0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0 0	2.0sec
└─ Seqn= 42 3-time(s) 2.0sec												
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0	2.0sec
└─ Seqn= 8 20-time(s) 160.0sec												
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0	46.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 1	47.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti												
Term	Pointing (x, y)	Comment										
03/05 18:19:30 - 03/05 18:25:24	Fixed (0.0, 0.0)	synoptic, shifted 16.5 min										
03/06 06:04:30 - 03/06 06:11:24	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min										
03/06 18:03:00 - 03/06 18:09:54	Fixed (0.0, 0.0)	synoptic										
03/07 06:07:00 - 03/07 06:13:54	Fixed (0.0, 0.0)	synoptic, shifted 4.0 min										
03/07 18:09:00 - 03/07 18:15:54	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min										
03/08 05:39:00 - 03/08 05:45:54	Fixed (0.0, 0.0)	synoptic, shifted -24.0 min										
PROG= 17 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0 0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0 0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0 0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0 0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0 0	2.0sec
└─ Seqn= 1 1-time(s) 2.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0	2.0sec
└─ Seqn= 99 1-time(s) 2.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0	2.0sec
└─ Seqn= 67 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0	2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AEC: G-Band Alignment with North Pole Q90 2x2 (G-band and VLS=CLS) - 1msec (Al/poly) - 4096msec - 5min cadence - Partial Sun-wNGT

Term	Pointing (x, y)	Comment
03/08 01:39:00 - 03/08 03:35:54	Fixed (0.0, 930.0)	N-limb Coalignment
PROG= 02 1-time(s)		
Subr= 1 24-time(s) 300.0sec		
Seqn= 98 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Seqn= 63 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Seqn= 45 1-time(s) 2.0sec		
Al-poly/Open	med-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x1536 (1024, 768) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1AED: G-Band Alignment with East limb Q90 2x2 (G-band and VLS=CLS) - 1msec - (Al/poly) 1443msec - 8 min cadence-wNGT

Term	Pointing (x, y)	Comment
03/08 03:39:00 - 03/08 05:35:54	Fixed (-945.0, 0.0)	E-limb Coalignment
PROG= 18 1-time(s)		
Subr= 1 15-time(s) 480.0sec		
Seqn= 19 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Seqn= 43 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Seqn= 70 1-time(s) 2.0sec		
Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 1536x2048 (1280, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)

Term	Pointing (x, y)	Comment
03/05 10:25:00 - 03/05 16:16:30	Fixed (-16.0, -946.0)	# OP start + 10min/ HOP206
03/05 16:41:00 - 03/05 17:53:00	Track (-425.0, 167.5) ^{03/05 16:22:00}	AR12510 tracking
03/05 18:28:30 - 03/06 06:01:24	Track (-407.4, 168.6) ^{03/05 18:26:30}	AR12510 tracking
03/06 06:14:30 - 03/06 09:51:30	Track (-304.8, 173.7) ^{03/06 06:11:30}	AR12510 tracking
03/06 10:07:30 - 03/06 17:59:54	Fixed (-16.0, -966.0)	HOP81 S-Pole
03/06 18:13:00 - 03/07 06:03:54	Track (-196.2, 177.4) ^{03/06 18:10:00}	AR12510 tracking
03/07 06:21:00 - 03/07 18:05:54	Track (-84.1, 179.4) ^{03/07 06:14:00}	AR12510 tracking
03/07 18:19:00 - 03/08 01:20:30	Track (28.8, 179.8) ^{03/07 18:16:00}	AR12510 tracking
03/08 05:49:00 - 03/08 09:23:30	Track (136.4, 178.5) ^{03/08 05:46:00}	AR12510 tracking
PROG= 07 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 84 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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Flare Detection

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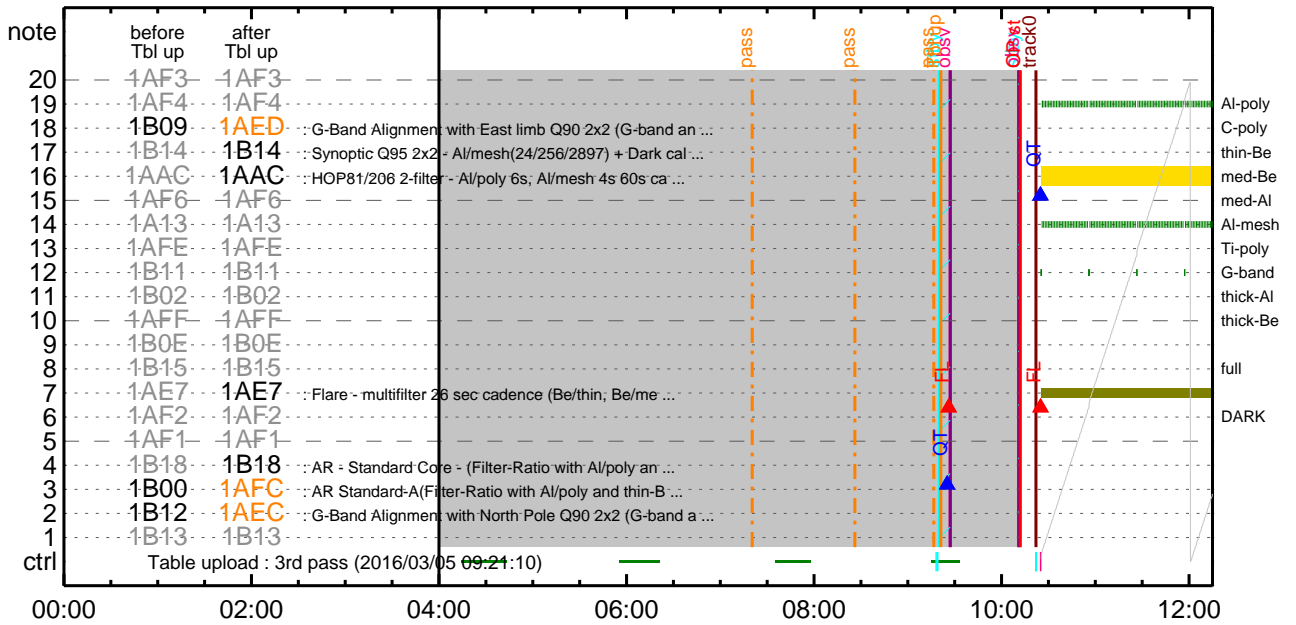
FLD Patrol

Term	Pointing (x, y)	Comment
03/05 18:25:48 - 03/06 06:01:48	Fixed (0.0, 0.0)	synoptic, shifted 16.5 min
03/06 06:11:48 - 03/06 18:00:18	Track (-304.8, 173.7) ^{03/06 06:11:30}	AR12510 tracking

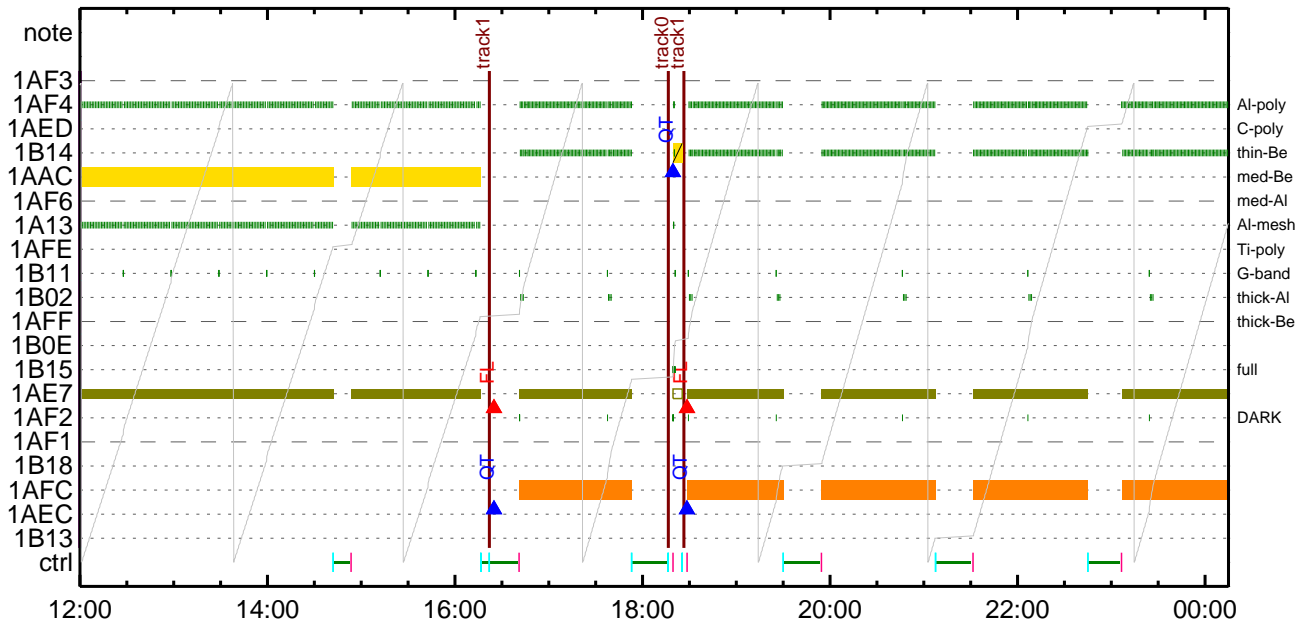
03/06 18:10:18 - 03/07 06:04:18 Track (-196.2, 177.4) @ 03/06 18:10:00 AR12510 tracking
 03/07 06:18:18 - 03/07 18:06:18 Track (-84.1, 179.4) @ 03/07 06:14:00 AR12510 tracking
 03/07 18:16:18 - 03/08 01:36:18 Track (28.8, 179.8) @ 03/07 18:16:00 AR12510 tracking
 03/08 05:46:18 - 03/10 10:27:00 Track (136.4, 178.5) @ 03/08 05:46:00 AR12510 tracking

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

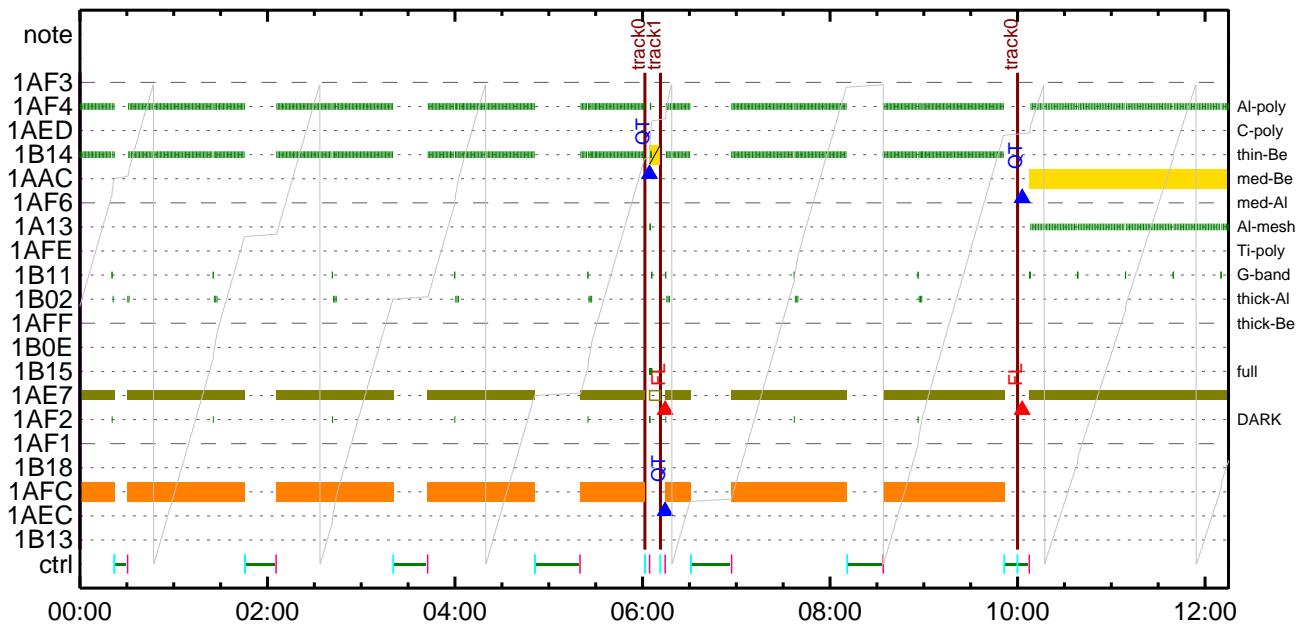
CMDI #0754 2016/03/05



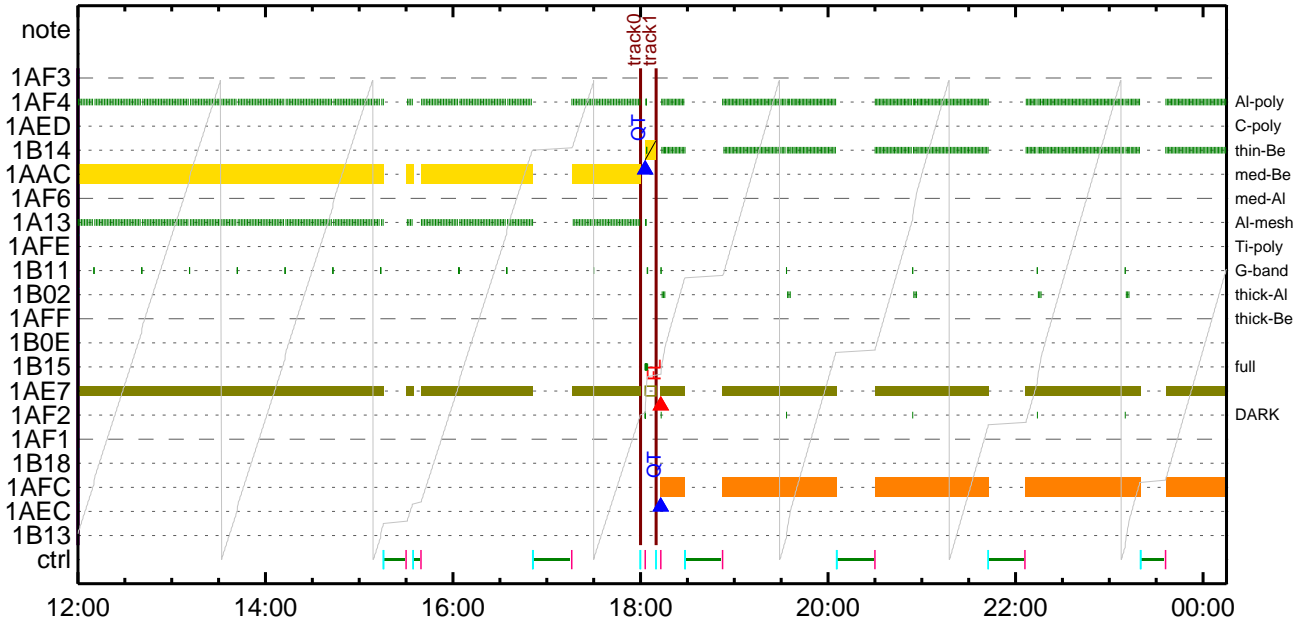
CMDI #0754 2016/03/05



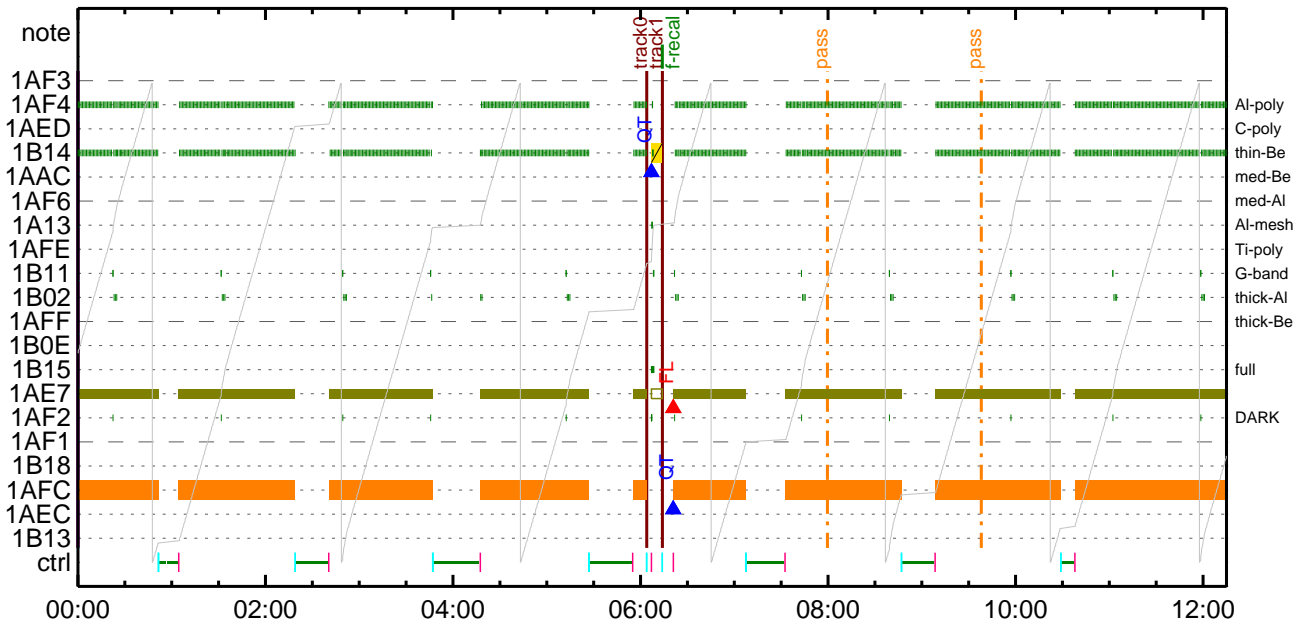
CMDI #0754 2016/03/06



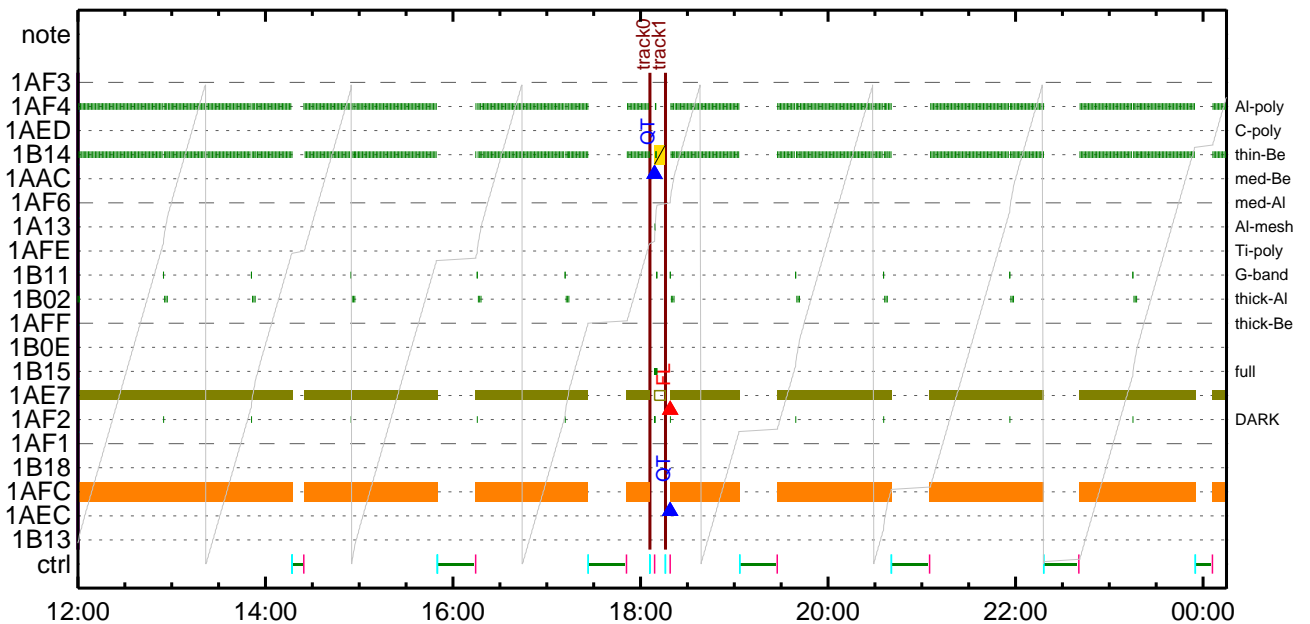
CMDI #0754 2016/03/06



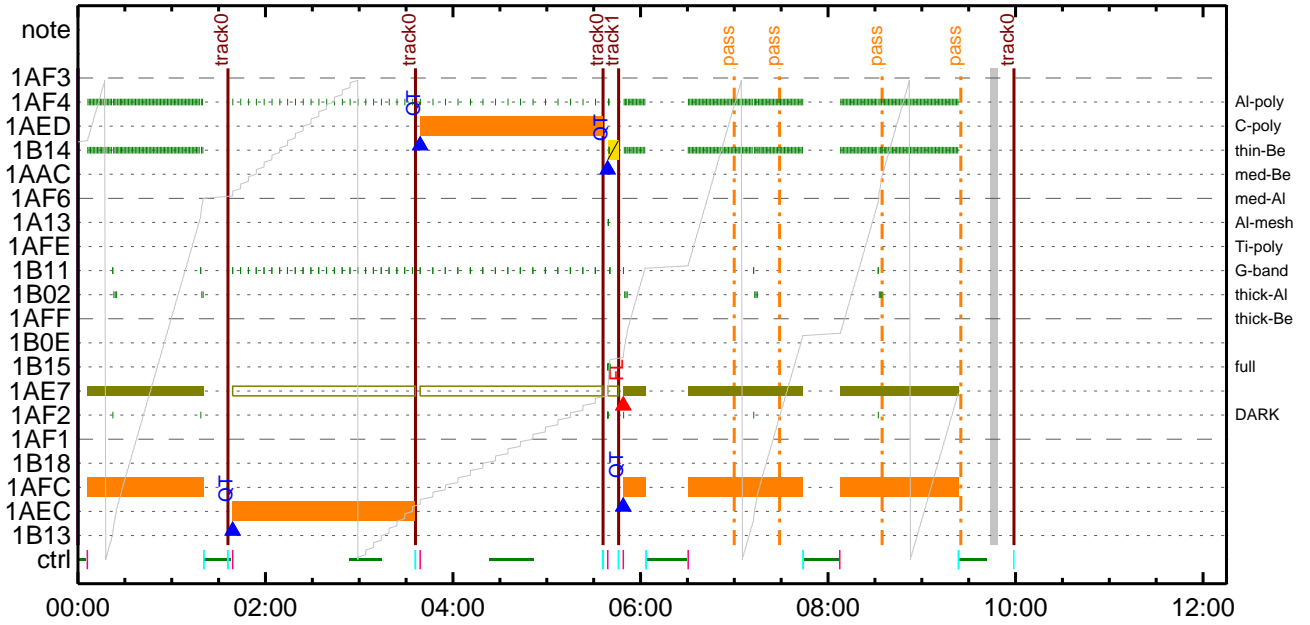
CMDI #0754 2016/03/07



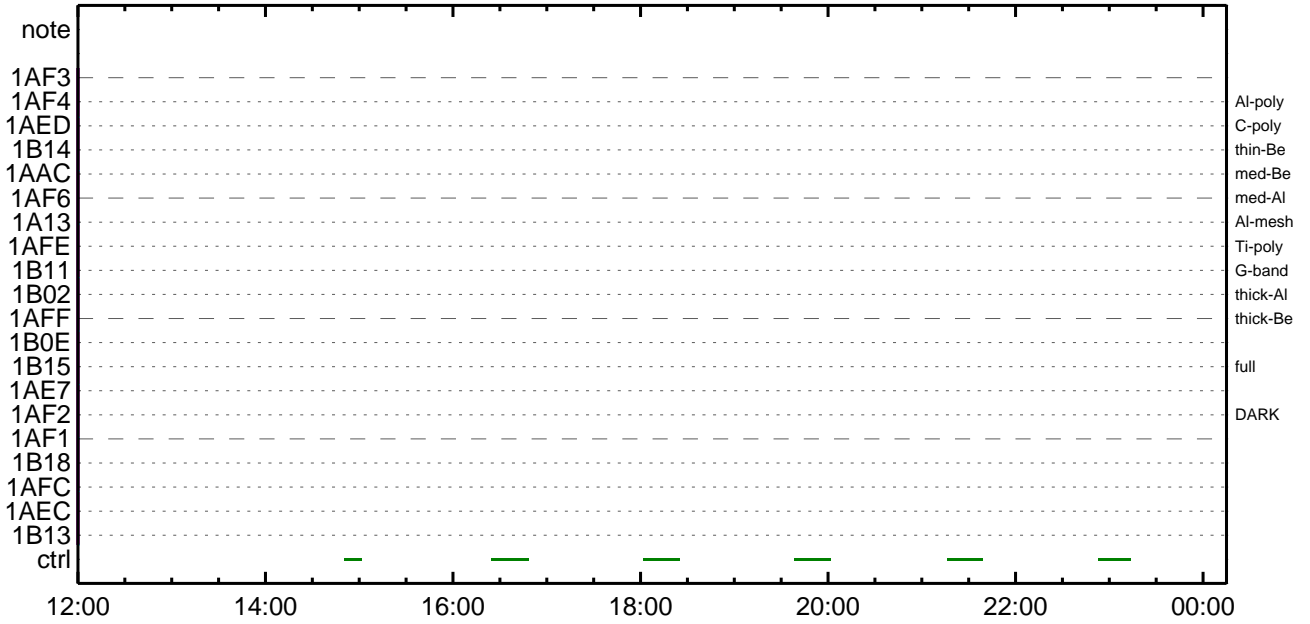
CMDI #0754 2016/03/07



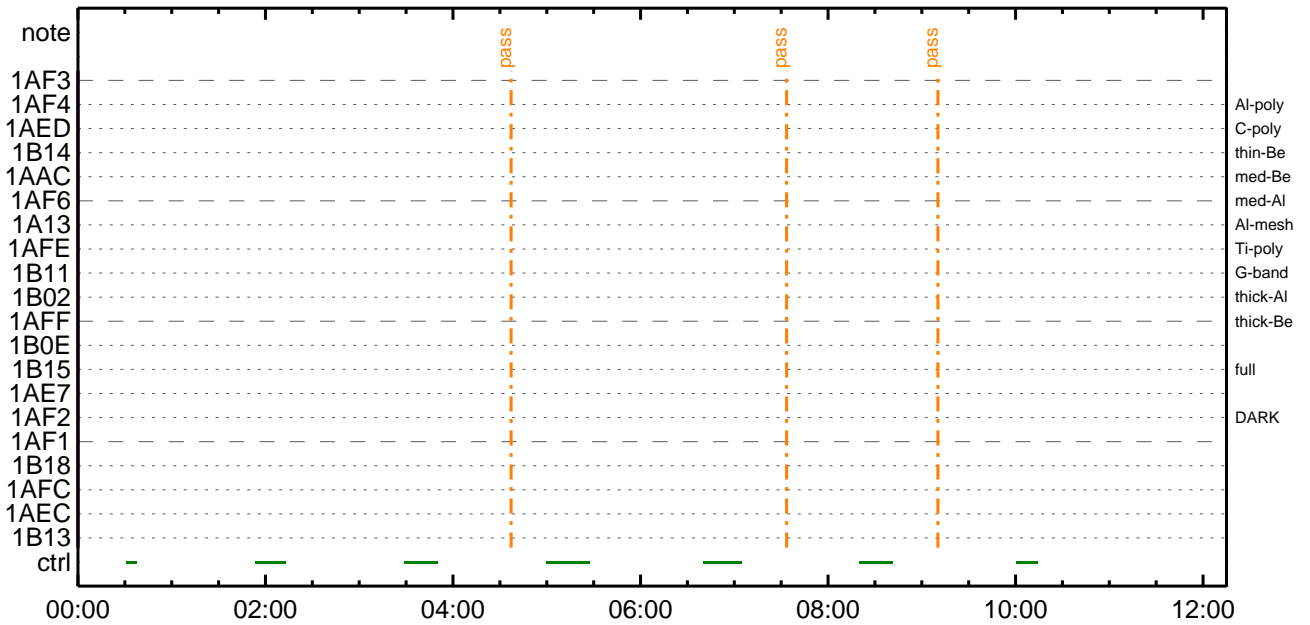
CMDI #0754 2016/03/08



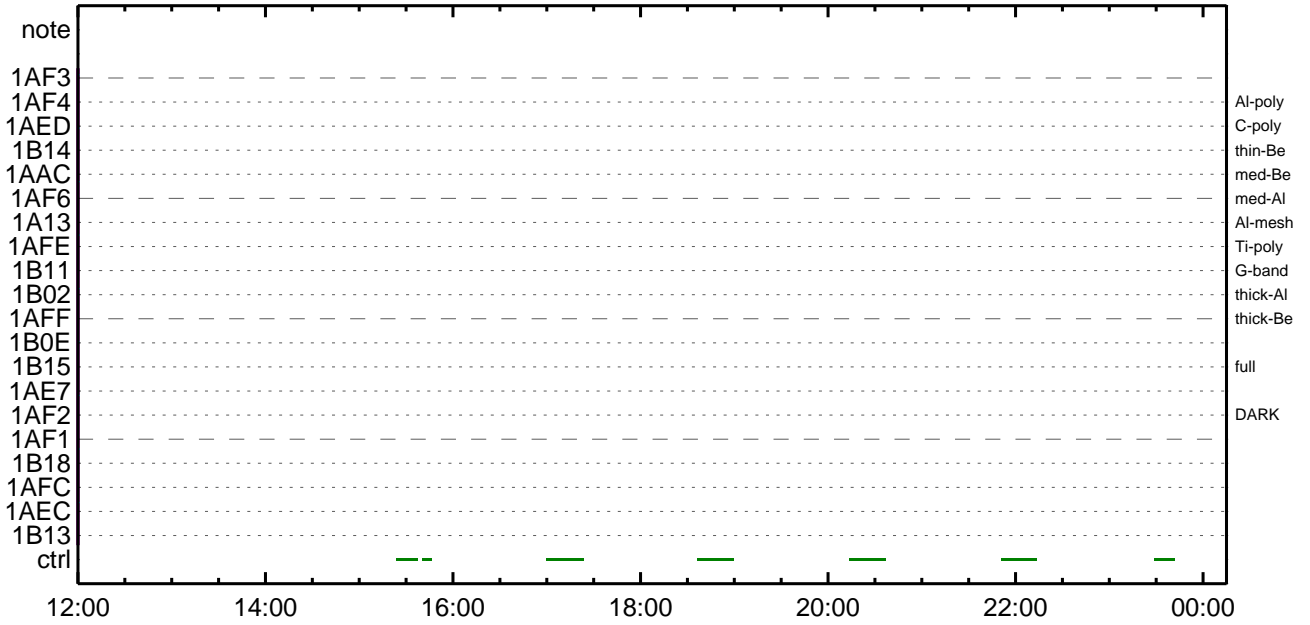
CMDI #0754 2016/03/08



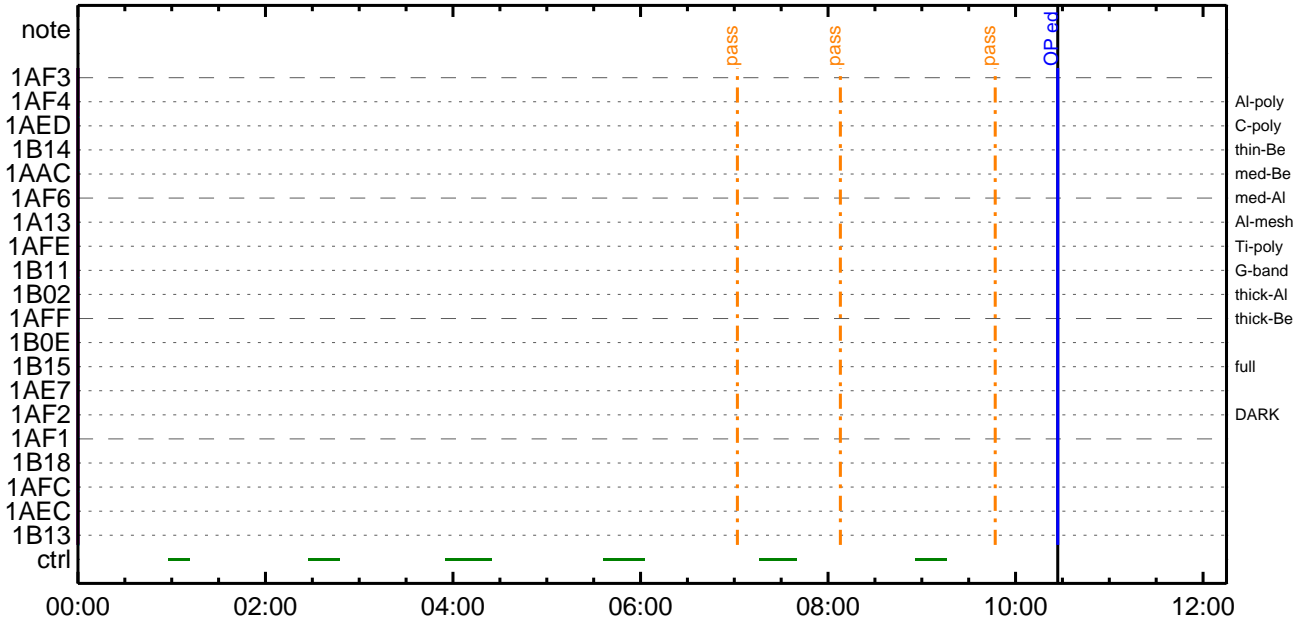
CMDI #0754 2016/03/09



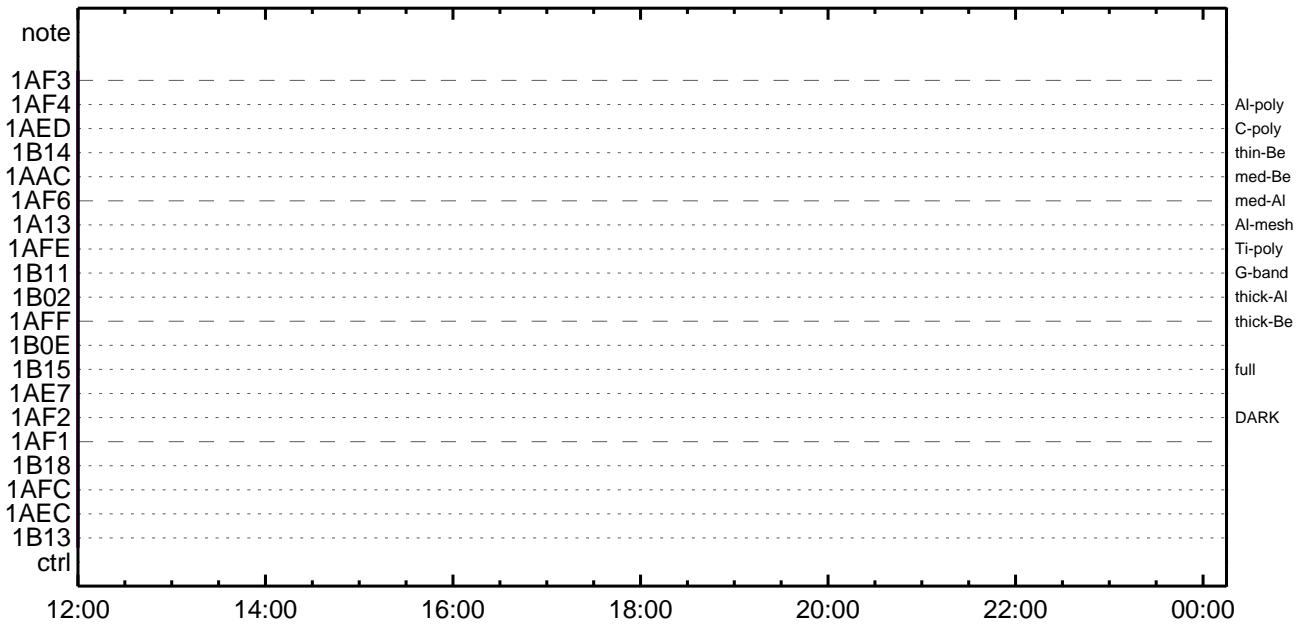
CMDI #0754 2016/03/09



CMDI #0754 2016/03/10



CMDI #0754 2016/03/10



(a) Spacecraft Operation Procedure (real-commands)

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main-711 2016-03-05 12:55:42 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Ü;ä
0005 C.
0006 C. YÀYB;¼Y³YBYÖYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ðÁð•µ°Æ»Í×ÁÇ¿ÍYÇYÁY×YÍ;¼YÉ;ÈÈèµ•ííÈ;ÈèÈ¼°ÇÒð•¿¿¼í¹Ç¿Í;çÀ®, ù¹àèðß¿Á÷¿®ð•ðÈðð³ðÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYÍ;¼YÉ;|YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYÍ;¼YÉ;ä
0019 . S. OP op-711:OP
0020 ()
0021 . S. OG og-711:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRES_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRES_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYÖY×½ª î»ðð³ îÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðî¼È¹ç•è² ÌOKðð³ îÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRES_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRES_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYÖY×½ª î»ðð³ îÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðî¼È¹ç•è² ÌOKðð³ îÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRES_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRES_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYÖY×½ª î»ðð³ îÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPðî¼È¹ç•è² ÌOKðð³ îÇ§
0081 C.
0082 . C. ***** òÈ²¼ðí¼Ä´ ¶¹°ðÈÈ¬ðÀ÷¿® (¼áµ-YÀYÖY×¼è¼çððÁÓÃæç¼ª ð¬ðè¼í¹ç¿çðâ) *****
0083 C. DHUYá;¼YÉ;È¼Y½, YÍ;¼YÉ;Èððíáð¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADð¬Á÷¿®NGðî¼È¹ç;ç°È²¼ðí¼TI-CMDÁ÷¿®ðî¼È¹Ôððð³ðÈ;£

```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 31s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2016-03-05 10:11:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP `uAÎuÎ»ö¼YpEÄDp¹eDCBC•x²è *****
0156 C. (¼a°iYÖYÄYÉYÞYÉYÁYçYèpE¼q¼A»Üp¹eè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** YD¥¹•Ĭ Daily±çĬNpĒ´Øp¹eDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥AY-¼A»Ü;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-713 2016-03-05 12:55:42 136 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿òÁò•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼ì¹çòÍ;çÀ®, ùò¹òèòòÇÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-287:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2016-03-05 10:11:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG_____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 85 83 06 06)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 85 83 08 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 80 80 20 20)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 80 80 20 08)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0a 80 80 08 20)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0b 80 60 20 18)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0c a0 80 18 20)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0f 80 80 06 06)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 10 80 80 08 08)
0094 +. DC 07-F0 MDP_XRT_FLD_ENA
0095 BC (d8)
```

```
0096 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0097 BC (c8)
0098 + DC 07-F0 MDP_XRT_ARS_DIS
0099 BC (d5)
0100 +. DC 07-F0 MDP_XRT_AEC_RESET
0101 BC (d0)
0102 +. DC 07-F0 MDP_XRT_FLD_RESET
0103 BC (da)
0104 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0105 BC (c4 04)
0106 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0107 BC (c5 07)
0108 . C. ----- Success Verify ? OK / NG ____
0109 C.
0110 C.
0111 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0112 C.
0113 +. DC 07-F0 MDP_XRT_MODE_OBSV
0114 BC (c2)
0115 +. TI 2016-03-05 10:11:02.0
0116 DC 07-F0 MDP_XRT_MODE_OBSV
0117 BC (c2)
0118 . C. ----- Success Verify ? OK / NG ____
0119 C.
0120 C. ***** XRT END *****
0121 C.
0122 . C. ***** MDP 'úÃîñî»ö¼ÝñÊÃðñ¹ñèDCBC•x²è *****
0123 C. (%ã°îÿÓÿÄÿÈÿÞÿËÿÀÿÇÿÈñ¼ññ¼Ã»Ûñ¹ñè)
0124 . S. DC-BC dcbc-402:DCBC
0125 (MDP_known_event)
0126 C.
0127 C.
0128 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎññË´Øñ¹ñèDCBC•x²è *****
0129 . S. DC-BC dcbc-153:DCBC
0130 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0131 C.
0132 C.
0133 . C. ;ãLOSÿÄÿ§ÿËÿËÿ-¼Ã»Û;ã
0134 C.
0135 . C. ***** LOS *****
0136 C.
```

*** OP Sequence for XRT ***

```

2016/03/05 10:21:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 10:21:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 10:21:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/03/05 10:22:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 54 18 01 68
2016/03/05 10:22:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/03/05 10:22:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/03/05 10:22:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/03/05 10:22:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/03/05 10:22:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/03/05 10:24:56.0 XRT_QT_PROG_SET_401_OG [0x191]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2016/03/05 10:24:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/03/05 10:25:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/03/05 14:42:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 14:42:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 14:42:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/03/05 14:42:06.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/03/05 14:45:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/03/05 14:52:30.0 XRT_Custom_430_OG [0x1ae]
2016/03/05 14:53:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/03/05 16:16:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 16:16:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 16:16:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/03/05 16:16:36.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/03/05 16:19:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/03/05 16:21:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 16:21:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 16:21:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/03/05 16:22:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 01 00 00 00 00
2016/03/05 16:22:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/03/05 16:22:20.5 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/03/05 16:22:22.5 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/03/05 16:22:24.5 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/03/05 16:22:26.5 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/03/05 16:24:56.5 XRT_QT_PROG_SET_409_OG [0x199]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 03
2016/03/05 16:24:58.5 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/03/05 16:40:00.0 XRT_Custom_430_OG [0x1ae]
2016/03/05 16:41:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/03/05 17:53:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 17:53:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 17:53:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/03/05 17:53:06.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/03/05 17:56:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/03/05 18:16:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 18:16:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/03/05 18:16:28.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2016/03/05 18:16:30.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2016/03/05 18:16:48.0 XRT_FLD_DIS_406_OG [0x196]

```

2016/03/05	18:19:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/03/05	18:19:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/03/05	18:19:28.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/03/05	18:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2016/03/05	18:25:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/05	18:25:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/05	18:25:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/05	18:25:48.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/03/05	18:25:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/03/05	18:25:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/03/05	18:25:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/03/05	18:25:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/03/05	18:26:30.0	AOCS_Or-e-point_Start_2_OG [0x098]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/05	18:28:26.0	XRT_QT_PROG_SET_409_OG [0x199]	AOCU_NM	5	02-76	01 00 00 00 00	
2016/03/05	18:28:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2016/03/05	18:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/03/05	19:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/05	19:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/05	19:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/05	19:30:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/05	19:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/05	19:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/05	19:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/03/05	21:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/05	21:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/05	21:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/05	21:07:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/05	21:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/05	21:30:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/05	21:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/03/05	22:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/05	22:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/05	22:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/05	22:45:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/05	22:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/05	23:05:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/05	23:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/03/06	00:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/06	00:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	00:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	00:22:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/06	00:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/06	00:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/06	00:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/03/06	01:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/06	01:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	01:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	01:45:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/06			MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2016/03/06	01:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/06	02:04:30.0	XRT_Custom_430_OG [0x1ae]							
2016/03/06	02:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/06	03:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	03:20:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	03:20:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/06	03:20:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/03/06	03:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/06	03:41:30.0	XRT_Custom_430_OG [0x1ae]							
2016/03/06	03:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/06	04:51:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	04:51:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	04:51:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/06	04:51:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/03/06	04:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/06	05:19:00.0	XRT_Custom_430_OG [0x1ae]							
2016/03/06	05:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/06	06:01:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	06:01:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	06:01:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/03/06	06:01:30.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/03/06	06:01:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/03/06	06:04:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/03/06	06:04:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/03/06	06:04:28.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2016/03/06	06:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/06	06:11:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	06:11:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	06:11:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/03/06	06:11:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2016/03/06	06:11:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/03/06	06:11:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/03/06	06:11:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/03/06	06:11:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/03/06	06:11:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/06	06:14:26.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2016/03/06	06:14:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/03/06	06:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/06	06:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	06:31:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	06:31:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/06	06:31:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/03/06	06:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/06	06:56:00.5	XRT_Custom_430_OG [0x1ae]							
2016/03/06	06:57:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/06	08:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	08:11:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/06	08:11:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/06	08:11:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							

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2016/03/06	08:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/03/06	08:33:00.0	XRT_Custom_430_OG [0x1ae]						
2016/03/06	08:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/06	09:51:30.5	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	09:51:32.5	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	09:51:34.5	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2016/03/06	09:51:36.5	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/03/06	09:54:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/03/06	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/03/06	10:00:00.0	AOCS_Or-point_Start_4_OG [0x09a]						
			AOCU_NM	5	02-76	00 55 db 01 68		
2016/03/06	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/03/06	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/03/06	10:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/03/06	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/03/06	10:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2016/03/06	10:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2016/03/06	10:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/03/06	10:06:30.0	XRT_Custom_430_OG [0x1ae]						
2016/03/06	10:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/06	15:15:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	15:15:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	15:15:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2016/03/06	15:15:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/03/06	15:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/03/06	15:29:00.0	XRT_Custom_430_OG [0x1ae]						
2016/03/06	15:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/06	15:34:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	15:34:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	15:34:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2016/03/06	15:34:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/03/06	15:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/03/06	15:38:30.0	XRT_Custom_430_OG [0x1ae]						
2016/03/06	15:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/06	16:51:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	16:51:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	16:51:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2016/03/06	16:51:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/03/06	16:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/03/06	17:15:00.0	XRT_Custom_430_OG [0x1ae]						
2016/03/06	17:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/06	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/06	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/03/06	18:00:00.0	AOCS_Or-point_Start_3_OG [0x099]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2016/03/06	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/03/06	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/03/06	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]						

2016/03/06	18:02:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/03/06	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2016/03/06	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/06	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	18:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/03/06	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01	00 00 00 00
2016/03/06	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/03/06	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/03/06	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/03/06	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/03/06	18:12:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/06	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2016/03/06	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/03/06	18:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/06	18:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	18:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	18:28:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/06	18:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/06	18:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/06	18:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/03/06	20:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/03/06	20:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/06	20:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	20:05:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	20:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/06	20:29:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/06	20:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/06	21:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/03/06	21:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/06	21:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	21:42:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	21:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/06	22:05:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/06	22:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/06	23:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/03/06	23:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/06	23:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	23:20:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/06	23:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/06	23:35:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/06	23:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/07	00:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2016/03/07	00:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/07	00:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	00:51:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	00:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/07	01:03:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/07	01:04:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2016/03/07	02:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	02:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	02:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/03/07	02:19:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/03/07	02:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/03/07	02:39:30.0	XRT_Custom_430_OG [0x1ae]				
2016/03/07	02:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/07	03:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	03:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	03:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/03/07	03:47:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/03/07	03:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/03/07	04:16:30.0	XRT_Custom_430_OG [0x1ae]				
2016/03/07	04:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/07	05:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	05:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	05:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/03/07	05:27:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/03/07	05:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/03/07	05:54:00.5	XRT_Custom_430_OG [0x1ae]				
2016/03/07	05:55:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/07	06:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	06:03:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	06:03:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/03/07	06:04:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2016/03/07	06:04:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/03/07	06:06:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/03/07	06:06:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/03/07	06:06:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2016/03/07	06:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/07	06:13:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	06:13:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	06:13:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78 00
2016/03/07	06:14:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00
2016/03/07	06:17:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/03/07	06:18:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/03/07	06:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/03/07	06:18:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/03/07	06:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/03/07	06:18:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/03/07	06:20:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2016/03/07	06:20:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/03/07	06:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/07	07:07:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	07:07:32.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/07	07:07:34.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/03/07	07:07:36.5	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/03/07	07:10:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]				

2016/03/07	07:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/07	07:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/07	08:47:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	08:47:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	08:47:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/07	08:47:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/07	08:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/07	09:07:30.0	XRT_Custom_430_OG [0x1ae]					
2016/03/07	09:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/07	10:29:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	10:29:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	10:29:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/07	10:29:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/07	10:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/07	10:37:00.0	XRT_Custom_430_OG [0x1ae]					
2016/03/07	10:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/07	14:17:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	14:17:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	14:17:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/07	14:17:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/07	14:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/07	14:23:30.0	XRT_Custom_430_OG [0x1ae]					
2016/03/07	14:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/07	15:50:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	15:50:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	15:50:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/07	15:50:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/07	15:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/07	16:13:30.0	XRT_Custom_430_OG [0x1ae]					
2016/03/07	16:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/07	17:26:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	17:26:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	17:26:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/07	17:26:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/07	17:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/07	17:50:00.0	XRT_Custom_430_OG [0x1ae]					
2016/03/07	17:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/07	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	18:05:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	18:05:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/03/07	18:06:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2016/03/07	18:06:18.0	XRT_FLD_DIS_406_OG [0x196]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/03/07	18:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/03/07	18:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/03/07	18:08:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2016/03/07	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/07	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	18:15:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/07	18:15:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	

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2016/03/07	18:16:00.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	01	00	00	00	00
2016/03/07	18:16:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/03/07	18:16:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/03/07	18:16:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/03/07	18:16:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/03/07	18:16:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/07	18:18:56.0	XRT_QT_PROG_SET_409_OG [0x199] MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2016/03/07	18:18:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/03/07	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/07	19:03:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/07	19:03:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/07	19:03:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/07	19:03:36.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/03/07	19:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/07	19:26:30.0	XRT_Custom_430_OG [0x1ae]							
2016/03/07	19:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/07	20:40:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/07	20:40:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/07	20:40:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/07	20:40:36.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/03/07	20:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/07	21:04:00.0	XRT_Custom_430_OG [0x1ae]							
2016/03/07	21:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/07	22:18:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/07	22:18:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/07	22:18:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/07	22:18:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/03/07	22:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/07	22:39:30.0	XRT_Custom_430_OG [0x1ae]							
2016/03/07	22:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/07	23:55:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/07	23:55:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/07	23:55:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/07	23:55:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/03/07	23:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/08	00:05:00.0	XRT_Custom_430_OG [0x1ae]							
2016/03/08	00:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/03/08	01:20:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/08	01:20:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/08	01:20:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/08	01:20:36.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/03/08	01:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/08	01:35:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/08	01:35:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/08	01:35:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/03/08	01:36:00.0	AOCS_OrE-point_Start_5_OG [0x09b] AOCU_NM	5	02-76	00	ad	59	00	00
2016/03/08	01:36:18.0	XRT_FLD_DIS_443_OG [0x1bb] MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/03/08	01:38:54.0	XRT_FLRCTRL_DIS_437_OG [0x1b5] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/03/08	01:38:56.0	XRT_ARS_DIS_435_OG [0x1b3]							

2016/03/08	01:38:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/03/08	01:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02	
2016/03/08	03:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/08	03:35:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	03:35:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	03:36:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2016/03/08	03:36:18.0	XRT_FLD_DIS_443_OG [0x1bb]	AOCU_NM	5	02-76	00	00 00 54 00	
2016/03/08	03:38:54.0	XRT_FLRCTRL_DIS_437_OG [0x1b5]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/03/08	03:38:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/03/08	03:38:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/03/08	03:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2016/03/08	05:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/08	05:35:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	05:35:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	05:36:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2016/03/08	05:36:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00	00 00 00 00	
2016/03/08	05:38:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/03/08	05:38:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/03/08	05:38:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/03/08	05:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2016/03/08	05:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/08	05:45:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	05:45:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	05:46:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2016/03/08	05:46:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01	00 00 00 00	
2016/03/08	05:46:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/03/08	05:46:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/03/08	05:46:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/03/08	05:46:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/03/08	05:48:56.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/03/08	05:48:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2016/03/08	05:49:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2016/03/08	06:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/08	06:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	06:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	06:03:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/03/08	06:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/03/08	06:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/03/08	06:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2016/03/08	07:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/03/08	07:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	07:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/03/08	07:44:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/03/08	07:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/03/08	08:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/03/08	08:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2016/03/08	09:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2016/03/08	09:23:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/08	09:23:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/03/08	09:23:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/03/08	09:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/03/08	09:58:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/03/08	09:59:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				