

Period: 2016/03/22 09:23:00 - 2016/03/26 10:07:00

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XOB #1AF4: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms														
Term		Pointing (x, y)					Comment							
03/23 12:38:00 - 03/23 12:44:54		Fixed (-528.4, 528.4)					4/4							
PROG= 19		1-time(s)												
Subr= 1		1-time(s)			2.0sec									
Seq= 3		1-time(s)			2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 512)		Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	1024x1024 (1536, 512)		Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 512)		Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	3ms	Obs	1x1	1024x1024 (1536, 512)		Q=98	0	0	2.0sec
Subr= 2		1-time(s)			2.0sec									

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and															
Term			Pointing (x, y)					Comment							
03/23 19:23:00 - 03/23 23:35:00			Track (162.2, 365.9) @ 03/23 19:20:00					AR 12524 obs.							
03/24 06:32:00 - 03/24 09:03:00			Track (259.1, 362.9) @ 03/24 06:29:00					AR 12524 obs.							
PROG= 10 Inf.-time(s)															
Subr= 1		1-time(s)		2.0sec											
Seqn= 56		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Subr= 2		5-time(s)		2.0sec											
Seqn= 75		1-time(s)		2.0sec											
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2	0	2.0sec
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2	0	2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Seqn= 96		4-time(s)		60.0sec											
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	15.0sec
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	1	2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	1	15.0sec
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	2	2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
XOB #1B1B: Limb_Obs 2-filter - Al/poly and Al/mesh (AEC2) 60s cadence, G-band - 384x384 3ms-2											
Term		Pointing (x, y)						Comment			
03/24 03:03:00 - 03/24 06:18:54		Track (-198.1, 24.1) @ 03/24 03:00:00						Equatorial coronal hole obs.			
PROG= 15 Inf-time(s)											
Subr= 1		1-time(s)		2.0sec							
Seqn= 9		2-time(s)		2.0sec							
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	2.0sec
Subr= 2		1-time(s)		2.0sec							
Seqn= 7		1-time(s)		30.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	2.0sec
Subr= 3		30-time(s)		2.0sec							
Seqn= 73		1-time(s)		60.0sec							
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=90	2	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -														
Term		Pointing (x, y)				Comment								
03/23 12:48:00 - 03/23 19:09:54		Fixed (-16.0, -946.0)				HOP 206 (S-pole)								
03/23 19:23:00 - 03/23 23:35:00		Track (162.2, 365.9) @ 03/23 19:20:00				AR 12524 obs.								
03/24 03:03:00 - 03/24 06:18:54		Track (-198.1, 24.1) @ 03/24 03:00:00				Equatorial coronal hole obs.								
03/24 06:32:00 - 03/24 09:03:00		Track (259.1, 362.9) @ 03/24 06:29:00				AR 12524 obs.								
PROG= 07 30-time(s)														
Subr= 1		20-time(s)		2.0sec										
Seqn= 11		1-time(s)		2.0sec										
Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn=100		1-time(s)		10.0sec										
thin-Be/Open		med-Be/Open close		Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)		Q=95	2	0	2.0sec
med-Be/Open		Open/thick-Al close		Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Open/thick-Al		Open/thick-Be close		Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 10		1-time(s)		2.0sec										
med-Al/Open		med-Al/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Seqn= 11		1-time(s)		2.0sec										
Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn= 84		1-time(s)		2.0sec										
Open/G-band		Open/G-band open		Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/G-band		Open/G-band close		Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al close		Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al close		Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

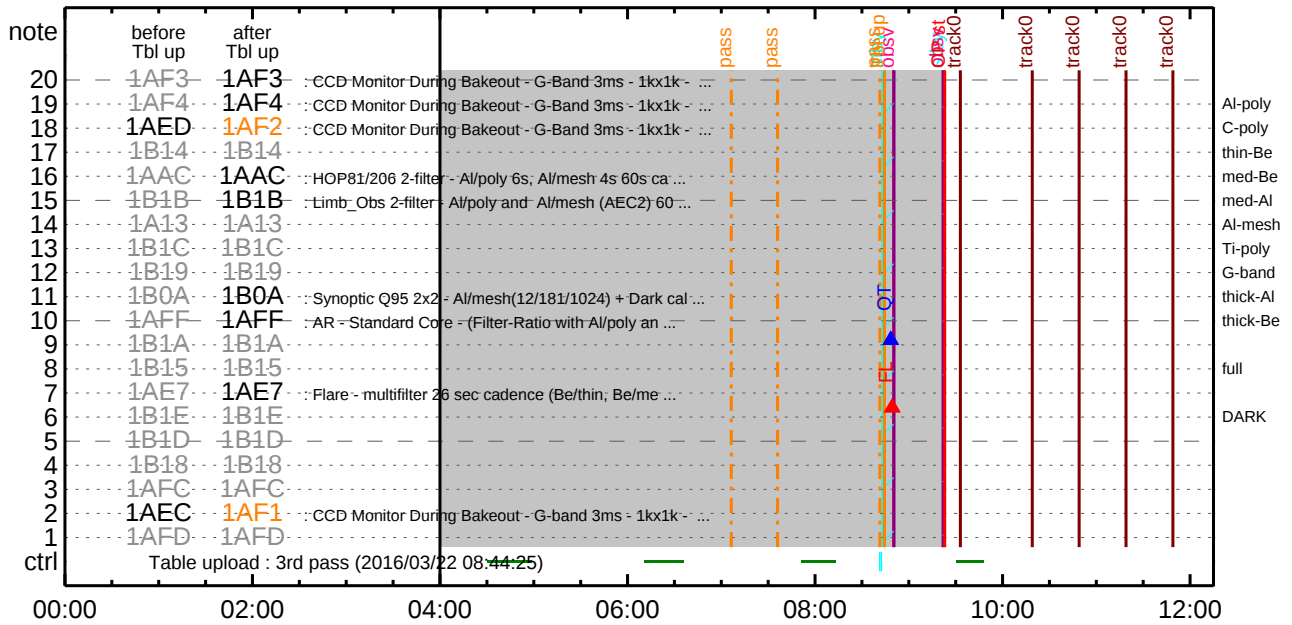
* * * * *

Flare Detection

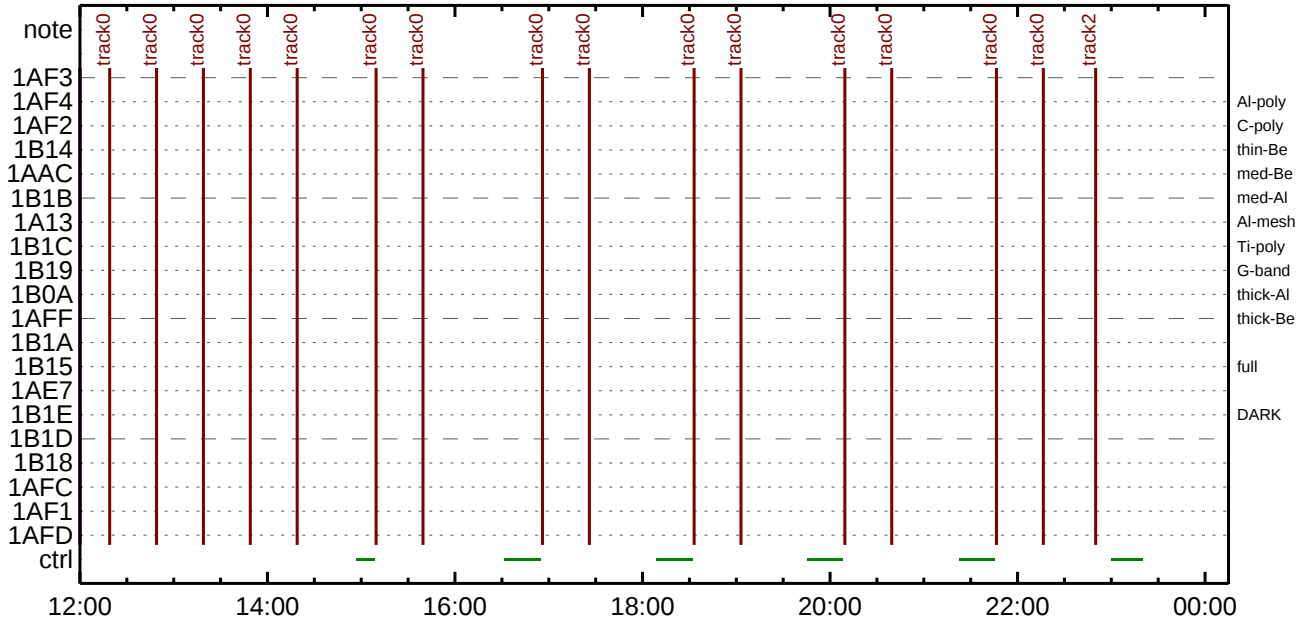
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FLD Patrol											
Term		Pointing (x, y)				Comment					
03/23 12:45:18 - 03/23 19:10:18		Fixed (-16.0, -946.0)				HOP 206 (S-pole)					
03/23 19:20:18 - 03/24 06:19:18		Track (162.2, 365.9) @ 03/23 19:20:00				AR 12524 obs.					
03/24 06:29:18 - 03/26 10:07:00		Track (259.1, 362.9) @ 03/24 06:29:00				AR 12524 obs.					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

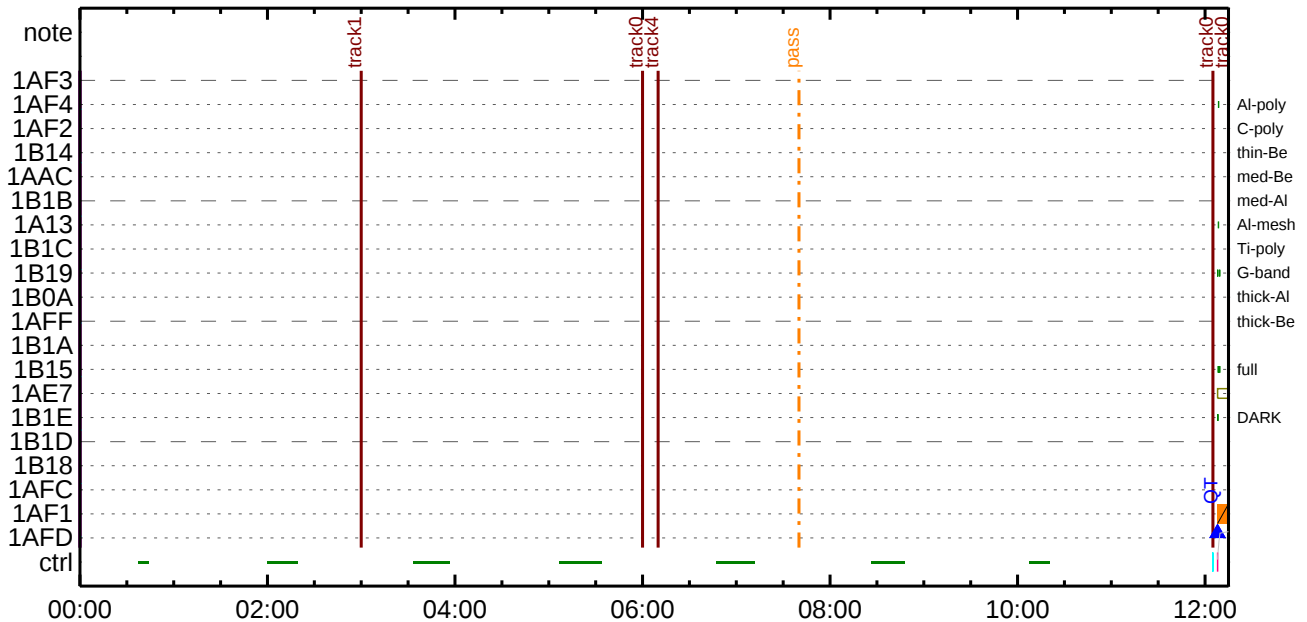
CMDI #0786 2016/03/22



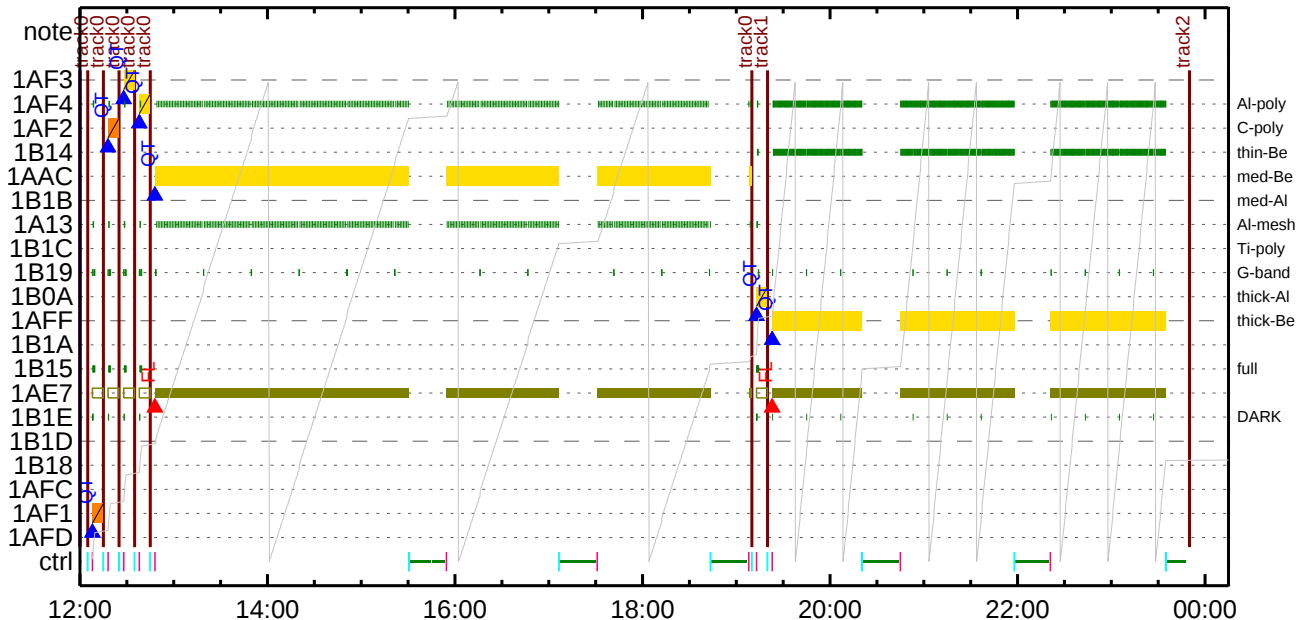
CMDI #0786 2016/03/22



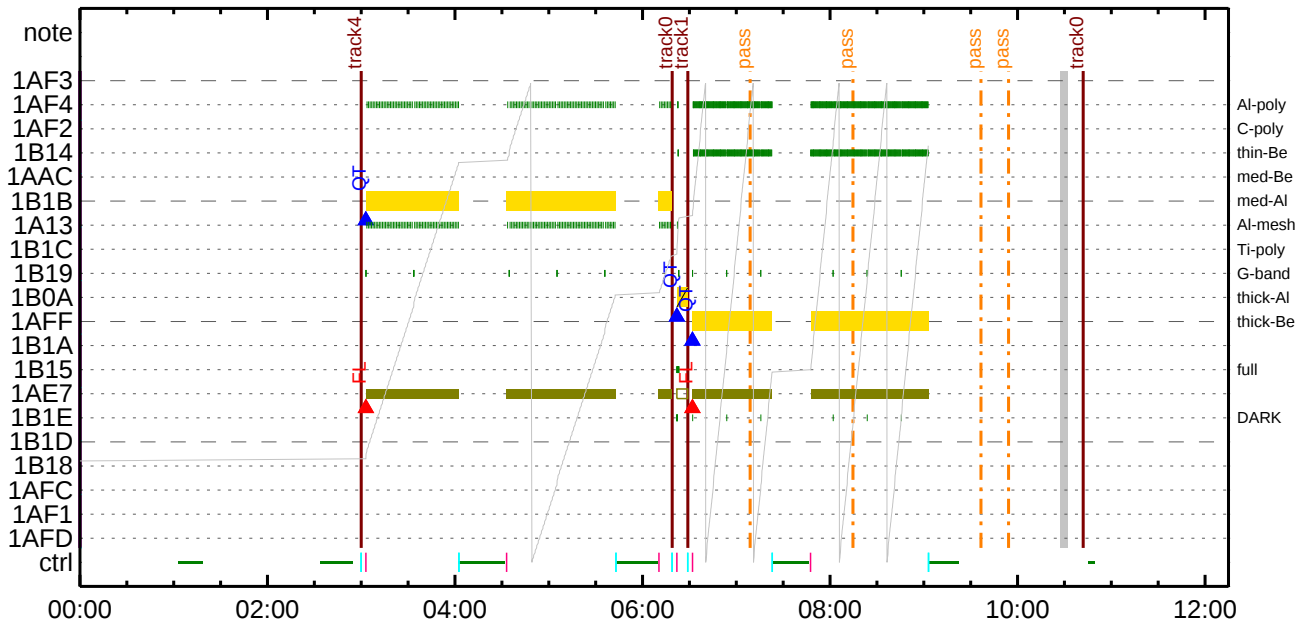
CMDI #0786 2016/03/23



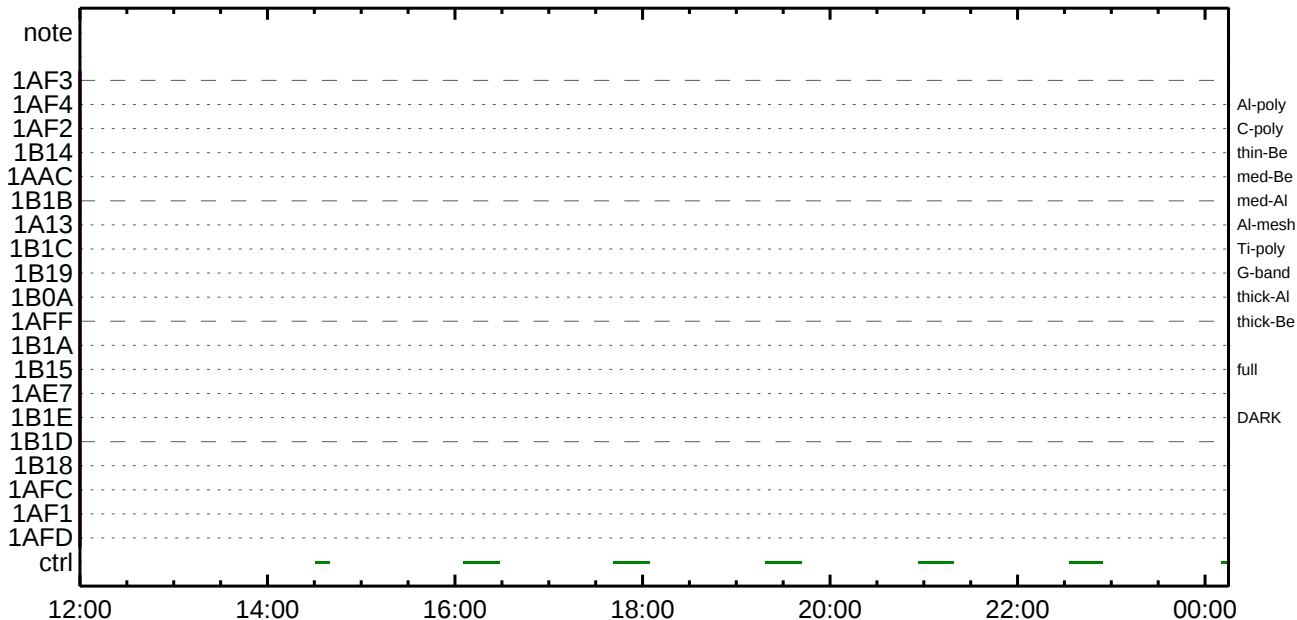
CMDI #0786 2016/03/23



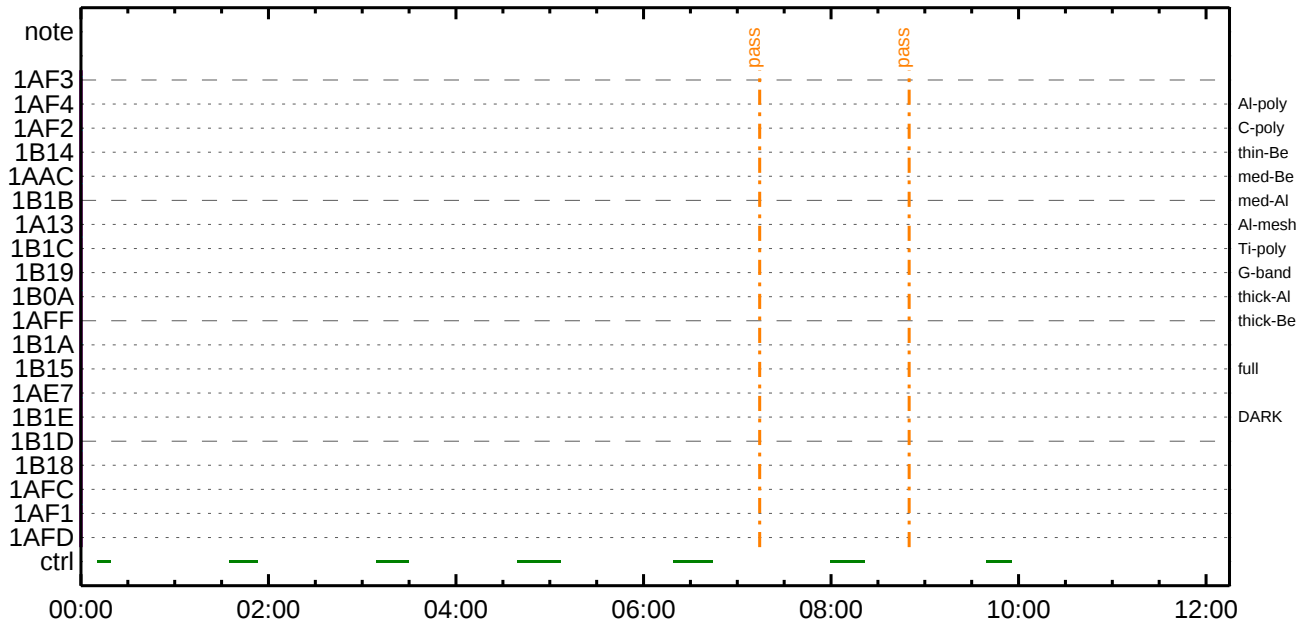
CMDI #0786 2016/03/24



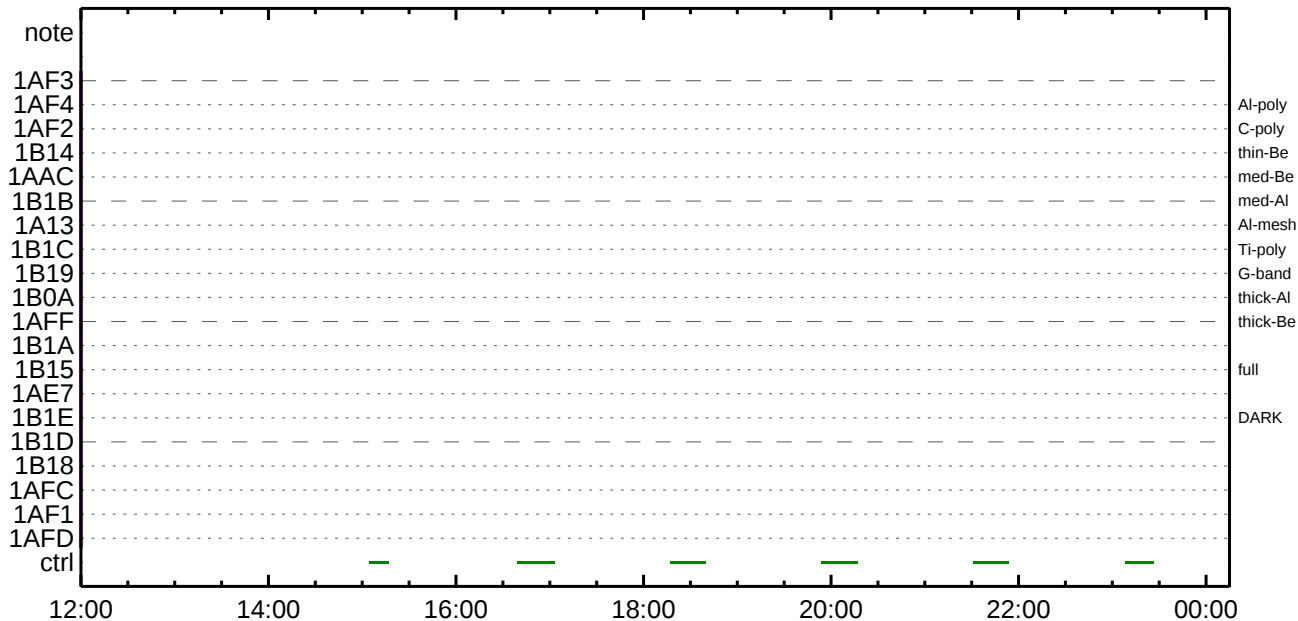
CMDI #0786 2016/03/24



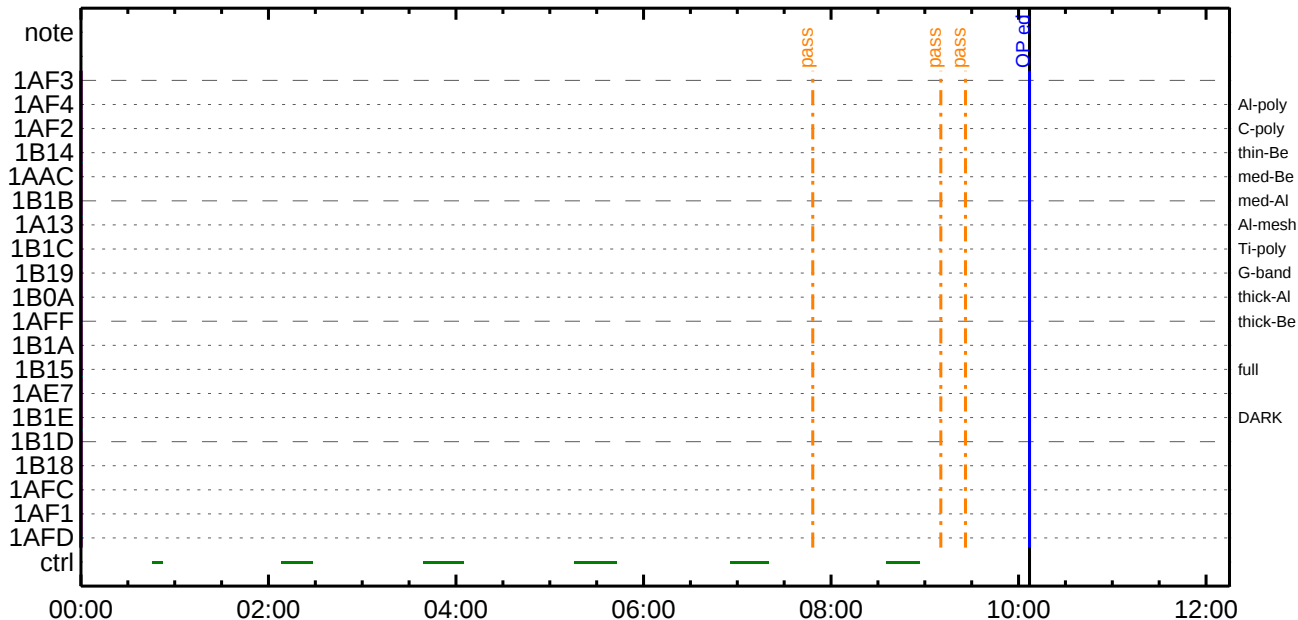
CMDI #0786 2016/03/25



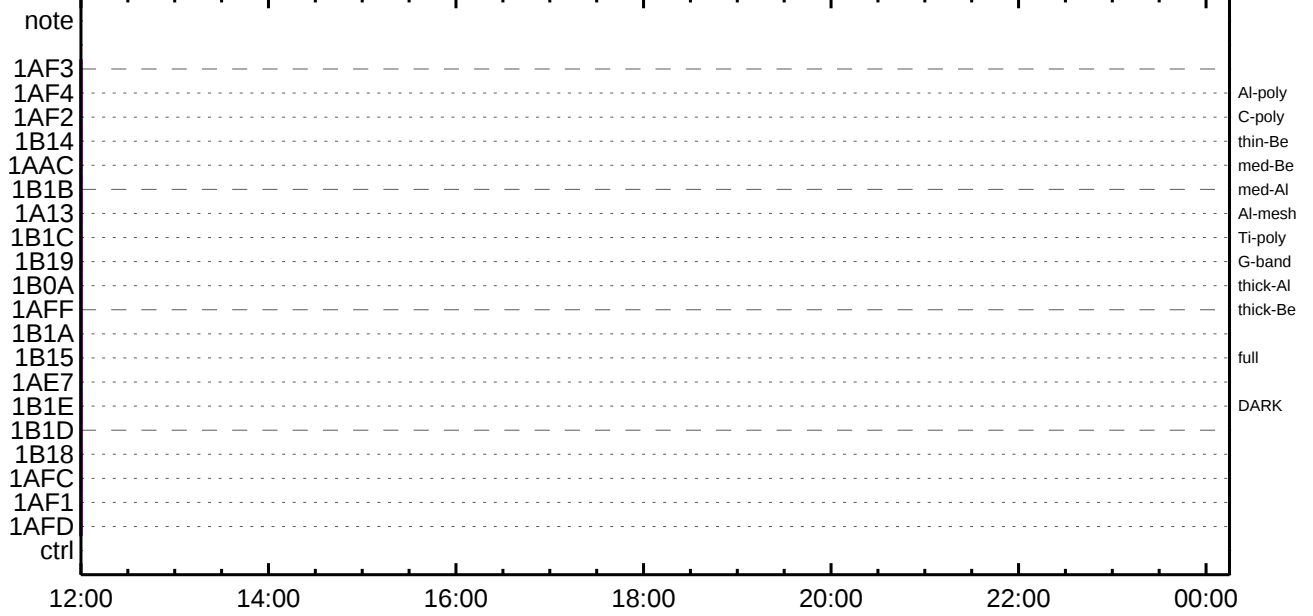
CMDI #0786 2016/03/25



CMDI #0786 2016/03/26



CMDI #0786 2016/03/26



(a) Spacecraft Operation Procedure (real-commands)

```
main-745 2016-03-22 13:09:38 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¿İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼¹Ç¿İjÇÀ®, Ü¤¹¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ĒĒ, İ×ĒY¤¤ŁOS¤¤¤Ç¿İ»p´Ŏ¤Ŏ¹İİ, ¤•jÇĒŎİ×¤ĒXĒŎON¤İ¹Ŏ¤Ēİ¤Ē¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ŏ¥Ē¥İ¥Á¥¼ŎĀŎ¤¬°ĒĒĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ĒĒ, ¼Ē½Ç¤Ŏ¼Ē¹Ŏ¤¹¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ĒĒ
0033 C. *****
0034 C. ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ē¹Ŏ¤»¤°jÇDCBC-150¤Ŏ¿ĒĒ¤¤jĒ
0035 C.
0036 . C. jãPT1°ĒĒ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Ŏ, j¼Ŏ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0045 C.
0046 . C. jã¥Ç¥Ŏ¥Ē¥ĒĒĒĒĒjĒĒ•Ē°²ŎĒŎjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Ŏ, j¼Ŏ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0052 C.
0053 C.
0054 . C. PT1°ĒĒ, ¤¬¼«Ē°ĒĒ»İ¤•¤¿, ĒjÇ°Ē²¼¤Ŏ¼Ē¹Ŏ¤¹¤ĒjĒ
0055 C. ¥Ç¥Ŏ¥Ē¥ĒĒĒĒĒĒĒĒ•Ē°²ŎĒŎ¤¬Ē¤¤¼¹Ç¿İ°İ»¤¹¤Ē¤¤¤ÇĒŎ¤ĒjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ĒĒ
0059 C. *****
0060 C. ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ē¹Ŏ¤»¤°jÇDCBC-151¤Ŏ¿ĒĒ¤¤jĒ
0061 C.
0062 . C. jãPT2°ĒĒ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Ŏ, j¼Ŏ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0071 C.
0072 . C. jã¥Ç¥Ŏ¥Ē¥ĒĒĒĒĒjĒĒ•Ē°²ŎĒŎjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Ŏ, j¼Ŏ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Ŏ, j¼Ŏ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Ŏ, j¼Ŏ)
0078 C.
0079 . C. *****
0080 C. DR°ĒĒ, Ē¤»İjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ĒĒ, Ē¤»İjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

[illegible]

0194	C.			
0195	+. TI	2016-03-22 09:22:59.5		
0196	DC	01-B2 DHU_OP_START		
0197	C.	C[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0198	C.			
0199	C.	°Ê²¼□İÄê%İİÎñİ¥Á¥\$¥Á¥¹àİÜ		
0200	C.	C[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0201	C.	C[HK1_TI_CMD_NUM]	EQ	4
0202	C.	C[HK1_NEXT_EXEC_PIM]	EQ	DHU
0203	C.	C[HK1_NEXT_EXEC_DC]	EQ	0xB3
0204	C.			
0205	C.	*****		
0206	C.	TIİİî°è¥Á¥Ó¥×		
0207	C.	*****		
0208	C.			
0209	C.	TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)		
0210	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0211	BC	(03 ab 03 01 02)		
0212	C.	C[HK1_DMP_TOP_ADRS_1]	EQ	07
0213	C.	C[HK1_DMP_TOP_ADRS_0]	EQ	2B
0214	C.	C[HK1_DMP_BLOCK_NUM]	EQ	3
0215	C.	C[HK1_DMP_REPEAT_NUM]	EQ	0
0216	C.	C[HK1_DMA_DMP_PIM]	EQ	DHU
0217	+. DC	01-22 DHU_MODE_CHNG		
0218	BC	(07 0b f8)		
0219	C.	C[HK1_PKT_FORM_NO]	EQ	7
0220	C.	C[HK1_PKT_GEN_TIME]	EQ	0.25 s
0221	C.	C[HK1_S_TLM_BIT_RATE]	EQ	32k
0222	C.	C[HK1_X_TLM_BIT_RATE]	EQ	4M
0223	C.	C[HK1_DMP_CHK_FLG]	EQ	EXEC
0224	C.			
0225	C.	¥Á¥Ó¥×¥½ªİ»ð³İÇ\$		
0226	C.	C[HK1_DMP_CHK_FLG]	EQ	NON
0227	C.			
0228	C.	RAM ID=TI_TBL□İ%Ê¹Ç•è²İOKð³İÇ\$		
0229	C.			
0230	C.	DHU¥âİ¼¥ÊİÊ¼Ý½¥İİ¼¥Êİ»ðİâ□¹		
0231	+. DC	01-22 DHU_MODE_CHNG		
0232	BC	(02 0a f8)		
0233	C.	C[HK1_PKT_FORM_NO]	EQ	2
0234	C.	C[HK1_PKT_GEN_TIME]	EQ	0.5S
0235	C.	C[HK1_S_TLM_BIT_RATE]	EQ	32K
0236	C.	C[HK1_X_TLM_BIT_RATE]	EQ	4M
0237	C.			
0238	C.	*****		
0239	C.	SOT TI command set		
0240	C.	*****		
0241	C.	Execute, after the success of OP upload.		
0242	+. TI	2016-03-22 09:22:16.0		
0243	DC	07-F0 MDP_SOT_MODE_STBY		
0244	BC	(41)		
0245	C.	-----		
0246	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0247	C.	-----		
0248	C.	***** SOT END *****		
0249	C.	Stop EIS observation and temporarily disable EIS mode changes		
0250	C.			
0251	C.			
0252	C.	***** Start EIS operation (TI set) *****		
0253	C.	Execute, after the success of OP upload.		
0254	C.	Set EIS TI-commands		
0255	+. TI	2016-03-22 09:22:30.0		
0256	DC	07-FC EIS_MODE_MANU		
0257	BC	(21 02)		
0258	+. TI	2016-03-22 09:22:40.0		
0259	DC	07-FC EIS_MODE_CHG_DIS		
0260	BC	(22)		
0261	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0262	C.	***** End EIS operation (TI set) *****		
0263	C.			
0264	C.			
0265	C.			
0266	C.	***** XRT START *****		
0267	C.	Execute, after the success of OP upload.		
0268	+. TI	2016-03-22 09:22:00.0		
0269	DC	07-F0 MDP_XRT_MODE_STBY		
0270	BC	(c3)		
0271	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0272	C.			
0273	C.	***** XRT END *****		
0274	C.			
0275	C.	***** MDP `ÜÄİ□İ»ð¼Ý□ÊÄð□¹ðDCBC•×²è *****		
0276	C.	(¼â°İ¥Ö¥Ä¥Ê¥ð¥Ê¥â¥Ç¥è¥Ê¼¼□¼A»Ü□¹è)		
0277	S.	DC-BC dcbc-402:DCBC		
0278		(MDP_known_event)		
0279	C.			
0280	C.			
0281	C.	***** ¥ð¥¹•İ Daily±çİÎñÈ´ð□¹èDCBC•×²è *****		
0282	S.	DC-BC dcbc-153:DCBC		
0283		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0284	C.			
0285	C.			
0286	C.	İ äLOS¥Á¥\$¥Á¥¼A»Üİä		
0287	C.			
0288	C.	***** LOS *****		
0289	C.			


```

0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload SP Observation Table>
0109 S. RAM ram-285:MDP_OBS_S
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2016-03-22 09:22:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 +. DC 07-F0 MDP_XRT_CTRL_MANU
0136 BC (c1)
0137 +. DC 07-F0 MDP_XRT_MODE_STBY
0138 BC (c3)
0139 C. ----- Success Verify ? OK / NG____
0140 C.
0141 C. XRT Obs. Table Upload
0142 S. RAM ram-291:MDP_OBS_X
0143 ( )
0144 C.
0145 +. DC 07-F0 MDP_DUMP_XRTTBL
0146 BC (84 07 00 00 00 3a d4)
0147 C. ----- Comparison Check ? OK / ERR ____
0148 C.
0149 C.
0150 +. DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 01 b1 b1 04 04)
0152 +. DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 02 b1 b1 08 08)
0154 +. DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 03 b1 b1 08 08)
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 04 b1 b1 06 06)
0158 +. DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 +. DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 c0 c0 10 10)
0162 +. DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 20)
0164 +. DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 40 c0 10 10)
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 40 40 10 10)
0168 +. DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0b c0 40 10 10)
0170 +. DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0c 80 80 20 08)
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 0d 80 80 08 20)
0174 +. DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 0f 80 80 06 06)
0176 +. DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 10 80 80 08 08)
0178 +. DC 07-F0 MDP_XRT_FLD_ENA
0179 BC (d8)
0180 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0181 BC (c8)
0182 +. DC 07-F0 MDP_XRT_ARS_DIS
0183 BC (d5)
0184 +. DC 07-F0 MDP_XRT_AEC_RESET
0185 BC (d0)
0186 +. DC 07-F0 MDP_XRT_FLD_RESET
0187 BC (da)
0188 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0189 BC (c4 0a)
0190 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0191 BC (c5 07)
0192 C. ----- Success Verify ? OK / NG ____
0193 C.

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0194 C.
0195 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0196 C.
0197 +. DC 07-F0 MDP_XRT_MODE_OBSV
0198 BC (c2)
0199 +. TI 2016-03-22 09:22:02.0
0200 DC 07-F0 MDP_XRT_MODE_OBSV
0201 BC (c2)
0202 . C. ----- Success Verify ? OK / NG ____
0203 C.
0204 C. ***** XRT END *****
0205 C.
0206 . C. ***** MDP 'ÛÃÎ»ÖÝðÊÂð¹ðÐDCBC•x²è *****
0207 C. (¾å°ï¥Ö¥Ã¥Ê¥ð¥Ê¥â¥¢¥èð½¼ð¼Ã»Û¹ðè)
0208 . S. DC-BC dcbc-402:DCBC
0209 (MDP_known_event)
0210 C.
0211 C.
0212 . C. ***** ¥Ð¥¹•Ï Daily±¿ÎÑðË´Ø¹ðÐDCBC•x²è *****
0213 . S. DC-BC dcbc-153:DCBC
0214 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0215 C.
0216 C.
0217 . C. ¡ãLOS¥Á¥S¥Ã¥¬¼Ã»Û¡ä
0218 C.
0219 . C. ***** LOS *****
0220 C.

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*** OP Sequence for XRT ***

2016/03/22	09:33:00.0	AOCS_ORe-point_Start_1_OG [0x097]	
		AOCU_NM	5 02-76 00 56 4d 01 68
2016/03/22	10:19:00.0	AOCS_ORe-point_Start_2_OG [0x098]	
		AOCU_NM	5 02-76 00 4d 68 01 68
2016/03/22	10:49:00.0	AOCS_ORe-point_Start_3_OG [0x099]	
		AOCU_NM	5 02-76 00 44 8b 01 68
2016/03/22	11:19:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	
		AOCU_NM	5 02-76 00 3b a5 01 68
2016/03/22	11:49:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	
		AOCU_NM	5 02-76 00 32 c0 01 68
2016/03/22	12:19:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	
		AOCU_NM	5 02-76 00 29 db 01 68
2016/03/22	12:49:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	
		AOCU_NM	5 02-76 00 20 fd 01 68
2016/03/22	13:19:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	
		AOCU_NM	5 02-76 00 18 18 01 68
2016/03/22	13:49:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	
		AOCU_NM	5 02-76 00 0f 33 01 68
2016/03/22	14:19:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	
		AOCU_NM	5 02-76 00 06 4d 01 68
2016/03/22	15:09:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]	
		AOCU_NM	5 02-76 00 fe 4e 01 68
2016/03/22	15:39:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]	
		AOCU_NM	5 02-76 00 f5 69 01 68
2016/03/22	16:56:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	
		AOCU_NM	5 02-76 00 ec 8c 01 68
2016/03/22	17:26:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	
		AOCU_NM	5 02-76 00 e3 a6 01 68
2016/03/22	18:33:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	
		AOCU_NM	5 02-76 00 da c1 01 68
2016/03/22	19:03:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	
		AOCU_NM	5 02-76 00 d1 dc 01 68
2016/03/22	20:09:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]	
		AOCU_NM	5 02-76 00 c8 fe 01 68
2016/03/22	20:39:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]	
		AOCU_NM	5 02-76 00 c0 19 01 68
2016/03/22	21:46:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]	
		AOCU_NM	5 02-76 00 b7 34 01 68
2016/03/22	22:16:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]	
		AOCU_NM	5 02-76 00 ae 4e 01 68
2016/03/22	22:50:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	
		AOCU_NM	5 02-76 02 00 00 00 00
2016/03/23	03:00:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	
		AOCU_NM	5 02-76 01 00 00 00 00
2016/03/23	06:00:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	
		AOCU_NM	5 02-76 00 00 00 00 00
2016/03/23	06:05:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_441_OG [0x1b9]	
		TCIB_XRT_S_HTR_A_DIS	0 04-C0
2016/03/23	06:10:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	
		AOCU_NM	5 02-76 04 00 00 00 00
2016/03/23	12:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2016/03/23	12:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2016/03/23	12:04:58.0	XRT_FOCUS_POSITION_444_OG [0x1bc]	
		XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2016/03/23	12:05:00.0	AOCS_ORe-point_Start_25_OG [0x0af]	
		AOCU_NM	5 02-76 00 2e f9 2e f9
2016/03/23	12:05:18.0	XRT_FLD_DIS_449_OG [0x1c1]	
		MDP_XRT_FLD_DIS	1 07-F0 d9
2016/03/23	12:05:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	
		MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2016/03/23	12:07:56.0	XRT_ARS_DIS_435_OG [0x1b3]	
		MDP_XRT_ARS_DIS	1 07-F0 d5
2016/03/23	12:07:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]	
		MDP_XRT_QT_PROG_SET	2 07-F0 c4 02
2016/03/23	12:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2016/03/23	12:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2016/03/23	12:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2016/03/23	12:14:58.0	XRT_FOCUS_POSITION_444_OG [0x1bc]	
		XRT_FOCUS_POSITION	4 07-F8 22 ff aa 00
2016/03/23	12:15:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]	
		AOCU_NM	5 02-76 00 2e f9 d1 07
2016/03/23	12:15:18.0	XRT_FLD_DIS_449_OG [0x1c1]	
		MDP_XRT_FLD_DIS	1 07-F0 d9
2016/03/23	12:15:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	
		MDP_XRT_FLRCTRL_DIS	1 07-F0 c9
2016/03/23	12:17:56.0	XRT_ARS_DIS_435_OG [0x1b3]	
		MDP_XRT_ARS_DIS	1 07-F0 d5
2016/03/23	12:17:58.0	XRT_QT_PROG_SET_427_OG [0x1ab]	
		MDP_XRT_QT_PROG_SET	2 07-F0 c4 12
2016/03/23	12:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	
		MDP_XRT_CTRL_AUTO	1 07-F0 c0
2016/03/23	12:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2016/03/23	12:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	
		MDP_XRT_CTRL_MANU	1 07-F0 c1
2016/03/23	12:24:58.0	XRT_FOCUS_POSITION_444_OG [0x1bc]	

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2016/03/23	12:25:00.0	AOCS_ORe-point_Start_27_OG [0x0b1]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	d1	07	d1 07
2016/03/23	12:25:18.0	XRT_FLD_DIS_449_OG [0x1c1]	MDP_XRT_FLD_DIS	1	07-F0		d9		
2016/03/23	12:25:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2016/03/23	12:27:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2016/03/23	12:27:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	14	
2016/03/23	12:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2016/03/23	12:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	12:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	12:34:58.0	XRT_FOCUS_POSITION_444_OG [0x1bc]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/03/23	12:35:00.0	AOCS_ORe-point_Start_28_OG [0x0b2]	AOCU_NM	5	02-76	00	d1	07	2e f9
2016/03/23	12:35:18.0	XRT_FLD_DIS_449_OG [0x1c1]	MDP_XRT_FLD_DIS	1	07-F0		d9		
2016/03/23	12:35:20.0	XRT_FLRCTRL_DIS_425_OG [0x1a9]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2016/03/23	12:37:56.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2016/03/23	12:37:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	13	
2016/03/23	12:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2016/03/23	12:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	12:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	12:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/03/23	12:45:00.0	AOCS_ORe-point_Start_29_OG [0x0b3]	AOCU_NM	5	02-76	00	54	18	01 68
2016/03/23	12:45:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8		
2016/03/23	12:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2016/03/23	12:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0		d0		
2016/03/23	12:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2016/03/23	12:45:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0		da		
2016/03/23	12:47:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	10	
2016/03/23	12:47:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	07	
2016/03/23	12:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2016/03/23	15:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	15:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	15:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2016/03/23	15:30:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2016/03/23	15:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2016/03/23	15:53:30.0	XRT_Custom_430_OG [0x1ae]							
2016/03/23	15:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2016/03/23	17:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	17:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	17:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2016/03/23	17:06:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2016/03/23	17:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2016/03/23	17:30:00.0	XRT_Custom_430_OG [0x1ae]							
2016/03/23	17:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2016/03/23	18:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	18:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/03/23	18:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2016/03/23	18:43:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2016/03/23	18:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2016/03/23	19:07:00.0	XRT_Custom_430_OG [0x1ae]							
2016/03/23	19:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

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2016/03/23	19:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/23	19:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	19:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	19:10:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/03/23	19:10:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/03/23	19:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/03/23	19:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/03/23	19:12:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/03/23	19:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/03/23	19:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/23	19:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	19:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	19:20:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/03/23	19:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2016/03/23	19:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/03/23	19:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/03/23	19:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/03/23	19:20:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/03/23	19:22:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/03/23	19:22:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2016/03/23	19:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/03/23	20:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/23	20:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	20:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	20:20:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/03/23	20:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/03/23	20:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/03/23	20:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/03/23	21:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/23	21:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	21:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	21:58:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/03/23	22:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/03/23	22:20:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/03/23	22:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/03/23	23:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/03/23	23:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	23:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/23	23:35:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/03/23	23:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/03/23	23:50:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/03/24	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	02 00 00 00 00
2016/03/24	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/24	02:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/03/24	03:00:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/03/24	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00
2016/03/24	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8

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2016/03/24	03:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/03/24	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/03/24	03:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/24	03:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2016/03/24	03:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/03/24	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/24	04:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	04:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	04:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/24	04:02:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/24	04:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/24	04:32:00.0	XRT_Custom_430_OG [0x1ae]					
2016/03/24	04:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/24	05:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	05:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	05:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/24	05:43:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/24	05:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/24	06:09:30.0	XRT_Custom_430_OG [0x1ae]					
2016/03/24	06:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/24	06:18:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	06:18:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	06:18:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2016/03/24	06:19:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	00 00 00 00
2016/03/24	06:19:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/03/24	06:21:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/03/24	06:21:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/03/24	06:21:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2016/03/24	06:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/24	06:28:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	06:28:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	06:28:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/03/24	06:29:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	01	00 00 00 00
2016/03/24	06:29:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/03/24	06:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/03/24	06:29:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/03/24	06:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/03/24	06:29:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/24	06:31:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2016/03/24	06:31:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/03/24	06:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/03/24	07:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	07:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/03/24	07:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/03/24	07:23:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/03/24	07:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/03/24	07:46:30.0	XRT_Custom_430_OG [0x1ae]					
2016/03/24	07:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2016/03/24	09:03:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/03/24	09:03:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2016/03/24	09:03:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2016/03/24	09:03:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2016/03/24	09:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2016/03/24	10:42:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]		
		AOCU_NM	5	02-76 00 00 00 00 00