

# XRT Timeline to be uploaded on 2016/04/02

Period: 2016/04/02 09:21:00 - 2016/04/07 10:07:00

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## Normal mode

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### XOB #1B21: AR - Standard Core - (Filter-Ratio with thin-Be and Med-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with

Term	Pointing (x, y)	Comment
04/02 09:47:30 - 04/02 10:59:54	Track ( 549.0, -7.8) @ 04/02 09:31:00	# OP start + 10min AR 12526
04/02 15:08:00 - 04/02 17:43:24	Track ( 589.8, -11.4) @ 04/02 15:00:00	# AR 12526
04/02 17:56:30 - 04/02 18:07:00	Track ( 610.6, -13.3) @ 04/02 17:53:30	# AR 12526
04/02 21:45:00 - 04/03 05:59:54	Track ( 635.9, -15.9) @ 04/02 21:30:00	# AR 12526 (Explosive Event Study w/ IRIS, 23:15-00:30)
04/03 06:13:00 - 04/03 06:29:54	Track ( 693.4, -22.4) @ 04/03 06:10:00	# AR 12526
04/03 08:03:00 - 04/03 16:49:54	Track ( 705.0, -23.8) @ 04/03 08:00:00	# AR 12526 (Explosive Event Study w/ IRIS, 09:40-11:00)
04/04 03:33:00 - 04/04 06:17:24	Track ( 813.2, -40.2) @ 04/04 03:30:00	# AR 12526
04/04 12:33:00 - 04/04 14:59:54	Track ( 853.6, -48.4) @ 04/04 12:30:00	# AR 12526
04/04 17:03:00 - 04/04 17:40:00	Track ( 871.3, -52.6) @ 04/04 17:00:00	# AR 12526
04/04 18:16:30 - 04/05 05:49:54	Track ( 875.8, -53.8) @ 04/04 18:13:30	# AR 12526
04/05 06:03:00 - 04/05 09:37:30	Track ( 913.0, -65.1) @ 04/05 06:00:00	# AR 12526

**PROG= 01 Inf.-time(s)**

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 48	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 97	4-time(s)	90.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

### XOB #1B20: AR - Standard Core - (Filter-Ratio with thin-Be and Med-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with

Term	Pointing (x, y)	Comment
04/02 11:03:00 - 04/02 14:54:30	Track ( 545.2, 22.1) @ 04/02 11:00:00	# HOP302 (Pore Obs.)

**PROG= 09 Inf.-time(s)**

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 48	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 97	4-time(s)	60.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

### XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T

Term	Pointing (x, y)	Comment
04/02 17:46:30 - 04/02 17:53:24	Fixed ( 0.0, 0.0)	synoptic, shifted -16.5 min
04/03 06:03:00 - 04/03 06:09:54	Fixed ( 0.0, 0.0)	synoptic
04/03 16:53:00 - 04/03 16:59:54	Fixed ( 0.0, 0.0)	synoptic
04/04 06:20:30 - 04/04 06:27:24	Fixed ( 0.0, 0.0)	synoptic, shifted 17.5 min
04/04 18:06:30 - 04/04 18:13:24	Fixed ( 0.0, 0.0)	synoptic, shifted 3.5 min
04/05 05:53:00 - 04/05 05:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted -10.0 min

**PROG= 11 1-time(s)**

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Open/Ti-poly	Open/thick-AL	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-AL	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 91 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 93 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-AL	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 77 1-time(s) 2.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 54 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms**

Term	Pointing (x, y)	Comment
04/02 18:33:00 - 04/02 21:21:00	Track ( 828.7, -186.1) @ 04/02 18:30:00	# Coronal Hole Boudbary Obs. w/ IRIS
04/03 06:33:00 - 04/03 07:59:54	Track ( 871.7, -197.1) @ 04/03 06:30:00	# Coronal Hole Boundary Obs. w/ IRIS
04/03 17:03:00 - 04/04 03:29:54	Fixed ( 880.0, -277.0)	# Filament Obs. w/IRIS at SW limb
04/04 06:30:30 - 04/04 07:59:54	Fixed ( 860.0, -367.0)	# HOP 300 (Coronal Loop Obs. @ SW limb)
04/04 11:03:00 - 04/04 12:29:54	Fixed ( 860.0, -367.0)	# HOP 300 (Coronal Loop Obs. @ SW limb)
04/04 15:03:00 - 04/04 16:59:54	Fixed ( 860.0, -367.0)	# HOP 300 (Coronal Loop Obs. @ SW limb)

**PROG= 16 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 9 2-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 7 1-time(s) 30.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
<b>Subr= 3 30-time(s) 2.0sec</b>												
<b>Seqn= 57 1-time(s) 60.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1A21: HOP255 - Coronal cavity - Al\_poly -DPCM 1x1-512FOV-AEC1-60s cad w/ light leak image and G-band**

Term	Pointing (x, y)	Comment
04/04 08:03:00 - 04/04 10:59:54	Fixed ( 860.0, -367.0)	# HOP 255 (filamen obs. @ SW limb)

**PROG= 17 Inf.-time(s)**

<b>Subr= 1 1-time(s) 120.0sec</b>												
<b>Seqn= 64 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	512x512 (1024, 1024)	DPCM	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 61 60-time(s) 60.0sec</b>												
Al-poly/Open	Al-poly/thick-AL	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1024, 1024)	DPCM	1	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
04/02 09:47:30 - 04/02 10:59:54	Track ( 549.0, -7.8) @ 04/02 09:31:00	# OP start + 10min AR 12526
04/02 11:03:00 - 04/02 14:54:30	Track ( 545.2, 22.1) @ 04/02 11:00:00	# HOP302 (Pore Obs.)
04/02 15:08:00 - 04/02 17:43:24	Track ( 589.8, -11.4) @ 04/02 15:00:00	# AR 12526
04/02 17:56:30 - 04/02 18:07:00	Track ( 610.6, -13.3) @ 04/02 17:53:30	# AR 12526
04/02 18:33:00 - 04/02 21:21:00	Track ( 828.7, -186.1) @ 04/02 18:30:00	# Coronal Hole Boudbary Obs. w/ IRIS
04/02 21:45:00 - 04/03 05:59:54	Track ( 635.9, -15.9) @ 04/02 21:30:00	# AR 12526 (Explosive Event Study w/ IRIS, 23:15-00:30)
04/03 06:13:00 - 04/03 06:29:54	Track ( 693.4, -22.4) @ 04/03 06:10:00	# AR 12526
04/03 06:33:00 - 04/03 07:59:54	Track ( 871.7, -197.1) @ 04/03 06:30:00	# Coronal Hole Boundary Obs. w/ IRIS
04/03 08:03:00 - 04/03 16:49:54	Track ( 705.0, -23.8) @ 04/03 08:00:00	# AR 12526 (Explosive Event Study w/ IRIS, 09:40-11:00)
04/03 17:03:00 - 04/04 03:29:54	Fixed ( 880.0, -277.0)	# Filament Obs. w/IRIS at SW limb
04/04 03:33:00 - 04/04 06:17:24	Track ( 813.2, -40.2) @ 04/04 03:30:00	# AR 12526
04/04 06:30:30 - 04/04 07:59:54	Fixed ( 860.0, -367.0)	# HOP 300 (Coronal Loop Obs. @ SW limb)
04/04 08:03:00 - 04/04 10:59:54	Fixed ( 860.0, -367.0)	# HOP 255 (filamen obs. @ SW limb)
04/04 11:03:00 - 04/04 12:29:54	Fixed ( 860.0, -367.0)	# HOP 300 (Coronal Loop Obs. @ SW limb)
04/04 12:33:00 - 04/04 14:59:54	Track ( 853.6, -48.4) @ 04/04 12:30:00	# AR 12526
04/04 15:03:00 - 04/04 16:59:54	Fixed ( 860.0, -367.0)	# HOP 300 (Coronal Loop Obs. @ SW limb)
04/04 17:03:00 - 04/04 17:40:00	Track ( 871.3, -52.6) @ 04/04 17:00:00	# AR 12526
04/04 18:16:30 - 04/05 05:49:54	Track ( 875.8, -53.8) @ 04/04 18:13:30	# AR 12526
04/05 06:03:00 - 04/05 09:37:30	Track ( 913.0, -65.1) @ 04/05 06:00:00	# AR 12526

**PROG= 07 30-time(s)**

Subr= 1		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn=100		1-time(s)		10.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec		
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11		1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 84		1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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### Active Region Search

\* \* \* \* \*

NOT USED

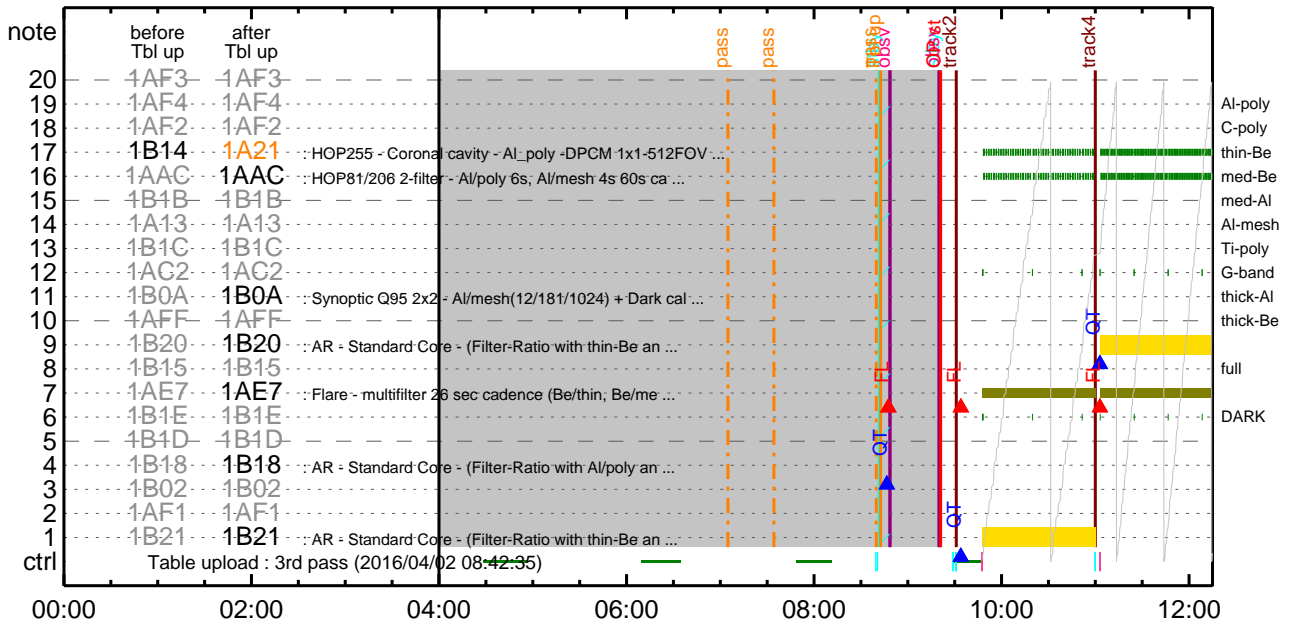
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### Flare Detection

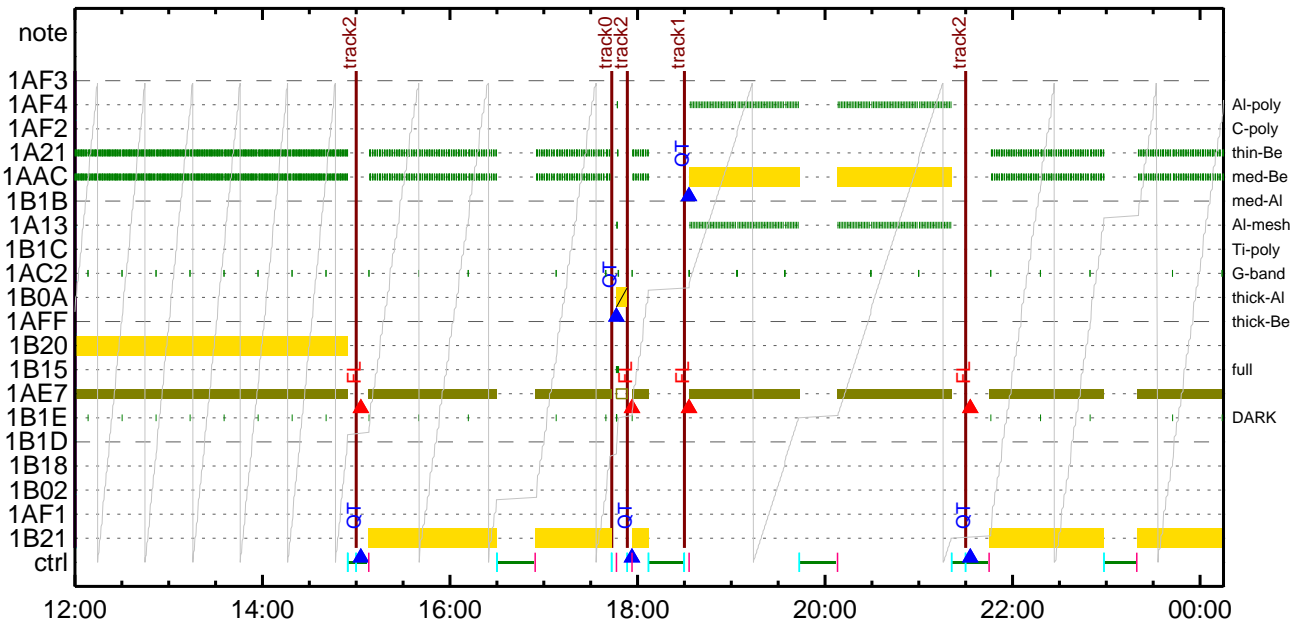
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FLD Patrol												
Term		Pointing (x, y)						Comment				
04/02 17:53:48 - 04/03 06:00:18	Track ( 610.6, -13.3)					@ 04/02 17:53:30	# AR 12526					
04/03 06:10:18 - 04/03 16:50:18	Track ( 693.4, -22.4)					@ 04/03 06:10:00	# AR 12526					
04/03 17:00:18 - 04/04 06:17:48	Fixed ( 880.0, -277.0)						# Filament Obs. w/IRIS at SW limb					
04/04 06:27:48 - 04/04 18:03:48	Fixed ( 860.0, -367.0)						# HOP 300 (Coronal Loop Obs. @ SW limb)					
04/04 18:13:48 - 04/05 05:50:18	Track ( 875.8, -53.8)					@ 04/04 18:13:30	# AR 12526					
04/05 06:00:18 - 04/07 10:07:00	Track ( 913.0, -65.1)					@ 04/05 06:00:00	# AR 12526					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

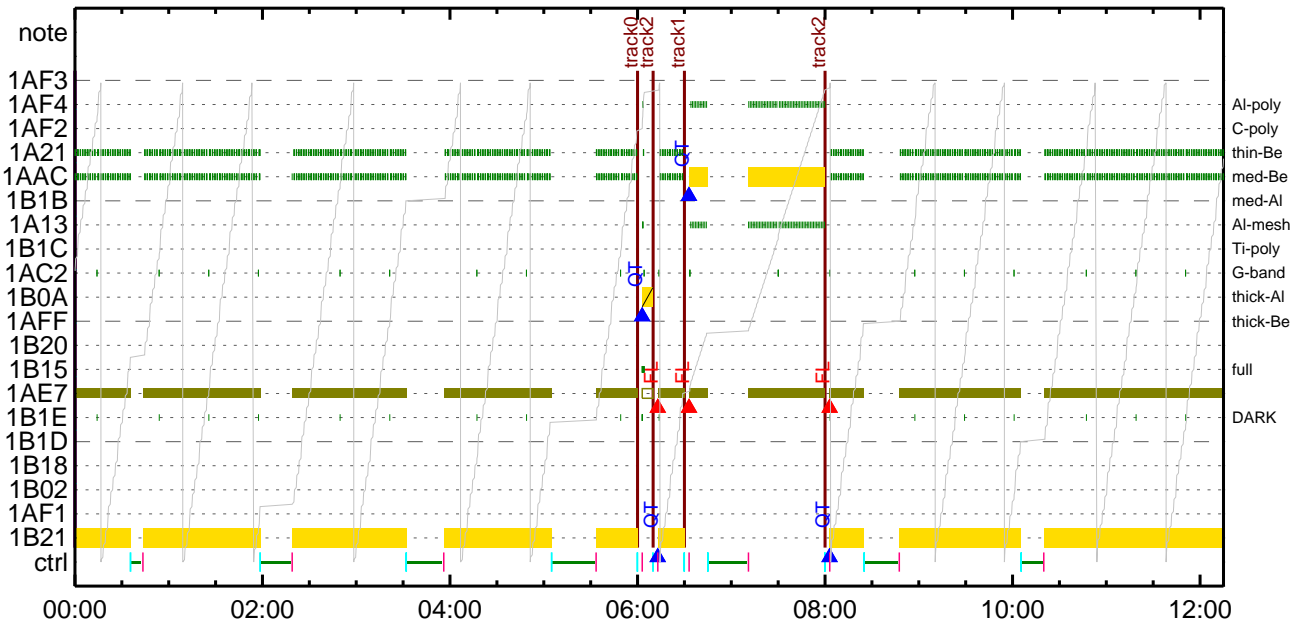
### CMDI #0824 2016/04/02



### CMDI #0824 2016/04/02

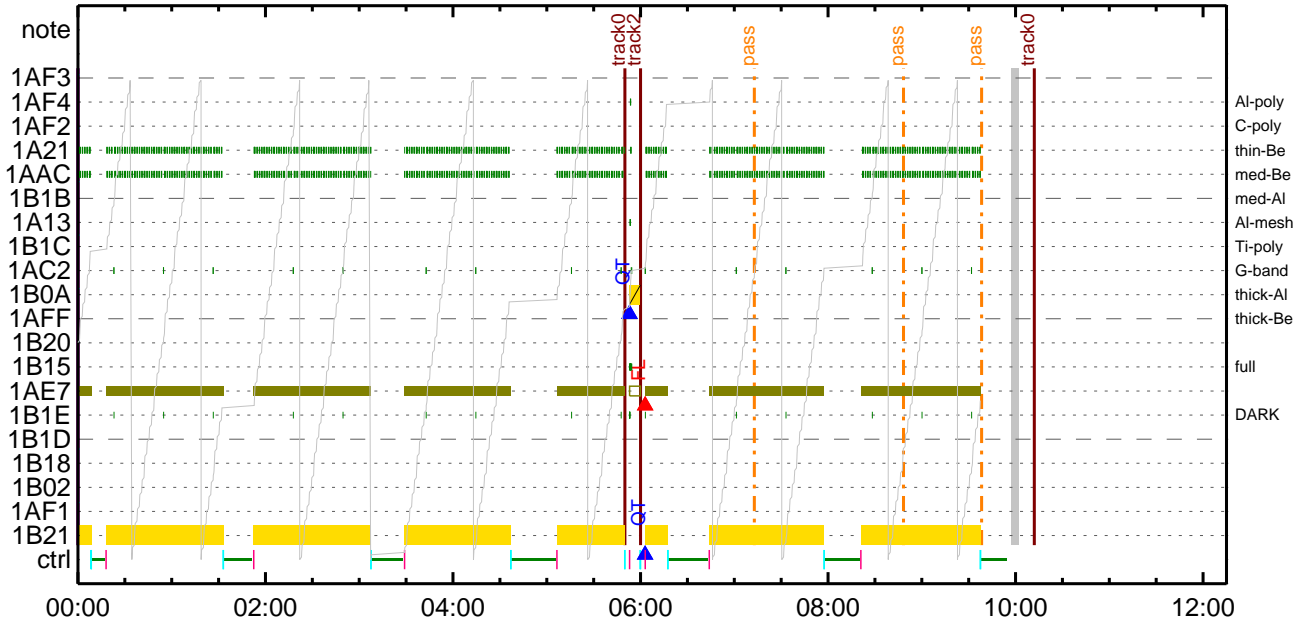


### CMDI #0824 2016/04/03

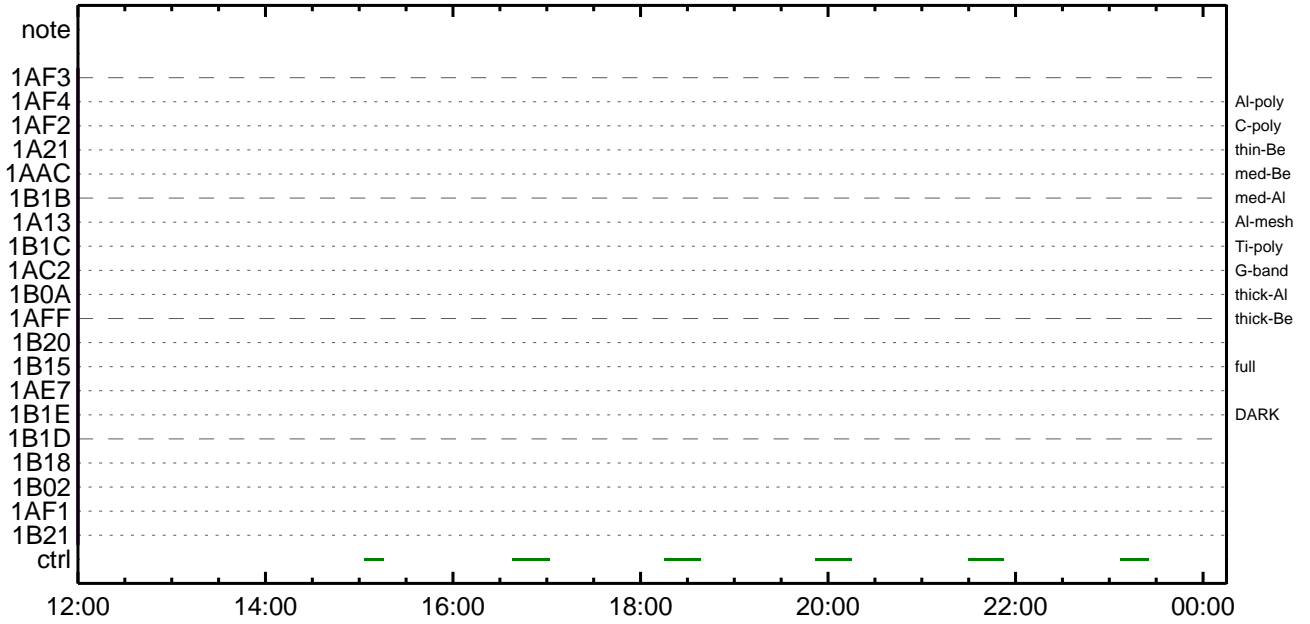




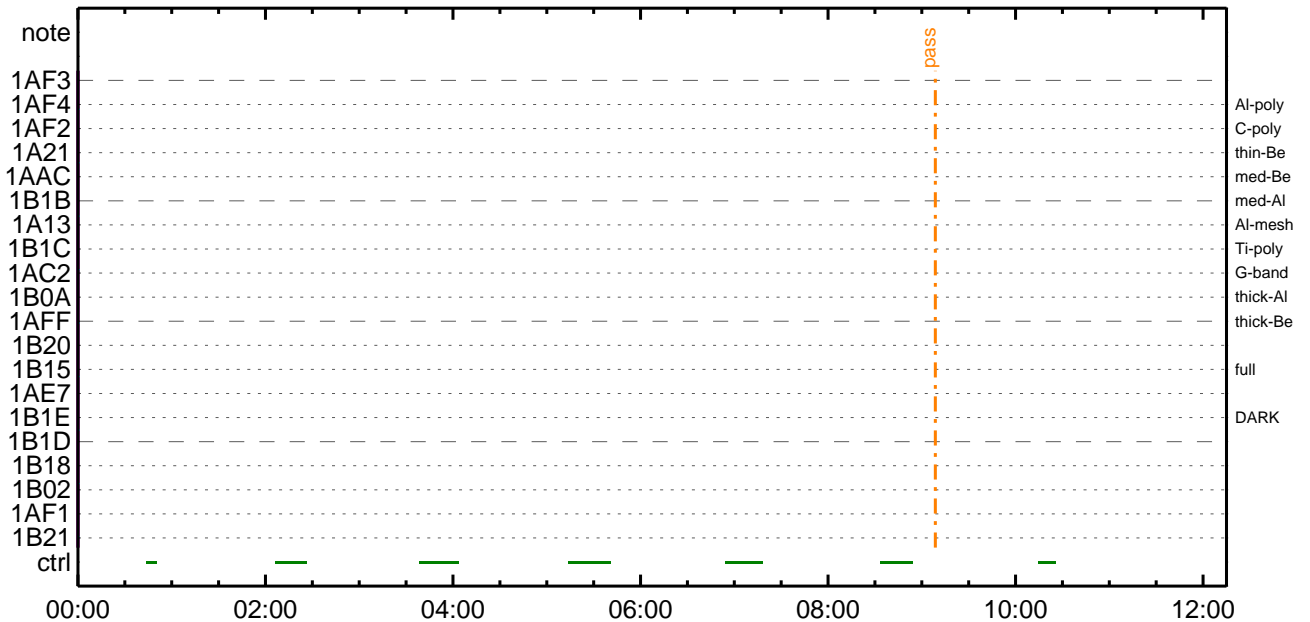
CMDI #0824 2016/04/05



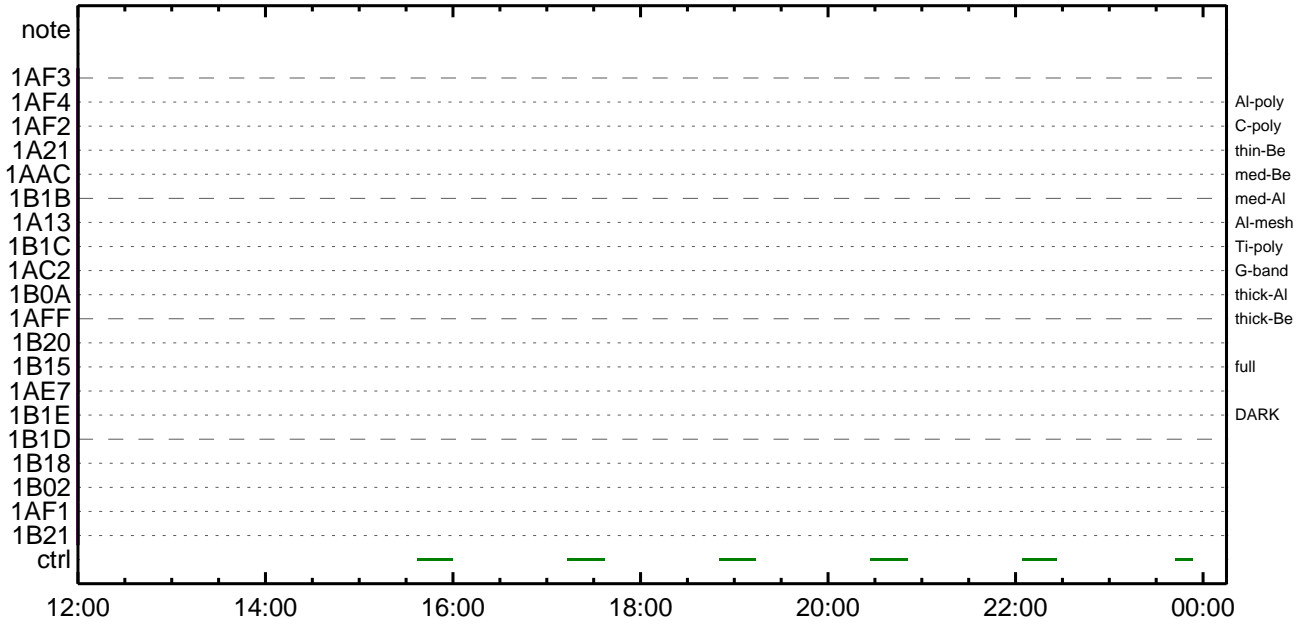
CMDI #0824 2016/04/05



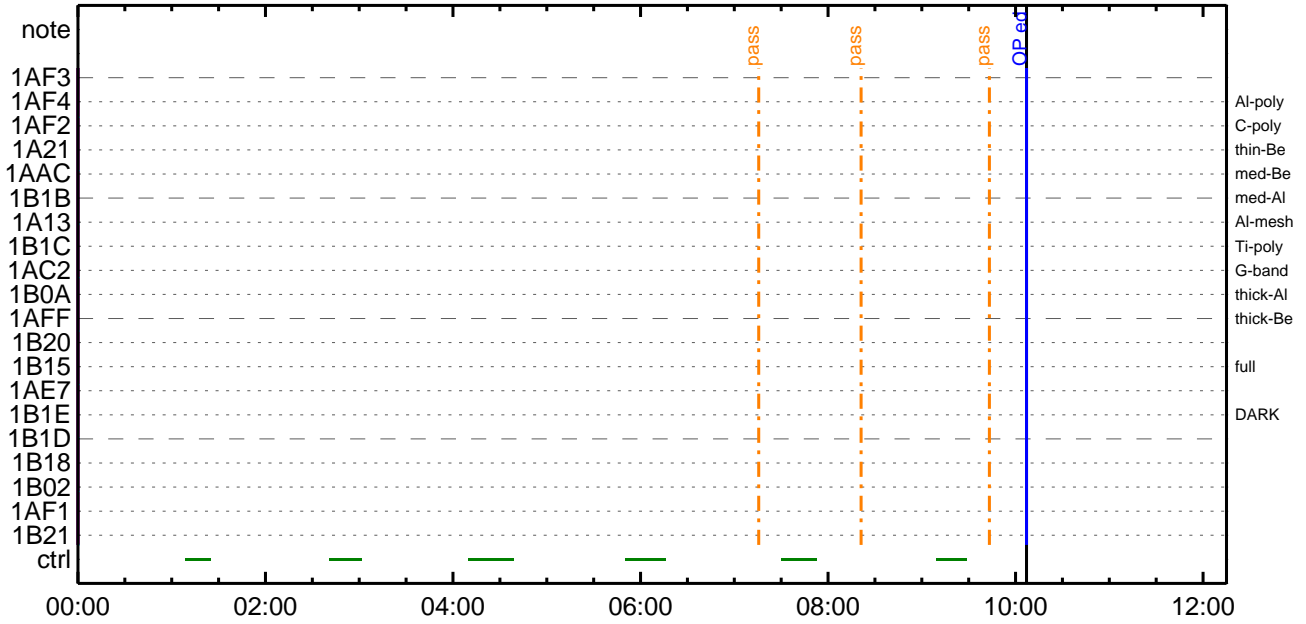
CMDI #0824 2016/04/06



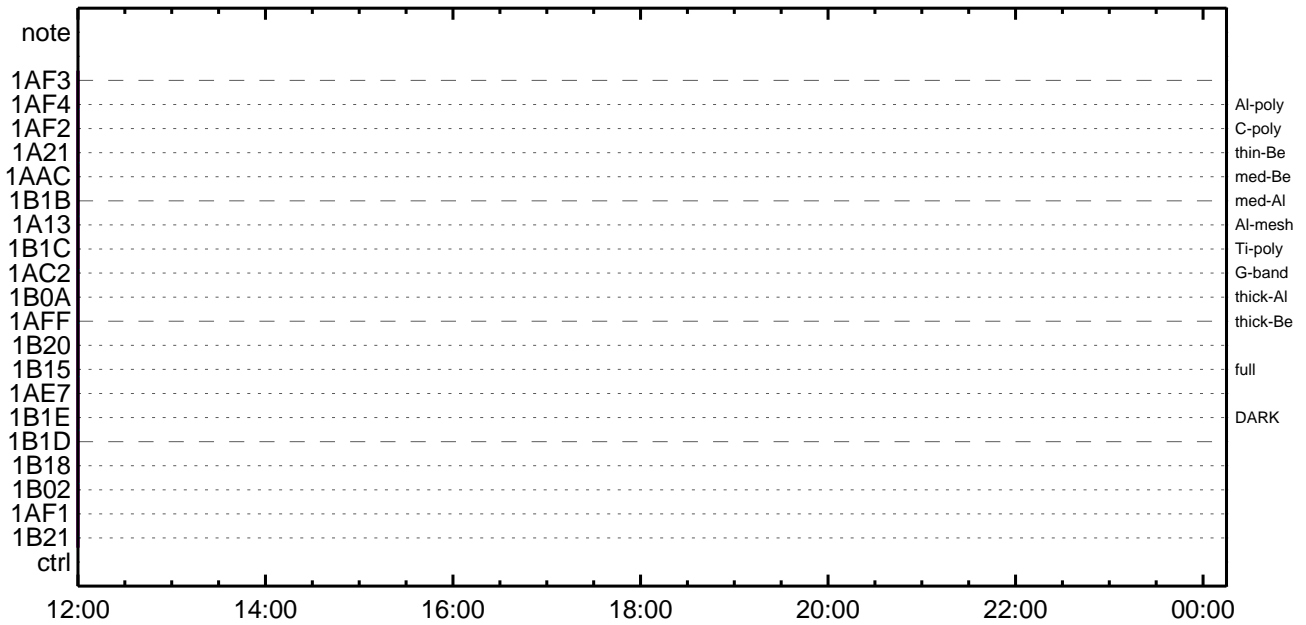
CMDI #0824 2016/04/06



CMDI #0824 2016/04/07



CMDI #0824 2016/04/07









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0194 C.
0195 +. TI 2016-04-02 09:20:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñαîî¥Á¥§¥Á¥-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHU¥â;¼¥É;È¼¥¼. ¥î;¼¥È;Èαòîáα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2016-04-02 09:20:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2016-04-02 09:20:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2016-04-02 09:20:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2016-04-02 09:20:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ÝαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îî¥Á¥È¥¥¥È¥á¥ç¥èè¼αα¼Á»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñèË´Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥-¼Á»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-289:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2016-04-02 09:20:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 +. DC 07-F0 MDP_XRT_CTRL_MANU
0136 BC (c1)
0137 +. DC 07-F0 MDP_XRT_MODE_STBY
0138 BC (c3)
0139 . C. ----- Success Verify ? OK / NG____
0140 C.
0141 C. XRT Obs. Table Upload
0142 . S. RAM ram-291:MDP_OBS_X
0143 ( )
0144 C.
0145 +. DC 07-F0 MDP_DUMP_XRTTBL
0146 BC (84 07 00 00 00 3a d4)
0147 . C. ----- Comparison Check ? OK / ERR ____
0148 C.
0149 C.
0150 +. DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 01 b1 b1 04 04)
0152 +. DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 02 b1 b1 08 08)
0154 +. DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 03 b1 b1 08 08)
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 04 b1 b1 06 06)
0158 +. DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 +. DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 +. DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 +. DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 80 08 08)
0168 +. DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 +. DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 +. DC 07-F0 MDP_XRT_FLD_ENA
0173 BC (d8)
0174 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175 BC (c8)
0176 +. DC 07-F0 MDP_XRT_ARS_DIS
0177 BC (d5)
0178 +. DC 07-F0 MDP_XRT_AEC_RESET
0179 BC (d0)
0180 +. DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0183 BC (c4 04)
0184 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0185 BC (c5 07)
0186 . C. ----- Success Verify ? OK / NG ____
0187 C.
0188 C.
0189 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0190 C.
0191 +. DC 07-F0 MDP_XRT_MODE_OBSV
0192 BC (c2)
0193 +. TI 2016-04-02 09:20:02.0

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0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 . C. ----- Success Verify ? OK / NG ____
0197 C.
0198 C. ***** XRT END *****
0199 C.
0200 . C. ***** MDP `úÃîñî»ò¼ŷñÊÃĐñ¹ñèDCBC•x²è *****
0201 C. (¼á°îŷÓŷÃŷÈŷŲŷËŷáŷçŷèñÊ¼ñ¼Ã»Ûñ¹ñè)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ŷĐŷ¹•İ Daily±¿İÑñÊ´Øñ¹ñèDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ;ãLOSŷÁŷŲŷÃŷ¬¼Ã»Û¿
0212 C.
0213 . C. ***** LOS *****
0214 C.
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\*\*\* OP Sequence for XRT \*\*\*

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2016/04/02 09:29:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 09:29:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 09:29:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/04/02 09:29:06.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/04/02 09:30:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 09:30:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 09:30:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/04/02 09:31:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 02 00 00 00 00
2016/04/02 09:31:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/04/02 09:31:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/04/02 09:31:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/04/02 09:31:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/04/02 09:31:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/04/02 09:32:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/04/02 09:33:56.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2016/04/02 09:33:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/04/02 09:46:30.0 XRT_Custom_430_OG [0x1ae]
2016/04/02 09:47:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/04/02 10:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 10:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 10:59:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/04/02 11:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 04 00 00 00 00
2016/04/02 11:00:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/04/02 11:00:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/04/02 11:00:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/04/02 11:00:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/04/02 11:00:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/04/02 11:02:56.0 XRT_QT_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 09
2016/04/02 11:02:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/04/02 11:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/04/02 14:54:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 14:54:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 14:54:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/04/02 14:54:36.0 XRT_PREFLR_STRT_432_OG [0x1b0]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2016/04/02 14:57:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2016/04/02 14:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 14:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/02 14:59:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/04/02 15:00:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 02 00 00 00 00
2016/04/02 15:00:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/04/02 15:00:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/04/02 15:00:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/04/02 15:00:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/04/02 15:00:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/04/02 15:02:56.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2016/04/02 15:02:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07

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2016/04/02	15:07:00.0	XRT_Custom_430_OG [0x1ae]			
2016/04/02	15:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/02	16:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	16:30:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	16:30:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/02	16:30:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/02	16:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/02	16:53:30.0	XRT_Custom_430_OG [0x1ae]			
2016/04/02	16:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/02	17:43:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	17:43:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	17:43:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/04/02	17:43:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2016/04/02	17:43:48.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2016/04/02	17:46:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/04/02	17:46:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/02	17:46:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/04/02	17:46:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/02	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	17:53:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	17:53:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/04/02	17:53:30.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2016/04/02	17:53:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/04/02	17:53:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/04/02	17:53:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/04/02	17:53:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/02	17:53:56.0	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/02	17:56:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/04/02	17:56:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/04/02	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/02	18:07:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	18:07:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	18:07:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/02	18:07:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/02	18:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/02	18:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	18:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/02	18:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/04/02	18:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	01 00 00 00 00
2016/04/02	18:30:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2016/04/02	18:30:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/04/02	18:30:22.5	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2016/04/02	18:30:24.5	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/02	18:30:26.5	XRT_FLD_RESET_433_OG [0x1b1]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/02	18:32:56.5	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2016/04/02	18:32:58.5	XRT_FL_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/04/02	18:33:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/02	19:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			



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2016/04/02	19:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/02	19:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/02	19:43:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/04/02	19:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/04/02	20:07:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/04/02	20:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/04/02	21:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/04/02	21:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/02	21:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/02	21:21:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/04/02	21:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/04/02	21:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/04/02	21:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/02	21:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/02	21:30:00.0	AOCs_Or-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/04/02	21:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCs_Or-point_Start_1_OG [0x097]	5	02-76	02 00 00 00 00		
2016/04/02	21:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM					
2016/04/02	21:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/04/02	21:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/04/02	21:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/04/02	21:32:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/04/02	21:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/04/02	21:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2016/04/02	21:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/04/02	22:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/04/02	22:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/04/02	22:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/02	22:58:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/02	23:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/04/02	23:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/04/02	23:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/04/03	00:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_430_OG [0x1ae]					
2016/04/03	00:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/04/03	00:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/04/03	00:35:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/03	00:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/03	00:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/04/03	00:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/04/03	01:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/04/03	01:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]					
2016/04/03	01:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2016/04/03	01:58:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/04/03	02:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/03	02:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/03	02:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/04/03	03:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/04/03	03:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/04/03	03:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_Custom_430_OG [0x1ae]					
2016/04/03	03:32:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	XRT_CTRL_AUTO_424_OG [0x1a8]					

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2016/04/03	03:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/03	03:55:00.5	XRT_Custom_430_OG [0x1ae]					
2016/04/03	03:56:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/03	05:05:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	05:05:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	05:05:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/03	05:05:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/03	05:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/03	05:32:30.0	XRT_Custom_430_OG [0x1ae]					
2016/04/03	05:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/03	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/04/03	06:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2016/04/03	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/04/03	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/04/03	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/03	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2016/04/03	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/03	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/04/03	06:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2016/04/03	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/03	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/03	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/03	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/03	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/03	06:12:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2016/04/03	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/04/03	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/03	06:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	06:29:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	06:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/04/03	06:30:00.0	AOCS_OrE-point_Start_4_OG [0x09a]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2016/04/03	06:30:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/03	06:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/03	06:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/03	06:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/03	06:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/03	06:32:56.0	XRT_QT_PROG_SET_401_OG [0x191]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2016/04/03	06:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/04/03	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/03	06:45:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	06:45:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/03	06:45:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/03	06:45:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	



2016/04/03	16:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/04/03	16:52:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2016/04/03	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/03	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	16:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2016/04/03	17:00:00.0	AOCS_OrE-point_Start_5_OG [0x09b]						
		AOCU_NM	5	02-76	00 18 9b b1 cb			
2016/04/03	17:00:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/04/03	17:00:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/04/03	17:00:22.5	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/04/03	17:00:24.5	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/04/03	17:00:26.5	XRT_FLD_RESET_433_OG [0x1b1]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/03	17:02:56.5	XRT_QT_PROG_SET_401_OG [0x191]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2016/04/03	17:02:58.5	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/04/03	17:03:00.5	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/03	17:04:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	17:04:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	17:04:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/03	17:04:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/04/03	17:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/04/03	17:28:30.0	XRT_Custom_430_OG [0x1ae]						
2016/04/03	17:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/03	18:42:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	18:42:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	18:42:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/03	18:42:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/04/03	18:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/04/03	19:05:00.0	XRT_Custom_430_OG [0x1ae]						
2016/04/03	19:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/03	20:19:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	20:19:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	20:19:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/03	20:19:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/04/03	20:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/04/03	20:42:30.0	XRT_Custom_430_OG [0x1ae]						
2016/04/03	20:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/03	21:56:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	21:56:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	21:56:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/03	21:56:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/04/03	21:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/04/03	22:18:30.0	XRT_Custom_430_OG [0x1ae]						
2016/04/03	22:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/04/03	23:33:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	23:33:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/04/03	23:33:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2016/04/03	23:33:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/04/03	23:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/04/03	23:46:00.0	XRT_Custom_430_OG [0x1ae]						

2016/04/03	23:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	01:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	01:01:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	01:01:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/04	01:01:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/04	01:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/04	01:16:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/04	01:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	02:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	02:32:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	02:32:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/04	02:32:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/04	02:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/04	02:53:00.0	XRT_Custom_430_OG [0x1ae]							
2016/04/04	02:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	03:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	03:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	03:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/04	03:30:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2016/04/04	03:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/04	03:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/04	03:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/04	03:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/04	03:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/04	03:32:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/04/04	03:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/04	03:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	04:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	04:01:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	04:01:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/04	04:01:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/04	04:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/04	04:30:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/04	04:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	05:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	05:41:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	05:41:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/04	05:41:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/04	05:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/04	06:07:30.0	XRT_Custom_430_OG [0x1ae]							
2016/04/04	06:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	06:17:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	06:17:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	06:17:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/04/04	06:17:30.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/04/04	06:17:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/04/04	06:20:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/04/04	06:20:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/04	06:20:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							

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2016/04/04	06:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2016/04/04	06:27:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/04	06:27:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	06:27:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	06:27:30.0	AOCS_OrE-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/04/04	06:27:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 20 9b b3 8e	
2016/04/04	06:27:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/04	06:27:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/04	06:27:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/04	06:27:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/04	06:30:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/04	06:30:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2016/04/04	06:30:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/04/04	07:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/04	07:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	07:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	07:21:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/04	07:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/04	07:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/04	07:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/04/04	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/04	07:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	07:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	08:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/04/04	08:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 20 9b b3 8e	
2016/04/04	08:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/04	08:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/04	08:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/04	08:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/04	08:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/04	08:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2016/04/04	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/04/04	09:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/04	09:01:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	09:01:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	09:01:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/04	09:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/04	09:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/04	09:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/04/04	10:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/04	10:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	10:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/04	10:43:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/04	10:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/04	10:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/04	10:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2016/04/04	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/04	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2016/04/04	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	11:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/04/04	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 20 9b b3 8e
2016/04/04	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/04/04	11:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/04/04	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/04/04	11:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/04	11:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/04	11:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2016/04/04	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/04/04	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/04	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	12:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	12:30:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/04/04	12:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2016/04/04	12:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/04/04	12:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/04/04	12:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/04/04	12:30:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/04	12:32:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/04	12:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/04/04	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/04/04	14:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/04	14:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	14:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	14:29:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/04	14:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/04	14:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/04	14:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2016/04/04	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/04	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	15:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/04/04	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 20 9b b3 8e
2016/04/04	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/04/04	15:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/04/04	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/04/04	15:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/04	15:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/04	15:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2016/04/04	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/04/04	16:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/04	16:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	16:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	16:03:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/04	16:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/04	16:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2016/04/04	16:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	16:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/04	17:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2016/04/04	17:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/04	17:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/04	17:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/04	17:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/04	17:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/04	17:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/04/04	17:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/04	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	17:40:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	17:40:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	17:40:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/04	17:40:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/04	17:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/04	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	18:03:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/04/04	18:03:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/04/04	18:03:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/04/04	18:06:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/04/04	18:06:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/04	18:06:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2016/04/04	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	18:13:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	18:13:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	18:13:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/04	18:13:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2016/04/04	18:13:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/04	18:13:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/04	18:13:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/04	18:13:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/04	18:13:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/04	18:16:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/04/04	18:16:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/04	18:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	19:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	19:17:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/04	19:17:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/04	19:17:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/04	19:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/04	19:40:00.0	XRT_Custom_430_OG [0x1ae]							
2016/04/04	19:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/04	20:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				



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2016/04/04	20:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	20:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/04	20:54:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/04	20:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/04	21:17:30.0	XRT_Custom_430_OG [0x1ae]				
2016/04/04	21:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/04	22:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	22:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/04	22:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/04	22:31:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/04	22:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/04	22:52:30.0	XRT_Custom_430_OG [0x1ae]				
2016/04/04	22:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	00:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	00:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	00:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	00:08:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	00:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	00:17:00.0	XRT_Custom_430_OG [0x1ae]				
2016/04/05	00:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	01:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	01:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	01:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	01:33:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	01:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	01:51:30.0	XRT_Custom_430_OG [0x1ae]				
2016/04/05	01:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	03:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	03:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	03:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	03:07:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	03:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	03:28:00.0	XRT_Custom_430_OG [0x1ae]				
2016/04/05	03:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	04:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	04:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	04:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	04:37:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	04:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	05:05:30.0	XRT_Custom_430_OG [0x1ae]				
2016/04/05	05:06:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	05:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	05:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	05:49:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/04/05	05:50:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2016/04/05	05:50:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/04/05	05:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/04/05	05:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/05	05:52:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/04/05	05:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]				

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2016/04/05	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	05:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	06:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/04/05	06:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2016/04/05	06:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/04/05	06:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/04/05	06:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/04/05	06:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/05	06:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	06:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/04/05	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/04/05	06:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	06:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	06:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	06:17:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	06:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	06:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	06:44:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/04/05	07:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	07:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	07:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	07:57:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	08:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	08:20:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	08:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/04/05	09:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	09:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	09:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	09:37:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	09:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	10:12:00.0	AOCS_OrE-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM	AOCS_OrE-point_Start_3_OG [0x099]			
			AOCU_NM	5	02-76	00 00 00 00 00