

# XRT Timeline to be uploaded on 2016/04/05

Period: 2016/04/05 10:02:00 - 2016/04/09 09:40:00

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**Normal mode**

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<b>XOB #1A21: HOP255 - Coronal cavity - Al_poly-DPCM 1x1-512FOV-AEC1-60s cad w/ light leak image and G-band</b>												
Term	Pointing (x, y)	Comment										
04/05 10:15:00 - 04/05 10:59:54	Fixed ( 814.0, -473.0)	# HOP 255										
04/06 08:03:00 - 04/06 10:59:54	Fixed ( -955.0, 58.0)	HOP 255										
<b>PROG= 17 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 120.0sec												
└─┬─ Seqn= 64 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 512x512 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 512x512 (1024, 1024) DPCM 0 0 2.0sec												
└─┬─ Subr= 2 1-time(s) 2.0sec												
└─┬─┬─ Seqn= 61 60-time(s) 60.0sec												
└─┬─┬─┬─ Al-poly/Open Al-poly/thick-Al close Safe Norm 8.00s Obs 1x1 512x512 (1024, 1024) DPCM 1 0 2.0sec												
└─┬─┬─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

<b>XOB #1AC2: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 120s cad</b>												
Term	Pointing (x, y)	Comment										
04/05 11:03:00 - 04/05 11:15:54	Fixed ( 936.0, 0.0)	HOP 130 - 01/15										
04/05 11:19:00 - 04/05 11:31:54	Fixed ( 468.0, 0.0)	HOP 130 - 02/15										
04/05 11:35:00 - 04/05 11:47:54	Fixed ( 0.0, 0.0)	HOP 130 - 03/15										
04/05 11:51:00 - 04/05 12:03:54	Fixed ( -468.0, 0.0)	HOP 130 - 04/15										
04/05 12:07:00 - 04/05 12:19:54	Fixed ( -936.0, 0.0)	HOP 130 - 05/15										
04/05 12:23:00 - 04/05 12:29:54	Fixed ( 815.0, 470.0)	HOP 130 - 06/15										
04/05 12:33:00 - 04/05 12:45:54	Fixed ( 411.0, 847.0)	HOP 130 - 07/15										
04/05 12:49:00 - 04/05 13:01:54	Fixed ( 0.0, 941.0)	HOP 130 - 08/15										
04/05 13:05:00 - 04/05 13:17:54	Fixed ( -411.0, 847.0)	HOP 130 - 09/15										
04/05 13:21:00 - 04/05 13:27:54	Fixed ( -815.0, 470.0)	HOP 130 - 10/15										
04/05 13:31:00 - 04/05 13:37:54	Fixed ( 815.0, -470.0)	HOP 130 - 11/15										
04/05 13:41:00 - 04/05 13:53:54	Fixed ( 411.0, -847.0)	HOP 130 - 12/15										
04/05 13:57:00 - 04/05 14:09:54	Fixed ( 0.0, -941.0)	HOP 130 - 13/15										
04/05 14:13:00 - 04/05 14:25:54	Fixed ( -411.0, -847.0)	HOP 130 - 14/15										
04/05 14:29:00 - 04/05 14:59:54	Fixed ( -815.0, -471.0)	HOP 130 - 15/15										
<b>PROG= 12 Inf.-time(s)</b>												
└─ Subr= 1 45-time(s) 120.0sec												
└─┬─ Seqn= 29 1-time(s) 4.0sec												
└─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─┬─ Subr= 2 1-time(s) 2.0sec												
└─┬─┬─ Seqn= 26 1-time(s) 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─┬─┬─┬─ Open/G-band Open/G-band close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─┬─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

<b>XOB #1B21: AR - Standard Core - (Filter-Ratio with thin-Be and Med-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with</b>												
Term	Pointing (x, y)	Comment										
04/05 15:03:00 - 04/05 16:38:00	Track ( 259.8, 183.8) <small>@ 04/05 15:00:00</small>	HOP 300, AR 12528										
04/05 17:03:00 - 04/05 17:51:54	Track ( 277.7, 183.2) <small>@ 04/05 17:00:00</small>	Track emerging region										
04/05 18:05:00 - 04/06 05:59:54	Track ( 286.8, 182.8) <small>@ 04/05 18:02:00</small>	Track emerging region										
04/06 06:13:00 - 04/06 06:54:00	Track ( 392.0, 177.9) <small>@ 04/06 06:10:00</small>	Track emerging region										
04/06 11:03:00 - 04/06 14:59:54	Track ( 432.5, 175.6) <small>@ 04/06 11:00:00</small>	Track emerging region										
04/06 17:13:00 - 04/07 06:25:54	Track ( 482.7, 172.4) <small>@ 04/06 17:10:00</small>	Track emerging region										
04/07 06:39:00 - 04/07 06:59:54	Track ( 585.8, 164.3) <small>@ 04/07 06:36:00</small>	Track emerging region										
<b>PROG= 01 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 56 1-time(s) 2.0sec												
└─┬─┬─ Open/G-band Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─┬─┬─ Open/G-band Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─┬─┬─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─┬─ Subr= 2 5-time(s) 2.0sec												
└─┬─┬─ Seqn= 48 1-time(s) 2.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─┬─┬─ med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec												
└─┬─┬─┬─ med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─┬─┬─ Seqn= 97 4-time(s) 90.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec												
└─┬─┬─┬─ med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec												
└─┬─┬─┬─ med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec												
└─┬─┬─┬─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec												
└─┬─┬─┬─ med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec												
└─┬─┬─┬─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

**XOB #1B0A: Synoptic Q95 2x2 - Al/mesh(12/181/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/1443) + T**

Term	Pointing (x, y)	Comment
04/05 17:55:00 - 04/05 18:01:54	Fixed ( 0.0, 0.0)	synoptic, shifted -8.0 min
04/06 06:03:00 - 04/06 06:09:54	Fixed ( 0.0, 0.0)	synoptic
04/06 17:03:00 - 04/06 17:09:54	Fixed ( 0.0, 0.0)	synoptic, shifted -60 min
04/07 06:29:00 - 04/07 06:35:54	Fixed ( 0.0, 0.0)	synoptic, shifted 26.0 min
<b>PROG= 11 1-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms	Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 91 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close Safe Norm 12ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 177ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 1.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 93 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close Safe Norm 24ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close Safe Norm 354ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close Safe Norm 1.41s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 77 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close Safe Norm 86ms	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 1.00s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 5.66s	Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 54 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms	Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 3ms	Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

**XOB #1AAC: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 3ms**

Term	Pointing (x, y)	Comment
04/06 07:19:00 - 04/06 07:59:54	Fixed (-955.0, 58.0)	HOP 300
04/06 15:03:00 - 04/06 16:59:54	Fixed (-955.0, 58.0)	HOP 300
<b>PROG= 16 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 9 2-time(s) 2.0sec		
Open/G-band	Open/G-band close Safe Norm 3ms	Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 7 1-time(s) 30.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms	Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
└─ Subr= 3 30-time(s) 2.0sec		
└─ Seqn= 57 1-time(s) 60.0sec		
Open/Al-mesh	Open/Al-mesh close Safe Norm 4.00s	Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Al-poly/Open	Al-poly/Open close Safe Norm 5.66s	Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

\* \* \* \* \* **Flare mode** \* \* \* \* \*

**XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
04/05 10:15:00 - 04/05 10:59:54	Fixed ( 814.0, -473.0)	# HOP 255
04/05 11:03:00 - 04/05 11:15:54	Fixed ( 936.0, 0.0)	HOP 130 - 01/15
04/05 11:19:00 - 04/05 11:31:54	Fixed ( 468.0, 0.0)	HOP 130 - 02/15
04/05 11:35:00 - 04/05 11:47:54	Fixed ( 0.0, 0.0)	HOP 130 - 03/15
04/05 11:51:00 - 04/05 12:03:54	Fixed (-468.0, 0.0)	HOP 130 - 04/15
04/05 12:07:00 - 04/05 12:19:54	Fixed (-936.0, 0.0)	HOP 130 - 05/15
04/05 12:23:00 - 04/05 12:29:54	Fixed ( 815.0, 470.0)	HOP 130 - 06/15
04/05 12:33:00 - 04/05 12:45:54	Fixed ( 411.0, 847.0)	HOP 130 - 07/15
04/05 12:49:00 - 04/05 13:01:54	Fixed ( 0.0, 941.0)	HOP 130 - 08/15
04/05 13:05:00 - 04/05 13:17:54	Fixed (-411.0, 847.0)	HOP 130 - 09/15
04/05 13:21:00 - 04/05 13:27:54	Fixed (-815.0, 470.0)	HOP 130 - 10/15
04/05 13:31:00 - 04/05 13:37:54	Fixed ( 815.0, -470.0)	HOP 130 - 11/15
04/05 13:41:00 - 04/05 13:53:54	Fixed ( 411.0, -847.0)	HOP 130 - 12/15
04/05 13:57:00 - 04/05 14:09:54	Fixed ( 0.0, -941.0)	HOP 130 - 13/15
04/05 14:13:00 - 04/05 14:25:54	Fixed (-411.0, -847.0)	HOP 130 - 14/15
04/05 14:29:00 - 04/05 14:59:54	Fixed (-815.0, -471.0)	HOP 130 - 15/15
04/05 15:03:00 - 04/05 16:38:00	Track ( 259.8, 183.8) @ 04/05 15:00:00	HOP 300, AR 12528
04/05 17:03:00 - 04/05 17:51:54	Track ( 277.7, 183.2) @ 04/05 17:00:00	Track emerging region
04/05 18:05:00 - 04/06 05:59:54	Track ( 286.8, 182.8) @ 04/05 18:02:00	Track emerging region
04/06 06:13:00 - 04/06 06:54:00	Track ( 392.0, 177.9) @ 04/06 06:10:00	Track emerging region
04/06 07:19:00 - 04/06 07:59:54	Fixed (-955.0, 58.0)	HOP 300
04/06 08:03:00 - 04/06 10:59:54	Fixed (-955.0, 58.0)	HOP 255
04/06 11:03:00 - 04/06 14:59:54	Track ( 432.5, 175.6) @ 04/06 11:00:00	Track emerging region
04/06 15:03:00 - 04/06 16:59:54	Fixed (-955.0, 58.0)	HOP 300
04/06 17:13:00 - 04/07 06:25:54	Track ( 482.7, 172.4) @ 04/06 17:10:00	Track emerging region
04/07 06:39:00 - 04/07 06:59:54	Track ( 585.8, 164.3) @ 04/07 06:36:00	Track emerging region

PROG= 07 30-time(s)													
Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

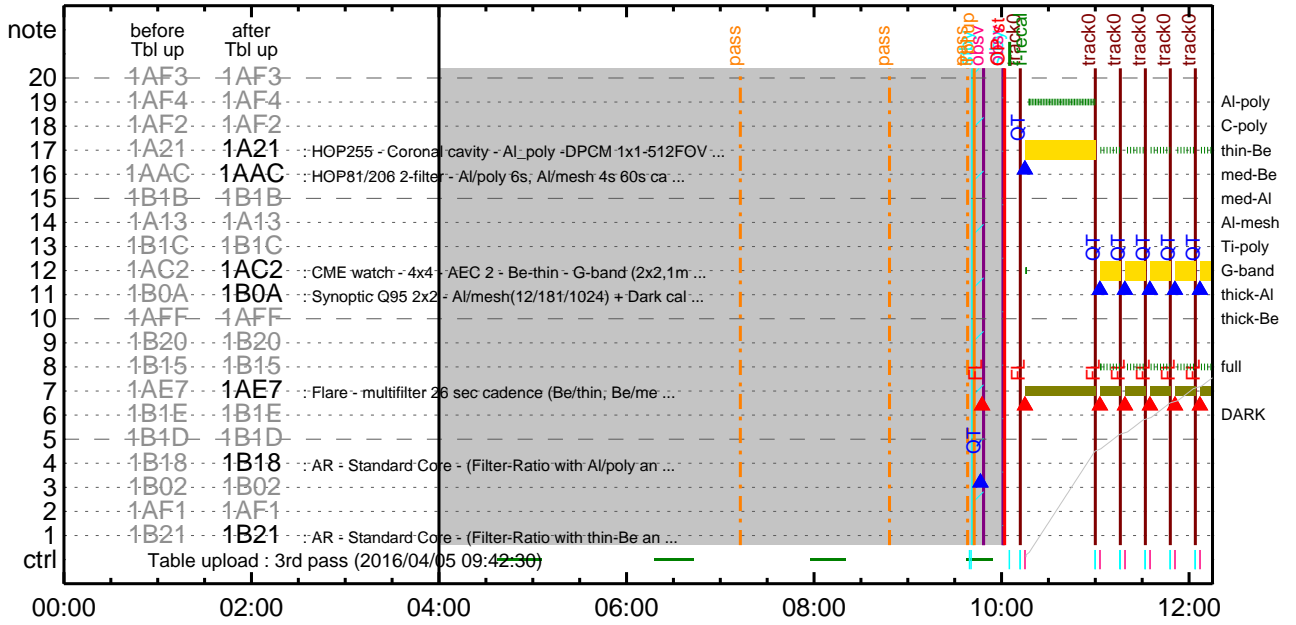
\* \* \* \* \*

### Flare Detection

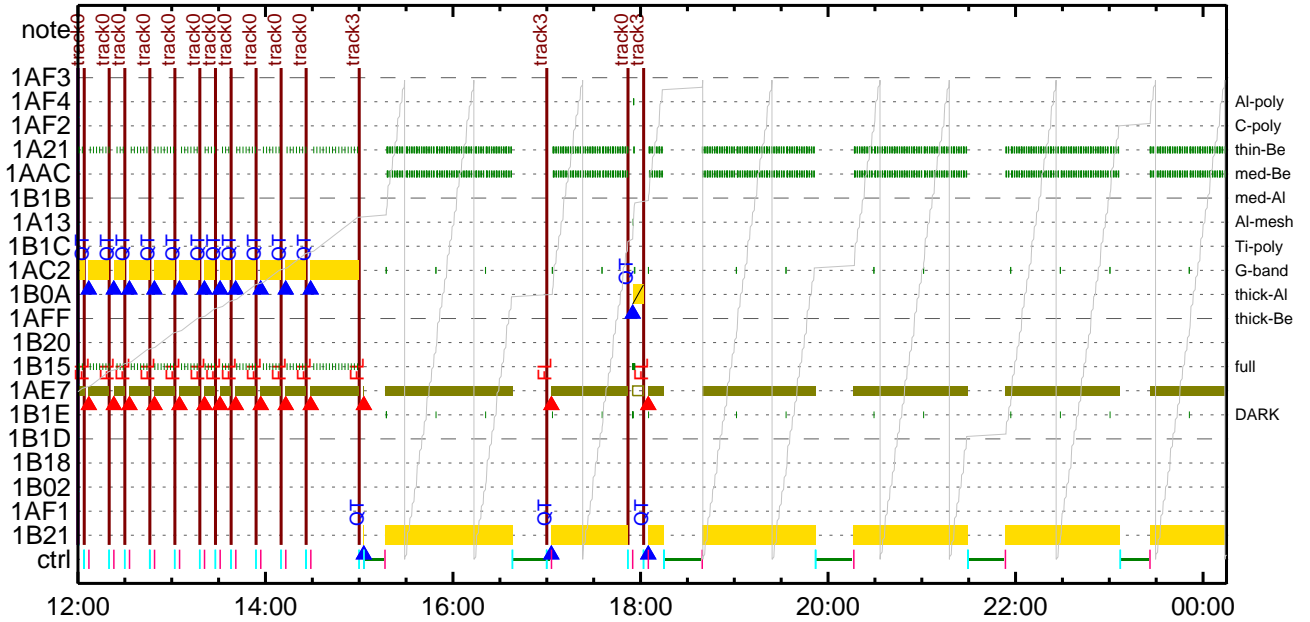
\* \* \* \* \*

FLD Patrol												
Term	Pointing (x, y)								Comment			
04/05 18:02:18 - 04/06 06:00:18	Track (	286.8,	182.8)	@ 04/05 18:02:00	Track emerging region							
04/06 06:10:18 - 04/06 17:00:18	Track (	392.0,	177.9)	@ 04/06 06:10:00	Track emerging region							
04/06 17:10:18 - 04/07 06:26:18	Track (	482.7,	172.4)	@ 04/06 17:10:00	Track emerging region							
04/07 06:36:18 - 04/09 09:40:00	Track (	585.8,	164.3)	@ 04/07 06:36:00	Track emerging region							
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

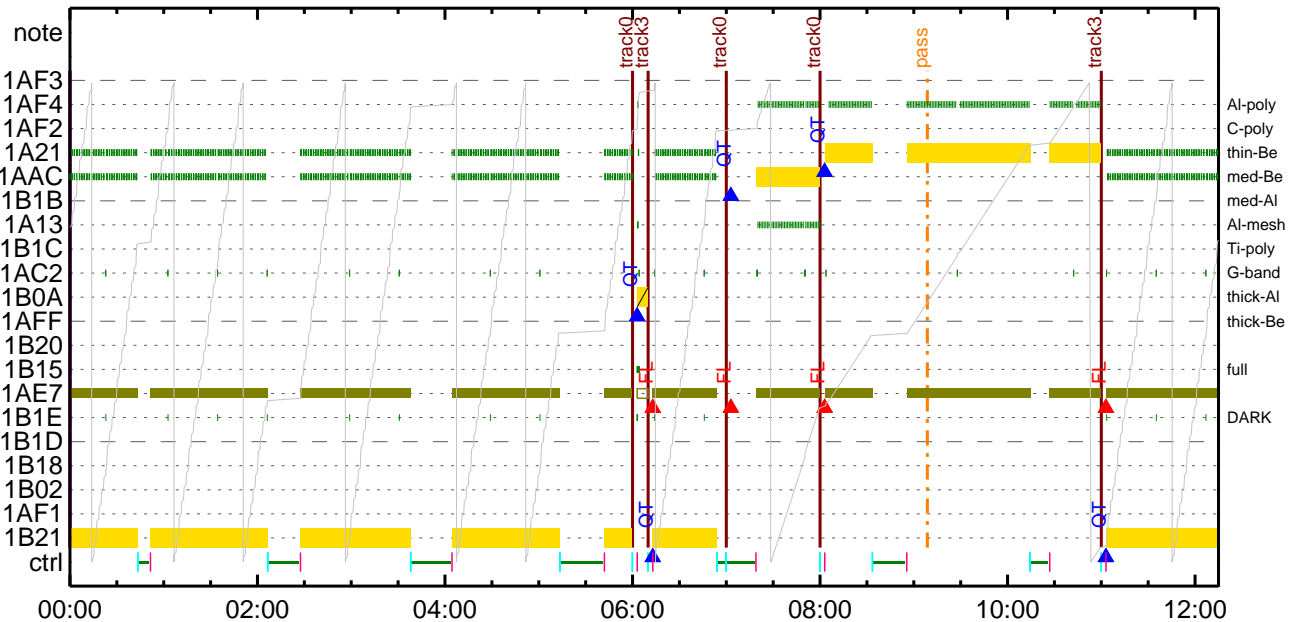
### CMDI #0830 2016/04/05



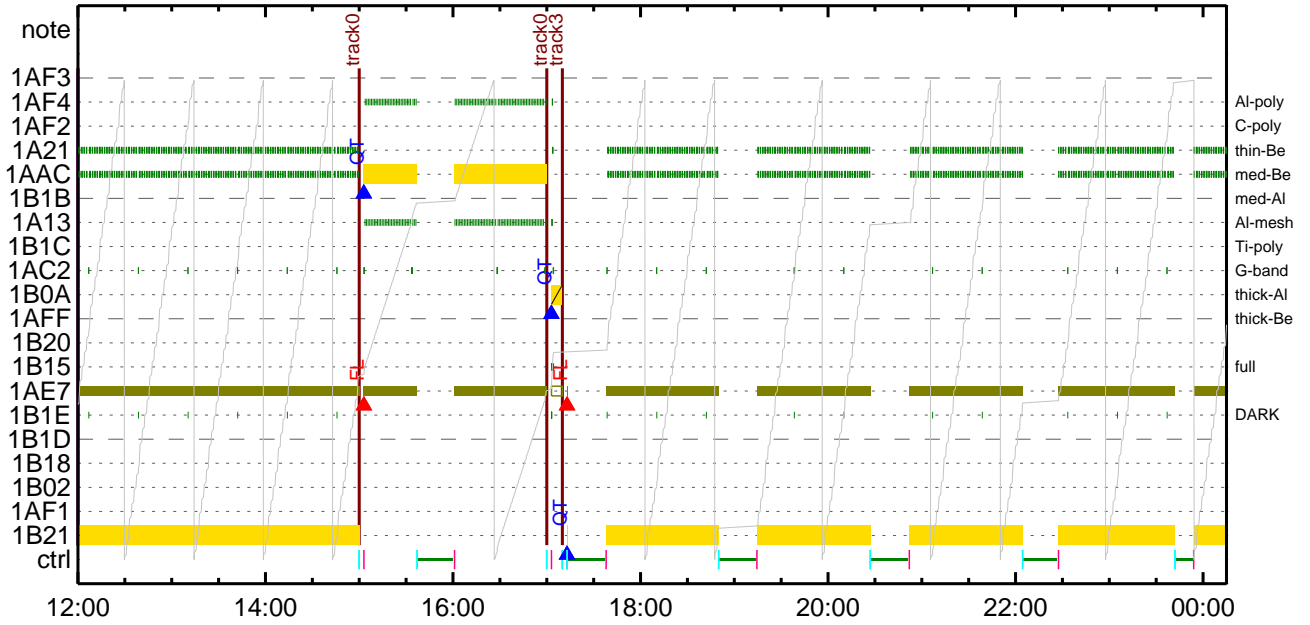
### CMDI #0830 2016/04/05



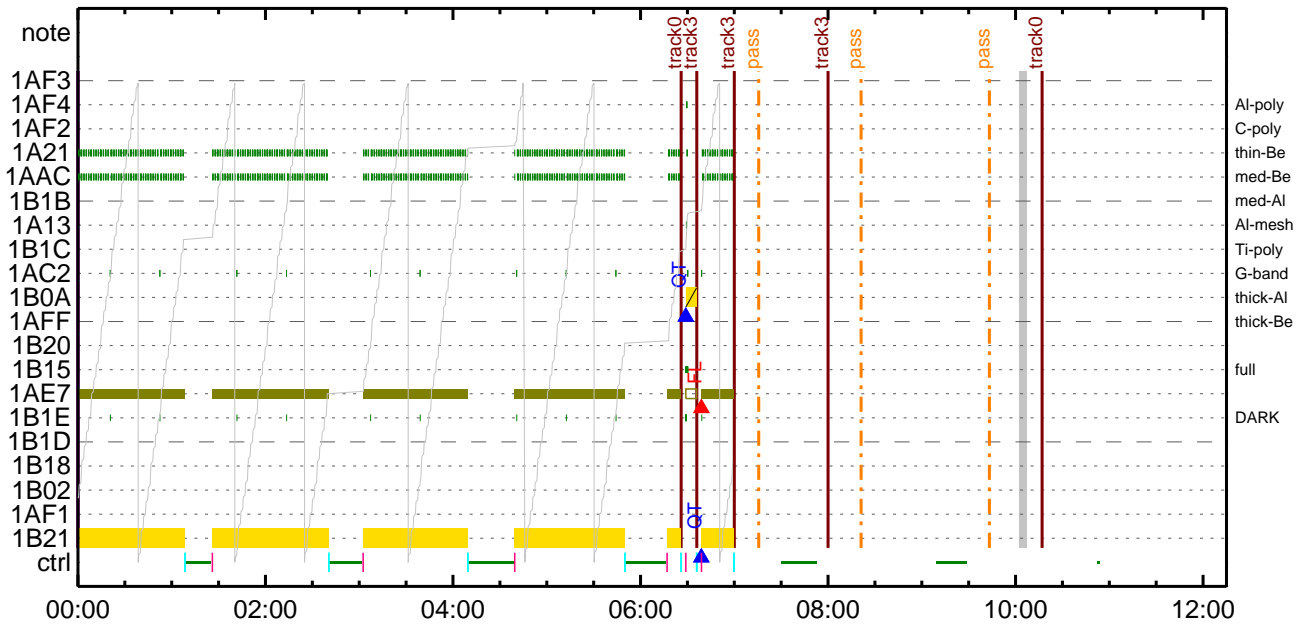
### CMDI #0830 2016/04/06



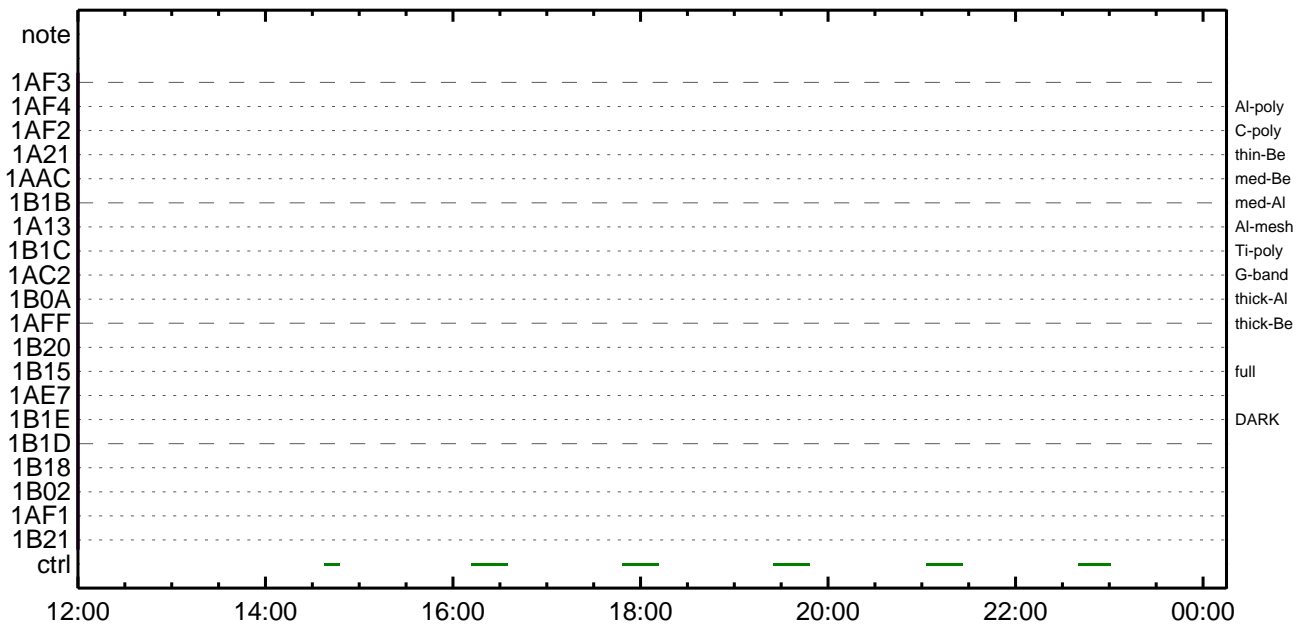
### CMDI #0830 2016/04/06



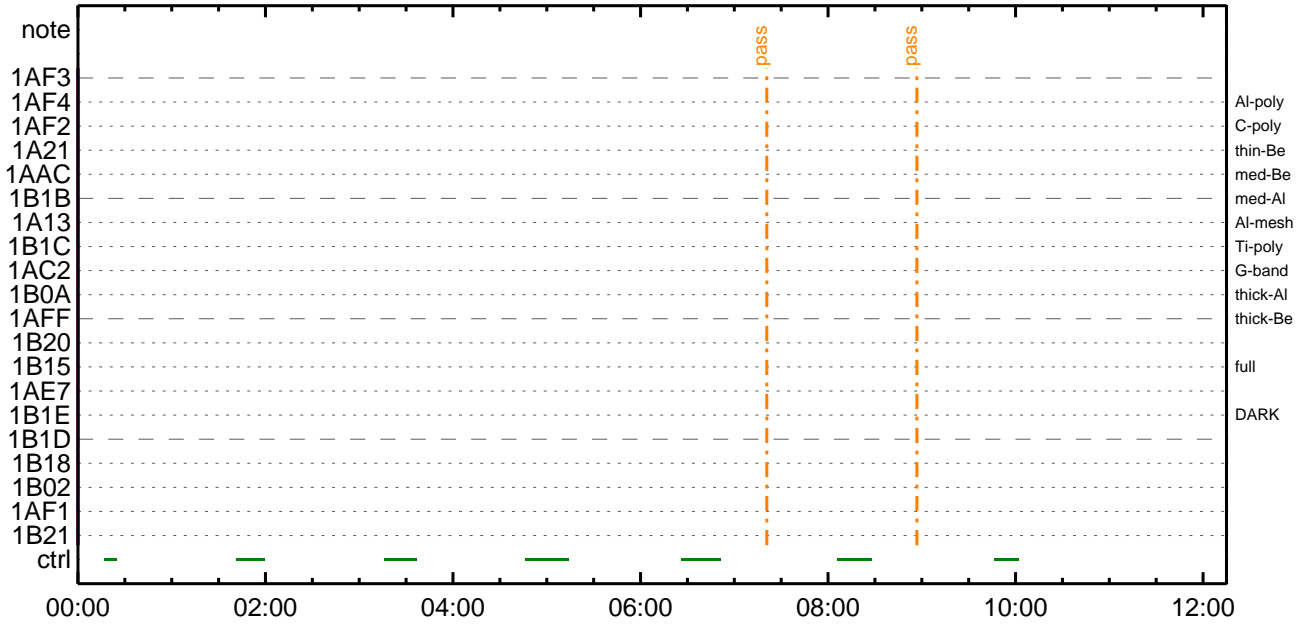
### CMDI #0830 2016/04/07



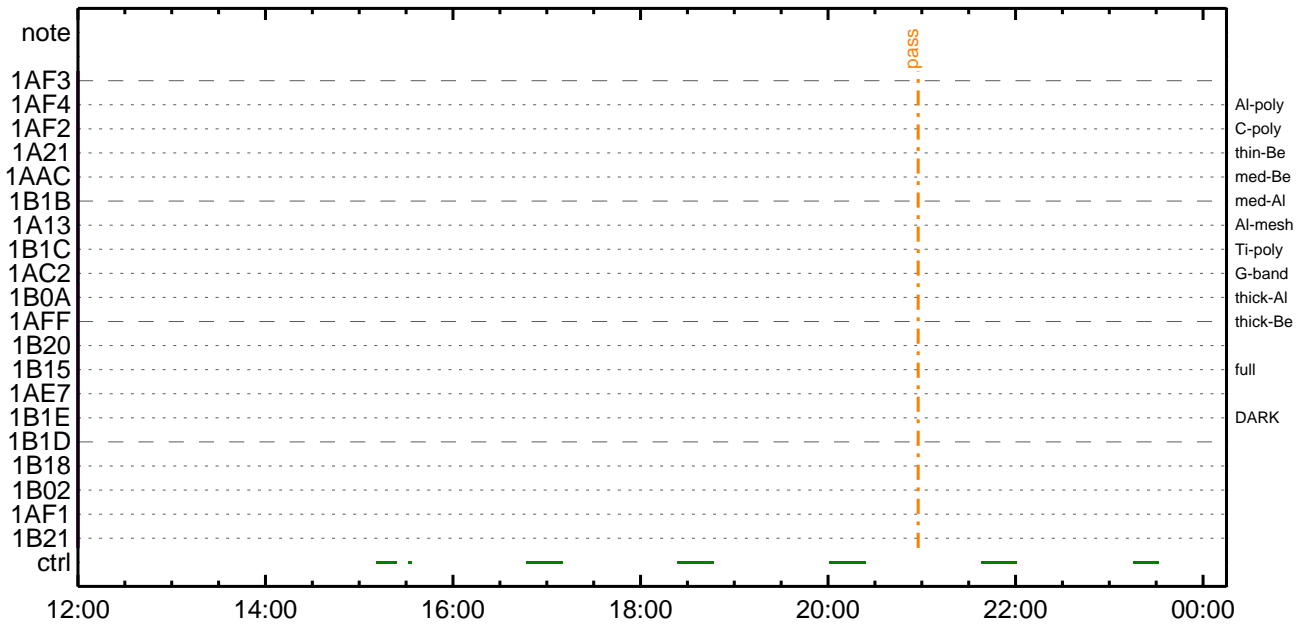
### CMDI #0830 2016/04/07



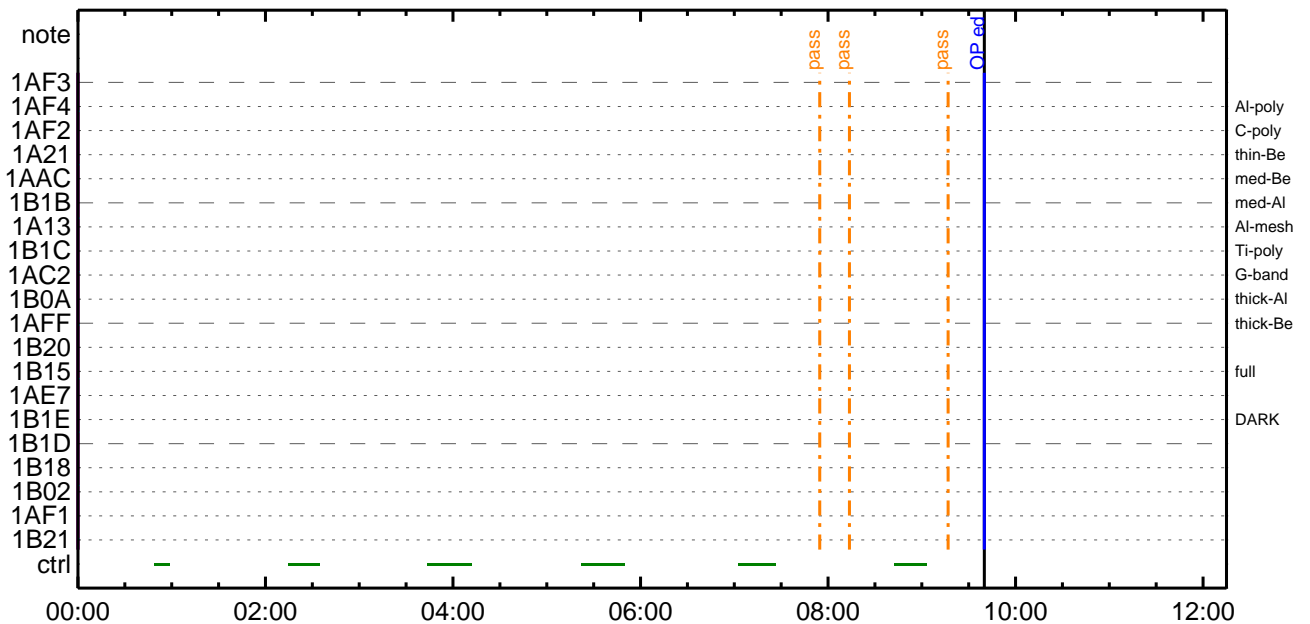
CMDI #0830 2016/04/08

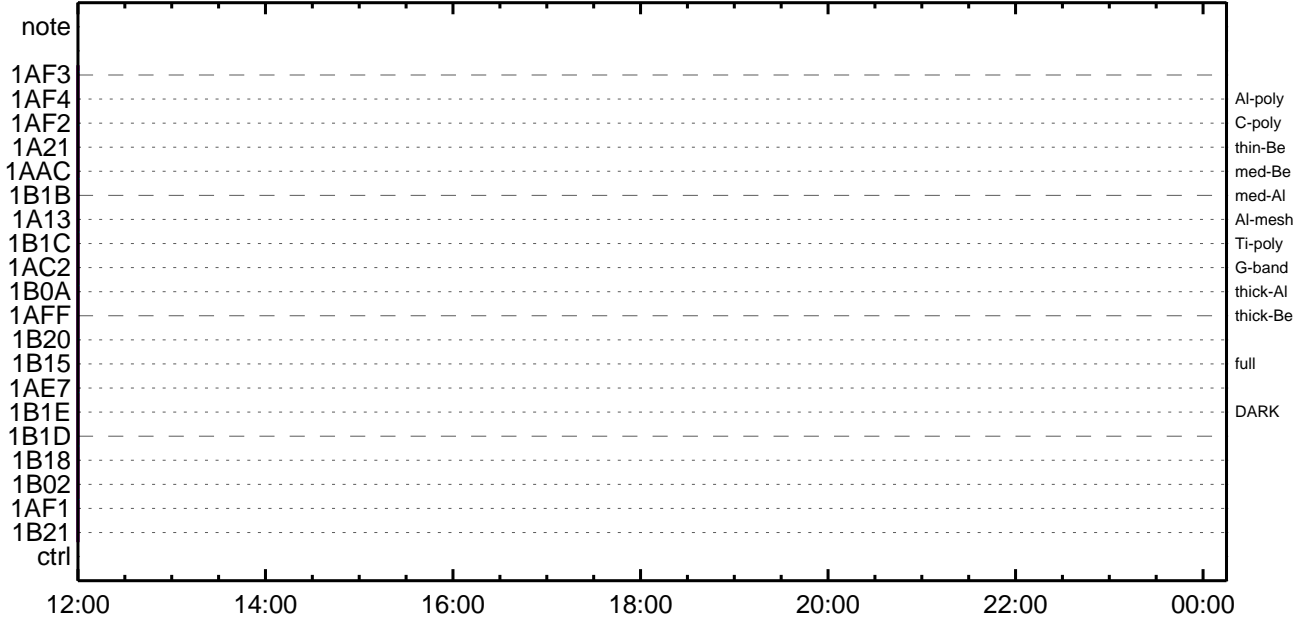


CMDI #0830 2016/04/08



CMDI #0830 2016/04/09









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0096 C.
0097 C.
0098 . C. *****
0099 C. OP/OGY1;4YE;|YAY6Yx
0100 C. *****
0101 C.
0102 . C. ;äOP/OGY1;4YE;ä
0103 . S. OP op-773:OP
0104 ()
0105 . S. OG og-773:OG
0106 ()
0107 C.
0108 . C. ;änMOG&OPfî°èYAY6Yx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAY6Yx½a î»ð³ îç§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG¼î¼E¹ç•è² îOK¼ð³ îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAY6Yx½a î»ð³ îç§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG¼î¼E¹ç•è² îOK¼ð³ îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAY6Yx½a î»ð³ îç§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OP¼î¼E¹ç•è² îOK¼ð³ îç§
0165 C.
0166 . C. ***** òE²¼o î¼Ä´¼À°EÈ¬o Æ÷;¿@ (¼åµ-YAY6Yx½e¼çoðÃÒÃæoç¼a°¼òE¼i¹çoç¼â) *****
0167 C. DHUÿâ;¼YE;È¼Y¼, Y;¼YE;Èoðîä¼¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ; § OPOG UPLOAD¼¬Á÷¿@NG¼î¼i¹ç;ç°E²¼o îTI-CMDÁ÷¿@¼î¼Á¹Ò¼•¼E¼o¼³¼E; f
0180 C. ¼P¼¿;çSET¼EDUMP¼î¼E±°i¼Y¼¹¼ç¹Ò¼|¼³¼E; f
0181 C.
0182 . C. TIY³Y½Y6YÈ¼ððÄî¿¿(UT)
0183 +. TI 2016-04-05 09:57:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-04-05 09:57:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-04-05 09:57:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2016-04-05 10:01:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼□îÄë%îîñ□îŷÄŷ§ŷÄŷ-¹âiü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»□ð³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]                        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL□îî¼È¹ç•è²îOK□ð³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;È□ðîâ□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2016-04-05 10:01:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2016-04-05 10:01:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2016-04-05 10:01:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2016-04-05 10:01:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃî□î»ö¼ŷ□èÄð□¹èDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷèèÈ¼□□¼Ä»Û□¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñ□è'Ø□¹èDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 BC (cd 01 b1 b1 04 04)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 02 b1 b1 08 08)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 03 b1 b1 08 08)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 04 b1 b1 06 06)
0103 + DC 07-F0 MDP_XRT_ROI_SET
0104 BC (cd 06 85 83 06 06)
0105 + DC 07-F0 MDP_XRT_ROI_SET
0106 BC (cd 07 80 80 08 08)
0107 + DC 07-F0 MDP_XRT_ROI_SET
0108 BC (cd 08 80 80 20 20)
0109 + DC 07-F0 MDP_XRT_ROI_SET
0110 BC (cd 09 80 80 20 08)
0111 + DC 07-F0 MDP_XRT_ROI_SET
0112 BC (cd 0a 80 80 08 20)
0113 + DC 07-F0 MDP_XRT_ROI_SET
0114 BC (cd 0f 80 80 06 06)
0115 + DC 07-F0 MDP_XRT_ROI_SET
0116 BC (cd 10 80 80 08 08)
0117 + DC 07-F0 MDP_XRT_FLD_ENA
0118 BC (d8)
0119 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0120 BC (c8)
0121 + DC 07-F0 MDP_XRT_ARS_DIS
0122 BC (d5)
0123 + DC 07-F0 MDP_XRT_AEC_RESET
0124 BC (d0)
0125 + DC 07-F0 MDP_XRT_FLD_RESET
0126 BC (da)
0127 + DC 07-F0 MDP_XRT_QT_PROG_SET
0128 BC (c4 04)
0129 + DC 07-F0 MDP_XRT_FL_PROG_SET
0130 BC (c5 07)
0131 . C. ----- Success Verify ? OK / NG ____
0132 C.
0133 C.
0134 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0135 C.
0136 + DC 07-F0 MDP_XRT_MODE_OBSV
0137 BC (c2)
0138 + TI 2016-04-05 10:01:02.0
0139 DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 . C. ----- Success Verify ? OK / NG ____
0142 C.
0143 C. ***** XRT END *****
0144 C.
0145 . C. ***** MDP `ûÃîñî»ò%ÿñÊÂð¹ñèDCBC•x²è *****
0146 C. (%ã°îÿÓÿÃÿÈÿPÿÈÿÿãÿçÿèñE%¼ññ¼Ã»Ûñ¹ñè)
0147 . S. DC-BC dcbc-402:DCBC
0148 (MDP_known_event)
0149 C.
0150 C.
0151 . C. ***** ÿÐÿ¹•Ï Daily±;îÑñÊ´Øñ¹ñèDCBC•x²è *****
0152 . S. DC-BC dcbc-153:DCBC
0153 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0154 C.
0155 C.
0156 . C. ;ãLOSÿÃÿSÿÿÿ¼Ã»Û;ã
0157 C.
0158 . C. ***** LOS *****
0159 C.

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\*\*\* OP Sequence for XRT \*\*\*

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2016/04/05 10:05:00.0 XRT_CTRL_MANU_422_OG [0x1a6]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/05 10:05:10.0 XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                        XRT_FOCUS_RECAL 2 07-F8 78 00
2016/04/05 10:09:10.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/04/05 10:11:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/05 10:11:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/05 10:11:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/04/05 10:12:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 2a 0c b7 a6
2016/04/05 10:12:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/04/05 10:12:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/04/05 10:12:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/04/05 10:12:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/04/05 10:12:26.0 XRT_FLD_RESET_433_OG [0x1b1]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/04/05 10:14:56.0 XRT_QT_PROG_SET_439_OG [0x1b7]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 11
2016/04/05 10:14:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/04/05 10:15:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/04/05 10:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/05 10:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/05 10:59:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/04/05 11:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 ac cd
2016/04/05 11:00:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/04/05 11:00:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/04/05 11:00:22.0 XRT_AEC_RESET_407_OG [0x197]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/04/05 11:00:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/04/05 11:00:26.0 XRT_FLD_RESET_414_OG [0x19e]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/04/05 11:02:56.0 XRT_QT_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0c
2016/04/05 11:02:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/04/05 11:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/04/05 11:15:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/05 11:15:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/05 11:15:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/04/05 11:16:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 d6 67
2016/04/05 11:16:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/04/05 11:16:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/04/05 11:16:22.0 XRT_AEC_RESET_407_OG [0x197]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2016/04/05 11:16:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2016/04/05 11:16:26.0 XRT_FLD_RESET_414_OG [0x19e]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2016/04/05 11:18:56.0 XRT_QT_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0c
2016/04/05 11:18:58.0 XRT_FL_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 07
2016/04/05 11:19:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2016/04/05 11:31:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/05 11:31:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2016/04/05 11:31:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2016/04/05 11:32:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM 5 02-76 00 00 00 00 00
2016/04/05 11:32:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2016/04/05 11:32:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2016/04/05 11:32:22.0 XRT_AEC_RESET_407_OG [0x197]

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## XRT\_OGLIST\_0830.chk

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2016/04/05	11:32:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/05	11:32:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/05	11:34:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2016/04/05	11:34:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/05	11:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/05	11:47:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	11:47:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	11:47:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/04/05	11:48:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	00	00	29	99
2016/04/05	11:48:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/05	11:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/05	11:48:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/05	11:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/05	11:48:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/05	11:50:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2016/04/05	11:50:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/05	11:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/05	12:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	12:03:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	12:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/04/05	12:04:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	00	00	53	33
2016/04/05	12:04:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/05	12:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/05	12:04:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/05	12:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/05	12:04:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/05	12:06:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2016/04/05	12:06:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/05	12:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/05	12:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	12:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	12:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/04/05	12:20:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	d6	36	b7	8e
2016/04/05	12:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/05	12:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/05	12:20:22.0	XRT_AEC_RESET_407_OG [0x197]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/05	12:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/05	12:20:26.0	XRT_FLD_RESET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/05	12:22:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2016/04/05	12:22:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/04/05	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/05	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	12:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/04/05	12:30:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	b4	b5	db	75
2016/04/05	12:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				



2016/04/05	12:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/05	12:30:22.0	XRT_AEC_RESET_407_OG [0x197]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/05	12:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/05	12:30:26.0	XRT_FLD_RESET_414_OG [0x19e]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/05	12:32:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2016/04/05	12:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/05	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/05	12:45:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	12:45:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	12:45:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/05	12:46:00.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 ac 5b 00 00				
2016/04/05	12:46:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/05	12:46:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/05	12:46:22.0	XRT_AEC_RESET_407_OG [0x197]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/05	12:46:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/05	12:46:26.0	XRT_FLD_RESET_414_OG [0x19e]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/05	12:48:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2016/04/05	12:48:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/05	12:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/05	13:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	13:01:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	13:01:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/05	13:02:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 b4 b5 24 8b				
2016/04/05	13:02:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/05	13:02:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/05	13:02:22.0	XRT_AEC_RESET_407_OG [0x197]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/05	13:02:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/05	13:02:26.0	XRT_FLD_RESET_414_OG [0x19e]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/05	13:04:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2016/04/05	13:04:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/05	13:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/05	13:17:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	13:17:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	13:17:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/05	13:18:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 d6 36 48 72				
2016/04/05	13:18:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/05	13:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/05	13:18:22.0	XRT_AEC_RESET_407_OG [0x197]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/05	13:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/05	13:18:26.0	XRT_FLD_RESET_414_OG [0x19e]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/05	13:20:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2016/04/05	13:20:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/05	13:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/05	13:27:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	13:27:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/05	13:27:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/05	13:28:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]							

2016/04/05	13:28:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	29	ca	b7	8e
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/04/05	13:28:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2016/04/05	13:28:22.0	XRT_AEC_RESET_407_OG [0x197]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2016/04/05	13:28:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/04/05	13:28:26.0	XRT_FLD_RESET_414_OG [0x19e]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/04/05	13:30:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]		2	07-F0	c4	0c			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0c			
2016/04/05	13:30:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	07			
2016/04/05	13:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/04/05	13:37:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/04/05	13:37:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/04/05	13:37:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2016/04/05	13:38:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]		5	02-76	00	4b	4b	db	75
		AOCU_NM		5	02-76	00	4b	4b	db	75
2016/04/05	13:38:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/04/05	13:38:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2016/04/05	13:38:22.0	XRT_AEC_RESET_407_OG [0x197]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2016/04/05	13:38:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/04/05	13:38:26.0	XRT_FLD_RESET_414_OG [0x19e]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/04/05	13:40:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]		2	07-F0	c4	0c			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0c			
2016/04/05	13:40:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	07			
2016/04/05	13:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/04/05	13:53:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/04/05	13:53:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/04/05	13:53:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2016/04/05	13:54:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]		5	02-76	00	53	a5	00	00
		AOCU_NM		5	02-76	00	53	a5	00	00
2016/04/05	13:54:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/04/05	13:54:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2016/04/05	13:54:22.5	XRT_AEC_RESET_407_OG [0x197]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2016/04/05	13:54:24.5	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/04/05	13:54:26.5	XRT_FLD_RESET_414_OG [0x19e]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/04/05	13:56:56.5	XRT_QT_PROG_SET_440_OG [0x1b8]		2	07-F0	c4	0c			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0c			
2016/04/05	13:56:58.5	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	07			
2016/04/05	13:57:00.5	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/04/05	14:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/04/05	14:09:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/04/05	14:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2016/04/05	14:10:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]		5	02-76	00	4b	4b	24	8b
		AOCU_NM		5	02-76	00	4b	4b	24	8b
2016/04/05	14:10:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/04/05	14:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2016/04/05	14:10:22.0	XRT_AEC_RESET_407_OG [0x197]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2016/04/05	14:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/04/05	14:10:26.0	XRT_FLD_RESET_414_OG [0x19e]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/04/05	14:12:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]		2	07-F0	c4	0c			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0c			
2016/04/05	14:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	07			
2016/04/05	14:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/04/05	14:25:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/04/05	14:25:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				

2016/04/05	14:25:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/04/05	14:26:00.0	AOCS_ORe-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	00	29	db	48 72
2016/04/05	14:26:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2016/04/05	14:26:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2016/04/05	14:26:22.0	XRT_AEC_RESET_407_OG [0x197] MDP_XRT_AEC_RESET	1	07-F0		d0		
2016/04/05	14:26:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2016/04/05	14:26:26.0	XRT_FLD_RESET_414_OG [0x19e] MDP_XRT_FLD_RESET	1	07-F0		da		
2016/04/05	14:28:56.0	XRT_QT_PROG_SET_440_OG [0x1b8] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2016/04/05	14:28:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/04/05	14:29:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2016/04/05	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/04/05	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/04/05	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/04/05	15:00:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	03	00	00	00 00
2016/04/05	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2016/04/05	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2016/04/05	15:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0		d0		
2016/04/05	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2016/04/05	15:00:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0		da		
2016/04/05	15:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa] MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2016/04/05	15:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/04/05	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2016/04/05	15:03:00.5	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/04/05	15:03:02.5	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/04/05	15:03:04.5	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2016/04/05	15:03:06.5	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2016/04/05	15:06:14.5	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2016/04/05	15:15:30.0	XRT_Custom_430_OG [0x1ae]						
2016/04/05	15:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2016/04/05	16:38:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/04/05	16:38:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/04/05	16:38:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2016/04/05	16:38:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2016/04/05	16:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2016/04/05	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/04/05	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2016/04/05	16:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/04/05	17:00:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	03	00	00	00 00
2016/04/05	17:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2016/04/05	17:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2016/04/05	17:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0		d0		
2016/04/05	17:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2016/04/05	17:00:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0		da		
2016/04/05	17:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa] MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2016/04/05	17:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/04/05	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2016/04/05	17:51:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		

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2016/04/05	17:51:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	17:51:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/04/05	17:52:00.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00 00 00 00 00
2016/04/05	17:52:18.0	XRT_FLD_DIS_406_OG [0x196] MDP_XRT_FLD_DIS	1	07-F0	d9
2016/04/05	17:54:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/04/05	17:54:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/05	17:54:58.0	XRT_QT_PROG_SET_417_OG [0x1a1] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2016/04/05	17:55:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	18:01:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	18:01:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	18:01:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/04/05	18:02:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	03 00 00 00 00
2016/04/05	18:02:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2016/04/05	18:02:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/04/05	18:02:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2016/04/05	18:02:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2016/04/05	18:02:26.0	XRT_FLD_RESET_433_OG [0x1b1] MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	18:04:56.0	XRT_QT_PROG_SET_426_OG [0x1aa] MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/04/05	18:04:58.0	XRT_FL_PROG_SET_436_OG [0x1b4] MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/04/05	18:05:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	18:15:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	18:15:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	18:15:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	18:15:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	18:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	18:38:30.0	XRT_Custom_430_OG [0x1ae]			
2016/04/05	18:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	19:52:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	19:52:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	19:52:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	19:52:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	19:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	20:15:30.0	XRT_Custom_430_OG [0x1ae]			
2016/04/05	20:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	21:29:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	21:29:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	21:29:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	21:29:36.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	21:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	21:52:30.0	XRT_Custom_430_OG [0x1ae]			
2016/04/05	21:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/05	23:07:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	23:07:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/05	23:07:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/05	23:07:06.0	XRT_PREFLR_STRT_432_OG [0x1b0] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/05	23:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/05	23:25:00.0	XRT_Custom_430_OG [0x1ae]			
2016/04/05	23:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/06	00:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			

2016/04/06	00:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	00:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	00:43:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	00:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/06	00:50:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/06	00:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/06	02:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	02:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	02:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	02:06:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	02:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/06	02:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/06	02:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/06	03:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	03:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	03:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	03:38:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	03:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/06	04:03:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/06	04:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/06	05:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	05:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	05:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	05:13:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	05:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/06	05:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/06	05:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/06	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	06:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2016/04/06	06:00:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00	
2016/04/06	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2016/04/06	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2016/04/06	06:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/06	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2016/04/06	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/06	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	06:10:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/04/06	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00	
2016/04/06	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/06	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/06	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/06	06:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/06	06:12:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	06:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2016/04/06	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2016/04/06	06:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	06:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	06:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	06:54:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/06	06:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/06	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	06:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	06:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/04/06	07:00:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 fa da 54 e5	
2016/04/06	07:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/06	07:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/06	07:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/06	07:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/06	07:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	07:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2016/04/06	07:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/04/06	07:18:00.0	XRT_Custom_430_OG [0x1ae]					
2016/04/06	07:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/06	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	07:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	07:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/04/06	08:00:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 fa da 54 e5	
2016/04/06	08:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/04/06	08:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/04/06	08:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/04/06	08:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/04/06	08:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	08:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2016/04/06	08:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2016/04/06	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/06	08:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	08:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	08:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	08:33:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/06	08:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/06	08:54:30.5	XRT_Custom_430_OG [0x1ae]					
2016/04/06	08:55:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/06	10:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	10:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	10:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/04/06	10:14:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/04/06	10:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/04/06	10:26:00.0	XRT_Custom_430_OG [0x1ae]					
2016/04/06	10:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/04/06	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/04/06	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2016/04/06	11:00:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03 00 00 00 00	

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2016/04/06	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/06	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/06	11:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/06	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/06	11:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/06	11:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/04/06	11:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/06	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/06	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/06	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/06	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/06	15:00:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00 fa da 54 e5				
2016/04/06	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/06	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/06	15:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/06	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/06	15:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/06	15:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2016/04/06	15:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/04/06	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/06	15:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/06	15:37:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/06	15:37:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/06	15:37:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/04/06	15:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/04/06	16:00:00.0	XRT_Custom_430_OG [0x1ae]							
2016/04/06	16:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/06	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/06	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/06	16:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/04/06	17:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/04/06	17:00:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/04/06	17:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/04/06	17:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/06	17:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2016/04/06	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/04/06	17:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/06	17:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/04/06	17:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/04/06	17:10:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/04/06	17:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/04/06	17:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/04/06	17:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/04/06	17:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/04/06	17:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/04/06	17:12:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2016/04/06	17:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				

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2016/04/06	17:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/06	17:13:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	17:13:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	17:13:04.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/06	17:13:06.5	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/06	17:16:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/06	17:37:00.0	XRT_Custom_430_OG [0x1ae]			
2016/04/06	17:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/06	18:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	18:50:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	18:50:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/06	18:50:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/06	18:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/06	19:13:30.0	XRT_Custom_430_OG [0x1ae]			
2016/04/06	19:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/06	20:27:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	20:27:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	20:27:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/06	20:27:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/06	20:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/06	20:51:00.0	XRT_Custom_430_OG [0x1ae]			
2016/04/06	20:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/06	22:04:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	22:04:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	22:04:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/06	22:04:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/06	22:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/06	22:26:30.0	XRT_Custom_430_OG [0x1ae]			
2016/04/06	22:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/06	23:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	23:42:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/06	23:42:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/06	23:42:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/06	23:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/06	23:53:00.0	XRT_Custom_430_OG [0x1ae]			
2016/04/06	23:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/07	01:08:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/07	01:08:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/07	01:08:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/07	01:08:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/07	01:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/07	01:25:00.0	XRT_Custom_430_OG [0x1ae]			
2016/04/07	01:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/04/07	02:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/07	02:40:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/04/07	02:40:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2016/04/07	02:40:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/04/07	02:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/04/07	03:01:30.0	XRT_Custom_430_OG [0x1ae]			
2016/04/07	03:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0



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2016/04/07	04:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	04:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	04:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/04/07	04:09:36.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/04/07	04:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/04/07	04:38:30.0	XRT_Custom_430_OG [0x1ae]						
2016/04/07	04:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/04/07	05:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	05:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	05:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/04/07	05:50:06.0	XRT_PREFLR_STRT_432_OG [0x1b0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/04/07	05:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/04/07	06:16:00.0	XRT_Custom_430_OG [0x1ae]						
2016/04/07	06:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/04/07	06:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	06:25:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	06:25:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/04/07	06:26:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00		
2016/04/07	06:26:18.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/04/07	06:28:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/04/07	06:28:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/04/07	06:28:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2016/04/07	06:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/04/07	06:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	06:35:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	06:35:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/04/07	06:36:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03 00 00 00 00		
2016/04/07	06:36:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/04/07	06:36:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/04/07	06:36:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/04/07	06:36:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/04/07	06:36:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2016/04/07	06:38:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2016/04/07	06:38:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/04/07	06:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/04/07	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	06:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/04/07	07:00:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03 00 00 00 00		
2016/04/07	08:00:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03 00 00 00 00		
2016/04/07	10:17:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00		